

TSG-RAN Working Group 1 meeting No. 18  
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***TSGR1-01-0173***

**Source:** TSG-RAN WG1  
**To:** TSG-RAN WG4  
**Cc:** TSG-RAN WG2, TSG RAN WG3  
**Title:** LS on impact of compressed mode on DPCCH gating benefits.  
**Contact:** Sarah Boumendil, Nortel Networks  
Email: boumendi@nortelnetworks.com

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RAN WG1 would like to inform RAN WG4 of the discussions which took place during RAN WG1#18 in Boston regarding the interactions between DPCCH gating and compressed mode.

The current assumption in RAN WG1 is that DPCCH gating cannot be activated when a compressed mode pattern sequence is active. RAN WG1 would like to draw RAN WG4's attention to the fact that, regardless of the fact that there may be long periods between successive compressed frames, in the current concept DPCCH gating cannot be used during these periods between compressed frames. In particular this means that either the UTRAN will have to continuously activate DPCCH gating when compressed mode is off and deactivate DPCCH gating when compressed mode is on for UEs that require compressed mode to perform inter-frequency or inter-system measurements.

From RAN WG1's point-of-view this could lead to either a degradation of the battery savings benefits brought by DPCCH gating or to performance degradation in mobility management. Therefore, to proceed further with the introduction of this feature in RAN WG1 release 4 specifications, RAN WG1 would like to seek guidance from RAN WG4 on the following question.

What is the foreseen use of compressed mode in terms of the percentage of time when the compressed mode is active? This question should be envisaged from the point of view of the percentage of time when at least one compressed mode pattern sequence is active. RAN WG1 understands that configuration of the compressed mode in terms of the percentage of compressed frames and the length of time when compressed mode is active is an issue for the operator, however, RAN WG4 may have some view on preferred configurations i.e. those leading to less degradation in terms of capacity.