

Agenda Item:

Source: TSG RAN WG1
To: TSG RAN WG2, WG3
Title: LS on DSCH TFCI Split Mode
Document for: Discussion and approval
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At RAN1#17 meeting held in Stockholm, the limitation of the current DSCH TFCI split mode was questioned with some proposed improvement [1]. The limitation identified by RAN1 was as follows:

The current specification does not support more than 32 TFCs for DCHs or DSCHs. Logical split can support variable bit length TFCI for DSCH and DCH, but logical split cannot support DSCH transmission over Iur.

However, it was recognized that RAN1's understanding has to be confirmed by WG2 and WG3, before continuing any further discussion. Therefore, RAN1 would like to ask RAN2 and RAN3 to confirm RAN1's understanding that

1. Hard split supports only up to 32 TFCs for DCHs and DSCHs, separately,
2. Logical split cannot support transmitting DSCH over Iur.

RAN1 would also like to ask RAN2 and RAN3 if, in RAN2's and RAN3's points of view, it is worth RAN1's studying the enhancement on hard split to support variable bit length TFCI for DCHs and DSCHs as a Rel. 5 issue.

Reference

[1] R1-00-1269, "Dynamic Split Mode for TFCI", Samsung Electronics Co., Ltd.