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Source: TSG-RAN WG1

To: TSG-RAN WG2

Cc: TSG-RAN WG3

Title: Response to LS (R2-002133) on TFCI in the case of invalid set of transport blocks and during DPCH synchronisation

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TSG-RAN WG1 would like to thank TSG-RAN WG2 for their answer on the LS on TFCI in the case of invalid set of transport blocks and during DPCH synchronisation. TSG RAN WG1 has discussed the TFCI values to be used in case of invalid set of transport blocks and during DPCH initialisation.

TSG RAN WG1 shares the views expressed by TSG RAN WG2 and TSG RAN WG3. TSG RAN WG1 has agreed to use the special TFCI word zero only during the transmission of an uplink power control preamble (PCP).

For the initialisation of downlink DPCH, TSG RAN WG1 assumes that a TFCI corresponding to a TFC of zero transport blocks should be used if available in the TFCS. If such a TFC is not available, this can be considered as invalid set of transport blocks and according to WG3 specifications, DTX shall be used in the TFCI field of the DL DPCH. Hence, TSG RAN WG1 sees no need to define a special TFCI for downlink DPCH initialisation.

TSG RAN WG1 kindly asks TSG RAN WG2 to introduce the changes corresponding to the use of the special TFCI word zero (i.e. TFCI word number zero before physical layer TFCI coding) during uplink PCP in their specifications.