

<h2>CHANGE REQUEST</h2>		Please see embedded help file at the bottom of this page for instructions on how to fill in this form correctly.	
25.224	CR	xxx	Current Version: 3.3.0
GSM (AA.BB) or 3G (AA.BBB) specification number ↑		↑ CR number as allocated by MCC support team	
For submission to: RAN#10 <small>list expected approval meeting # here</small> ↑	for approval <input checked="" type="checkbox"/>	Strategic <input type="checkbox"/>	<small>(for SMG use only)</small>
	for information <input type="checkbox"/>	non-strategic <input type="checkbox"/>	

Form: CR cover sheet, version 2 for 3GPP and SMG The latest version of this form is available from: <ftp://ftp.3gpp.org/Information/CR-Form-v2.doc>

Proposed change affects: (U)SIM ME UTRAN / Radio Core Network
(at least one should be marked with an X)

Source: CWTS **Date:** _____

Subject: CR for TS25.224 regarding 1.28 Mcps TDD

Work item: Low Chip Rate TDD option, Physical Layer

Category:	F Correction	<input type="checkbox"/>	Release: Phase 2	<input type="checkbox"/>
<small>(only one category shall be marked with an X)</small>	A Corresponds to a correction in an earlier release	<input type="checkbox"/>	Release 96	<input type="checkbox"/>
	B Addition of feature	<input checked="" type="checkbox"/>	Release 97	<input type="checkbox"/>
	C Functional modification of feature	<input type="checkbox"/>	Release 98	<input type="checkbox"/>
	D Editorial modification	<input type="checkbox"/>	Release 99	<input type="checkbox"/>
			Release 00	<input checked="" type="checkbox"/>

Reason for change: This CR collects the principally agreed wording of the changes necessary for introducing the feature 'Low Chip Rate TDD option' in the TS25.224. In its last revision it should be editorially changed to reflect the correct changes needed for the latest approved version of this specification.

Clauses affected: _____

Other specs affected:	Other 3G core specifications	<input type="checkbox"/>	→ List of CRs:	_____
	Other GSM core specifications	<input type="checkbox"/>	→ List of CRs:	_____
	MS test specifications	<input type="checkbox"/>	→ List of CRs:	_____
	BSS test specifications	<input type="checkbox"/>	→ List of CRs:	_____
	O&M specifications	<input type="checkbox"/>	→ List of CRs:	_____

Other comments: In this first version, a proposed structure has been included in this CR. The structure was copied from TS25.224 vers. 3.3.0. The sections on 'physical channels' and 'mapping of TrCH to physical channels' were duplicated to distinguish between the different chip rate options. In addition to that the proposals that were agreed in principle in WG1#14 were included in this CR.



<----- double-click here for help and instructions on how to create a CR.

2 References

<For clarity, this chapter will currently collect only the references that are needed in addition to the already existing abbreviations. In its last version this chapter has to be modified, so that it includes the revisions with respect to the latest versions of TS25.224.>

3 Abbreviations

<For clarity, this chapter will currently collect only the abbreviations that are needed in addition to the already existing abbreviations. In its last version this chapter has to be modified, so that it includes the revisions with respect to the latest versions of TS25.224.>

4 Physical layer procedures for the 3.84 Mcps option(TDD)

<No changes will be made in this chapter in this CR, only the title has to be changed. >

5 Physical layer procedures for the 1.28 Mcps option

5.1 Transmitter Power Control

5.1.1 Uplink Control

5.1.1.1 General limits

5.1.1.2 UpPTS

5.1.1.3 PRACH

5.1.1.4 DPCH and PUSCH

5.1.1.4.1 Out of synchronization handling

5.1.2 Downlink Control

5.1.2.1 P-CCPCH, PICH

5.1.2.2 S-CCPCH

5.1.2.3 DPCH, PDSCH

5.1.2.3.1 out of synchronisation handling

5.2 Timing Advance

5.2.1 With UL Synchronization

5.2.1.1 General limits

5.2.1.2 UpPTS

5.2.1.3 PRACH

5.2.1.4 DPCH and PUSCH

5.2.1.4.1 Out of synchronization handling

5.2.1.5 The establishment of uplink synchronization

5.2.1.5.1 Preparation of uplink synchronization (downlink synchronization)

5.2.1.5.2 Establishment uplink synchronization

5.2.1.6 Maintenance of uplink synchronisation

5.3 Synchronisation and Cell Search Procedures

5.3.1 Cell search

5.3.2 DCH synchronization

5.3.2.1 Synchronization primitives

5.3.2.1.1 General

5.3.2.1.2 Downlink synchronization primitives

5.3.2.1.3 Uplink synchronization primitives

5.3.2.2 Radio link monitoring

5.3.2.2.1 Downlink radio link failure

5.3.2.2.2 Uplink radio link failure/restore

5.4 (DTX) of Radio Frames

5.5 Downlink Transmit Diversity

5.5.1 Transmit Diversity for DPCH

5.5.2 Transmit Diversity for DwPTS

5.5.3 Transmit Diversity for FPACH

5.6 Random Access Procedure

5.6.1 Preparation of random access

5.6.2 Random access procedures

5.6.3 Random access collision