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## 2 References

*<For clarity, this chapter will currently collect only the references that are needed in addition to the already existing abbreviations. In its last version this chapter has to be modified, so that it includes the revisions with respect to the latest versions of TS25.223.>*

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## 3 Symbols and abbreviations

*<For clarity, this chapter will currently collect only the symbols and abbreviations that are needed in addition to the already existing ones. In its last version this chapter has to be modified, so that it includes the revisions with respect to the latest versions of TS25.223.>*

### 3.1 Symbols

### 3.2 Abbreviations

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## 4 General

In the following, a separation between the data modulation and the spreading modulation has been made. The data modulation is defined in clause 5 and the spreading modulation in clause 6.

**Table 1: Basic modulation parameters**

Chip rate	same as FDD basic chiprate: 3.84 Mchip/s	Low chiprate: 1.28 Mchip/s
Data modulation	QPSK	QPSK
Spreading characteristics	Orthogonal Q chips/symbol, where $Q = 2^p$ , $0 \leq p \leq 4$	Orthogonal Q chips/symbol, where $Q = 2^p$ , $0 \leq p \leq 4$

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## 5 Data modulation for the 3.84 Mcps option

*<No changes will be made in this chapter in this CR, only the title has to be changed. >*

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## 6 Data modulation for the 1.28 Mcps option

### 6.1 Symbol rate

### 6.2 Mapping of bits onto signal point constellation

#### 6.2.1 QPSK modulation

#### 6.2.2 8PSK modulation

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## 67 Spreading modulation

*<The numbering has to be changed. >*

### 7.5 Modulation for the 3.84 Mcps option

*<No changes will be made in this chapter in this CR, only the title and numbering have to be changed. >*

### 7.6 Modulation for the 1.28 Mcps option

#### 7.6.1 Combination of physical channels in uplink

#### 7.6.2 Combination of physical channels in downlink

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## 78 Synchronisation codes for the 3.84 Mcps option

*<No changes will be made in this chapter in this CR, only the title and numbering have to be changed. >*

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## 9 Synchronisation codes for the 1.28 Mcps option

### 9.1 The downlink pilot timeslot (DwPTS)

### 9.2 The uplink pilot timeslot (UpPTS)

### 9.3 Code Allocation

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## Annex B (Normative) Synchronisation sequence

### B.1 SYNC sequence

### B.2 SYNC1 sequence