**3GPP TSG-RAN WG1 Meeting #114 R1-23xxxxx**

**Toulouse, France, 21-25 August, 2023**

**Agenda Item: 9.17**

**Source: Moderator (Huawei)**

**Title: Summary of email discussion [Post114-38.212-NR\_cov\_enh2-Core]**

**Document for: Discussion and Decision**

# Introduction

This document summarizes the discussions on the 38.212 draft CR on further NR Coverage enhancement, and aims to stabilize the 38.212 draft CR.

[Post114-38.212-NR\_cov\_enh2-Core] Email discussion on Rel-18 draft CRs by September 7 – Editors

# First round discussions

This section summarize the first round email discussions on draft CR v00. Companies are encouraged to provide the first round views by 09/05 (Tuesday), 6:00am UTC, then we can update the draft CR accordingly for the next step discussions.

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| --- | --- |
| *Company* | *View* |
| Intel | Based on the following conclusion, dynamic waveform switching for Type 2 configured grant PUSCH is not supported. We suggest to remove the following highlighted text in the draft CR.   |  | | --- | | - 1 bit if the higher layer parameter *dynamicTransformPrecoderIndicationDCI-0-1* is configured to 'enabled ' and if the UE is configured to monitor DCI format 0\_1 with CRC scrambled by C-RNTI or CS-RNTI or MCS-C-RNTI, where the bit value of 0 indicates that transform precoder is enabled and the bit value of 1 indicates that transform precoder is disabled. For a DCI format 0\_1 with CRC scrambled by CS-RNTI and the value indicated by new data indicator field is 1, or for a DCI format 0\_1 with CRC scrambled by SP-CSI-RNTI, the bit is reserved.  - 0 bit otherwise. |   Conclusion  There is no consensus to support “Dynamic waveform switching to PUSCH transmissions with a Type 2 configured grant” in R18. |
| NTT DOCOMO | On Intel’s point above, we agree Type 2 CG is not supported. Meanwhile, we think Editor’s draft here is rather correct. Because we do not think DCI size should be changed depending on RNTI.  Our interpretation of the Conclusion is that DWS field (Transform precoder indicator) doesn’t have a valid meaning in its codepoint. It doesn’t necessarily mean the whole field shouldn’t exist. |
| vivo | According to following agreement, CS-RNTI is only supported with DWS for retransmissions in CG case:   |  | | --- | | **Agreement**  Dynamic waveform switching enhancement in R18 is applicable to PUSCH scheduled by DCI format 0\_1 or 0\_2 in PDCCH with CRC scrambled with C-RNTI, MCS-C-RNTI, or CS-RNTI with NDI=1.   * Note: The above does not imply that dynamic switching enhancement in R18 is applicable or not applicable to other cases of PUSCH (e.g. PUSCH transmission with a Type 1 or Type 2 configured grant, PUSCH scheduled by DCI format 0\_0). |   Therefore, some update is needed:   |  | | --- | | Transform precoder indicator – 0 or 1 bit  - 1 bit if the higher layer parameter *dynamicTransformPrecoderIndicationDCI-0-1* is configured to 'enabled ' and if the UE is configured to monitor DCI format 0\_1 with CRC scrambled by C-RNTI or CS-RNTI with the value indicated by new data indicator field is 1 or MCS-C-RNTI, where the bit value of 0 indicates that transform precoder is enabled and the bit value of 1 indicates that transform precoder is disabled. ~~For a DCI format 0\_1 with CRC scrambled by CS-RNTI and the value indicated by new data indicator field is 1, or for a DCI format 0\_1 with CRC scrambled by SP-CSI-RNTI, the bit is reserved.~~  - 0 bit otherwise. |   For the update below, could you clarify which agreement it is based on?   |  | | --- | | - DMRS sequence initialization – 0 bit if transform precoder is enabled; 1 bit if transform precoder is disabled or if the Transform precoder indicator field is present. |   Same comments for DCI 0\_2. |
| Sharp | Regarding the input by companies, we agree vivo that DWS is supported for CS-RNTI with NDI=1 (retransmission case).  Then, reserved bit is necessary for the case with CS-RNTI with NDI = “0” even though it is not supported. |
| CATT | Based on the agreements, our suggestion is as follows. The same applies for DCI format 0\_2.   |  | | --- | | Transform precoder indicator – 0 or 1 bit  - 1 bit if the higher layer parameter *dynamicTransformPrecoderIndicationDCI-0-1* is configured to 'enabled ' and if the UE is configured to monitor DCI format 0\_1 with CRC scrambled by C-RNTI or CS-RNTI with the value indicated by new data indicator field is 1 or MCS-C-RNTI, where the bit value of 0 indicates that transform precoder is enabled and the bit value of 1 indicates that transform precoder is disabled. For a DCI format 0\_1 with CRC scrambled by CS-RNTI and the value indicated by new data indicator field is 0~~1~~, or for a DCI format 0\_1 with CRC scrambled by SP-CSI-RNTI, the bit is reserved.  - 0 bit otherwise. | |
| Nokia, NSB | We share similar view as CATT. Alternatively, we simply replace “1” by “0” in “For a DCI format 0\_1 with CRC scrambled by CS-RNTI and the value indicated by new data indicator field is 0~~1~~, or for a DCI format 0\_1 with CRC scrambled by SP-CSI-RNTI, the bit is reserved.” |
| Intel2 | We agree with other companies that DWS is only supported for CG-PUSCH retransmission. The update from Vivo looks fine with us. We also do not think the following text is needed.   |  | | --- | | ~~For a DCI format 0\_1 with CRC scrambled by CS-RNTI and the value indicated by new data indicator field is 1, or for a DCI format 0\_1 with CRC scrambled by SP-CSI-RNTI, the bit is reserved~~ | |

# Second round discussions

TBD