**3GPP TSG RAN WG1 #112bis-e R1-230xxxx**

**e-Meeting, April 17th – April 26th, 2023**

**Agenda item:** 9.1.2

**Source:** Moderator (Samsung)

**Title:** Moderator Summary#2 on Rel-18 CSI enhancements: Round 1

**Document for:** Discussion and Decision

## Introduction

The scope given in the Rel-18 NR Evolved MIMO WID pertaining to CSI enhancement is as follows:

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| 1. Study, and if justified, specify CSI reporting enhancement for high/medium UE velocities by exploiting time-domain correlation/Doppler-domain information to assist DL precoding, targeting FR1, as follows:
	* Rel-16/17 Type-II codebook refinement, without modification to the spatial and frequency domain basis
	* UE reporting of time-domain channel properties measured via CSI-RS for tracking
2. Study, and if justified, specify enhancements of CSI acquisition for Coherent-JT targeting FR1 and up to 4 TRPs, assuming ideal backhaul and synchronization as well as the same number of antenna ports across TRPs, as follows:
	1. Rel-16/17 Type-II codebook refinement for CJT mTRP targeting FDD and its associated CSI reporting, taking into account throughput-overhead trade-off
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## Summary of companies’ views

Issue 1: CJT

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Issue 2: Doppler

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Issue 3: TDCP

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### Issue 1: Type-II codebook refinement for CJT

Table 1A Summary: issue 1

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| **#** | **Issue** | **Companies’ views** |
| 1.1 | [110bis-e] **Agreement**On the Type-II codebook refinement for CJT mTRP, regarding W2 quantization group, for each layer:* Support the following: (Alt1) One group comprises one polarization across all N CSI-RS resources (*C*group,phase=1, *C*group,amp=2)
	+ FFS: Amplitude quantization table enhancement
	+ For the amplitude group other than the group associated with the SCI, the reference amplitude is reported
* Working assumption: Alt3 is supported in addition to Alt1 (to be confirmed in RAN1#111)
	+ (Alt3). One group comprises one polarization for one CSI-RS resource with a common phase reference across N CSI-RS resources (Cgroup,phase=1, Cgroup,amp=2N)
		- For each of the (2N–1) amplitude groups (other than the group associated with the SCI), the reference amplitude is reported
* If the support Alt3 in addition to Alt1 is confirmed, only one of the two schemes will be a basic feature for UEs supporting Rel-18 Type-II CJT codebook

**Proposal 1.A.1**: On the Type-II codebook refinement for CJT mTRP, *revert* the following working assumption: * Working assumption: Alt3 is supported in addition to Alt1 (to be confirmed in RAN1#111)
	+ (Alt3). One group comprises one polarization for one CSI-RS resource with a common phase reference across N CSI-RS resources (Cgroup,phase=1, Cgroup,amp=2N)
		- For each of the (2N–1) amplitude groups (other than the group associated with the SCI), the reference amplitude is reported

**FL Note**: Just as what we did in RAN1#110bis-e, this has to be decided based on empirical evidence (i.e. SLS results). Per agreement this needs to be concluded in this meeting. Since the WA was made conditioned upon the benefit of Alt3 over Alt1* If there is no confirmed benefit from Alt3 over Alt1 in the alleged scenarios (inter-site CJT, 500m ISD), the WA should be **reverted** (hence no support of Alt3).
* Otherwise, **confirmed** as an agreement.

The available SLS results are summarized as follows for the alleged “missing” scenarios from Alt3 proponents in RAN1#110bis-e (500m ISD or larger, inter-site CJT):* “Notable” (small in FL perspective) gain: Huawei (2-3% mean UPT), ZTE (0.2-1.2% mean UPT)
* No demonstrable gain: Samsung, vivo
 | **Support/fine (want to revert WA):** vivo, Samsung, OPPO, MediaTek, Fraunhofer IIS/HHI, Apple, DOCOMO, Intel, Nokia/NSB, Ericsson, Sharp, Google, Sony, AT&T**Not support (want to confirm WA)**: ZTE, Spreadtrum, CATT, LG, Huawei/HiSi, Lenovo, Fujitsu, NEC, Xiaomi,  |
| 1.2 | [112bis-e] **Agreement** On the Type-II codebook refinement for CJT mTRP, *for mode-1*, support the use of per-CSI-RS-resource FD basis selection offset (relative to a reference CSI-RS resource) for independent FD basis selection across *N* CSI-RS resources, i.e. (example formulation) $W\_{f,n}=diag(\left[1 e^{j\frac{2π}{N\_{3}}φ\_{n}}…. e^{j\frac{2π}{N\_{3}}(N\_{3}-1)φ\_{n}}\right]^{})W\_{f}$ where: * $W\_{f}$ is commonly selected across *N* CSI-RS resources
* $φ\_{n}$ is the layer-common FD basis selection offset for CSI-RS resource *n* relative to a layer-common reference CSI-RS resource $\tilde{n}$ with $φ\_{\tilde{n}}=0$
	+ Therefore, (*N* – 1) FD basis selection offset values $\left\{φ\_{n}\right\}\_{n\ne \tilde{n}}$ are reported
	+ Basic feature: $φ\_{n}\in \left\{0,1,2,…,N\_{3}-1\right\}$
	+ Optional feature: $φ\_{n}\in \left\{0,\frac{1}{4},\frac{1}{2}…,N\_{3}-\frac{1}{4}\right\}$
* FFS: UCI design details, details on $\tilde{n}$

**Question 1.B**: The only pending UCI design detailed issue is the reference CSI-RS resource $\tilde{n}$. Three proposals have been mentioned. Please share your preference (with technical justification of the benefits):* Alt1. Fixed to the first configured CSI-RS resource (lowest CSI-RS resource ID}
* Alt2. Selected by the UE and signalled together with the (N-1) FD window offsets
* Alt3. Derived from SCI(s) (note: SCI is layer-specific while this reference is layer-common)
 | **Alt1 (fixed):** **Alt2 (signalled):****Alt3 (derived from SCI):** |
| 1.3 | [112bis-e] **Agreement**On the Parameter Combination of Type-II codebook refinement for CJT mTRP, only the following linkages are supported (marked ‘x’), for Rel-16 eType-II based* For *NTRP* =1,
	+ fully reuse seven out of the eight Parameter Combinations from Rel-16 eType-II as indicated in the table below
		- FFS (by RAN1#112bis-e): whether to add one more Parameter Combination for L=4 based on the legacy Rel-16 eType-II FD combo {½, ½, ¼, ¼; ½} or the agreed FD combo {½, ½, ½, ½; ½}, or not to add from the indicated seven below
* ….

|  |  |  |
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| **NTRP** | **SD combo** | **FD combo {pv},** |
| {1/8, 1/8, 1/16, 1/16}, ¼ | {1/8, 1/8, 1/16, 1/16}, ½  | {1/4, ¼, 1/8, 1/8}, ¼  | {1/4, ¼, 1/8, 1/8}, ½  | {1/4, ¼, ¼, ¼}, ¾  | {1/2, ½, ½, ½}, ½  |
| 1 | 2 |  |  | x | x |  |  |
| 4 |  |  | x  | x | x |  |
| 6 w/ restriction |  |  |  | x | x |  |
| … |  |  |  |  |  |  |  |

**Question 1.C.5**: Please share your view on the following alternatives for the FFS point on Parameter Combination for NTRP=1 with technical justification (much preferably backed with SLS):* Alt0 (default outcome if no consensus between Alt1 and Alt2). Not adding another Parameter Combination in addition to the agreed seven
* Alt1. Add a combination based on the legacy Rel-16: L=4 and FD combo {**{pv};** {½, ½, ¼, ¼; ½}
* Alt2. Add a combination with L=4 and FD combo {**{pv};** {½, ½, ½, ½; ½}

**FL Note:** Alt0 is the default outcome if there is no consensus on this. | **Alt0:****Alt1:****Alt2:** |
| [112] **Agreement**On the Parameter Combination of Type-II codebook refinement for CJT mTRP, support linkage between the list of supported {*Ln*} combinations and list of supported {*pv,*} combinations via pairing each combination for {*pv,*} with at least one combination for {*Ln*}, for each *NTRP* value.* FFS (by RAN1#112bis-e): The exact list of supported pairs/linkage, or restriction of {*Ln*} when paired to each of {*pv,*}
* FFS (by RAN1#112bis-e): Whether/How to support configuration signalling for indicating the linkage
* Note: While no additional codebook parameter will be introduced, the total number of SD basis vectors across CSI-RS resources can still be used as a criterion for choosing the supported pairs/linkage

[111] **Agreement**On the Type-II codebook refinement for CJT mTRP, regarding the SD basis selection, for a configured value of *NTRP*, a set of *NL* combinations of values for {*L*1, ..., *LNTRP*} is gNB-configured via higher-layer (RRC) signaling* When *NL*>1, the selected combination of values for {*L*1, ..., *LNTRP*} is reported in CSI part 1 using an indicator, selected from the *NL* configured combinations
	+ *NL* =1 is one of the supported candidate values

…In regard to whether several FD combos can be configured for a UE, the following agreement clearly precludes this because: * Wording “the value” implies singular, not plural
* If >1 FD combo values can be configured as a part of the configured linkages, it allows dynamic (UE selection and reporting) FD combo selection which violates the agreement (gNB-configured by RRC configuration)

 [112] **Agreement**On the Type-II codebook refinement for CJT mTRP, for Rel-16-based refinement, support at least the following combinations of {pv,} from where *the value* of {*pv,*} is gNB-configured via higher-layer (RRC) signaling:…Therefore, to allow configuring multiple FD combos in relation to SD and linkages, the group would have to revert a previous agreement (not advisable in such a late stage). **Conclusion 1.C.2**: (**for clarification**) On the Parameter Combination of Type-II codebook refinement for CJT mTRP, no additional configuration signalling for indicating the linkage is needed. Per previous agreements (RAN1#111 and 112):* “The [single] value of {*pv,*} is gNB-configured via higher-layer (RRC) signalling”
* “[The] set of *NL* combinations of values for {*L*1, ..., *LNTRP*} is gNB-configured via higher-layer (RRC) signalling”

Such configuration shall be according to the supported/agreed linkages.**FL Note**: This conclusion is clarification in nature, clearly implied from previous agreements  |
| [112] **Agreement**On the Type-II codebook refinement for CJT mTRP, for Rel-16-based refinement, support *at least* the following combinations of {*Ln*} for the higher-layer-configured value of NTRP:

|  |  |
| --- | --- |
| **NTRP** | **{Ln} combination** |
| 1 | {2} |
| {4} |
| {6} (analogous to legacy, only for total # ports =32, rank 1-2, R=1) |
| 2 | {2,2} |
| {2,4}, {4,2} |
| {4,4} |
| 3 | {2,2,2} |
| {2,2,4}, {2,4,2}, {4,2,2} |
| {4,4,4} |
| 4 | {2,2,2,2} |
| {2,2,2,4}  |
| {2,2,4,4}  |
| {4,4,4,4} |

FFS: For *NTRP*>1, in addition to the supported combinations/permutations, whether to support at least one additional combination where at least one of the *Ln* values (*n*=1, …, *NTRP*) is 6**Conclusion 1.C.2:** On the Type-II codebook refinement for CJT mTRP, for Rel-16-based refinement, for *NTRP*>1, in addition to the supported SD combinations/permutations, there is no consensus on supporting at least one additional combination where at least one of the *Ln* values (*n*=1, …, *NTRP*) is 6**FL Note**: This was discussed offline [1] and the current situation* **Support/fine:** Huawei/HiSi, NTT DOCOMO (when N=1), ZTE, NEC (when N=1), CATT, CMCC (when N=1) , vivo (as long as Ltot≤16)
* **Not support/concern:** Samsung, Apple, MediaTek, LG, Spreadtrum, OPPO, Qualcomm, Intel, Xiaomi, AT&T, Nokia/NSB, Ericsson, Lenovo/MotM, Sony, Sharp, Google

Note that the conclusion (1.C.2) is based on the fact/reality that there is no consensus hence the implication follows whether one can accept (cope with) reality (that no consensus means no support) or not. |
| 1.4 | [112bis-e] **Agreement**On the Type-II codebook refinement for CJT mTRP, regarding CBSR, amplitude restriction is CSI-RS-resource-specific.* FFS: Whether CBSR is always configured for each CSI-RS resource or not

**Proposal 1.D.3:** On the Type-II codebook refinement for CJT mTRP, regarding CBSR, each of the NTRP configured CSI-RS resources can be ‘configured’ or ‘not configured’ with CBSR**FL Note: No CBSR config option per resource? (No company seems to have concern on this)*** **Yes:** Huawei/HiSi, NEC, Nokia/NSB
* **No:**
 | **Proposal 1.D.3:*** **Support/fine:** Huawei/HiSi, NEC, Nokia/NSB,
* **Not support:**
 |
| 1.5 | [112] **Agreement**On the Type-II codebook refinement for CJT mTRP, regarding UCI omission, down-select between the following three alternatives (by RAN1#112-bis where n denotes the n-th CSI-RS resource):* Alt1. Prio(,l,m,n)=($ \sum\_{k=0}^{n-1}2L\_{k}$) .N.RI.P(m)+N.RI.l(n)+N.n
	+ Note: This implies that CSI-RS resource is designated the highest priority
* Alt2. Prio(,l,m,n)=2L’.Q(n).RI.N3+2L’.RI. P(m)+RI.l(n)+
	+ Note: This implies that CSI-RS resource is designated the lowest priority (after FD basis)
	+ Note: L’ denotes the max value of Ln from all selected N CSI-RS resources
	+ FFS: Q(n) maps the index n according to a rule, e.g., Q(n)=n, or Q(n)=0 if n corresponds to strongest TRP/SCI.
* Alt3. Replace SD basis index *l* in legacy Prio calculation with $\sum\_{k=0}^{n-1}2L\_{k}+l\_{n}$, i.e., SD basis index over all resources: Prio(,l,m,n) = 2Ltot.RI.P(m)+ RI.$\sum\_{k=0}^{n-1}2L\_{k}$+RI.l(n)+

FFS: FD permutation P(.) as Rel-16-analogous, or no permutation i.e. P(m)=m**Proposal 1.E.1**: On the Type-II codebook refinement for CJT mTRP, regarding UCI omission, support reusing the legacy UCI omission mechanism while (Alt3) replacing SD basis index *l* in legacy Prio calculation with $\sum\_{k=0}^{n-1}2L\_{k}+l\_{n}$, i.e., SD basis index over all resources: Prio(,l,m,n) = 2Ltot.RI.P(m)+ RI.$\sum\_{k=0}^{n-1}2L\_{k}$+RI.l(n)+ * FFS: FD permutation P(.) as Rel-16-analogous, or no permutation i.e. P(m)=m

**FL Note**: This was discussed offline [1]. * Alt2 and Alt3 are almost equally supported
* Based on the available SLS results, Alt2 results in larger performance loss over Alt3 upon UCI overflow
* Alt2 opponents argue that since UE reporting of dynamic TRP selection is already supported, truncating CJT reporting to sTRP in case of UCI overflow is overkill and leaves NW with the least CSI for CJT operation (which is technically valid)

**FL Note**: The only available SLS results (Samsung) show that Alt3 performs slightly better than Alt2 upon UCI omissionAlt1: Samsung, NTT DOCOMO (2nd), *Apple*, MediaTek, Nokia/NSB (2nd), *IDC* Alt2: *ZTE, Fraunhofer IIS/HHI*, NEC, vivo (2nd), *Spreadtrum, OPPO, Qualcomm, CATT*, *Huawei/HiSi, Fujitsu*, Ericsson (2nd), *CMCC, Lenovo, Sony*  [1.E.1] Alt3: Samsung (2nd), NTT DOCOMO, MediaTek (2nd), LG, NEC (2nd), vivo, Intel, Xiaomi, Nokia/NSB, Ericsson, Google | **Proposal 1.E.1:*** **Support/fine:** Samsung, NTT DOCOMO, MediaTek (P=m), LG, NEC, vivo, Intel, Xiaomi, Nokia/NSB, Ericsson, Google, AT&T, ZTE (ok, 2nd pref though 1st pref is Alt2), OPPO, Qualcomm (P=m), CMCC, IDC, Sony, Apple,
* **Not support (want Alt2)**: Huawei/HiSi, Spreadtrum, CATT, Lenovo/MotM, Fujitsu, Fraunhofer IIS/HHI,
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Table 1B Type II CJT: summary of observation from SLS

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| **Company** | **SLS results** |
| **Issue #** | **Metric** | **Observation** |
| Huawei/HiSi | 1.1 | Mean UPT gain vs overhead | Observation 9: For inter-site CJT with large inter-site distance, Alt 3 (Cgroup,amp=2N) has better performance compared to Alt1 (Cgroup,amp=2). |
| 1.3 | Mean UPT gain vs overhead | For {Ln} combinations where each Ln equals 2, adding overhead by increasing pv and/or beta (such as {pv, beta} combo #3~#6) has no significant performance improvement compared with other {Ln} combinations.For a given NTRP, the {Ln} combinations with at least one Ln=4 have similar performance-overhead tradeoff. It may be hard to select some of the pairs. Therefore, it is more reasonable to configure {Ln} and {pv, beta} pairs based on gNB implementation other than predefined pairs/linkage.Observation 6: For {Ln} combinations where each Ln equals 2, adding overhead by increasing pv and/or beta has no significant performance improvement.Observation 7: The uneven {Ln} combination and its permutations with the same Ltot (such as {2,2,4},{2,4,2}, {4,2,2}) should be treated as one combination, due to the same overhead and performance with proper gNB configuration.Observation 8: Adding {Ln} combinations including Ln=6 does not increase the overhead and UE complexity as long as Ltot does not exceed the current maximum Ltot value, and can increase performance. |
| ZTE | 1.1 | Avg UPT gain vs overhead,5% UPT gain vs overhead | We observe that 0.2%~1.2% average UPT gain and 2.2%~12.1% cell-edge UE gain can be achieved using Alt 3 compared with Alt1. |
| 1.3 | Avg UPT gain vs overhead | Ln=6 combination pairs for NTRP=2/3 can also show good performance under medium & high overhead; then considering the CSI report overhead is still acceptable, we prefer to have them as in the candidate list for SD-basis.Then, clearly, pv = {1/2,1/2} combined with Ln={4,6} can provide good performance under medium & high overhead. |
| 1.5 | Avg UPT gain | That can be observed that, if going with Alt-2, n (n-th CSI-RS resource) should be taken as the most significant parameter (after FD basis), that is, fall-back to less co-ordinated TRP(s). That is beneficial for releasing some TRPs for serving other Ues, which is the reason why we observe some performance benefits for that. |
| Vivo | 1.1 | SE gain vs overhead | Alt3 shows negligible performance improvement over Alt1 for the scenario with 500m ISD and the high payload case of the scenario with 200m ISD.Combining the payload and the SE gain, Alt1 outperforms Alt 3. |
| Nokia/NSB | 1.3 | Average UPT gain vs mean overhead | We observe that for $N\_{TRP}=2,3,4$, the combination(s) with a single $L\_{n}=4$ achieves most of the UPT gain of the combination with $\{L\_{n}=4$,$ n=0,1,…,N\_{TRP}-1\}$, but with smaller overhead and complexity.For $N\_{TRP}=1$, we note that, with 16 ports per TRP, the combinations with $L=6$ achieve similar UPT-overhead trade-off as with $L=4$. Therefore, we propose to keep the same restrictions and supported combinations as for Rel16, with $L=6$ applicable only for 32 ports. |
| Samsung | 1.1 | Average UPT gain vs overhead | There is no benefit of Alt3 over Alt1 shown in our SLS results for both mode 1 and mode 2 cases even in the inter-site inter-cell scenarios. |
| 1.3 | Average UPT gain vs overhead | We support the offline proposal 1.C.1 as we have verified that the selected linkages yield good performance overall compared to other linkages and the overhead of them are well uniformly-spaced. |
| 1.5 | Average UPT loss w.r.t. paraComb | UCI omission with Alt3 is more beneficial than Alt2 in CJT operation.  |
| MediaTek | 1.1 | Average UPT gain vs different paraComb | We observe that Alt 3 cannot provide consistent performance benefit over Alt 1. Further, the cost of this little performance benefit must be borne by the increased overhead of feeding back multiple reference amplitudes. Therefore, supporting quantization Alt 3 is not necessary. |
| Ericsson | 1.3 | Average and cell-edge UPT vs overhead | Evaluated the performance of the six combinations with $\left\{L\_{n}\right\}=\left\{2,2,2\right\}$ and $\left\{4,4,4\right\}$ for three TRPs. For $\{L\_{n}\}=\{2,2,2\}$, only $\{p\_{v}, β\}$ combinations #1 and #2 may be supported, while for $L\_{n}=4$, all 6 combinations of $\left\{p\_{v}, β\right\}$ may be supported.  |

Table 2 Additional inputs: issue 1

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| **Company** | **Input** |
| Mod V0 | **Please share your inputs on each of the issues and, if applicable, proposals in TABLE 1A** |
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### Issue 2: Type-II codebook refinement for high/medium UE velocities (with time/Doppler-domain compression)

Table 3A Summary: issue 2

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| **#** | **Issue** | **Companies’ views** |
| 2.1 | [112] **Agreement**…* X=2 and
	+ The 1st CQI is associated with the first/earliest slot of the CSI reporting window (slot *l*) and the first/earliest of the *N*4 **W**2 matrices, and
	+ The 2nd CQI is associated with the middle slot of the CSI reporting window (slot *l*+*WCSI*/2) and the (*N*4 /2)-th**W**2 matrix
	+ FFS: Whether/how to include CQI overhead reduction for X=2

**Proposal 2.A.2 (V1)**: For the Type-II codebook refinement for high/medium velocities, when a UE is configured with X=2 for CQI calculation and reporting, the 2nd CQI includes 4-bit wideband CQI and 2-bit sub-bands CQIs calculated independently from the 1st CQI**Proposal 2.A.2 (V2)**: For the Type-II codebook refinement for high/medium velocities, when a UE is configured with X=2 for CQI calculation and reporting, the 2nd CQI includes [2]-bit wideband CQI and 2-bit sub-bands CQIs * The 2nd (differential) wideband CQI is defined relative to the 1st wideband CQI, reusing the alphabet from the legacy 2-bit differential CQI table
* The 2nd (differential) sub-band CQIs are calculated relative to the 2nd (differential) wideband CQI, reusing the alphabet from the legacy 2-bit differential CQI table

**FL Note**: This topic was discussed OFFLINE [1]* From SLS results, it seems UPT vs overhead performance between v1 and v2 is almost none. At the same time v2 offers 2 bits 😊 of “overhead saving”
 | **Proposal 2.A.2****V1:*** **Support/fine**: Samsung, NTT DOCOMO, ZTE, vivo, Spreadtrum, OPPO, Qualcomm, Intel, Xiaomi, Nokia/NSB, Fujitsu, Ericsson, IDC, CMCC, Sony, CATT, Sharp, Apple
* **Serious concern**:

**V2:*** **Support/fine**: MediaTek, Huawei/HiSi, Lenovo/MotM, Google, NEC, Fraunhofer IIS/HHI, LG
* **Serious concern**:
 |
| 2.2 | [112bis-e] **Agreement**For the Type-II codebook refinement for high/medium velocities, regarding the bitmap(s) for indicating the locations of the NZCs, * When the UE is configured with Q=1: for each layer, one 2-dimensional bitmap of size-2LM reusing the legacy design is used
* When the UE is configured with Q=2: for each layer,
	+ Basic feature: two 2-dimensional bitmaps, each of size-2LM reusing the legacy design for each of the two selected DD basis vectors, are used
	+ Optional feature, if the following down-selection succeeds: down-select from the following two alternatives in RAN#112bis-e:
		- Alt3A: A single 2-dimensional bitmap of size $MQ$ to report the selected $S$ pairs of FD basis vector and DD basis vector and a single 2-dimensional bitmap of size $2LS$ for indicating the location of the NZCs, where each row corresponds to a selected SD basis vector and each column corresponds to one of the selected $S$ pairs of FD basis vector and DD basis vector.
		- Alt4’: Q different bitmaps are supported for each layer, each of the Q bitmaps corresponds to DD basis q = 0 or 1.
			* For each polarization, each of the Q bitmaps contains bits included in a set of SD basis and FD basis pairs $\{(s, f)\}$, satisfying $min(f,M\_{v}-f)+ min(|s-s\_{ref} |, L-|s-s\_{ref} |)\leq D$, where
				+ $s\in \left\{0,…,L-1\right\}$, $f\in \left\{0,…,M-1\right\}$
				+ $s\_{ref}\in \{0,…,L-1\}$ is the SD basis indicated by SCI
				+ Two polarizations have same set of $\{(s, f)\}$ in the bitmap

**FL Note**:  | **Optional Q=2*** **Alt3A**: Fraunhofer IIS/HHI
* **Alt4’**: vivo
 |
| 2.3 | [112bis-e] **Agreement**….* Select at most 3 additional Parameter Combinations from the list below

|  |  |  |  |
| --- | --- | --- | --- |
| $$L$$ | $$p\_{υ}$$ | $$β$$ | **Companies’** **views** |
| $$υ \in \left\{1,2\right\}$$ | $$υ \in \left\{3,4\right\}$$ |
| 2 | 1/8 | 1/16 | 1/4 | **Support/fine**:**Not support**: |
| 2 | 1/8 | 1/16 | 1/2 | **Support/fine**:**Not support**: |
| 2(\*) | ¼  | 1/8  | ¼  | **Support/fine**:**Not support**: |
| 2 (\*) | ¼  | 1/8 | ½  | **Support/fine**:**Not support**: |
| 4 | 1/8 | 1/16 | 1/4  | **Support/fine**:**Not support**: |
| 4 (\*) | ¼  | 1/8  | 1/4  | **Support/fine**:**Not support**: |

(\*) Note: From legacy.**Question 2.C**: Please share your view in the above table**FL Note**: As suggested by vivo in ROUND 0, the most natural grouping would be Alt1 (legacy) with combos 3, 4, 6 vs Alt2 (new) with combos 1, 2, 5.   |
| 2.4 | [112bis-e] **Agreement**On the Type-II codebook refinement for high/medium velocity, regarding CBSR, a single SD beam group restriction per legacy design is used from signalling perspective, * FFS: Whether amplitude restriction is summed across FD bases for each DD basis, *or* summed across FD and DD bases, or applied per DD unit
* FFS: Whether the legacy (optional) soft amplitude restriction is also supported or only hard amplitude restriction is supported

**Question 2.D**: Please share your view on the two FFS points | **Def of amplitude restriction:*** **Per DD basis:**
* **Summed across DD bases:**

**Hard-only vs soft amplitude restriction:*** **Hard-only:**
* **Hard + soft:**
 |
| 2.5 | **Proposal 2.E.1**: On the Type-II codebook refinement for high/medium velocities, regarding UCI omission, support reusing the legacy UCI omission mechanism with (Alt3) the following priority function: Prio(,l,m,q)=2L.RI.Mv.q + 2L.RI.P(m)+ RI.l +  where P(m) = m* Note: This implies that DD basis is designated the least priority
* FFS: Details on the location of the new UCI parameters in G0/1/2

**FL Note**:  | **Proposal 2.E.1:*** **Support/fine:** ZTE (P(m)=m), Spreadtrum, Fraunhofer IIS/HHI (P=m, S=q), Intel, Xiaomi, LG, Samsung, MediaTek, Qualcomm, Fujitsu, OPPO, Google, CATT, Ericsson, NTT DOCOMO, Nokia/NSB, CMCC, Huawei/HiSi, Apple
* **Not support:** vivo, Lenovo/MotM,
 |

Table 3B Type II Doppler: summary of observation from SLS

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| --- | --- |
| **Company** | **SLS results** |
| **Issue #** | **Metric** | **Observation** |
| Issue # 2.1 |
| Samsung | 2.1 | UPT vs overhead | There is no benefit with Alt1.2/1.3 (differential w.r.t. the 1st CQI) over Alt1.1 (independent of the 1st CQI) |
| Issue # 2.2 |
| Huawei | 2.2 | UPT vs overhead | Alt.3A has better UPT vs. overhead tradeoff than Alt.1. |
| ZTE | UPT vs overhead | ***On Alt1 vs 4’***In addition, we evaluate the performance on average UPT vs overhead between Alt1, Alt4\_1 based on d=3 and Alt4\_2 based on d=5 in Figure 2. Parameter combination is shown in Table2. There are some performance benefits in the case of low-overhead region in Figure 2. However, serious performance loss is observed in SLS on Alt4 both d=3 and d=5 in Figure 2 in high-overhead region. ***On Alt3A***For Alt3A, we have the concerns that Alt3A may violate previous agreements for “Q different two-dimensional bitmaps”, to some extent. Then, we provide SLS simulation in Figure 2 with Alt3A\_1 based on S = 0.5\*MQ and Alt3A\_2 based on S = 0.75\*MQ. It is observed that, with sufficient small parameter (e.g., S =0.5\*MQ) for reducing value of S, there are some performance benefits in the case of low CSI report overhead.  |
| Vivo | UPT vs overhead | * Under Q=2 and legacy CB parameter combinations (pv, beta, L), Alt 4’ UPT-overhead curve outperforms Alt 1 and Alt 3A.
* For lower overhead or ideal prediction, for each (pv, beta, L) configuration, Alt 4’ can save about 50 bits for each layer with nearly no performance loss.
* The benefit from Alt 4’ in terms of performance is even clearer in high overhead and real prediction. Alt 4’ can address the issue of coefficient unreliability caused by prediction error.
* Alt 3A does not provide better performance-overhead trade-off than Alt 1.
 |
| OPPO | UPT vs overhead | Alt3A can reduce 10% overall overhead without UPT loss. |
| Fraunhofer | UPT vs overhead | Alt 3A with $S=0.5MQ$ results in feedback overhead saving of 48 bits, 160 bits and 84 bits for parameter combinations 1-4, 5 and 6, respectively, compared to Alt 1 with negligible loss in performance. For Alt 3A, using S = 0.5MQ results in a similar average UPT to that of Alt 1 with large feedback overhead saving. |
| CATT | UPT vs overhead | The average throughput versus bitmap overhead is shown in Figure 1. Based on the simulation results, it is observed that Alt3A has negligible performance loss compared with Alt1 with less bitmap overhead. |
| Intel | UPT vs overhead | * Performance degradation of up to 0.8% in average UE throughput and up to 2% for cell-edge UE throughput is observed for Alt3A comparing to Alt1.
* 48 bits can be saved for configurations with M = 4 and 84 bits for configuration with M = 7 for Alt3A comparing to Alt1
 |
| Samsung | UPT vs overhead | * Alt3A and Alt1 are similar in UPT vs overhead trade-off for all of avg. UPT, 50% UPT, and 5% UPT.
* For any (UPT, overhead) achieved by Alt3A, there is a similar (UPT, overhead) achieved by Alt1
* Alt4’ can improve UPT vs overhead trade-off
 |
| MediaTek | UPT vs overhead | NZC indication by Alt 3A can provide 50~60 bits overhead saving compared with Alt 1 with <1 % performance loss.NZC indication by Alt 4 and D = 3 can achieve similar performance as Alt 1 without significant overhead saving.NZC indication by Alt 4 and D = 2 degrades in performance especially at higher parameter combinations, due to forcing zero coefficients in certain SD, FD positions. |
| Qualcomm | Separate UPT, and overhead | For Type-II-Doppler, Alt1 2-stage (MQ+2LS)-bit bitmap (Alt3A) achieves similar average throughput as 2LMQ-bit 3D bitmap, while overall feedback overhead can be reduced by more than 10% (659 to 575 bits). |
| Ericsson | Separate UPT, and overhead | Bitmap alternative Alt1 with reporting of only non-empty DD bitmaps is close to Rel-16 Type-II implementation in complexity and is a simpler reporting format  |
| Issue # 2.3 |
| Huawei | 2.3 | UPT vs overhead | The following values paraComb achieves the best UPT vs overhead trade-off:

|  |  |  |  |
| --- | --- | --- | --- |
| *paramCombination-Type II doppler* | $$L$$ | $$p\_{υ}$$ | $$β$$ |
| $$υ \in \left\{1,2\right\}$$ | $$υ \in \left\{3,4\right\}$$ |
| 1 | 2 | 1/8 | 1/16 | 1/2 |
| 2 | 4 | 1/8 | 1/16 | 1/4 |
| 3 | 4 | 1/8 | 1/16 | 1/2 |
| 4 | 4 | 1/4 | 1/8 | 1/4 |
| 5 | 4 | 1/4 | 1/8 | 1/2 |
| 6 | 4 | 1/4 | 1/4 | 1/2 |
| 7 | 6 | 1/4 | 1/8 | 1/2 |
| 8 | 6 | 1/4 | 1/4 | 1/2 |

 |
| ZTE | UPT vs overhead | Based on SLS results, the following is proposed

|  |  |  |
| --- | --- | --- |
| $$L$$ | $$p\_{υ}$$ | $$β$$ |
| $$υ \in \left\{1,2\right\}$$ | $$υ \in \left\{3,4\right\}$$ |
| 2 | 1/8 | 1/16 | 1/8 |
| 2 | 1/8 | 1/16 | 1/4 |
| 4 | 1/8 | 1/16 | 1/8  |
| 4 | 1/8 | 1/16 | 1/4  |
| 4 | 1/4 | 1/4 | 1/4  |
| 4 | 1/4 | 1/4 | 1/2  |
| 4 | 1/4  | 1/4  | 3/4  |
| 4 | 1/2  | 1/2  | 1/2  |
| 6 | 1/4 | 1/4 | 1/2  |
| 6 | 1/4  | 1/4  | 3/4  |

 |
| OPPO | UPT vs overhead | We evaluated R16 and R17 parameter combination, where AP CSI-RS overhead is not considered. For R17 parameter combination, legacy parameter is good. For R16, we used $K0=β2LM$ and there is no any significant gain for large K0, which imply legacy parameter combination can be reused for N4 > 1 |
| CATT | UPT vs overhead | Based on our simulation results, we identified several Parameter Combinations that offer a good tradeoff between performance and overhead. As a result, we recommend using the Parameter Combinations outlined in Table 2Table 2 Codebook parameter configurations for *L*, $β$ and $p\_{v}$

|  |  |  |
| --- | --- | --- |
| *L* | $$p\_{v}$$ | $$β$$ |
| $$v\in \{1,2\}$$ | $$v\in \{3,4\}$$ |
| 2 | 1/8 | 1/16 | 1/4 |
| 2 | 1/8 | 1/16 | 1/2 |
| 4 | 1/8 | 1/16 | 1/4 |
| 4 | 1/8 | 1/16 | 1/2 |
| 4 | 1/4 | 1/8 | 1/2 |
| 4 | 1/2 | 1/4 | 1/2 |
| 6 | 1/8 | - | 1/2 |
| 6 | 1/4 | - | 1/2 |

 |
| Intel | UPT vs overhead | * Parameter combinations {p1,2, beta} = {1/8, 1/4}, {1/8, 1/2}, {1/4, 1/2}, {1/4, 3/4} provide good performance/overhead tradeoff considering both average and cell-edge UE throughput
 |
| Nokia | UPT vs overhead | * For Type-II-Doppler, for average and cell-edge UPT gain over Rel-16 Type-II increase with overhead, for the same parameter combinations.
* For Type-II-Doppler, cell-edge UPT gain over Rel-16 Type-II tend to be noticeably higher than average UPT gain.
 |
| Samsung | UPT vs overhead | Different (smaller) beta than legacy (beta=1/8)* Smaller $β$ than legacy can be beneficial
* Weak coefficients increase overhead, but don’t provide UPT gain (🡪 beta can be small)
 |

Table 4 Additional inputs: issue 2

|  |  |
| --- | --- |
| **Company** | **Input** |
| Mod V0 | **Please share your inputs on each of the issues and, if applicable, proposals in TABLE 3A** |
|  |  |
|  |  |
|  |  |

### Issue 3: TRS-based reporting of time-domain channel properties (TDCP)

Table 5A Summary: issue 3

|  |  |  |
| --- | --- | --- |
| **#** | **Issue** | **Companies’ views** |
| 3.1 | [112bis-e] **Working assumption:** For the Rel-18 TRS-based TDCP reporting, for TDCP measurement and calculation, * KTRS ≥1 TRS resource set(s) can be configured in the CSI reporting setting when ReportQuantity is ‘tdcp’
	+ Note: the TRS resource set(s) configured for TDCP report do not impact or impose any new requirements on the UE behavior when processing TRS used as QCL type A/D source for reception of PDxCH.
* No further spec enhancement on TRS is supported
* [All the TRS resources in the configured resource set(s) share the same RE locations]
* FFS: Whether to add further restrictions on the TRS resource set(s) on, e.g. QCL relationship, power control, [RE location], slot offset between TRS resource set(s), relation with resource set used for legacy usage

**Question 3.A**: Please share your view on the bracketed text from Google |
| 3.2 | [112bis-e] **Agreement**For the Rel-18 TRS-based TDCP reporting, regarding the quantization of wideband normalized amplitude value, * At least the following size-*Q* quantization alphabet is supported: $\left\{1-2^{-\left(N-q\right)s}\right\}$ where $q=0,1,…,2^{Q}-1$
	+ TBD: supported value(s) of *N* (e.g. $2^{Q}-1$ or a larger value), *Q*, s (e.g. ½, ¼, 1/8, …), whether a center threshold is also supported (and if so, higher-layer configured)
* FFS: Whether different schemes can be supported for different use cases

**Question 3.B**: Please share your views on the TBD points (N, Q, s, and whether a configurable center threshold should be supported)**FL Note**:  |
| 3.3 | [112] **Agreement**For aiding gNB determination of codebook switching and SRS periodicity with the Rel-18 TRS -based TDCP reporting, support reporting quantized wideband normalized amplitude/phase of the time-domain correlation profile with Y≥1 delay(s) as follows:* Basic feature: Y=1 with delay≤ Dbasic symbols, only wideband quantized normalized amplitude is reported
	+ FFS: Candidate values for delay
* Optional feature: Y=1 with delay>Dbasic symbols and Y≥1, wideband quantized normalized amplitude and phase for each delay are reported
	+ For Y>1, the phase can be configud to be absent for all the Y delays
	+ TBD: Whether the value of Y is configurable or following the delays from the configured TRS resource
	+ TBD: Candidate value(s) for Y>1
* FFS: Value of Dbasic

**Proposal 3.C.1:** For the Rel-18 TRS-based TDCP reporting, regarding the value of parameter Y, in addition to Y=1, support Y=2, 3, 4, [7]**Proposal 3.C.2:** For the Rel-18 TRS-based TDCP reporting, support the following D (delay) values where Dbasic is equal to 2 slots* 4 symbols, 1 slot, 2 slots, 3 slots, 4 slots, 5 slots, 6 slots, [10 slots]
 | **Proposal 3.C.1:*** **Support/fine:**
* **Not support:**

**Proposal 3.C.2:*** **Support/fine:**
* **Not support:**
 |
| 3.4 | **Proposal 3.D**: For the Rel-18 TRS-based TDCP reporting, regarding the value of parameter Y for Y>1, the value of Y is gNB-configured via higher-layer (RRC) signalling* Note: whether value 0 can be reported for a given delay will be decided separately

**FL Note**: The note (albeit obvious and not precluded by any means – since this is related to the alphabet design in issue 3.2) was added per Samsung request  | **Proposal 3.D:*** **Support/fine:** Huawei/HiSi, ZTE, Google, vivo, Ericsson, NTT DOCOMO, Qualcomm, Spreadtrum, Nokia/NSB, CMCC, Ericsson, LG, Apple, Samsung
* **Not support:** Lenovo/MotM, Xiaomi,
 |
| 3.5 | [112] **Agreement**For the Rel-18 TRS-based TDCP reporting, the priority of the CSI report(s) associated with TDCP reporting is down-selected from the following alternatives:* Alt1. Lower than other CSI reports
* Alt2. Same as CSI report(s) not carrying L1-RSRP or L1-SINR
* Alt3. Higher than other CSI reports
* Other alternatives are not precluded

**Proposal 3.E**: For the Rel-18 TRS-based TDCP reporting, the priority of the CSI report(s) associated with TDCP reporting is lower than other CSI reports**FL Note**: While Alt2 seems to result in the least spec effort, this may not work since TDCP is a stand-alone report and can collide with regular CSI reports and co-multiplexing isn’t always an available option.  | **Proposal 3.E:*** **Support/fine:** Samsung, Intel, ZTE, Ericsson, OPPO, Fujitsu, Google, Spreadtrum, Sony, Apple, NTT DOCOMO, Qualcomm, Nokia/NSB, CMCC,
* **Not support (want Alt2, no spec impact: ”***Same as CSI report(s) not carrying L1-RSRP or L1-SINR***”):** vivo, Huawei/HiSi, Lenovo/MotM, Xiaomi, LG

 |

Table 5B TDCP: summary of observation from simulation

|  |  |
| --- | --- |
| **Company** | **SLS results** |
| **Issue #** | **Metric** | **Observation** |
| ZTE | 3.2 | UPT vs speed, use case = SRS periodicity | *Amplitude quantization scheme* $q\_{3}$ outperforms $q\_{1}$ and $q\_{2}$ with higher DL throughput in the use case of SRS periodicity determination.1. $q\_{1}(k)=1-q(k)^{2}, k=0, 1, …, 2^{n}-1$
2. $q\_{2}(k)=1-q(k), k=0, 1, …, 2^{n}-1$
3. $q\_{3}(k)=2\frac{k+1}{ 2^{n}}-\left(\frac{k+1}{2^{n}}\right)^{2}, k=0, 1, …, 2^{n}-$1

*Phase quantization scheme q1 outperforms q0 and q2 with higher throughput in the use case of SRS periodicity determination*1. $q\_{0}(l)=\frac{l}{2^{n}}∙2π, l=0, 1, …, 2^{n}-1$

1. $q\_{1}\left(l\right)=\left\{\begin{matrix}q(l)^{2}∙2π, mode=0\\\left(1-q(l)^{2}\right)∙2π, mode=1\end{matrix}\right. , l=0, 1, …, 2^{n}-1$
2. $q\_{2}\left(l\right)=\left\{\begin{matrix}q\left(l\right)∙2π, mode=0\\\left(1-q(l)\right)∙2π, mode=1\end{matrix}\right. , l=0, 1, …, 2^{n}-1$
 |
| OPPO | 3.2 | SE vs UE speed, use case: T1/T2 CB switch | *Observation 2: The threshold of codebook switching is close to 1, and R16 amplitude is coarse for TDCP reporting.* |
| Xiaomi | 3.1 | Switching accuracy vs delay | *Observation 1: Two TRS resource sets with delay 5 slots can obtain better TDCP measurement.* |
| Nokia | 3.2 | UPT vs UE speed, use case: T1/T2 CB switch | By comparing the performance gains in 1ms delay scenario and 10ms delay scenario one can notice that codebook with N=41 shows best performance, while all other codebooks lead to preferring Type-II too often, what is explained by the fact that highest quantisation level is still is not high enough for 1ms delay correlation profile calculation. But in case of 10ms delay (see Figure 15) codebook with N=20 shows best performance, and N=41 shows very poor performance.Performance degradation of Type-I/Type-II switching with noisy TDCP measurements does not increase for shorter delays. |
| Mavenir | 3.3 | Doppler spread vs UE speed | Observation 2. 20-slot delay has shown worse accuracy. Delay <= 5 slots can ensure the estimation for time variation of channel. 5-slot delay is better for smaller UE velocity (<=30km/h), whereas 1-slot delay is suitable in scenario of higher velocity.  |
| Samsung | 3.2 | UPT vs UE speed, use case: T1/T2 CB switch | *For T1/T2 CB switch based on threshold = 0.86, and Y=1* * *3-bit R16-based quantization is sufficient*
	+ *1-v^2 is the best at low speed (<=10kmph)*
	+ *1-v is good overall*
* *4-bit/5-bit doesn’t offset any gain over 3-bit*

*Based on LLS evaluations,** *The BLER performance of un-quantized and 1st 8 levels from Rel-16 legacy 4-bit reference codebook is almost same*

*Based on LLS evaluations,** *The BLER performance with 16-PSK for phase quantization is least, provides close match with un-quantized performance*
* *QPSK has highest BLER among 3 phase quantization methods.*
 |
| MediaTek |  | UPT vs speed, use case: T1/T2 CB switch | If $p\_{i}$ are the quantization levels from E-Type amplitude quantization, then using $1-p\_{i}$ for TDCP quantization offers better quantization performance compared to $1-p\_{i}^{2}$ for TDCP values well below 1. |
| Ericsson | 3.1 | UPT vs UE speed, use case: T1/T2 CB switch | In Figure 15 ,we show the performance of time correlation-based switching between CSI Type I and CSI type II for 100MHz bandwidth for small correlation delays, without averaging over time and with averaging over ten consecutive measurement occasions. In both cases we see that there is a significant improvement in performance when averaging over time is done. |
| 3.2 | UPT vs UE speed, use case: T1/T2 CB switch | In the simulations in Figure 8 and Figure 9 we see the performance for the quantization schemes for s equal to ½, 1/3, ¼ and 1/8 for a correlation delay of 5 slots and 3 slots. We see that higher granularity (i.e. smaller s) gives better performance but the difference is small, less than one percent in throughput…Thus, we confirm that at least for the use case of CSI Type I - Type II switching, already the granularity $s=\frac{1}{2}$ is sufficient.For TDCP amplitude, an upper limit of 0.995 for the quantization range needs to be considered. |
| 3.3 | UPT vs speed, use case: T1/T2 CB switch | For case with TRS colliding with PDSCH, a delay of 84 symbols gives the best performance at low SNRs.For case with TRS colliding with PDSCH, a delay of 36 symbols gives good performance at medium to high SNRs.For case with TRS colliding with TRS, a delay of 140 symbols is needed for good switching performance. |

Table 6 Additional inputs: issue 3

|  |  |
| --- | --- |
| **Company** | **Input** |
| Mod V0 | **Please share your inputs on each of the issues and, if applicable, proposals in TABLE 5A** |
|  |  |
|  |  |
|  |  |
| vivo | **Proposal 3.E**We are wondering whether there is any reason to select a certain priority for TDCP compared with other CSI reports? Anyway we have the report ID to distinguish the priority of different CSI reports other than L1-RSRP/SINR. If a certain NW wants to give lower priority for TDCP report, NW can just assign TDCP a larger report ID. Therefore, if we follow Alt 2, there is no specification impact at all.**Issue 3.4**We support to configure Y in RRC separately. |
| ZTE | **Question 3.B.2:**Regarding the range-adaptive phase quantization scheme, the range should be also relevant to the direction of UE velocity. More specifically, as the delay increases, the phase may varies from 0 to 2$π$ or 0 to -2$π$, depending on the direction of UE velocity or equivalently the sign of the dominant Doppler shift. * + In the formula $φ\_{n}= e^{j\frac{2πn}{k×2^{N}}}$, if k is set as 2, the quantization range is 0 ~ $π$, but the phases may concentrate within 0 ~ -$π$. Hence, we propose that UE should report a 1-bit indicator indicating whether the phase varies from 0 to 2$π$ or 0 to -2$π$ as the delay increases, and use different quantization schemes (or different quantization ranges) based on the indicator.

From our perspective, finer granularity should be set for phases corresponding to small delays in slow-speed scenarios, because:* Both the behaviors of changing SRS periodicity and switching codebook happen in slow-speed scenarios (≤30 km/h);
* Due to phase consistency problem at UE side, phases corresponding to smaller delays have better accuracy.

However, finer granularity should be set around 0 or 2$π$, depending on the direction of UE velocity. Therefore, we propose to use the following phase quantization scheme in our contribution:$$q\_{1}\left(l\right)=\left\{\begin{matrix}q(l)^{2}∙2π, mode=0 (finer granularity around 0)\\\left(1-q(l)^{2}\right)∙2π, mode=1(finer granularity around 2π)\end{matrix}\right. , l=0, 1, …, 2^{n}-1$$where “mode” is the indicator indicating whether the phase varies from 0 to 2$π$ or 0 to -2$π$ as the delay increases.Consider other companies’ proposals, we propose the following proposal for discussion:* UE reports a 1-bit indicator indicating whether the phase varies from 0 to 2$π$ or 0 to -2$π$ as the delay increases, and use different phase quantization schemes based on the indicator.
* FFS: Specific phase quantization schemes based on the indicator.

**Question 3.C:**Considering the FFT length is usually one of {2, 4, 8}, and the channel correlation corresponding to zero delay is equal to 1 and does not need to be reported. Therefore, the candidate values of Y should be {1, 3, 7}Based on previous simulation results provided by a plurality of companies, the proper selections of Y and corresponding delays are* Y = 1, delay = 5 slots (i.e., Dbasic = 5 slots);
* Y > 1, delays = {5, 10, ..., 5Y}.

**Question 3.D:**Support Alt 1. If either Alt 2 or Alt 3 is adopted, a plurality of restrictions are needed to ensure Y and the corresponding delays can be properly selected at UE side. This certainly increases the spec complexity. Compared to Alt 2 and Alt 3, Alt 1 is more straightforward and simpler.**Support Proposal 3.E.****Issue 2.6:** Regarding the next level issues, we have the initial thoughts as follows: * Regarding the time line of TDCP reporting, we prefer to define (Z, Z’) = (Z2, Z2’) as a baseline, where (Z2, Z2’) are defined in clause 5.4 in [TS 38.214]. And we can further study whether (Z, Z’) should be associated with UE capability, e.g., (Z, Z’) = (Z2 + Q, Z2’ +Q), where Q is a UE capability.
* Apparently, the computational cost of TDCP measurement is relevant to Y. Therefore, for a TDCP report, the number of occupied CPUs OCPU should be determined by Y in our initial thought.
 |
| Ericsson | **Question 3.C**:* We prefer Dbasic=10 slots since that is needed for the TRS colliding with neighbor cell TRSs scenario, but we are open to compromise and settle for Dbasic=5 slots if 10 slots is deemed to complex for the UE. If Dbasic=5slots, then up to 10 slots should be supported based on additional UE capability.
* We think Y=3 could be useful to cover a large range of UE velocities
* We think the correlation delay should be flexibly configurable up to the maximum value. This is needed to handle different TDD frame patterns and also to avoid collisions with other RSs.

**Question 3.D**:* We want Alt1. The value of Y is gNB-configured via higher-layer (RRC) signalling. This is most straightforward. Only the gNB knows what the measurement will be used for so the gNB needs to configure Y. To make it implicit based on the configured TRSs would be unnecessary complicated and could open up for ambiguities.

**Proposal 3.E:*** We are fine with the proposal 3.E that the priority of the CSI report(s) associated with TDCP reporting is lower than other CSI reports

**Question 3.B.2**:* We agree with Samsungs that the phase of the autocorrelation is either close to 0 or pi. However, whether the phase is close to 0 or pi can’t be known beforehand since it depends not only on the correlation delay but also on the UE speed/doppler spread. We think this could be handled by using one bit for the sign (or equivalently a 0 or pi radians phase offset), and then some additional bits for an additional small phase. We think the sign could be useful, but we don’t know what the phase could be used for. The phase is related to the odd moments of the Doppler power spectrum, i.e. to how asymmetric the Doppler power spectrum is. This could possibly be useful for some use-case, but since we don’t know of any ourselves it would be nice to hear what companies have in mind.
 |
| OPPO | **Question 3.C**:We support Dbasic = 4 or 5 slots and delay based on the periodicity/offset of TRS resource set configuration**Question 3.D**:Alt1 is preferred. **Proposal 3.E:**Fine with the proposal.  |
| Intel | **Question 3.D**We support Alt.1 as robust and simple design. **Proposal 3.E:**Support |
| Fujitsu | **Question 3.C**:Sorry for making misunderstandings, we also support Dbasic = 5 slots.For the value of Y, although we prefer Y> 5 for better performance, but we can accept multiple candidate values (<5 & >5) suggested by ZTE.**Question 3.D**:Since we have supported Y is a UE capability, Alt1 is more straightforward. **Proposal 3.E:**Fine with the proposal.  |
| Ericsson | **Regarding Question 3.C:** On the candidate delay values, it should be noted that different time correlation delays may be needed due to different TDD frame structure. So, we think for the basic feature, the supported delay values should be from 1 slot to Dbasic slots with granularity of 1 slot (e.g., if Dbasic = 5 slots, then candidate delay values of 1, 2, 3, 4, 5 slots should be suppoted.For the optional features, candidate delay values should be from 1 slot to Dmax slots with granularity of 1 slot.**Regarding Question 3.B.2:**We have a question to ZTE. It seems to us that you assume that the UE has perfect knowledge of the transmission frequency of the TRS from the gNB. Is this true? If so, is the intention to tighten the requirements on the gNB and UE clocks with many orders of magnitude compared to the current RAN4 requirements?The complex time correlation as estimated by the UE is strongly dependent on the UE clock used (or equivalently on the reference frequency used). A change of the frequency of the oscillator used as UE clock, results in a phase rotation of the estimated time correlation:The clock of the gNB and the clock of the UE are so imprecise that the estimated phase of the time correlation becomes completely useless if the UE clock is used as a reference as it is (for a quantitative analysis, see our contribution R1-2303783).This problem can be ‘solved’ by the UE tuning it’s oscillator to the signal received from the gNB. This means, however, that the UE clock is affected by the Doppler shift of the received signal. As a result the phase of the time correlation estimate will not have the strong linear dependence on delay and UE speed that ZTE mention in their response. UE tuning to the Doppler shifted received signal removes this strong linear dependence.Before deciding on a quantization scheme for the phase of the time correlation, it’s necessary to clearly define the clock frequency the UE should use in estimating the phase. Is it a clock tuned to the received signal or is it a new high performance clock which should be used by the gNB and the UE, with corresponding new extremely tight performance requirements in RAN4? In our view it should be the clock tuned to the received signal. |
| Huawei, HiSilicon | Question 3.D:We prefer Alt1, the value of Y is gNB-configured via higher-layer (RRC) signalling.Proposal 3.E:We prefer Alt2. We don’t think it is an important issue to distinguish the priority between TDCP with other CSI reports. As Vivo mentioned, the priority can be configured by gNB with different reportConfigID, which is more flexible and no spec impact. |
| Mod V21 | **Added proposal 3.D based on majority view** |
| ZTE | **Regarding Question 3.B.2:** Thank you so much for E///’s question to us. In short, we think the frequency compensation (tuned to the received signal) should be assumed, but the central Rx frequency (after fine sync) should be stable in UE side. That means that, from the perspective of centrical frequency, if having a bias of 1220Hz in terms of Doppler shift offset + oscillator impact, the UE may compensate the majority bias (like 1200Hz) and there is still some resident (e.g., 20 Hz). From the estimating of relative Doppler shift/spread, the key issue is to describe the relative or spread information (i.e., relative shift or Doppler spread from 1120Hz to 1320Hz is almost the same as -80Hz to 120Hz, in our views, e.g., for determining SRS periodicity or codebook switching). Therefore, the improvement of phase quantization seems to be needed for Doppler spread/relative shift estimation in gNB side (that is also mentioned by Samsung, if our understanding is correct). Then, the bias/resident is definitely relevant to UE velocity, delay, etc., and then we tend to agree that the UE have some more information.  |
| MediaTek | After further offline discussion and clarification, we would like to update our proposal for the **Dbasic value to 1 slot.** |
| Lenovo / MotM | **Issue 3.1:**Support Proposal 3.A**Issue 3.2:**OK with Proposal 3.B.1 given the majority support, however we prefer to keep candidate q values as part of FFS along with N, s valuesRegarding Question 3.B.2, we appreciate the good discussion from Samsung, Ericsson and ZTE. Samsung and Ericsson’s arguments are clear to us, however ZTE’s comment about “compensating the majority of bias” is not clear, so we prefer if more discussion is allowed for companies to express their views. **Issue 3.3:**Regarding Question 3.C, we support Y≤4. We also prefer if Dbasic values is configurable to multiple values including {1,5} slots, where for Y>1 the delay value is y.Dbasic for the yth autocorrelation value, y=1,…,Y**Issue 3.4:**We prefer a UE-assisted value of Y (Alt3) based on the measured autocorrelation by the UE**Issue 3.5:**Since one use case of the TDCP report is to help determine the codebook type for future CSI reports, it is preferred to have higher, or at least the same priority as other CSI reports not carrying L1-RSRP |
| Google | Proposal 3.A: We think the restriction is needed. During the offline discussion, no company raised concern on the restriction of RE location, but the discussion was about the spatial domain configuration. We suggest changing the last sub-bullet as follows:* **The TRS resource sets share the same configuration of RE location for each TRS resource**
	+ **FFS: other restrictions, e.g., QCL relationship, power offset, and so on**

[Mod: Added that bullet in brackets reworded. Please check. We can see if this is agreeable. If so, it is also better for progress 😊]Proposal 3.B.1: SupportQuestion 3.B.2: In our view, the phase is useless, since the phase continouty is not guranteed. We think 1-bit is enough.Question 3.3: Y = {1, 2, 3, 4}. Dbasic = 1 or 2 slots (depending on the supported TRS structure). Delay values = {4, 14, 18} symbols for intra-burst TDCP and {1, 2, 3, 4} TRS offset for inter-burst TDCP. Question 3.D: Support Alt1. UE has no information how gNB would use this. So it is better that this is configured by the gNB.Proposal 3.E: Support |
| NEC | **Proposal 3.A:**Fine with the proposal. Considering the delays for TDCP reporting, we think the offset between TRS resource sets also needs further discussion.**Updated Proposal 3.A:** For the Rel-18 TRS-based TDCP reporting, for TDCP measurement and calculation, * KTRS ≥1 TRS resource set(s) can be configured in the CSI reporting setting when ReportQuantity is ‘tdcp’
	+ Note: the TRS resource set(s) configured for TDCP report do not impact or impose any new requirements on the UE behavior when processing TRS used as QCL type A/D source for reception of PDxCH.
* No further spec enhancement on TRS is supported
* FFS: Whether to add further restrictions on the TRS resource set(s) on, e.g. QCL relationship, power control, RE location, slot offset between TRS resource set(s), relation with resource set used for legacy usage

[Mod: OK] |
| Mod V28 | **Minor revision on 3.A per inputs****Added 3.C.1 on Y values**  |
| NTT DOCOMO | Support **Proposal 3.D, and Proposal 3.E** |
| Qualcomm | **Proposal 3.A.1, 3.B.1:** OK**Question 3.C**We understand the motivation of longer delay for use case like codebook switch.But still, the incremental memory cost is expensive – note that this is buffering the raw demodulations of FD-dense TRS.For smaller delay like 1 slot, it is still beneficial for some use case like DMRS time density (although more limited use cases), and it is friendly to UE since existing TRS processing also require this – no incremental costTherefore, as a basic UE feature, we **propose Dbasic=2 slots**Besides, modified a little bit on our view of delay value captured by FL**Proposal 3.C.1**2 or 3**Proposal 3.D**One motivation for UE-selective report a quantity smaller than configured Y is, sometimes some certain pair of resources may be unavailable for certain delay (lag). But this can be resolved by report invalid value e.g. zero-autocorrelationTherefore, we prefer the simplest Alt1, based on that **invalid or zero-autocorrelation can be reported** **Proposal 3.E**May need clarification: Lower priority means larger value of pri( ) function? If yes, we are OK |
| Spreadtrum | **Proposal 3.A:** We can support the proposal. **Proposal 3.B.1**:We can support the proposal.**Proposal 3.D**:Support the proposal for simplicity.**Proposal 3.E**:Support. |
| Nokia/NSB | **Proposal 3.A**Support. Basically the idea is that the additional periodic TRS(s) used for TDCP share the same time and frequency domain configurations (except for slot offset) with a “legacy” periodic TRS. Regarding the QCL assumption, one possibility is to configure the additional periodic TRS(s) used for TDCP as QCL-ed with a “legacy” periodic TRS.**Question 3.C**Regarding D­basic our preference is 5slotsRegarding the delay(s), we think these can be inferred by the slot offset of the $K\_{TRS}$ TRSs if $K\_{TRS}>1$ and it can be assumed 1slot if the report setting is linked to a single TRS (in this case the TRS resource set has 4 resources)Regarding the supported values of Y, we think 2,3,4 or a subset of these values is sufficient**Proposal 3.D**Fine, although we think the value Y may be inferred by the number $K\_{TRS}$ of TRS(s) linked to the reporting setting**Proposal 3.E**Support |
| CMCC | **Proposal 3.D**Support.**Proposal 3.E**Support. |
| Xiaomi | **Proposal 3.A:** Support. We also think the restriction on the location of TRS resource is necessary.**Proposal 3.B.1:**In principle, we are fine with the proposal. According to observation, it is feasible that $1-2^{-\left(N-q\right)s}$ is applied to aid gNB determining the switching of Type I and Type II codebook. But, for determining SRS periodicity, we think it is not good method to obtain accurate quantization amplitude. **Question 3.C:**For value Dbasic, we think 5 slots is suitable value.For delay, it depends on the slot offset between TRS resource set. The candidate value belongs to [1,5] slots.According to our simulation results, larger value Y can help to estimating more accurate Doppler shift. We think Y=7 or 8 is a suitable value.**Question 3.D**We support Alt3. according to UE speed and the calculated the quantization amplitude, it is not necessary to report all values. For example, if the quantized amplitude is zero, it is not necessary to report. Accordingly, the phase does not need to report as well.**Issue 3.5**We support Alt2. TDCP reporting is used to aid gNB switch Type I and Type II codebook or determine SRS periodicity, which is applied to data schedule. For other CSI reporting not carrying L1-RSRP or L1-SINR, it is also used to data schedule. From this perspective, they should have the same priority. |
|  InterDigital | **Proposal 3.B.1** Support**Question 3.C**In the last meeting agreement, Dbasic was defined as the number of symbols, not sure why now we are considering slots. In our view, in a high Doppler scenario, the lag for correlation measurement does not need to be too long as the channel is expected to vary at a sufficiently high rate. Besides, an unnecessary long lag has some implications on the UE memory usage by forcing it to hold samples for an unreasonably extended period of time. Therefore, a Dbasic in the range of 1 or 2 slots should be sufficient.  |
| Ericsson | **Regarding Proposal 3.B.1:**We are ok with the proposal. But we have a question **@Lenovo/MotM** regarding the change they proposed to make candidate values of q FFS. According to the proposal, a size-Q quantization alphabet is used. Then, there will be 2Q codepoints in the alphabet. It is quite natural then that q=0,1, …, 2Q-1 where each q value corresponding to one of the 2Q codepoints. Since your suggestion is to make the range of q FFS, could you explain how else to choose the range of q that results in 2Q codepoints?[Mod: You raised a good point. I’ll add that back then]**Regarding Question 3.B.2:**@ ZTE Thank you for the quick reply. Great that you agree that the phase reporting should be based on UE frequency compensation to the RX frequency. We think it would be good to capture this in an agreement. It could be formulated in different ways but maybe the following note would do:* note: the average frequency of the received signal is used as reference frequency when estimating the time correlation.

Ok with **Proposal 3.C.1**, although we think a value of Y=7 may be a bit high. Support **Proposal 3.D**. |
| Mod V39 | **Minor revision per inputs** **Added proposal 3.C.2 of delay values. I have narrowed down the candidates of Dbasic between 2 and 5 slots based on companies’ preferences (which are scattered everywhere 😊)** |
| Samsung | **Proposal 3.B.1**: * same Question as E///, with Q bits, the range has to be 0,1,…2^Q-1 @Lenovo: are your proposing a function, e.g. Q(q), where q=0,1,…,2^Q-1? If so, why Q(q)=q is not sufficient? And what is Q(q) if you propose it?
* Based on our simulation results (in updated Tdoc 2303901), N>2^Q-1 is needed to low speed or small delay. So, we suggest include $\geq $ in the TBD subbullet.
	+ TBD: supported value(s) of *N* (e.g. $\geq 2^{Q}-1$),

**[Mod: OK but rephrased your suggestion to “e.g. 2^Q-1 or a larger value” since the above formulation is confusing]****Proposal 3.D**: we still think a value $y\leq Y$ (where $Y$ is gNB-configured) can be reported by the UE, since all Y correlation values may not need reporting, for example, for a low speed UE or small delay values associated with Y delay. So, suggest to add the highlighted bullet below.**Proposal 3.D**: For the Rel-18 TRS-based TDCP reporting, regarding the value of parameter Y for Y>1, the value of Y is gNB-configured via higher-layer (RRC) signalling, * UE can report a value $y\leq Y$

**[Mod: This addition seems to contradict the main bullet since Alt1 and Alt3 in the current formulation exclude each other. With the above formulation, Y becomes the maximum value which is not the intention of Alt1. So I will not add the bullet since it won’t be agreeable to the majority]** |
| Lenovo / MotM | **Proposal 3.B.1:****@EE/SS**To illustrate on our comment, for Q=4, s=1/2, N=2Q-1, the codebook of values is:{0.9945 0.9922 0.9890 0.9844 0.9779 0.9688 0.9558 0.9375 0.9116 0.8750 0.8232 0.7500 0.6464 0.5000 0.2929 0}. Clearly, most of the codebook values are centered around 0.9, e.g., the difference between the first and fourth values is only 0.01, which includes significant redundancy in our opinion. This issue can be resolved via supporting smaller values of s, however this would reduce the largest value of the amplitude codeword, e.g., at s=1/8, Q=4, and N=2Q-1, the largest amplitude value of the codebook is 0.73. Given that, our preference is to further study the values of q, s, and N, to ensure the following three criteria are met:* + - 1. The largest autocorrelation value of the codebook is close to 1
			2. The majority of the codeword values are greater 0.5
			3. The codeword values greater than 0.5 are not too concentrated around the largest codeword value

Based on our analysis, the proposed q=0,…2Q-1 may not be able to achieve the first and third criteria simultaneously, and hence we prefer to keep q values FFS for now. I am not sure if Samsung’s proposal to support N>2Q-1 (while keeping q the same) can help resolve this issue, we can probably discuss that in the meeting.  |
| MediaTek | **Proposal 3.C.2:** Not support. As agreed previously, **Dbasic**  by definition is only applicable to basic feature so we don’t understand why there are two values of [2] and [5] slots associated with **Dbasic** . We believe, **Dbasic** should be defined as a single value, preferably 1-slot. For the sake of progress if companies have strong opinion on making **Dbasic** larger, we can at most compromise to 2-slots. It should be noted that **Dbasic** has a significant impact on UE sample buffering capability and is which is extremely costly for the UE.We are fine with **D** (delay) having multiple values; however, we do not agree with the range proposed. We believe the range should be capped at the smaller value, i.e., 4 slots. The system benefits of introducing any value higher than 4 slots is questionable as it will be impacted by the Aisling phenomenon.  |
| ZTE | **Proposal 3.A:** The restriction may be relevant to time-domain behaviour of K TRS. We are open to capture the bracket, but we need to have a note of clarifying that P+AP TRS and P+P TRS should be supported (otherwise, the current spec restriction in Section 5.1.6.1.1 in TS 38.214 seems sufficient).**Proposal 3.B:** The range of q is relevant to the support value(s) of N. If having “$q=0,1,…,2^{Q}-1$”, the N should be $2^{Q}$. Otherwise, the candidate value of “0” does not make sense in the typical case. **Proposal 3.C.2:** The range of D, we do not need to clarify inter-burst or intra-burst measurement which is just relevant to TRS configuration. For instance, we may have two TRS bursts ({n, n+1}, {n+2, n+3} slot), in which we may configure Y=3 and D=1. Therefore, we have the following general description. Alternatively, just remove “[at least for inter-burst measurement]”:**Proposal 3.C.2:** For the Rel-18 TRS-based TDCP reporting, support the following D (delay) values where Dbasic is equal to [2]/[5] slots* ~~[~~Regardless of inter/intra-burst measurement~~]~~ 1, 2, 3, 4, 5, 10 slots

**Regarding Question 3.B.2:** Thanks for E///’s reply. We are open to have a note (even though we do not have a clear proposal), but it should be the estimate of average frequency is used as reference (in the multi-path channel, the central frequency-estimate residence can NOT be avoided). Then, the estimation (freq-sync) is performed based on TRS/SSB measurement.* note: the actual estimate of average frequency of the received signal is used as reference frequency when estimating the time correlation.
 |
| LG | **Proposal 3.A: Support****Proposal 3.B.1: Support****Proposal 3.D: Support****Proposal 3.E: Not Support**Since TDCP is used to determine CSI configuration, it make sense for TDCP to have higher priority than CSI report. So, our preference is Alt 3. However, as a compromise, we are also fine with Alt 2 in which gNB determines and configures the priority between CSI and TDCP by using reporting ID. |
| Mod V51 | **Minor edit on 3.B.1 per Samsung comment** |
| Xiaomi | **Proposal 3.C.2**How to determine the value Dbasic? We think the following two factors should be considered. * UE buffering capability
* The system performance of aiding gNB determination for different use case.

According to our simulation results, the accuracy of codebook switching is significantly decreased when Dbasic =2 slots for lower velocities. Thus, we prefer to Dbasic =5 slots. |
| Apple | **Proposal 3.A:** We are fine**Proposal 3.B.1:** We are fine**Proposal 3.B.2:** We prefer Alt 1 **Proposal 3.C.1:** This should be discussed after **Proposal 3.C.2:****Proposal 3.C.2:** We do not support anything beyond one TRS transmission.In other words, for 1 slot TRS, the maximum D is just the number of symbols between the first and the last TRS symbol in the same slotFor 2 slot TRS, the maximum D is just the number of symbols between the first TRS symbol in the first slot and the last TRS symbol in the second slot**Proposal 3.D:** We are fine**Proposal 3.E:** We support |
| Mod V58 | **Revision on proposal 3.C.2 based on further inputs (will be discussed in later rounds)** |

# References

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