**3GPP TSG RAN WG1 Meeting #112bis-e R1-23xxxxx**

**e-Meeting, April 17th – April 26th, 2023**

**Source: Moderator (vivo)**

**Title: FL summary of Rel-17 FR2-2 maintenance (RS and timeline)**

**Agenda item: 7.2**

**Document for: Discussion and decision**

# Introduction

In this contribution, we summarize the following discussion on issues regarding PDSCH/PUSCH RS and timeline for new SCSs on supporting NR from 52.6 GHz to 71 GHz in RAN1 #112bis-e.

[112bis-e-R17-FR2\_2-04] Email discussion on Rel-17 FR2\_2 maintenance (RS and timeline) by April 20 – Huaming (vivo)

# Remaining issues

## 2.1. timeline for CSI feedback

In [1, Huawei], it is pointed out that in RAN1#110, the timelines for AP-CSI feedback for 480 kHz and 960 kHz SCS are added in the RRC parameters of *reportSlotOffsetList-r17*, *reportSlotOffsetListDCI-0-1-r17* and *reportSlotOffsetListDCI-0-2-r17* for 480 kHz and 960 kHz SCS and the CR for section 6.1.2.1 of TS 38.214 is endorsed. However, the corresponding changes were not added in the section of 5.2.1.4 of TS 38.214 where the report configurations for semi-persistent or aperiodic CSI report on PUSCH are defined. [1, Huawei] provided a draft CR to correct this.

Moderator’s comment:

The correction in the draft CR seems necessary and very straightforward. The corresponding CR to TS38.214 in [1] is copied below for convenience.

========================= Start of TP #1 for TS 38.214, clause 5.2.1.4===================

#### 5.2.1.4 Reporting configurations

The UE shall calculate CSI parameters (if reported) assuming the following dependencies between CSI parameters (if reported)

- LI shall be calculated conditioned on the reported CQI, PMI, RI and CRI

- CQI shall be calculated conditioned on the reported PMI, RI and CRI

- PMI shall be calculated conditioned on the reported RI and CRI

- RI shall be calculated conditioned on the reported CRI.

\*\*\* unchanged text omitted\*\*\*

For a semi-persistent or aperiodic CSI report on PUSCH, the allowed slot offsets are configured by the following higher layer parameters:

- if triggered/activated by DCI format 0\_2 and the higher layer parameter *reportSlotOffsetListDCI-0-2* or *reportSlotOffsetListDCI-0-2-r17* is configured, the allowed slot offsets are configured by *reportSlotOffsetListDCI-0-2* or *reportSlotOffsetListDCI-0-2-r17*, and

- if triggered/activated by DCI format 0\_1 and the higher layer parameter *reportSlotOffsetListDCI-0-1* or *reportSlotOffsetListDCI-0-1-r17* is configured, the allowed slot offsets are configured by *reportSlotOffsetListDCI-0-1* or *reportSlotOffsetListDCI-0-1-r17,* and

- otherwise, the allowed slot offsets are configured by the higher layer parameter *reportSlotOffsetList* or *reportSlotOffsetList-r17*.

The offset is selected in the activating/triggering DCI.

\*\*\* unchanged text omitted\*\*\*

##### Proposal 1-1

Endorse in principle the draft CR to TS38.214 in R1-2303794.

Companies are encouraged to provide comments.

|  |  |
| --- | --- |
| Company Name | Comments/Views |
| Xiaomi | Support the proposal |
| Huawei, HiSilicon | We support the proposal as the proponents |
| CATT | OK |
| vivo | Support the proposal |
| Samsung | Support |
| Ericsson | Support |
| Nokia, NSB | Support |
| LGE | Support the proposal |
| Qualcomm | Support |
| ZTE | Support |

# Conclusion

TBD

# Reference

1. R1-2303794 Corrections to timeline for CSI feedback in TS38.214, Huawei, HiSilicon