**3GPP TSG RAN WG1 #112bis-e R1-23xxxxx**

**e-Meeting, April 17th – April 26th, 2023**

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| *CR-Form-v12.2* | | | | | | | | |
| **DRAFT CHANGE REQUEST** | | | | | | | | |
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|  | **38.213** | **CR** |  | **rev** |  | **Current version:** | **17.5.0** |  |
|  | | | | | | | | |
| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* | | | | | | | | |
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| ***Proposed change affects:*** | UICC apps |  | ME | **X** | Radio Access Network | **X** | Core Network |  |

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| ***Title:*** | Draft CR on transmission power of DCI format 4\_0 | | | | | | | | | |
|  |  | | | | | | | | | |
| ***Source to WG:*** | Moderator (CMCC), CATT, CBN | | | | | | | | | |
| ***Source to TSG:*** |  | | | | | | | | | |
|  |  | | | | | | | | | |
| ***Work item code:*** | NR\_MBS-Core | | | | |  | ***Date:*** | | | 2023-04-17 |
|  |  | | | |  | |  | | |  |
| ***Category:*** | F |  | | | | | ***Release:*** | | | Rel-17 |
|  | *Use one of the following categories:* ***F*** *(correction)* ***A*** *(mirror corresponding to a change in an earlier release)* ***B*** *(addition of feature),* ***C*** *(functional modification of feature)* ***D*** *(editorial modification)*  Detailed explanations of the above categories can be found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | | | | | | | | *Use one of the following releases: Rel-8 (Release 8) Rel-9 (Release 9) Rel-10 (Release 10) Rel-11 (Release 11) … Rel-16 (Release 16) Rel-17 (Release 17) Rel-18 (Release 18) Rel-19 (Release 19)* | |
|  |  | | | | | | | | | |
| ***Reason for change:*** | | In current specification, if the UE has not been provided any dedicated RRC signalling, the transmission power of SI PDCCH is determination by assuming that the ratio of PDCCH DMRS EPRE to SSS EPRE is within -8dB and 8dB. However, for broadcast reception, if the UE has not been provided dedicated RRC signalling, it’s not clear how to determine the transmission power of PDCCH for DCI format 4\_0. | | | | | | | | |
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| ***Summary of change:*** | | Reuse the legacy transmission power determination method of SI PDCCH to the PDCCH for DCI format 4\_0 by adding ‘or for a DCI format 4\_0’. | | | | | | | | |
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| ***Consequences if not approved:*** | | When the UE has not been provided dedicated RRC signalling, the transmission power of PDCCH for DCI format 4\_0 for broadcast cannot be determined. | | | | | | | | |
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| ***Clauses affected:*** | | 4.1 | | | | | | | | |
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|  | | **Y** | **N** |  | | | |  | | |
| ***Other specs*** | |  | **X** | Other core specifications | | | |  | | |
| ***affected:*** | |  | **X** | Test specifications | | | |  | | |
| ***(show related CRs)*** | |  | **X** | O&M Specifications | | | |  | | |
|  | |  | | | | | | | | |
| ***Other comments:*** | |  | | | | | | | | |
|  | |  | | | | | | | | |
| ***This CR's revision history:*** | |  | | | | | | | | |

4 Synchronization procedures

4.1 Cell search

Cell search is the procedure for a UE to acquire time and frequency synchronization with a cell and to detect the physical layer Cell ID of the cell.

A UE receives the following synchronization signals (SS) in order to perform cell search: the primary synchronization signal (PSS) and secondary synchronization signal (SSS) as defined in [4, TS 38.211].

A UE assumes that reception occasions of a physical broadcast channel (PBCH), PSS, and SSS are in consecutive symbols, as defined in [4, TS 38.211], and form a SS/PBCH block. The UE assumes that SSS, PBCH DM-RS, and PBCH data have same EPRE. The UE may assume that the ratio of PSS EPRE to SSS EPRE in a SS/PBCH block is either 0 dB or 3 dB. If the UE has not been provided dedicated higher layer parameters, the UE may assume that the ratio of PDCCH DMRS EPRE to SSS EPRE is within -8 dB and 8 dB when the UE monitors PDCCHs for a DCI format 1\_0 with CRC scrambled by SI-RNTI, P-RNTI, or RA-RNTI, or for a DCI format 2\_7, or for a DCI format 4\_0.

For a half frame with SS/PBCH blocks, the first symbol indexes for candidate SS/PBCH blocks are determined according to the SCS of SS/PBCH blocks as follows, where index 0 corresponds to the first symbol of the first slot in a half-frame.

<Unchanged Text Omitted>