3GPP TSG RAN WG1 #110bis-e R1-22xxxxx

e-Meeting, Oct 10-19, 2022

**Agenda item: 8.13**

**Source: Moderator (Ericsson)**

**Title: Summary#1 of Email discussion [110bis-e-R17-DSS-01]**

**WI: NR\_DSS**

**Document for: Discussion and Decision**

# 1 Introduction

This document summarizes discussions for RAN1#110bis-e for Rel17 NR DSS WI considering below documents [1]-[7] submitted for A.I. 8.13

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Ref#** | **TDoc** | **Title** | **Source** | **Agenda item** |
| 1 | [R1-2208621](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_110b-e/Docs/R1-2208621.zip) | Corrections on Scell scheduling Pcell | vivo | 8.13 |
| 2 | [R1-2209036](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_110b-e/Docs/R1-2209036.zip) | Correction on different SCSs between P(S)Cell and sSCell | Intel Corporation | 8.13 |
| 3 | [R1-2209037](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_110b-e/Docs/R1-2209037.zip) | Discussion on different SCSs between P(S)Cell and sSCell | Intel Corporation | 8.13 |
| 4 | [R1-2209450](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_110b-e/Docs/R1-2209450.zip) | Discussion on simultaneous PDCCH monitoring between USS set on sSCell and CSS set on PCell | LG Electronics | 8.13 |
| 5 | [R1-2209469](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_110b-e/Docs/R1-2209469.zip) | Draft CR for Rel-17 DSS | ZTE | 8.13 |
| 6 | [R1-2209851](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_110b-e/Docs/R1-2209851.zip) | Correction for DCI size alignment for Rel-17 DSS | Huawei, HiSilicon | 8.13 |
| 7 | [R1-2209962](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_110b-e/Docs/R1-2209962.zip) | Discussion on clarification for cross-carrier scheduling from SCell to P(S)Cell | Qualcomm Incorporated | 8.13 |
|  | [R1-2210191](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_110b-e/Docs/R1-2210191.zip) | Disabling EN-DC power split when SCG is deactivated | Nokia, Nokia Shanghai Bell | 8.13 |

# 2 Discussion

## 2.1 Topics for discussion

Following topics for DSS WI were discussed in the tdocs

1. Alignment of capability parameter names for FG 34-3 and FG 34-4 – [1],[5]
2. Clarification related to P(S)Cell SCS > sSCell SCS case – [2],[3]
3. Additional clarification on simultaneous PDCCH monitoring between sSCell USS sets and P(S)Cell CSS sets – [4]
4. Clarification related to *monitoringCapabilityConfig* - [5]
5. Further clarifications on DCI size alignment – [6]
6. Clarification related to *CSI-MeasConfig* when SCell to PCell scheduling is used [ 7]

Companies are requested to provide comments (if any) on the topics to consider for discussion in RAN1#110bis-e in the Table below.

|  |  |
| --- | --- |
| Company Name | Comments |
| Moderator Notes1 | Topic 1 - can request editor reflect correct parameter names for FG 34-3 and FG 34-4 in 38.213 editor’s alignment CR.Topic 2 – was also discussed in RAN1#110 but no agreement. Topic 3 – is there need for additional clarification(s) considering what is already captured for FG 34-1 and 34-1a?Topics 4, 5, 6 – suggest discussing these in this meeting. |
| OPPO | Topic 1: This should be a category-D change per editorial. Agree with moderator to leave this modification to editor.Topic 2: As mentioned by moderator, this was already discussed in RAN1 #110 with no consensus. From our view, it was not a RAN1 agreement or study in Rel-17 that the comparison of SCS between PCell and sSCell can invalidate or turn over the overall configuration of sSCell scheduling PCell. We are ok to discuss Topics {3,4,5,6} |
| Qualcomm | Agree with OPPO. |
| LG Electronics | Agree with OPPO. |
| ZTE | Agree with moderator, i.e., “ Topics 4, 5, 6 – suggest discussing these in this meeting“. |
| Intel | For Topic 2, if majority companies think dynamic switching between BWPs with $μ\_{P}\leq μ\_{S}$ and BWPs with $μ\_{P}>μ\_{S}$ is not supported in Rel-17 DSS, we prefer to make a conclusion on it. For other topics, agree with Moderator’s views.  |
| Huawei | Generally OK with moderator suggestions. For Intel’s conclusion, also acceptable. |
| vivo | OK with moderator suggestions and fine with Intel’s conclusion. |
| Samsung | Agree with suggestion by Moderator/OPPO.For Topic 2, the issue was already settled as follows. We don’t see a need for additional conclusion. **Agreement (RAN1#106bis-e)**Option A is supported in Rel-17* …
* When P(S)Cell SCS ($μ$) is larger than sSCell SCS ($μ1$), for CCS from sSCell to P(S)Cell and, it is not supported Rel-17 DSS.
 |
| Spreadtrum | OK with moderator suggestions and Intel’s conclusion. |

# 3 Conclusion

TBD

# 4 References

[1] R1-2208621 Corrections on Scell scheduling Pcell, vivo

[2] R1-2209036 Correction on different SCSs between P(S)Cell and sSCell, Intel Corporation

[3] R1-2209037 Discussion on different SCSs between P(S)Cell and sSCell, Intel Corporation

[4] R1-2209450 Discussion on simultaneous PDCCH monitoring between USS set on sSCell and CSS set on PCell, LG Electronics

[5] R1-2209469 Draft CR for Rel-17 DSS, ZTE

[6] R1-2209851 Correction for DCI size alignment for Rel-17 DSS, Huawei, HiSilicon

[7] R1-2209962 Discussion on clarification for cross-carrier scheduling from SCell to P(S)Cell, Qualcomm Incorporated