**3GPP TSG-RAN WG1 Meeting #110R1-22ddddd**

**Toulouse, France, August 22nd – 26th, 2022**

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| *CR-Form-v12.2* |
| **DRAFT CHANGE REQUEST** |
|  |
|  | **38.214** | **CR** |  | **rev** |  | **Current version:** | **17.2.0** |  |
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| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* |
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| ***Proposed change affects:*** | UICC apps |  | ME | **X** | Radio Access Network | **X** | Core Network |  |

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| ***Title:***  | Correction for aperiodic CSI triggering offset for FR2-2 in TS 38.214 |
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| ***Source to WG:*** | Moderator (vivo), LG Electronics, Samsung |
| ***Source to TSG:*** | R1 |
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| ***Work item code:*** | NR\_ext\_to\_71GHz-Core |  | ***Date:*** | 2022-08-24 |
|  |  |  |  |  |
| ***Category:*** | F |  | ***Release:*** | Rel-17 |
|  | *Use one of the following categories:****F*** *(correction)****A*** *(mirror corresponding to a change in an earlier release)****B*** *(addition of feature),* ***C*** *(functional modification of feature)****D*** *(editorial modification)*Detailed explanations of the above categories canbe found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | *Use one of the following releases:Rel-8 (Release 8)Rel-9 (Release 9)Rel-10 (Release 10)Rel-11 (Release 11)…Rel-16 (Release 16)Rel-17 (Release 17)Rel-18 (Release 18)Rel-19 (Release 19)* |
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| ***Reason for change:*** | In RAN1#109-e meeting, it was agreed that the range of *aperiodicTriggeringOffset-r17* for 480kHz and 960kHz was scaled by 4 of the corresponding range of *aperiodicTriggeringOffset-r16* as {0,…,31}\*4. These values are not specified as the CSI-RS triggering offset value in the 38.214 specification. |
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| ***Summary of change:*** | Add *aperiodicTriggeringOffset-r17 and c*larify the applicable value range of the aperiodic CSI triggering offset for 480kHz and 960kHz SCS.The CSI-RS triggering offset has the values of {0, 4, 8, 12, …, 60, 64, 96} slots for 480/960 kHz if µPDCCH >= µCSIRSThe CSI-RS triggering offset has the values of {0, 4, 8, …, 124} slots for 480/960 kHz if µPDCCH < µCSIRS |
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| ***Consequences if not approved:*** | Aperiodic CSI triggering is incomplete for 480kHz and 960kHz SCS |
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| ***Clauses affected:*** | 5.2.1.5.1, 5.2.1.5.1a |
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|  | **Y** | **N** |  |  |
| ***Other specs*** |  | **X** |  Other core specifications  | TS/TR ... CR ...  |
| ***affected:*** |  | **X** |  Test specifications | TS/TR ... CR ...  |
| ***(show related CRs)*** |  | **X** |  O&M Specifications | TS/TR ... CR ...  |
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| ***Other comments:*** |  |
|  |  |
| ***This CR's revision history:*** |  |

##### 5.2.1.5.1 Aperiodic CSI Reporting/Aperiodic CSI-RS when the triggering PDCCH and the CSI-RS have the same numerology

\*\*\* Unchanged text is omitted \*\*\*

When aperiodic CSI-RS is used with aperiodic reporting, the CSI-RS offset is configured per resource set by the higher layer parameter *aperiodicTriggeringOffset* or *aperiodicTriggeringOffset-r16 or aperiodicTriggeringOffset-r17*. The CSI-RS triggering offset has the values of {0, 1, 2, 3, 4, 5, 6, …, 15, 16, 24} slots for or {0, 4, 8, 12, …, 60, 64, 96} slots for and , where is the subcarrier spacing configurations for CSI-RS. If the UE is not configured with *minimumSchedulingOffsetK0* for any DL BWP and *minimumSchedulingOffsetK2* for any UL BWP and if all the associated trigger states do not have the higher layer parameter *qcl-Type* set to 'typeD' in the corresponding TCI states, the CSI-RS triggering offset is fixed to zero. The aperiodic triggering offset of the CSI-IM follows offset of the associated NZP CSI-RS for channel measurement. The aperiodic CSI-RS is transmitted in a slot , , if UE is configured with ca-SlotOffset for at least one of the triggered and triggering cell, and in slot , otherwise, and where

*- n* is the slot containing the triggering DCI, *X* is the CSI-RS triggering offset according to the higher layer parameter *aperiodicTriggeringOffset* or *aperiodicTriggeringOffset-r16 or aperiodicTriggeringOffset-r17*,

*-* and are the and the  which are determined by higher-layer configured *ca-SlotOffset* for the cell receiving the PDCCH, and are the and the  which are determined by higher-layer configured *ca-SlotOffset* for the cell transmitting the CSI-RS respectively, as defined in [4, TS 38.211] clause 4.5.

\*\*\* Unchanged text is omitted \*\*\*

##### 5.2.1.5.1a Aperiodic CSI Reporting/Aperiodic CSI-RS when the triggering PDCCH and the CSI-RS have different numerologies

\*\*\* Unchanged text is omitted \*\*\*

Aperiodic CSI-RS timing:

- When the aperiodic CSI-RS is used with aperiodic CSI reporting, the CSI-RS triggering offset *X* is configured per resource set by the higher layer parameter *aperiodicTriggeringOffset* or *aperiodicTriggeringOffset-r16 or aperiodicTriggeringOffset-r17,* including the case that the UE is not configured with *minimumSchedulingOffsetK0* for any DL BWP or *minimumSchedulingOffsetK2* for any UL BWP and all the associated trigger states do not have the higher layer parameter *qcl-Type* set to 'typeD' in the corresponding TCI states. The CSI-RS triggering offset has the values of {0, 1, …, 31} slots for or {0, 4, 8, …, 124} slots for and when the µPDCCH < µCSIRS and {0, 1, 2, 3, 4, 5, 6, …, 15, 16, 24} for or {0, 4, 8, 12, …, 60, 64, 96} slots for and when the µPDCCH > µCSIRS.. The aperiodic CSI-RS is transmitted in a slot , if UE is configured with ca-SlotOffset for at least one of the triggered and triggering cell, and *Ks* = , otherwise, and where

*- n* is the slot containing the triggering DCI, *X* is the CSI-RS triggering offset in the numerology of CSI-RS according to the higher layer parameter *aperiodicTriggeringOffset* or *aperiodicTriggeringOffset-r16 or aperiodicTriggeringOffset-r17*,

- and are the subcarrier spacing configurations for CSI-RS and PDCCH, respectively,

- and are the and the, respectively, which are determined by higher-layer configured ca-SlotOffset for the cell receiving the PDCCH respectively, and   are the and the, respectively, which are determined by higher-layer configured ca-SlotOffset for the cell transmitting the CSI-RS respectively, as defined in [4, TS 38.211] clause 4.5

- If the µPDCCH < µCSIRS, the UE is expected to be able to measure the aperiodic CSI RS, if the CSI-RS starts no earlier than the first symbol of the CSI-RS carrier's slot that starts at least *Ncsirs* PDCCH symbols after the end of the PDCCH triggering the aperiodic CSI-RS.

- If the µPDCCH > µCSIRS, the UE is expected to be able to measure the aperiodic CSI RS, if the CSI-RS starts no earlier than at least *Ncsirs* PDCCH symbols after the end of the PDCCH triggering the aperiodic CSI-RS.

\*\*\* Unchanged text is omitted \*\*\*