3GPP TSG RAN WG1#106-e R1-210XXXX

E-meeting, August 16th- 27th, 2021

Source: Moderator (vivo)

Title: FL summary#1 of DCI-based power saving adaptation

Agenda Item: 8.7.2

Document for: Discussion and Decision

# Introduction

This contribution is a summary of the AI 8.7.2 after 2nd round discussion.

* Section 2 is a list of the issues to be discussed/decided.
* Section 3 is void.
* Section 4 is a summary of previous meeting agreements.
* Section 5 is a summary of proposals from companies’ contributions submitted.
* Section 6 is void.
* Section 7 is the decription of WI.
* Section 8 is the reference documents.
* Section 9 is the history of the FL summary.
* Section 10 is annex

# Issue list

## Issue 1: scheduling DCI based SSSG switching and/or PDCCH skipping

In RAN1#105-E, it is agreed that

Agreement:

* PDCCH schedules data and also indicates PDCCH monitoring adaptation by SSSG switching and PDCCH skipping for a duration is supported.
  + At least DCI format(s) 1-1, 0-1, 1-2 and 0-2 can be used for the indication(s)

Agreement:

* ~~At least~~ one of  Alt 1 and Alt 2 is supported, to be decided in RAN1#106,
* Alt 1: Supporting SSSG  switching to emulate PDCCH skipping functionality,
  + Alt 1-1: by an ‘empty’ SSSG which no SS set(s) is configured for the ‘empty’ SSSG, UE does not monitoring PDCCH on the ‘empty’  SSSG,
  + Alt1-2: by a ‘dormant SSSG’ which may have associated SS sets, and monitored conditionally (e.g., depending on HARQ NACK or RTT/ReTx timers)
* Alt 2: PDCCH schedules data and also indicates PDCCH monitoring adaptation by PDCCH skipping for a duration is supported.
  + FFS details, including
    - e.g., joint / separate indication of SSSG switching and PDCCH skipping
    - Determination of the duration(s) for PDCCH skipping, e.g.,
      * by RRC signaling,
      * by DCI indication
      * Implicitly, to the end of C-DRX active time

Agreement:

At least SSSG#0 and SSSG#1 switching is supported for Rel-17 SSSG switching indicated by PDCCH scheduling data and/or timer.

* FFS: support of more than 2 SSSGs

### Initial proposals

1. **Multiple levels of PDCCH monitoring adaptation**

Many companies proposed to introduce 3 or even more SSSGs, for which at least 3 ‘PDCCH monitoring behaviours is supported. [Samsung, NEC, Nordic, Qualcomm, CMCC, ETRI, DOCOMO, ASUSTeK,Nokia, Interdigital]

Many companies proposed to adopt skipping indication by DCI within a SSSG. Hence at least 2 ‘PDCCH monitoring behaviours exists within a SSSG. While considering at least 2 SSSGs are supported, it is expected that 3 or more ‘PDCCH monitoring behaviours will be supported.

Hence, it is suggested that at least 3 PDCCH monitoring behaviours (including PDCCH skipping like behaviour) can be adapted.

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| **[High] Proposal 1a:**   * DCI-based PDCCH monitoring adaptation over at least 3 different monitoring behaviors (including PDCCH skipping, PDCCH monitoring by SSSG#0, PDCCH monitoring by SSSG#1) within an active BWP is supported in Rel-17. |

In order to supporting at least 3 ‘PDCCH monitoring pattern’, at least 2-bit is needed for a scheduling DCI for indication. How the 2-bit is indicating depends on the selection of Alt 1-1/1-2/2.

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| **[High] Proposal 1b:**   * At least 2-bit field in scheduling DCIs (i.e., DCI format 1-1/0-1/1-2/0-2) can be configured for triggering the PDCCH monitpring adaptation |

1. **Number of SSSG groups**

In the last meeting,it is agreed that at least SSSG#0 and SSSG#1 switching is supported for Rel-17 SSSG switching indicated by PDCCH scheduling data and/or timer. More than 2 SSSG is for FFS.

* Up to 2 groups: Huawei/Hisilicon, ZTE/Sanchips, CATT, MediaTek, Ericsson
* More than 2 groups: Samsung, NEC, Nordic, Qualcomm, CMCC, ETRI, DOCOMO, ASUSTek, Nokia, Interdigital

Two groups of SS sets are supported by current specification. Companies have different understanding for introducing more than 2 SSSGs. Some pros and cons claimed are as follows,

* + (Pros)UE can achieve higher power saving gain if finer adaptation granularity and wider adaptation aspects can be supported. (x6901)
  + (Cons) there will also introduce more complicate state transition design and error handling (x6481)(x7521)
  + (Cons) Rel-16 only supports 2 SSSGs, and if Rel-17 supports 3 SSSGs, it is not possible to reuse the framework as much as possible, such as *searchSpaceGroupIdList-r16* need to be re-designed for Rel-17 SSSG switching. (x6481)

Considering slightly majority companies supporting more than 2 SSSGs, can we try to agree on the followings first? If not, it is recommended to conclude no more than 2 SSSGs should be supported.

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| **[High] proposal 1c:**   * Up to 3 SSSGs is supported for Rel-17 SSSG switching in the active BWP. |

1. **Alt 1 vs Alt 2**

**Alt 1 (12 companies)**

* Alt 1-1: Samsung(\*), NEC(\*), Nordic(\*), Qualcomm(\*), CMCC(\*), ETRI(\*), Fraunhofer, DOCOMO(\*), ITRI, ASUSTeK(\*), Nokia(\*), Interdigital(\*)
* Alt 1-2: Nordic, Qualcomm, CMCC, Fraunhofer

Note:

* (\*): support more than 2 SSSGs can be configured.

**Alt 2 (12 companies)**

* Huawei/Hisilicon(\*), ZTE/Sanchips(\*), Spreadtrum, CATT(\*), Lenovo/Motorola Mobility(\*\*), OPPO(\*), LGE(\*), MediaTek(\*\*\*), Intel(\*), Panasonic(\*\*), Apple, Ericsson(\*\*\*)

Note:

* (\*): support to configure multiple candidate values of skipping duration by RRC signaling and use DCI to dynamically indicate one of the configured skipping durations
* (\*\*): support to configure by RRC a list of PDCCH monitoring adaptation behaviours, including which search space to be monitored with/without timer. DCI indicates which index in the list the UE should follow.
* (\*\*\*): skipping duration is configured by RRC

There isn’t clear majority for either Alt 1 or Alt2. And they are almost identical in terms of the functionality. Some pros and cons for Alt 1 and Alt 2 are as follows,

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| Alt 1 is better than Alt 2   * PDCCH signalling commands for SSSG switching outperforms PDCCH skipping (x8124) * In terms of codepoint mapping, Alt 1 is homogeneous, and Alt 2 is heterogeneous. Heterogeneous codepoint mapping is not found in Rel-15 and Rel-16 standards. (x7358) |
| Alt 2 is better than Alt 1   * By Alt 2, the current configurations of SS set can be reused (x6481) * The difference is that the existing timer to switch to SS group #1 -> #0 must be set 1 slot shorter than timer for PDCCH skipping. This due to applicable switching time currently specified (x7046) * Explicit indication of PDCCH skipping for a duration can work for different SSSG configurations, e.g. larger skipping step with sparse SSSG configuration, or smaller skipping step with dense SSSG configuration.(x7601) * With 2 SSSG, there is limited flexibility to dynamically switching between SSSG switching and skipping for Alt 1 (x7752). |

Proposal for Alt 1

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| **[High] proposal 1d-1:**   * If alt 1 is supported,   + supporting SSSG  switching to emulate PDCCH skipping functionality by an ‘empty’ SSSG which no SS set(s) is configured for the ‘empty’ SSSG, UE does not monitoring PDCCH on the ‘empty’  SSSG     - Y-bit is configured for scheduling DCIs (i.e., DCI format 1-1/0-1/1-2/0-2) indicating SSSG index.     - FFS timers for switching between SSSGs     - FFS: a ‘dormant SSSG’ which may have associated SS sets, and monitored conditionally (e.g., depending on HARQ NACK or RTT/ReTx timers) |

Proposal for Alt 2

If alt 2 is supported, the skipping duration can be indicated by DCI, RRC with one or multiple values. The proposals are as follows,

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| **[High] proposal 1d-2:**   * If alt 2 is supported,   + PDCCH schedules data and also indicates PDCCH monitoring adaptation by PDCCH skipping for a duration is supported.     - Y-bit to is configured for scheduling DCIs (i.e., DCI format 1-1/0-1/1-2/0-2),       * Alt 1: 1 bit for PDCCH skipping adaptation (supported by MTK, Ericsson)         + Value ‘0’ to indicate PDCCH skipping is not activated         + Value ‘1’ to indicate PDCCH skipping for a duration   Determination of the duration(s) for PDCCH skipping, e.g.,  by RRC signaling,  by specification   * + - * + FFS: SSSG switching       * Alt 2:more than 1-bit for PDCCH skipping adaptation         + Alt 2a: (supported by Huawei, ZTE,CATT,OPPO,LGE, Intel)   Value ‘0’ to indicate PDCCH skipping is not activated  Multiple candidate values of skipping duration configured by RRC signaling and use DCI to dynamically indicate one of the configured skipping duration   * + - * + Alt 2b: (supported by Lenovo/MotM, Panasonic)   Y equals to the number of configured SSS or SSSG  each bit of the bitmap corresponds to a configured SSS or SSSG   * + - * + FFS: SSSG switching |

Considering relatively infrequent occurrence, CONNECTED mode UEs should monitor Type0/0A/1/2-PDCCH CSS. Vivo and Nokia proposed the followings,

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| **[Medium]Proposal 1e:**  Type0/0A/1/2-PDCCH CSS monitoring is not impacted by PDCCH monitoring adaptation |

### Companies views (1st round)

Companies are encouraged to provide comments in the table below.

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| **Company** | **Comment** |
| Nordic | **1A:** to be fair at this point:  DCI-based PDCCH monitoring adaptation over at least 3 different monitoring behaviors including   * PDCCH skipping/empty SSSG monitoring * PDCCH monitoring by SSSG#0, * PDCCH monitoring by SSSG#1)   within an active BWP is supported in Rel-17.  **1B:** DCI format 2-0 and 2-6 could be left as FFS  **1C:** This could be conditioned on “if Alt 1 is selected”  **1D-1:**   * If alt 1 is supported,   + supporting SSSG  switching to emulate PDCCH skipping functionality by an ‘empty’ SSSG which no SS set(s) is configured for the ‘empty’ SSSG, UE does not monitoring PDCCH on the ‘empty’  SSSG     - Y-bit bit is configured for scheduling DCIs (i.e., DCI format 1-1/0-1/1-2/0-2) indicating SSSG index.     - FFS support of single timer to switch to default SSSG#0 or support of multiple timers between SSSGs     - FFS dynamic indication of initial timer value(s)     - FFS: a ‘dormant SSSG’ which may have associated SS sets, and monitored conditionally (e.g., depending on HARQ NACK or RTT/ReTx timers)   **1D-2:** Suggestion to clarifyFFS: interaction with SSSG switching, e.g. impact to skipping when SSSG timer expires, which SSSG after PDCCH skipping is monitored, etc.  **1E:** Support, UE shall monitor during paging window, RAR window, SI update window irrespective of whether skipping or which SSSG is active during active time. |
| Apple | Proposal 1a: On the definition of 3 adaption of monitoring behaviors, there is our proposed 3 different monitoring behaviors are:   1. PDCCH skipping: one or two skipping size can be RRC configured 2. SSSG switching: two SSSG can be RRC configured 3. Joint PDCCH skipping and SSSG switching. One DCI trigger skipping then monitoring using a different SSSG. For example, current SSSG is per slot monitoring. One DCI can trigger UE to skip next 8 slots, and resume monitoring using 2 slot SSSG pattern. This can easily be done using 2 bit, one bit for SSSG indication, one bit for skipping indication.   Proposal 1b: Agree with the proposal with modification. The number of bits can be RRC configured.  Proposal 1c: do not agree with the proposal. Support of 3 SSSGs have large spec impact. With Alt 2, we see 2 SSSG is enough.  Proposal 1d-1: SSSG switching with more than 3 SSSGs will have large spec impact. It is unclear of how many timers are defined, and state switching diagram between the state. In addition, as explained in our paper, it is not clear how UE can flexiblely move to different states after timer expire, when >2 SSSG are configured.  Proposal 1d-2: We do not see Alt 1 and Alt 2 are conflicting to each other. When the skipping step size is associated with SSSG periodicity, multiple step size (alt 2) can be signaled via one bit (alt 1). Here is the example:  Table I: example of joint triggering SSSG switching and PDCCH skipping   |  |  |  | | --- | --- | --- | |  | SSSG | Skipping | | 00 | Periodicity 1 slot | No skipping | | 01 | Periodicity 1 slot | Skipping 4x1=4 slots | | 10 | Periodicity 4 slot | No skipping | | 11 | Periodicity 4 slot | Skipping 4x4=16 slots | |
| Samsung | 1A: We have concern about “at least 3”. We already agreed to all the three monitoring behaviors in the bracket. The proposal doesn’t make any progress. We suggest to discuss whether or not to be more than 3. “At least 4” monitoring behaviors makes more sense, as we need as least 2 bits in DCI.  1B: Fine  1c: there are many adaptation aspects/dimensions can be supported based on SSSG switching. We think it should be “At least 3 SSSGs” rather than “up to 3 SSSGs”,  1d: the two alternatives can be discussed together. There is no much difference between the two alternatives. The key is codepoint in DCI and how to determine associated PDCCH skipping duration. |
| CATT | We have following advantages of Alt-2 (PDCCH skipping) comparing to Alt-1 (SSSG) and Alt-2   * PDCCH skipping is UE-specific configuration and the PDCCH monitoring can be dynamically adapted continuously to the traffic arrival process. For SSSG, it is one instance of PDCCH monitoring adaptation within one valid interval. * There is no application delay for PDCCH skipping. There is application delay for SSSG. * PDCCH skipping does not associate with HARQ process and its A/N feedback. The PDCCH skipping could indicate the change of PDCCH monitoring intervals in the initial transmission or retransmission of any HARQ process and take effect after receiving the DCI. SSSG switching would need to confirmation from A/N of one HARQ processes. If more than one HARQ processes running concurrently, additional mechanism need to be addressed to identify the switching time of SSSG, which is additional delay and UE power consumption. * PDCCH skipping use the same procedure of Abnormal handling of DCI miss-detectoin when DCI carrying skipping information is missed detected since UE. SSSG needs to define the procedure of Search space used for DCI miss-detection.   For Proposal 1 (a) – we don’t see the proposal would help the progress since it is one special scenario for configuring more than one search spaces with PDCCH skipping  For Proposal 1 d (2) – the PDCCH skipping is not continuous PDCCH monitoring adaptation. UE would change the monitoring interval only receiving new PDCCH skipping command. We would like to replace “a duration” by “the indicated interval”  PDCCH schedules data and also indicates PDCCH monitoring adaptation by PDCCH skipping the indicated interval ~~for a duration~~ is supported.  The number of bits for PDCCH skipping indication could be configured by the network. |
| Qualcomm | Proposal 1a: We are generally fine with the proposal. However, as discussed during the GTW session, it is a bit doubtful whether this proposal is a step forward compared to the previous agreements. Since we have already agreed to support both PDCCH skipping and SSSG switching functions in previous meetings, this proposal looks like a direct conclusion.  Proposal 1b: In our view, the size of DCI field should depend on the configuration of the PDCCH monitoring adaptation, and we don’t see any reason to limit the minimum bit field size. For example, if the UE is configured only with SSSG switching between two SSSGs, 1-bit field would be enough. If the question is whether the DCI field size is “at most” 2 bits, not “at least” 2 bits, we think it’s worth discussing further.  Proposal 1c: We think more than 3, e.g., 4, SSSGs are beneficial – if we support 2-bit field in DCI, limiting up to 3 SSSGs may not be efficient since one codepoint would be wasted. However, as many companies have mentioned in their contribution, this proposal is dependent on Proposal 1d discussion, so we think this should be deferred after Proposal 1d has been agreed.  Proposal 1d: As a proponent of Alt 1, we generally support Proposal 1d-1. However, in the main bullet of Proposal 1d-1, we think it’s fair to add both empty SSSG and dormant SSSG, since they are not a subset of each other:   * + supporting SSSG  switching to emulate PDCCH skipping functionality by an ‘empty’ SSSG or a ‘dormant’ SSSG~~which no SS set(s) is configured for the ‘empty’ SSSG, UE does not monitoring PDCCH on the ‘empty’  SSSG~~     - Y-bit is configured for scheduling DCIs (i.e., DCI format 1-1/0-1/1-2/0-2) indicating SSSG index.     - FFS timers for switching between SSSGs     - FFS: down selection between ‘empty SSSG’ and ‘dormant SSSG’     - ~~FFS: a ‘dormant SSSG’ which may have associated SS sets, and monitored conditionally (e.g., depending on HARQ NACK or RTT/ReTx timers)~~   Note: Since the difference between ‘empty’ and ‘dormant’ SSSGs are already captured in last meeting’s agreement (RAN1 #105-e), it could be omitted.  Proposal 1e: If Alt 1 or Proposal 1d-1 is agreed, we believe Proposal 1e directly follows, since SSSG configuration is allowed only for USS and Type3 CSS. Other CSSs (Type0/0A/1/2) are not impacted by SSSG switching, as in Rel-16. |
| Intel | We could consider the following by merging 1a, 1b to make a step forward:  **Proposal:**  At least 2-bit field in scheduling DCIs (i.e., DCI format 1-1/0-1/1-2/0-2) can be configured for triggering the PDCCH monitoring adaptation over at least 3 different monitoring behaviors (including PDCCH skipping for a duration, PDCCH monitoring by SSSG#0, PDCCH monitoring by SSSG#1) within an active BWP is supported in Rel-17.   * FFS details   Regarding 1c), we do not support. 2 SSSGs seem sufficient  Regarding 1d), it can be discussed after we agree how many bits will be supported |
| Huawei，Hisilicon | **Proposal 1a:**  We are also not sure the progess made by proposal 1a compared with previous agreements. If companies want to do some further clarification, maybe it would be better to clearly define three functionalities that could be supported as proposed by Apple.   1. PDCCH skipping: one or two skipping size can be RRC configured 2. SSSG switching: two SSSG can be RRC configured 3. Joint PDCCH skipping and SSSG switching. One DCI trigger skipping then monitoring using a different SSSG. For example, current SSSG is per slot monitoring. One DCI can trigger UE to skip next 8 slots, and resume monitoring using 2 slot SSSG pattern. This can easily be done using 2 bit, one bit for SSSG indication, one bit for skipping indication.   **Propsoal 1b**  The proposal 1b seems to say a new/dedicated field shall be configured to support the purpose of proposal 1a. We have proposal to extend dormancy indication field for the indication of PDCCH skipping or SSSG switching. Therefore, we think we should discuss the potential DCI format and fields directly and we don’t see a need to agree proposal 1b.  **proposal 1c:**  Not agree. When Alt 2（not use SSSG switching to emulate PDCCH skipping）is adopted, there is no need to introduce more than 2 SSSGs.  If Alt.1 is adopted, the benefit of Alt.1 claimed by the supporting companies is reusing SSSG switching framework. However, we don’t see this is true when more than 2 SSSGs needs to be introduced or any specific behaviour based on dormant SSSG needs to be defined.  It would significantly increase the UE complexity and standard work if more than 2 SSSG(s) shall be introduced.  **proposal 1d-1/2:**  It seems we should also list all the potential options for Alt.1.   * If alt 1 is supported,   + supporting SSSG  switching to emulate PDCCH skipping functionality by an ‘empty’ SSSG which no SS set(s) is configured for the ‘empty’ SSSG, UE does not monitoring PDCCH on the ‘empty’  SSSG     - Y-bit is configured for scheduling DCIs (i.e., DCI format 1-1/0-1/1-2/0-2) indicating SSSG index.       * Alt.1: 1 bit for switching between up to 2 SSSG(s);       * Alt.2: more than 1-bit for switching between more than 2 SSSG(s);     - FFS timers for switching between SSSGs       * Alt.1: multiple timer(s) are configured by RRC signaling and one of them indicated by DCI;       * Alt.2: a timer configured per SSSG;     - FFS: a ‘dormant SSSG’ which may have associated SS sets, and monitored conditionally (e.g., depending on HARQ NACK or RTT/ReTx timers)   In our view, it was already agreed in RAN1#104 and RAN1#105 that PDCCH skipping functionality is supported. It is the most straight forward way and the baseline to explicitly specify it, i.e. Alt.2. Alt.1 needs to jutify the benefit over Alt.2. Otherwise, Alt. 2 should be adopted. |
| LG | 1a: We agree that 3 monitoring adaptation behaviors should be like the proposal 1a. PDCCH skipping of two durations would be one monitoring adaptation behavior because they are essentially the same behavior, only with a difference in duration. Therefore, even if 2 bits are agreed to support indication of monitoring adaptation, 3 different behaviors are enough. We suggest a modificaiton as follow:  DCI-based PDCCH monitoring adaptation over at least 3 different monitoring behaviors (including PDCCH skipping of multiple durations, PDCCH monitoring by SSSG#0, PDCCH monitoring by SSSG#1) within an active BWP is supported in Rel-17.  1b: We are fine with the proposal. The codepoint design suggested by the chair in the last meeting would be a good starting point. We think 2-bit codepoint mapping is okay, for example, 1 bit for indicating switching/skipping and 1 bit for group #0/#1 if switching is indicated and 1 bit for duration A/B if skipping is indicated. We are open to discuss whether more than 2 bits are needed or not.  1c: Supporting more than 2 SSSGs cannot avoid a spec impact as the contribution(x6481) claimed. Proposal 1c is the conditionally required that Alt 1 is supported.  1d: Alt 1 needs large spec impact that at least 3 SSSGs are needed. They are one for empty/dormant group and two for group 0 and 1. Thus, it is not true that Alt 1 can be implemented by using the exsiting spec (NR-U) without impact. Also, as stated in our contribution, we don’t believe that SSSG switching to empty group and PDCCH skipping are not the same behaviors with regard to monitoring Type0/0A/1/2-PDCCH CSS sets.  1e: As sated above, PDCCH skipping behavior regarding Type0/0A/1/2-PDCCH CSS sets can be different from SSSG switching to empty group. Therefore, we need to further discuss. |
| ZTE, Sanechips | (1)We are also not sure what the additional information provided by proposal 1a is. If it is needed, we agree with LG that 2 PDCCH skipping durations can be supported which does not increase signaling overhead for the 2-bit indication.  (2) We are not sure why more than 2 bits are needed for PDCCH adaptation.  (2)For Proposal 1c, the additional power saving gain from 3 SSSGs has not been simulated yet. According to our understanding, we don’t believe there is attractive power saving gain from more than 2 SSSG.  (3) for proposal 1d-1, we think all the possible solutions (such as the state transition when more than 2 SSSGs are supported) should be listed as proposal 1d-2.  (4) for proposal 1d-2, the alt1 and alt 2a are not exclusive. While for alt 2b, it seems to describe how to perform SSSG switch, instead of PDCCH skipping. |
| DOCOMO | We are fine with Proposal 1a and Proposal 1b.  For Proposal 3, we prefer “At least 3 SSSGs” rather than “up to 3 SSSGs”. |
| MTK | **Proposal 1a/1b:**  Support proposal 1a/1b. According to our Tdoc (x7521) observation, at least the following 3 monitoring behaviors are needed for different scenerios.   * Per-slot monitoring (SSSG 0): The default monitoring behaviour during scheduling of data packets * PDCCH skipping for a duration: Switch to this behaviour after the last TB scheduling * Periodical PDCCH monitoring (SSSG 1): Switch to this behaviour when there is potential timing critical data scheduling (e.g., for AR/VR UL traffic))   Thus, at least 2 bits are needed in scheduling DCI.  **Proposal 1c:**  Regarding proposal 1c, we are not sure whether it is dedicated for Alt 1. Futher clarification is needed.  **Proposal 1d-1:**  It also has to clarify whether proposal 1d-1 is to down select Alt 1-1. If so, FFS for dormant SSSG is not needed.  **Proposal 1d-2:**  Regarding proposal 1d-2, we think it is too early to discussion this issue. It will be beneficial to determine how many skip duration should be supported first.  In addition, we are not sure whether last FFS in proposal 1d-2 clarify that which SSSG UE will switch to when the skip duration expires. |
| IDCC | 1-a: We understand what the proposal is trying to achieve but have some doubts whether it is necessary. However, we are ok with it.  1-b: Agree.  1-c: We think only 2 SSSGs are sufficient. 3 SSSGs will have a large spec impact. We can achieve the agreed functionalities with Alt. 2 in which only 2 SSSGs are used.  1d-1: Agree in principle.  1d-2: We do not agree with this proposal. We think there is no reason to limit the options to Alt.1 and Alt2. As we explained in our Tdoc, with 2 bits, both skipping and switching can be supported. In general, the interpretation of codepoints can be configured per SSSG. For example, 2 codepoints can be used to indicate switching and two codepoints can be used for to indicate skipping (Example 1 below). Or, all four codepoints can be allocated to indicate skipping only (Example 2 below). If actuualy the alternatives cobver this general approach, then soms clarification is needed.  Example 1:   |  |  |  | | --- | --- | --- | | Current SSSG | DCI bits | Next SSSG | | 0 | 00 | 0 | | 0 | 01 | 1 | | 0 | 10 | 0 (skip duration T0) | | 0 | 11 | 0 (skip duration T1) | |  |  |  | | 1 | 00 | 0 | | 1 | 01 | 1 | | 1 | 10 | 1 (skip duration T2) | | 1 | 11 | 1 (skip duration T3) |   Example 2:   |  |  |  | | --- | --- | --- | | Current SSSG | DCI bits | Next SSSG | | 0 | 00 | 0 | | 0 | 01 | 1 | | 0 | 10 | 0 (skip duration T0) | | 0 | 11 | 0 (skip duration T1) | |  |  |  | | 1 | 00 | 1 (skip duration T2) | | 1 | 01 | 1 (skip duration T3) | | 1 | 10 | 1 (skip duration T4) | | 1 | 11 | 1 (skip duration T5) | |
| Nokia | Proposal 1a: Like expressed by some companies, not sure if this proposal adds much on top of earlier discussion but we would be fine with the wording proposed by Nordic.  In terms of having multiple configurable durations for stopping PDCCH monitoring, not sure if this is needed. Majority of the companies (and the justification for the gains) have assumed to cease PDCCH monitoring till next DRX cycle/eof OnDuration, thus there does not seem to be need for it. Furthermore, if PDCCH skipping is done separately of SSSG switching (i.e. not as emptu SSSG), this would complicate the transition diagrams.  Proposal 1b: In principle we are fine having 2 bits in DCI, but depending on the other agreements this could be either one 2-bit field, or two 1-bit fields. I.e. if SSSG and skipping are defined as independent features, it is counter intuitive to enforce mixing via DCI field. Also, the option of configuring only 1-bit field should be allowed.  Proposal 1c: At least in the case if the Alt1 is selected, this should be agreed. In the alternative case, if supporting multiple skipping durations can be considered to increase the flexibility, it seems counter intuitive if same argumentation would not apply to SSSG switching. We don’t see the timer based state transitions between SSSG’s overly complicated, this can be determined. It would seem more complicated to determine the interaction between skipping and SSSG switching if these are independent functionalities.  Proposal 1d: Similar as in last meeting we would support Alt1/Propsoal 1d-1. Also, as has been discussed, the targeted PDCCH monitoring adaptation behaviour can be achieved throught the SSSG switching frame work, thus there does not appear to be strong reason to select Alt-2.  Proposal 1e: We think that this proposal is necessary. UE should not cease/skip Type2 PDCCH CSS monitoring as it may carry ETWS indication, nor any PDCCH monitoring adaptation should hinder UE acquiring up-to date system information. In case of PDCCH monitoring adaptation based SSSG it should be assumed that these CSS are not allocated to any SSSG (and hence always monitored). |
| CMCC | 1a: Support  1b: Support  1c: As companies says, if we agree 2bits in DCIs and Alt1 i.e., SSSG switching is adopted, the 3 SSSGs cause the waste of 2 bits. One way is to extend to 4 SSSGs, SSSG#2 and SSSG#3 are empty or dormant SSSGs aut with different PDCCH skipping duration. Another way is define two PDCCH skipping dutaions for SSSG#2 and indicated by DCI.  1d-1: We think ‘dormant’ SSSG should be added in the main bullet, since it is a simple way to solve the HARQ retransmission issue#4 and also aligns with proposal 4a.  1d-2: Ok |
| OPPO | We agree that the proposal 1a is a good start for the unified solution. We can also consider the 2 bits indication.  Regarding the 1c, this proposal is unclear about what kind of 3 SSSG we are going to introduced. Going though companies contributions, we seem some of the contributions acuatlly think about empty SSSG / dormant SSSG are among the 3 or 4 SSSG. However, it is noted that we actually did not make clear difination of empty SSSG / dormant SSSG, comparing with PDCCH skipping.  For us, empty SSSG can be seen a “virtual” dormant SS set, which may only be taken into effect for one time duration. In that sense it has nothing different to the PDCCH. The corresponding SSSG state and timer should not even introduced. For that sense, we see the companies counting on number of SSSG should not be included. Thus, we should clarify what type of SSSG we are in mind, firstly.  For our point of view simpely PDCCH skipping is enough, and then 2 SSSG is sufficient. We can avoid very complex state machine in the specification.  Suggest proposal 1c: Clarify the empty SSSG or dormant SSSG. The basic behaviour which have essential different to PDCCH skipping is to be identified. If not, we can combine the PDCCH skiping concept with empty SSSG.  This will also help for merge the Alt1-1 Alt1-2 and Alt-2. Then the proposal 1d-1/2 can be simplified. |
| ETRI | We support Proposal 1a and 1b. For Proposal 1a, Nordic’s modification is preferred.  We support Proposal 1c. In our view, up to 3 SSSGs are sufficient for Rel-17 operation.  We are generally fine with Proposal 1d-1 and 1d-2. Prefer to discuss 1d-1/1d-2 after making decision on 1a/1b. |
| Panasonic | On 1a, we think what is stated does not progress from what was agreed before on the common design for PDCCH skipping and SSSG switching.  Firstly trying to agree on one of the two alternatives should be prioritized. Also agree on some of the companies that describing which functionalities is clearer for discussion, e.g. 3 functions including PDCCH skipping, SSSG switching and combination of the two.  On 1b, okay to us.  On 1c, 2 or 3 are either okay with us in the condition that all SSSGs are “real” and does not emulate anything else.  On 1d-1/2, to deprioritize and discuss later after doing a hard decision. |
| Spreadtrum | 1a) There are just two PDCCH monitoring behaviors. For PDCCH skipping, they are the normal PDCCH monitoring and PDCCH skipping for a duration. For R17 SSSG switching, they are SSSG#0 and SSSG#1. We fail to understand the mixture of PDCCH skipping and SSSG switching. In semi-static configuration, PDCCH skipping and SSSG switching can be selected by gNB. We don’t remember whether there is any evaluation for the mixture of two techniques. The mixture is over-design without additional power saving gain in our view.  1b) 1 bit is enough. If 2 bits is used, for PDCCH skipping, it can indicate 3 different durations. We don’t know the benefit of the mixture of PDCCH skipping and SSSG switching.  1c) 2 SSSGs are enough.  1d-1) not support  1d-2) support |
| Fraunhofer | Proposal 1a: The benefit of agreeing to this proposal is unclear to us. However, we also do not have any concern with it. As mentioned by Apple, clearly defining the behaviors in the proposal seems benefical.  Proposal 1b: The number of bits required for signaling is also tied to the interworking with other mechanisms. For example, we proposed that the minimum scheduling offset indication can be used to trigger an SSG switch if cross-slot scheduling is used. In that case a fewer number of bits are required.  Proposal 1c: Supporting more than 2 SSGs does not seem to be required if PDCCH skipping is supported separately.  Proposal 1d-1+2: We think discussing both points jointly would benefit the discussion. |
| Ericsson | Proposal 1a : As discussed in GTW it may be efficient to directly agree to Proposal 1b, but if discussion on this is continued, we suggest a slight wording update as below (replace “by” with “according to” to align with existing SSSGS text in 38.213)  *DCI-based PDCCH monitoring adaptation over at least 3 different monitoring behaviors (including PDCCH skipping, PDCCH monitoring according to ~~by~~ SSSG#0, PDCCH monitoring according to ~~by~~ SSSG#1) within an active BWP is supported in Rel-17.*  Proposal 1b : OK (our understanding is that this is Y bit(s) being discussed in other proposals)  Proposal 1c : Do not support – 2 SSSGs are sufficient, and then PDCCH skipping for a duration can be directly supported instead of emulating it with a new enhancement of SSSG switching.  Proposal 1d:  For (1d-1), it seems the Alt 1-1 and 1-2 are merged – some clarification is appreciated on the need for merging ‘empty’ and ‘dormant’. Other than timer, for (1d-1), need to also address the SSSG switching procedure upon timer expiry, application delays while switching between different pairs of SSSG, etc.  For (1d-2), this is too detailed - focus should be on deciding between the Alts 1-1, 1-2 and 2 from last meeting. However, if needed, below text is sufficient.   * *Y-bit is configured for scheduling DCIs (i.e., DCI format 1-1/0-1/1-2/0-2) indicating SSSG index, and/or PDCCH skipping duration(s) (explicitly configured by higher layers)*   + *FFS details.*   Proposal 1e : Support – this is needed to avoid impact to system operation – note this is already supported for SSSGS functionality. |
| Lenovo, Motorola Mobility | Fine with proposal 1a and 1b.  For proposal 1c, the number of SSSG may be dependent on the outcome of how to support PDCCH skipping. Not agree at this point.  For proposal 1d-1, we think direct indication of PDCCH skipping with configured/dynamically indicated skipping duration is much simpler in terms of specifying UE behaviors and spec efforts.  In proposal 1d-2, we suggest to modify Alt 2b as follows:   * Alt 2b: (supported by Lenovo/MotM, Panasonic)   + SSS/SSSG specific skipping indication via e.g. bitmap, codepoint, joint indication with a minimum scheduling offset value   Fine with proposal 1e. |

### Updated proposals (2nd round before GTW session)

For proposal 1a,

* Samsung, Qualcomm, CATT, Huawei, ZTE, IDCC, Panasonic commented that agreeing on Proposal 1a does not make much progress since it can be a natural outcome of the previous agreements. While other companies has different view.
* Nordic and Intel proposes changes in ‘including….’ and it is addressed by adding ‘e.g.’
* ***Recommendation***: try to see the following revisions after addressing comments from companies are acceptable

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| **[High] Proposal 1a:**   * DCI-based PDCCH monitoring adaptation over at least 3 different monitoring behaviors (e.g., including PDCCH skipping for a duration(s), PDCCH monitoring by SSSG#0, PDCCH monitoring by SSSG#1) within an active BWP is supported in Rel-17. |

For proposal 1b,

* CATT, Huawei, Apple, Qualcomm, Nokia commented that the size of the DCI field should be configurable. Details to how to configure can be FFS.
* Qualcomm commented size of DCI field should depend on the configuration of the PDCCH monitoring adaptation, and don’t see any reason to limit the minimum bit field size. If the question is whether the DCI field size is “at most” 2 bits, not “at least” 2 bits, we think it’s worth discussing further. This comment is addressed.
* ***Recommendation***: suggest to agree on the followings first.

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| **[High] Proposal 1b:**   * At most 2 bit indication in scheduling DCIs (i.e., DCI format 1-1/0-1/1-2/0-2) can be configured for triggering the PDCCH monitoring adaptation   + FFS: the size of the indication is configurable   + FFS: bit mapping to the PDCCH monitoring behaviour |

For proposal 1c, many companies think it depends on the outcome of down-slection of Alt 1 and 2.

* ***Recommendation***: suggest to discuss proposal 1c later after down-selection.

For proposal 1d,

* ***Recommendation***: it is suggest to describe the specification impact for each alternative as much as possible. And after addressing comments from multiple companies, the description of Alt 1 and Alt 2 are provided in the following **proposal 1d-1 and proposal 1d-2,** and it is suggested to agree on this.

Proposal for Alt 1

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| **[High] proposal 1d-1:**   * If alt 1 is supported,   + supporting SSSG  switching to emulate PDCCH skipping functionality by an ‘empty’ SSSG (i.e. Alt 1-1)or ‘dormant’ SSSG(i.e. Alt 1-2)     - Y bits is configured for scheduling DCIs (i.e., DCI format 1-1/0-1/1-2/0-2) indicating SSSG index.       * FFS dynamic indication of initial timer value(s)       * FFS details     - At most [3] SSSGs is supported to be configured.     - ~~FFS support of single timer to switch to default SSSG#0  or support of multiple timers between SSSGs~~     - FFS whether the following timer(s) is supported for switching between       * Option 1: Non-default SSSG to default SSSG (i.e., SSSG#0)       * Option 2: Non-default SSSG to another non-default SSSG       * Option 3: Default SSSG (i.e., SSSG#0) to non-default SSSG(s)     - FFS: down selection between ‘empty’ SSSG (i.e. Alt 1-1)or ‘dormant’ SSSG(i.e. Alt 1-2)     - Note: description of ‘empty’ SSSG and ‘dormant’ SSSG has been provided in RAN1#105-E |

Proposal for Alt 2

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| **[High] proposal 1d-2:**   * If alt 2 is supported,   + PDCCH schedules data and also indicates PDCCH monitoring adaptation by PDCCH skipping for a duration is supported.     - Y bits is configured for scheduling DCIs (i.e., DCI format 1-1/0-1/1-2/0-2) indicating PDCCH monitoring adaptation (including  SSSG index, and/or PDCCH skipping duration(s))       * Alt 2-1:         + FFS: Determination of the duration for PDCCH skipping, e.g.,   One skipping duration configured by RRC signaling,  Multiple candidate values of skipping duration configured by RRC signaling and use DCI to dynamically indicate one of the configured skipping duration  by specification   * + - * + FFS: possible value(s) of the duration         + FFS: joint or separate indication with SSSG switching       * Alt 2-3:         + SSS/SSSG specific skipping indication via e.g. bitmap, codepoint, joint indication with a minimum scheduling offset value     - FFS: interaction with SSSG switching (if configured), e.g. impact to skipping when SSSG timer expires, which SSSG after PDCCH skipping is monitored, etc. |

### Updated proposals (2nd round after GTW session)

For proposal 1a,

Many companies rthink agreeing the previous poposal 1a does not make much progress. Nordic raised an issue to know what other behaviors in addition to below the companies have in mind, in other words what is UE behavior after receiving PDCCH indication of monitoring adaptation. And if companies have different understanding what agreed PDCCH skipping and SSSG switching mean.

* ***Recommendation***: suggest to discuss and complete the lists in new proposal 1a.

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| **[High] Proposal 1a(new):**  UE behavior after receiving PDCCH indication of monitoring adaptation can be,   * Beh 1: PDCCH skipping means stopping monitoring in all USS and TYPE3 CSS search-space sets  for a period of time * Beh 2: SSSG#0 is active means monitoring SS sets not associated with any SSSG and monitoring  of search-space-sets  associated to SSSG#0 (legacy behaviour) * Beh 3: SSSG#1 is active means monitoring SS sets not associated with any SSSG and monitoring  of search-space-sets  associated to SSSG#1 (legacy behaviour) * Beh 4: SSSG#2 is active means monitoring SS sets not associated with any SSSG and monitoring  of search-space-sets  associated to SSSG#2 (if SSSG#2 is specified and configured) |

For proposal 1b,

During the Wednesday GTW session, proposal 1b is modified and agreed as follows

Agreement

* At most 2 bit indication in self-scheduling DCIs (i.e., DCI format 1-1/0-1/1-2/0-2) can be specified for triggering the PDCCH monitoring adaptation in a single cell
  + FFS: the bit size of the indication is configurable
  + FFS: bit mapping to the PDCCH monitoring behaviour
  + FFS: details of indication of multiple cells case
* ***Recommendation***: companies provide details of the FFS is encouraged.

For proposal 1c, many companies think it depends on the outcome of down-slection of Alt 1 and 2.

* ***Recommendation***: suggest to discuss proposal 1c later after down-selection.

For proposal 1d,

* ***Recommendation***: it is suggest to describe the specification impact for each alternative as much as possible. And after addressing comments from multiple companies, the description of Alt 1 and Alt 2 are provided in the following **proposal 1d-1 and proposal 1d-2,** and it is suggested to agree on this.

Proposal for Alt 1

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| --- |
| **[High] proposal 1d-1:**   * If alt 1 is supported,   + supporting SSSG  switching to emulate PDCCH skipping functionality by an ‘empty’ SSSG (i.e. Alt 1-1)or ‘dormant’ SSSG(i.e. Alt 1-2)     - Y bits is configured for scheduling DCIs (i.e., DCI format 1-1/0-1/1-2/0-2) indicating SSSG index.       * FFS dynamic indication of initial timer value(s)       * FFS details     - At most [3] SSSGs is supported to be configured.     - ~~FFS support of single timer to switch to default SSSG#0  or support of multiple timers between SSSGs~~     - FFS whether the following timer(s) is supported for switching between       * Option 1: Non-default SSSG to default SSSG (i.e., SSSG#0)       * Option 2: Non-default SSSG to another non-default SSSG       * Option 3: Default SSSG (i.e., SSSG#0) to non-default SSSG(s)     - FFS: down selection between ‘empty’ SSSG (i.e. Alt 1-1)or ‘dormant’ SSSG(i.e. Alt 1-2)     - Note: description of ‘empty’ SSSG and ‘dormant’ SSSG has been provided in RAN1#105-E |

Proposal for Alt 2

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| **[High] proposal 1d-2:**   * If alt 2 is supported,   + PDCCH schedules data and also indicates PDCCH monitoring adaptation by PDCCH skipping for a duration is supported.     - Y bits is configured for scheduling DCIs (i.e., DCI format 1-1/0-1/1-2/0-2) indicating PDCCH monitoring adaptation (including  SSSG index, and/or PDCCH skipping duration(s))       * Alt 2-1:         + FFS: Determination of the duration for PDCCH skipping, e.g.,   One skipping duration configured by RRC signaling,  Multiple candidate values of skipping duration configured by RRC signaling and use DCI to dynamically indicate one of the configured skipping duration  by specification   * + - * + FFS: possible value(s) of the duration         + FFS: joint or separate indication with SSSG switching       * Alt 2-3:         + SSS/SSSG specific skipping indication via e.g. bitmap, codepoint, joint indication with a minimum scheduling offset value     - FFS: interaction with SSSG switching (if configured), e.g. impact to skipping when SSSG timer expires, which SSSG after PDCCH skipping is monitored, etc. |

### Companies views (2nd round)

Companies are encouraged to provide comments in the table below.

|  |  |
| --- | --- |
| **Company** | **Comment** |
| Nordic | **Proposal 1a : OK**  **Proposal 1d-1: We do not think that timers are mutually exclusive**  **FFS whether one or more of the following timer(s) is supported for switching between**  **• Option 1: Non-default SSSG to default SSSG (i.e., SSSG#0)**  **• Option 2: Non-default SSSG to another non-default SSSG**  **• Option 3: Default SSSG (i.e., SSSG#0) to non-default SSSG(s)**  **Proposal 1d-2:** interaction with SSSG switching (if configured) -> **:** interaction with SSSG switching (when configured) |
| OPPO | Proposal 1a： The new version is a big leap to previous. We actually need to define what is the number of SSSGs should be introduced. Now it goes to the details of SSSG 1/2/3. We don’t even have agreement on how the SSSG behaviour is and how many it will be.  Thus, the earlier version is more useful .  DCI-based PDCCH monitoring adaptation over at least 3 different monitoring behaviors (e.g., including PDCCH skipping for a duration(s), PDCCH monitoring by SSSG#0, PDCCH monitoring by SSSG#1) within an active BWP is supported in Rel-17.  Alt1 and Alt2, as wen commented in earlier email, can actually have some common part on how SSSG will emulate the PDCCH skipping. Acatually, with minimum definition of SSSG, it will be the same as skipping. We don’t think one bullet empty SSSG description already fully define the empty SSSG.Thus, we have suggestion on both proposal 1d-1 and 1d-2.  **proposal 1d-1:**   * If alt 1 is supported,   + supporting SSSG  switching to emulate PDCCH skipping functionality by an ‘empty’ SSSG (i.e. Alt 1-1)or ‘dormant’ SSSG(i.e. Alt 1-2)     - Y bits is configured for scheduling DCIs (i.e., DCI format 1-1/0-1/1-2/0-2) indicating SSSG index.       * FFS dynamic indication of initial timer value(s)       * FFS details     - ~~At most [3] SSSGs is supported to be configured.~~     - ~~FFS support of single timer to switch to default SSSG#0  or support of multiple timers between SSSGs~~     - FFS whether the following timer(s) is supported for switching between       * Option 1: Non-default SSSG to default SSSG (i.e., SSSG#0)       * Option 2: Non-default SSSG to another non-default SSSG       * Option 3: Default SSSG (i.e., SSSG#0) to non-default SSSG(s)     - FFS: down selection between ‘empty’ SSSG (i.e. Alt 1-1)or ‘dormant’ SSSG(i.e. Alt 1-2)     - FFS: ‘empty’ SSSG and ‘dormant’ SSSG, can be looked as a skipping duration and whether to introduce a SSSG state.   ~~Note: description of ‘empty’ SSSG and ‘dormant’ SSSG has been provided in RAN1#105-E~~  **proposal 1d-2:**   * If alt 2 is supported,   + PDCCH schedules data and also indicates PDCCH monitoring adaptation by PDCCH skipping for a duration is supported.     - Y bits is configured for scheduling DCIs (i.e., DCI format 1-1/0-1/1-2/0-2) indicating PDCCH monitoring adaptation (including  SSSG index, and/or PDCCH skipping duration(s))       * Alt 2-1:         + FFS: Determination of the duration for PDCCH skipping, e.g.,   One skipping duration configured by RRC signaling,  Multiple candidate values of skipping duration configured by RRC signaling and use DCI to dynamically indicate one of the configured skipping duration  by specification   * + - * + FFS: possible value(s) of the duration         + FFS: joint or separate indication with SSSG switching       * Alt 2-3:         + SSS/SSSG specific skipping indication via e.g. bitmap, codepoint, joint indication with a minimum scheduling offset value   FFS: interaction with SSSG switching (if configured), e.g. impact to skipping when SSSG timer expires, which SSSG after PDCCH skipping is monitored, etc.  Also regarding the Y bits for both of proposal. Is it already agree to have up to 2 bits? Wondering what the bullets will bring. |
| Qualcomm | In our view, there is no major functional difference between Alt 1 and Alt 2. However, we think Alt 1 has less specification impact:   * Alt 1 is based on a single scheme, SSSG switching, which can be specified by a single configuration and a single UE capability. In addition, the existing design of Rel-16 SSSG switching can largely be leveraged. On the other hand, there are two separate schemes underlying Alt 1, i.e., SSSG switching and DCI-indicated skip duration, and their configuration and related UE capability should be specified separately. * All DCI fields in Rel-15 and Rel-16 either indicates a state change (e.g., BWP switch, minK0/minK2 indication, etc.) or a single-time action (e.g., CSI request, SRS request, etc.). In that sense, all the DCI fields in Rel-15/16 are homogeneous. However, with Alt 2, some codepoints of the DCI field will be mapped to a state chage (SSSG switch) and some others to an action (PDCCH skipping). Thus, it is heterogeneous. On the other hand, for Alt 1, the DCI field is homogeneous (SSSG switch only), and more friendly to the existing specification. * Some companies shows a concern on the complexity of Alt 1 when the number of SSSGs are larger than 2. However, empty or dormant SSSGs are not a full-fledged SSSG, and the added complexity to the two full-fledged (SSSG #0/#1) would be marginal. To address the complexity concern, we can consider separate restriction to the number of dormant SSSGs and the number of non-dormant SSSGs (e.g., up to 2 non-dormant SSSGs + up to 2 dormant SSSGs). * Many companies supported Proposal 1e in the first round, that is, “Type0/0A/1/2-PDCCH CSS monitoring is not impacted by PDCCH monitoring adaptation”. With Alt 1, since SSSG switching is only applied to Type3 CSS and USS, Proposal 1e is already an integrated feature. On the other hand, for Alt 2, we need to separately specify a related behavior during a PDCCH skipping duration. * From the contributions, we observed that some proponent companies of Alt 2 also want to introduce some ‘Alt 1-like’ behaviors to Alt 2, for example:   + The PDCCH skip duration depends on the current SSSG   + The UE switch to the derault or configured SSSG after PDCCH skipping   However, if any kind of state-dependence between PDCCH skipping and SSSG switching is introduced, as the above examples, we think it already implies Alt 1, not Alt 2, because PDCCH skipping is not just a one-time action, but can be mapped to a node (state) in the state transition diagram (i.e., the previous and next state of PDCCH skip state is determined).  Based on the third bullet above, we suggest a modification of Proposal 1d-1:   * If alt 1 is supported,   + supporting SSSG  switching to emulate PDCCH skipping functionality by an ‘empty’ SSSG (i.e. Alt 1-1)or ‘dormant’ SSSG(i.e. Alt 1-2)     - Y bits is configured for scheduling DCIs (i.e., DCI format 1-1/0-1/1-2/0-2) indicating SSSG index.       * FFS dynamic indication of initial timer value(s)       * FFS details     - At most [3] SSSGs is supported to be configured.       * Whether the number of empty/dormant SSSGs and the number of non-empty/non-dormant SSSGs are separately restricted     - ~~FFS support of single timer to switch to default SSSG#0  or support of multiple timers between SSSGs~~     - FFS whether the following timer(s) is supported for switching between       * Option 1: Non-default SSSG to default SSSG (i.e., SSSG#0)       * Option 2: Non-default SSSG to another non-default SSSG       * Option 3: Default SSSG (i.e., SSSG#0) to non-default SSSG(s)     - FFS: down selection between ‘empty’ SSSG (i.e. Alt 1-1)or ‘dormant’ SSSG(i.e. Alt 1-2)     - Note: description of ‘empty’ SSSG and ‘dormant’ SSSG has been provided in RAN1#105-E |
| vivo | Response to Nordic’s comments,  For Beh2 and Beh3, not sure whether the legacy behavior is like this. In TS38.213, it is said, ‘*…the UE starts monitoring PDCCH according to search space sets with group index 0, and stops monitoring PDCCH according to search space sets with group index 1,…*’so the PDCCH monitoring behavior is only impacted for the SSS with an group index. My interpretation of the Beh2 and Beh3 is as follows,   * Beh 1: PDCCH skipping means stopping monitoring in all USS and TYPE3 CSS search-space sets for a period of time. * Beh 2: SSSG#0 is active means stop monitoring SS sets associated with SSSG#1 and SSSG#2 (if specified and configured) and monitoring  of search-space-sets  associated to SSSG#0 (legacy behaviour) * Beh 3: SSSG#1 is active means stop monitoring SS sets associated with SSSG#0 and SSSG#2 (if specified and configured)  and monitoring  of search-space-sets  associated to SSSG#1 (legacy behaviour)   And I also added Beh 4 if 3 SSSGs is specified in Rel-17   * Beh 4: SSSG#2 is active means stop monitoring SS sets associated with SSSG#0 and SSSG#1  and monitoring  of search-space-sets  associated to SSSG#2  (if specified and configured)   And perhaps we also need an Behaviour that PDCCH skipping is not activated as well.   * Beh 0: PDCCH skipping (if specified and configured) is not activated   Reponse to OPPO’s comments,  The descriptions of ‘empty’ SSSG and ‘dormant’ SSSG I copy and paste are as follows,   * + Alt 1-1: by an ‘empty’ SSSG which no SS set(s) is configured for the ‘empty’ SSSG, UE does not monitoring PDCCH on the ‘empty’  SSSG,   + Alt1-2: by a ‘dormant SSSG’ which may have associated SS sets, and monitored conditionally (e.g., depending on HARQ NACK or RTT/ReTx timers)   With a fallback timer configured for the ‘empty’ SSSG and ‘dormant’ SSSG， and after timer expirations, the UE can switch to default SSSG. And such operation is equavalant to the functionality of PDCCH skipping for a duration.  Response to Qualcomm’s comments  Regarding to Qualcomm’s observation on **Heterogeneous codepoint mapping** and **Homogeneous codepoint mapping** (also explained in x7358), I agree with the observation that for the DCI missing case, Homogeneous codepoint mapping may have some benefits over Heterogeneous codepoint mapping in terms of recovery latency. However, the DCI missing rate is 1% for usual setting, and it is expected that HARQ-ACK feedback latency would not be very long in NR. We think eventually the difference between each are very minor and both them can be accepatable. |
| Samsung | 1a: we think it can be discussed after down-selection of Alt1 and Alt2. We will have a better idea about how many and what PDCCH monitoring states we needed; otherwise we may need revisit this issue.  1d-1:   * We want to clarify [3] SSSGs. Does it include the “empty” or “dormant” SSSG? We think it doesn’t. * A timer associated with “empty” or “dormant” SSSG is also needed.   1d-2:   * For Alt2-1: since we already agreed Y<=2. We suggest to clarify up to 2 candidate values of skipping duration configured by RRC signaling. * Alt2-3 duplicates Alt1 in 1d-1. No need to repeat here. * For the last FFS: intraction with SSSG switching seems to be a common issue for Alt1 and Alt2. It can be discussed sepereatly. |
| Spreadtrum | Proposal 1-a): We think it is too comfusing. The 4 behaviors in the proposal seems misleading that the at most 2 bits will trigger the listed 4 behaviors as 4 states. In our view, PDCCH skipping and SSSG switching are separate techniques, and we don’t know the benefit of joint indication. If the proposal is not related to the 4 states for the at most 2 bits, we don’t know why it should be explicitly agreed, since PDCCH skipping proponents and SSSG switching proponents all know the behaviors of two techniques respectively.  Propsal 1-d2) In our view, Alt 2 listed in RAN1#105e is to solve the signaling design of PDCCH skipping, and the signaling design of SSSG switching can be separately discussed. However, the current wording of proposal 1-d2) means the joint indication of PDCCH skipping and SSSG switching. We share the similar view that the joint indication needs the further justification, since there is no evaluation/discussion on it. We suggest removing the wording related to SSSG switching in at least Alt 2-1:   * If alt 2 is supported,   + PDCCH schedules data and also indicates PDCCH monitoring adaptation by PDCCH skipping for a duration is supported.     - Y bits is configured for scheduling DCIs (i.e., DCI format 1-1/0-1/1-2/0-2) indicating PDCCH monitoring adaptation ~~(including  SSSG index, and/or PDCCH skipping duration(s))~~       * Alt 2-1:         + FFS: Determination of the duration for PDCCH skipping, e.g.,   One skipping duration configured by RRC signaling,  Multiple candidate values of skipping duration configured by RRC signaling and use DCI to dynamically indicate one of the configured skipping duration  by specification   * + - * + FFS: possible value(s) of the duration         + FFS: joint ~~or separate~~ indication with SSSG switching       * Alt 2-3:         + SSS/SSSG specific skipping indication via e.g. bitmap, codepoint, joint indication with a minimum scheduling offset value   FFS: interaction with SSSG switching (if configured), e.g. impact to skipping when SSSG timer expires, which SSSG after PDCCH skipping is monitored, etc.  For the “**Heterogeneous codepoint mapping**” for Alt 2, we think it is misunderstanding. Only when the joint indication is supported (both for Alt-1 and 2), there is “**Heterogeneous codepoint mapping**”, since the dormant/empty SSSG to emulate PDCCH skipping and the explicit indication of PDCCH skipping are equivalent, i.e. both mean the different UE behavior from SSSG switching. I don’t think the dormant/empty SSSG to emulate PDCCH skipping actually belongs the SSSG switching technique. Whether it is “**Heterogeneous codepoint mapping**” depends on whether we support the joint indication instead of Alt1 or Alt 2. |
| Huawei, HiSilicon | **For proposal 1a(new)：**  It is not agreed to have three SSSG(s) and even be controversial among companies supporting Alt.1. We don’t think we need define so many behaviors for different flavors. We are also not sure how the new proposal 1a helps the progress. Maybe we should focus on the fundamental question what is additional benefit to use empty/dormancy SSSG compared with directly specifying PDCCH skipping. In our understanding, we have already agreed supporting PDCCH skipping functionality. The claimed less specification work is invalid, e.g. dormant SSSG may need define inherent relationship with another SSSG, SSSG number may be increased causing more complicated state transitions, etc. If companies cannot show additional benefit to do the emulating, we should move forward and directly specify it.  **For proposal 1d-1:**  We agree with Oppo’s comments that it is still FFS on whether empty or dormant SSSG can achieve the same effect as PDCCH skipping. Also, as moderator listed as questions in section 2.3.3 for further discussion, we should add questions in question 3d as FFS points also.  **[High] proposal 1d-1:**   * If alt 1 is supported,   + supporting SSSG  switching to emulate PDCCH skipping functionality by an ‘empty’ SSSG (i.e. Alt 1-1)or ‘dormant’ SSSG(i.e. Alt 1-2)     - Y bits is configured for scheduling DCIs (i.e., DCI format 1-1/0-1/1-2/0-2) indicating SSSG index.       * FFS dynamic indication of initial timer value(s)       * FFS details     - At most [3] SSSGs is supported to be configured.     - ~~FFS support of single timer to switch to default SSSG#0  or support of multiple timers between SSSGs~~     - FFS whether the following timer(s) is supported for switching between       * Option 1: Non-default SSSG to default SSSG (i.e., SSSG#0)       * Option 2: Non-default SSSG to another non-default SSSG       * Option 3: Default SSSG (i.e., SSSG#0) to non-default SSSG(s)     - FFS: down selection between ‘empty’ SSSG (i.e. Alt 1-1)or ‘dormant’ SSSG(i.e. Alt 1-2)     - Note: description of ‘empty’ SSSG and ‘dormant’ SSSG has been provided in RAN1#105-E     - FFS: whether ‘empty’ SSSG and ‘dormant’ SSSG, can be looked as a skipping duration and whether to introduce a SSSG state.     - FFS: whether the timer is configured per SSSG, per BWP, or other approaches.     - FFS: whether multiple timer duration(s) can be configured by RRC, and DCI dynamically indicates a timer duration     - FFS: do we need to define default SSSGs and for what purpose?   **For proposal 1d-2:**  We think Alt 2-1 and Alt 2-3 are two different aspects and actually are not two alternatives.   * If alt 2 is supported,   + PDCCH schedules data and also indicates PDCCH monitoring adaptation by PDCCH skipping for a duration is supported.     - Y bits is configured for scheduling DCIs (i.e., DCI format 1-1/0-1/1-2/0-2) indicating PDCCH monitoring adaptation (including  SSSG index, and/or PDCCH skipping duration(s))       * ~~Alt 2-1:~~          + FFS: Determination of the duration for PDCCH skipping, e.g.,   One skipping duration configured by RRC signaling,  Multiple candidate values of skipping duration configured by RRC signaling and use DCI to dynamically indicate one of the configured skipping duration  by specification   * + - * + FFS: possible value(s) of the duration         + FFS: joint or separate indication with SSSG switching       * ~~Alt 2-3:~~         + FFS: whether introduce SSS/SSSG specific skipping indication via e.g. bitmap, codepoint, joint indication with a minimum scheduling offset value         + FFS: interaction with SSSG switching (if configured), e.g. impact to skipping when SSSG timer expires, which SSSG after PDCCH skipping is monitored, etc. |

## Issue 2: non-scheduling DCI based SSSG switching and/or PDCCH skipping

### Initial proposals

Support of proposal 2a: Huawei/HiSilicon, Spreadtrum, Qualcomm, CMCC, ETRI, Intel, Apple, Nokia

Objec of proposal 2a: [TBC]

* It is important to know how to use the DCI field(s) to indicate. Companies’ proposals are appreciated. Details can be discussed and solved after Alt 1-1/1-2/Alt 2 in proposal 1d being down-selected.

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| **[Medium] proposal 2a:**   * PDCCH does not schedules data and indicates SSSG switching or PDCCH skipping for an active BWP in active time is supported by   + DCI Format 1\_1 (SCell dormancy case 2 like) |

For DCI format 2\_6, inside active time and outside active time are differentialed.

Support of proposal 2b:

* Outside active time: Lenovo/MotM, Qualcomm, LGE, Apple
* Inside active time: Huawei/HiSilicon, LGE, ETRI, Intel

Object of proposal 2b: [TBC]

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| **[Medium] proposal 2b:**   * DCI format 2\_6 outside active time is supported to indicate SSSG switching or PDCCH skipping for an active BWP in active time when DRX is configured. (Supported by Lenovo/MotM, Qualcomm, LGE, Apple) * DCI format 2\_6 being received in active time is supported to indicate SSSG switching or PDCCH skipping for an active BWP in active time when DRX is configured. (Supported by Huawei/HiSilicon, LGE, ETRI, Intel) |

### Companies views (1st round)

Companies are encouraged to provide comments in the table below.

|  |  |
| --- | --- |
| **Company** | **Comment** |
| Nordic | 2\_6 outside time OK to consider, for inside time we use currently non-fall-back formats for dormancy. We do not support 2\_6 inside active time. |
| Apple | Proposal 2a: support the proposal.  Proposal 2b: clarification on Apple’s proposal. We support DCI format 2-6 being received in active time. For DCI format 2-6 outside active time, does the proposal here to use it indicate the SSSG in the following active time? It is not clear how it is used for PDCCH skipping. Does this refer to the R16 WUS function of skipping until next DRX cycle? Or this indicates skipping the first few slots of ON duration? |
| Samsung | 2a: the cost of using scheduling DCI format without scheduling PDSCH/PUSCH is too high. So, we cannot agree to support it.  2b: We support to consider GC PDCCH based triggering to reduce signaling overhed. For outside DRX active time, it can be extension of DCI format 2\_6, but the adaptation should provided only when UE is indicated to wake up for DRX On duration. For within active time, we can FFS whether and how to reuse DCI format 2\_6, as currently it’s not supported within active time. |
| CATT | We don’t support using DCI format 2\_6 for SSSG in proposal 2b. |
| Qualcomm | Proposal 2a: We support the proposal.  Proposal 2b: We support only the first bullet, i.e., DCI format 2\_6 outside active time. DCI format 2\_6 within active time may have larger standard impact, including DCI size alignment. Thus, at least in Rel-17, we think DCI format 2\_6 outside active time would be sufficient to discuss. |
| Intel | Support Proposal 2a  Regarding Proposal 2b, we support DCI format 2\_6 received during active time. The reason being since PDCCH monitoring adaptation would take place during active time, it makes sense to have the option to provide the trigger during active time so that adaptation could start right way, without depending on a scheduling DCI. Also, reception of format 2\_6 during active time does not conflict with Rel16 design since monitoring occasions are mutually exclusive. |
| Huawei, Hisilicon | **proposal 2a:**  support.  **proposal 2b:**  we support the second bullet to use DCI format 2\_6 inside active time as the non-scheduling DCI for the indication. |
| LG | We support the proposal 2b. DCI format 2\_6 outside DRX Active Time can be used to indicate which group UE monitors when drx-onDruationTimer starts. Regarding PDCCH skipping behavior, we are open to discuss how to use DCI format 2\_6 outside DRX Active Time. DCI format 2\_6 cannot be monitored inside DRX Active Time by the current spec, so defining monitoring window can help UE to monitor DCI format 2\_6 inside DRX Active Time. |
| ZTE, Sanechips | The power saving gain from other non-scheduling DCI is not clear. What’s more, carrying PDCCH adaptation via DCI format 2-6 outside ative time in addition to wake-up and SCell dormancy seems unneccesary, and carrying PDCCH adaptation via DCI format 2-6 within ative time would complicate the implementation at UE and gNB. |
| MTK | Deprioritize non-scheduling DCI format until the detail of scheduling DCI has been determined. |
| Nokia | On proposal 2a, we would support DCI format 1\_1 (Scell dormancy case 2) approach for non-schduling DCI based PDCCH monitoring adaptation.  On proposal 2b, for outside active time operation, having separate DCI (2\_6 with PS-RNTI) indication to adapt PDCCH monitoring for the active time does not seem necessary from power saving perspective. If UE is triggered to monitor the OnDuration, it would imply that there will be scheduling, thus defining default monitoring behaviour (configurable or via specification, e.g. see Proposal 3c) would seen sufficient. On inside active time operation, having additional non-schduling DCIs (in addition to DCI 1\_1 case 2) to trigger adaptation does not seem necessary. Hence we do not support proposal 2b. |
| CMCC | 2a: Support  2b: Not support, we think the PDCCH adaptation behaviouies are dynamically various among Ues, it is not suable to introduce a group-common DCI to indicate UE-specific PDCCH adaptation behaviour |
| OPPO | We also think the group DCI should be decided later. There is no clear benefit to support so many format yet. |
| ETRI | We support Proposal 2a.  We support first bullet of Proposal 2b, i.e., outside active time. Our position was corrected on the above proposal (there was an error in our tdoc. Sorry for confusion.) |
| Panasonic | On 2a, okay with us.  On 2b, considering the specification impact, we suggest to deprioritize at this moment. |
| Spreadtrum | 2a) support  2b) FFS |
| Ericsson | Proposal 2a : OK with the proposal  Proposal 2b : Do not support. DCI format 2\_6 is used to wake-up the UE to receive data during On duration, which means UE should be ready to receive data (using default or pre-defined configuration)– the motivation to indicate a skipping command, SSSGS in WUS for this is unclear. Since DCI 2\_6 is monitored outside active, we do not see the motivation to add it as another new DCI format that a UE monitors during active time. |
| Lenovo, Motorola Mobility | Support the proposal 2a and the first bullet of proposal 2b. During the active time, can use a scheduling DCI format without scheduling information for SSSG switching, instead of DCI format 2\_6, which can reduce UE’s PDCCH blind decoding. |

### Updated proposals (2nd round)

Support of proposal 2a: Huawei/HiSilicon, Spreadtrum, Qualcomm, CMCC, ETRI, Intel, Apple, Nokia, Panasonic, Spreadtrum, Ericsson, Lenovo/ Motorola Mobility

Object of proposal 2a: Samsung,

* It is important to know how to use the DCI field(s) to indicate. Companies’ proposals are appreciated. Details can be discussed and solved after Alt 1-1/1-2/Alt 2 in proposal 1d being down-selected.
* ***Recommendation:*** suggest to agree on proposal 2a and recommend companies to further provide details on DCI field design.

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| **[Medium] proposal 2a:**   * PDCCH does not schedules data and indicates SSSG switching or PDCCH skipping for an active BWP in active time is supported by   + DCI Format 1\_1 (SCell dormancy case 2 like) |

For DCI format 2\_6, inside active time and outside active time are differentialed.

For proposal 2b:

* Outside active time:
  + Support: Lenovo/MotM, Qualcomm, LGE, Samsung, Qualcomm, ETRI, Lenovo/ Motorola Mobility
  + Object: CATT, ZTE/Sanechips, Nokia, CMCC, Ericsson
* Inside active time:
  + Support: Huawei/HiSilicon, LGE, ETRI, Intel, Apple
  + Object: Nordic, CATT, Qualcomm, ZTE/Sanechips, Nokia, CMCC, Ericsson

And some companies think we should deprioritize proposal 2b at this moment.

* ***Recoomendation***: FL recommend objecting /supporting companies continue reply the questions/comments raised from supporting/objecting companies.

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| **[Medium] proposal 2b:**   * DCI format 2\_6 outside active time is supported to indicate SSSG switching or PDCCH skipping for an active BWP in active time when DRX is configured. * DCI format 2\_6 being received in active time is supported to indicate SSSG switching or PDCCH skipping for an active BWP in active time when DRX is configured. |

### Companies views (2nd round)

Companies are encouraged to provide comments in the table below.

|  |  |
| --- | --- |
| **Company** | **Comment** |
| Nordic | We did not take stand on 2a in previous round  2a : we believe it would be difficult to find spare bits in dormancy DCI not scheduling PDSCH and painfull to discuss validation differentiation, it would be much more clear to support DCI 2\_0 inside active time, and take SSSG switching desing as starting point. |
| OPPO | We think the adaptiton outside of active time may not bring much gain and would need more study.  In side active time, there is not the format 2\_6 used and adding that will bring more overhead. |
| Huawei, HiSilicon | Support 2a. Regarding Nordic’s question, for SCell dormancy case 2, there are still unused bits even if there are 15 configured SCells need to be indicated. It is easy to use.  Also, DCI format 2\_0 is not mandatory, and even not related to power saving. So we think DCI format 2\_6, which is introduced aiming for power saving, is a much better candidate.  For 2b, we support the second bullet. But for the first bullet, i.e. outside Actiev Time, we don’t see strong motivation to indicate it outside Active Time. |

## Issue 3: implicit PDCCH monitoring adaptation

### Initial proposals

**Timer**

Most companies think for PDCCH monitoring adaptation for an active BWP in active time, timer-based SSSG switching is supported. It is beneficial so that during the inactivity the UE can based on timer expiry move to less frequent PDCCH monitoring without network to sending another PDCCH command when UE is not scheduled.

A list of related questions for timer is as follows,

* Q1: whether the timer is configured per SSSG, per BWP, or other approaches.
* Q2: whether multiple timer duration(s) can be configured by RRC, and DCI dynamically indicates a timer duration
* Q3: do we need to define default SSSGs and for what purpose?

Companies are encouraged to provides view on Q1, Q2 and Q3.

**SR/RACH**

At least for SSSG switching based PDCCH monitoring adaptation, switching to default SSSG based on UL scheduling request or RACH can reduce UL traffic latency when switching from power saving state to normal state.

Support of proposal 3a/3b Huawei/HiSilicon, Qualcomm, LGE, Nokia, CMCC, Ericsson (3a only)

Object of proposal 3a/3b: TBC

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| **[Medium] Proposal 3a:**  For implicit indication of PDCCH monitoring adaptation , SSSG switching triggered by SR is supported.  **[Medium] Proposal 3b:**  For implicit indication of PDCCH monitoring adaptation , SSSG switching triggered by RACH is supported. |

Some companies think it is open for discussion and can be handled later for example after decision of framework.

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| [Medium] Proposal 3c  For UE configured with DRX, higher layer signaling can configure SSSG that a UE monitors when coming out of DRX to monitor an ON duration. |

### Companies views (1st round)

Companies are encouraged to provide comments in the table below.

|  |  |
| --- | --- |
| **Company** | **Comment** |
| Nordic | Behaviours 3a, 3b and 3c seem reasonable. We support |
| Apple | Proposal 3a: Do not support this proposal. Does proponent company think default SSSG will be always the SSSG with dense monitoring pattern? Also, many cases UE needs to transmit small amount of data. Automatically switch to dense pattern to transmit small amount of data is not beneficial for UE poer saving. BSR status needs to be considered.  Proposal 3b: Do not support this feature. When RACH is used for SR, similar reasoning as above. When RACH is used for other purposes like UL sync, we donot see the value of SSSG switching.  Proposal 3c: Open to discussion. We see this proposal is related to proposal 2b bullet 1, DCI format 2-6 outside of DRX cycle for SSSG switching. This default SSSG is RRC configured, proposal 2b bullet 1 may not needed. |
| Samsung | Scheduling DCI format can be used in all these cases, and is sufficient to trigger the PDCCH monitoring adaptation. No need to consider 3a/3b/3c. |
| CATT | We don’t support proposals 3a, 3b, and 3c since there is no power saving gain being shown on these proposal. |
| Qualcomm | Q1: We support timer configuration per SSSG, at least for empty/dormant SSSGs of Alt 1. Per-SSSG timer configuration can enable emulation of different PDCCH skip durations.  Q2: In terms of configuration complexity and indication overhead, we see little difference between 1) configuring multiple empty/dormant SSSGs with different timer values, 2) configuring a single empty/dormant SSSG and indicate a timer duration (among multiple RRC configured values) by a DCI.  Q3: Similar to Rel-16 SSSG switching, we think a default SSSG is necessary. As summarized by FL, the default SSSG may be configured with less frequent PDCCH monitoring occasions, and let the UE to sparsely monitor PDCCH when there is no traffic. For empty/dormant SSSGs, we think it should switch to another configured SSSG (‘associated SSSG’ in our contribution), which may or may not be the default SSSG.  Proposal 3a/3b: We think the implicit switching by SR/RACH should be limited to the PDCCH skipping function. For example, if UE transmits SR/RACH while skipping PDCCH monitoring (e.g., in empty/dormant SSSG), it autonomously terminate PDCCH skipping or switch to non-empty/non-dormant SSSG.  Proposal 3c: We are open to discuss this proposal. |
| Intel | We intend to discuss only DCI trigger based PDCCH monitoring adaptation as it seems to be the scope of WID. Do not support 3a, 3b, 3c |
| Huawei，Hisilicon | **Proposal 3a/3b:**  We agree SSSG switching triggered by SR and RACH. Besides SSSG switching, SR and RACH can also triggers the stopping of PDCCH skipping since SR/RACH implies UL traffic arriving. When UE is in the duration of PDCCH skipping, if SR/RACH is transmitted, UE should resume to monitor PDCCH.  Proposal 3c  We don’t see strong reason to support it. |
| LG | We support the proposals 3a and 3b.  We are open to discuss the proposal 3c. |
| ZTE, Sanechips | For proposal 3a and 3b: agreed. The SR and RACH triggered PDCCH monitoring adaptation should be supported.  For Proposal 3c,  Not agree. gNB will not configure it as PDCCH skipping when the UE needs to receive a large amount of data. Therefore, it is not necessary to worry that the UE will skip ON duration when there is data to be transmitted. At the beginning of DRX ON duration, if the UE needs to be switch to an SSSG according to the configuration of higher layer signaling, unnecessary switching may occur, which will result in power consumption and delay.  In addition, a timer based triggering mechanism for SSSG switching can help UE to adapt PDCCH monitoring without indication of scheduling DCI, which should be considered. |
| DOCOMO | We support the proposals 3a and 3b only during the PDCCH skipping/empty SSSG. |
| Nokia | Q1: Option to configure timer for non-default SSSG’s should be supported (i.e. for SSSG#1 and SSSG#2, but not SSSG#0).  Q2: We don’t see need to have dynamic DCI based timer selection.  Q3: In context of timer based SSSG switching there could be a ‘default’ SSSG (to which all roads lead, directly or via other SSSG). This could also be benefitted in other context (e.g. C-DRX).  Proposal 3a: We are OK with this proposal.  Proposal 3b: We are OK with this proposal, with a note that in our understanding this covers CBRA as well as CFRA case (i.e. also BFR).  We think that these proposals are needed to ensure quality of service and fast recovery from any issues.  Proposal 3c: We are OK with this proposal. The final need could depend on the timer based adaptation. |
| CMCC | 3a/3b: Ok  3c: open to discuss, we also think UE can deflaut use the SSSG in previous DRX ON . |
| OPPO | We can further discuss 3c.  Not support 3a/3b. |
| ETRI | We do not support Proposal 3a and 3b.  We think that Proposal 3c helps and are okay to discuss it. |
| Panasonic | On 3a/3b, we think the system does work without these function and we see them as optimization.  On 3c, we think DRX shall work well without such configuration and thus it can be deprioritized |
| Ericsson | Proposals 3a: Support.  Proposals 3b: Support.  Proposals 3c: Support. As shown in R1-2108002, using dense monitoring during DRX on-duration provides better UPT vs UE power saving tradeoff. |
| Lenovo, Motorola Mobility | Fine with the proposals 3a and 3b.  For proposal 3c, we don’t think that a separate configuration for SSSG out of DRX is necessary. UE can assume SSSG0 when starting a DRX On duration timer, unless detecting a DCI format 2\_6 indicating SSSG1. |

### Updated proposals (2nd round)

**SR/RACH**

Support of proposal 3a/3b Huawei/HiSilicon, Qualcomm(only during PDCCH skipping/empty SSSG), DOCOMO(only during PDCCH skipping/empty SSSG), LGE, Nokia, CMCC, Ericsson , Nordic, ZTE/Sanechips, Lenovo/Motorola Mobility

Object of proposal 3a/3b: Apple(BSR status needs to be considered), Samsung, CATT, Intel, OPPO, ETRI, Panasonic

* ***Recommendations***: FL recommend objecting /supporting companies continue reply the questions/comments raised from supporting/objecting companies. Considering the following aspects:
  + BSR status needs to be considered
  + no power saving gain being shown on these proposal.
  + the system does work without these function and we see them as optimization.
  + implicit switching by SR/RACH should be limited during PDCCH skipping/empty SSSG

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| **[Medium] Proposal 3a:**  For implicit indication of PDCCH monitoring adaptation , SSSG switching triggered by SR is supported.  **[Medium] Proposal 3b:**  For implicit indication of PDCCH monitoring adaptation , SSSG switching triggered by RACH is supported. |

Some companies think it is open for discussion and can be handled later for example after decision of framework.

Support: Nordic, Nokia, Ericsson

Not support: Apple, Samsung, CATT, Intel, ZTE/Sanechips, Lenovo/Motorola Mobility

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| --- |
| [Medium] Proposal 3c  For UE configured with DRX, higher layer signaling can configure SSSG that a UE monitors when coming out of DRX to monitor an ON duration. |

**Timer**

* Q1: whether the timer is configured per SSSG, per BWP, or other approaches.
  + Per SSSG: Qualcomm, Nokia(only for non-default SSSG)
* Q2: whether multiple timer duration(s) can be configured by RRC, and DCI dynamically indicates a timer duration
  + No: Nokia
* Q3: do we need to define default SSSGs and for what purpose?
  + Yes: Qualcomm, Nokia

***Recommendations***: FL recommend companies to continue comments on the following questions

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| [Medium] Questions 3d   * Q1: whether the timer is configured per SSSG, per BWP, or other approaches. * Q2: whether multiple timer duration(s) can be configured by RRC, and DCI dynamically indicates a timer duration * Q3: do we need to define default SSSGs and for what purpose? |

### Companies views (2nd round)

Companies are encouraged to provide comments in the table below.

|  |  |
| --- | --- |
| **Company** | **Comment** |
| Nordic | We believe 3a/3b are not prposed for power saving 😊, but for latency recovery due to power saving.  On Timer:  Q1: Per SSSG and per non-default SSSG, timer switches to default SSSG  Q2: Yes, for empty SSSG two initial values could be configured  Q3: Yes |
| OPPO | 3a/3b need justification, we should not have them now.  Q1. One of the SSSG as default can be configured with timer to fallback to the default SSSG  Q2. No  Q3. Can be considered for one of the SSSG |
| Huawei，Hisilicon | **Proposal 3a/3b:**  We agree SSSG switching triggered by SR and RACH. Besides SSSG switching, SR and RACH can also triggers the stopping of PDCCH skipping since SR/RACH implies UL traffic arriving. When UE is in the duration of PDCCH skipping, if SR/RACH is transmitted, UE should resume to monitor PDCCH.  Could moderator consider to update the proposal to reflect them?  For Proposal 3c: you could add Huawei and HiSilicon into the list who do not support this prospoal. |

## Issue 4: interaction with HARQ/retransmission

In RAN1#104-E, it is agreed that,

Agreements:

* Further study whether and how to minimize the impact to data scheduling for new transmissions and retransmissions.
  + FFS details
* Further study the application delay for PDCCH adaptation indication

Some companies pointed out that the SSSG switching/skipping should consider PDCCH monitoring behavior by considering interaction with data decoding and/or HARQ retransmission, in order to reduce service latency for retransmission. This can be useful for the following cases,

* PDCCH monitoring adaptation Alt 2: special handling of HARQ-retransmission when PDCCH skipping, e.g., UE suspends or stops PDCCH skipping . [OPPO][MediaTek][Ericsson][Apple][ZTE],
* PDCCH monitoring adaptation Alt 1-2: ‘dormant’ SSSG which may have associated SS sets, and monitored conditionally depending on HARQ NACK or RTT/ReTx timers. [Nordic, Qualcomm, CMCC, Fraunhofer]
* PDCCH monitoring adaptation Alt 1-1: switch from ‘empty’ SSSG to normal normal SSSG when retransmission occurs [vivo,Nokia]

Some specific examples are as follows,

MediaTek thinks UE should resume a short term PDCCH monitoring during the indicated PS duration, depending on the HARQ processing outcome. If HARQ processing outcome is valid (situation 1, about 90% of DL data scheduling), the UE does not have to wake up for the retransmission. If not (situation 2, about 10% of DL and UL data scheduling), the UE monitors the possible retransmission scheduling in a configured duration. As illustrated in Figure 5.



Figure 5. Illustration of UE power saving adaptation for retransmission handling

For PDCCH skipping, OPPO proposed a retransmission period can be introduced for the retransmission to allow a retransmission window. ***In the delay window for retransmission, PDCCH monitoring can be only after PDCCH-PDSCH-HARQ-ACK timing and in few consecutive monitoring occasions.***

Apple thinks that when PDCCH monitoring adaptation is triggered by DCI format 1-1 and 1-2, the switching or skipping command can be applied after ACK transmission. Also, when NACK is received by the gNB, the previous triggering commanded is cancelled, and the gNB needs to send another triggering commend with retransmissions scheduling DCI.

ZTE thinks that to serve the purpose of reducing UE power consumption, the UE does not need to monitor PDCCH scheduling an initial-transmission data during the PDCCH skipping period. When DRX is configured, the duration for monitoring PDCCH scheduling retransmission data (no initial transmission) can be determined by the *drx-RetransmissionTimerDL* and *drx-RetransmissionTimerDL*.

Vivo proposed the following alternatives,

The following additional mechanisms is supported for PDCCH switching/skipping when interaction with HARQ,

* UE switches to SSSG0 (from SSSG1),
  + Alt 1-1: UE Tx NACK,
  + Alt 1-2: *k* slot after UE Tx NACK
  + Alt 2: after drx-RetransmissionTimer starts

And after UE successfully complete retransmission,

* UE Switching SSSG1,
  + Alt 1: UE Tx an ACK which corresponds to the PDCCH indicates SSSSG switching from 0 to 1
  + Alt 2: after drx-RetransmissionTimer expired

### Initial proposals

The following moderator recommendations are made.

|  |
| --- |
| **[High] proposal 4a:**   * + After being indicated to skipping PDCCH monitoring and/or switched to ‘dormant’/’empty’ SSSG, the UE still performs PDCCH monitoring for HARQ retransmission at least during a ‘retransmission period’.     - FFS How to perform PDCCH monitoring during the retransmission period, e.g.,       * UE switch to another SSSG, e.g., default SSSG or a SSSG specially configured only for retransmission period .       * UE suspends or stops PDCCH skipping.       * UE performs discontinuously PDCCH monitoring according to the roundtrip and retransmission timers to receive any HARQ retransmissions       * Others not precluded     - FFS ‘retransmission period’       * Alt 1: When triggered by DL DCI, the start and end of ‘retransmission period’ is defined as HARQ-ACK condition is satisfied         + FFS HARQ-ACK condition, e.g., the start of ‘retransmission period’ is when the UE transmit NACK       * Alt 2: the start and end of ‘retransmission period’ is defined as the *start of drx-RetransmissionTimerDL(UL)* and expiration of *drx-RetransmissionTimerDL(UL)* respectively if DRX is configured.       * others not precluded |

### Companies views (1st round)

Companies are encouraged to provide comments in the table below.

|  |  |
| --- | --- |
| **Company** | **Comment** |
| Nordic | In general fine with main sub-bullet. The first sub-bullet is not relevant to dormant SSSG, since UE monitors according to configured SS-sets in that SSSG. For Re-Tx period we prefer Alt 1. |
| Apple | Need further clarification on the proposal, particularly on the interaction with HARQ timers.   1. Does the retransmission period counted into the skipping slots? For example, assume 15KHz SCS, skipping is 8 slots, retransmission timer is 8ms, does this proposal means skipping starts after 8ms retransmission timer expires? Or retransmission period is part of skipping, therefore effectively there is no skipping after retransmission period ends? 2. Does the retransmission period starts after *drx-HARQ-RTT-TimerUL/DL? Or after ACK/NACK transmission? Or after application delay from PDCCH?* |
| Samsung | An application associated with retransmission time can be considered for this case, such as Option d 5a. |
| CATT | We believe that PDCCH skipping does not need to associate with HARQ processes we described in Issues 1 as follows,   * PDCCH skipping is UE-specific configuration and the PDCCH monitoring can be dynamically adapted continuously to the traffic arrival process. For SSSG, it is one instance of PDCCH monitoring adaptation within one valid interval. * There is no application delay for PDCCH skipping. There is application delay for SSSG. * PDCCH skipping does not associate with HARQ process and its A/N feedback. The PDCCH skipping could indicate the change of PDCCH monitoring intervals in the initial transmission or retransmission of any HARQ process and take effect after receiving the DCI. SSSG switching would need to confirmation from A/N of one HARQ processes. If more than one HARQ processes running concurrently, additional mechanism need to be addressed to identify the switching time of SSSG, which is additional delay and UE power consumption. * PDCCH skipping use the same procedure of Abnormal handling of DCI miss-detectoin when DCI carrying skipping information is missed detected since UE. SSSG needs to define the procedure of Search space used for DCI miss-detection. |
| Qualcomm | For dormant SSSG, i.e., Alt 1-2, HARQ handling is already integrated as a part of the design. As a starting point of the discussion, we are fine with the proposal. |
| Intel | It depends on what solution we support. As CATT mentioned, we do not see strong need to couple HARQ outcome and start of PDCCH monitoring adaptation at this point. It can be handled by gNB implementation. For example, skipping duration can be short or if skipping is about to start then gNB could schedule the last transmission in a conservative manner. |
| Huawei, Hisilicon | There is benefit to perform PDCCH monitoring for HARQ retransmission after being indicated to skipping PDCCH monitoring and/or switched to ’empty’ SSSG.  For potential UL retransmission, there is no HARQ feedback for uplink, UE will always start the UL retransmission timer after UL HARQ RTT timer. In the worst case, even the PUSCH is successfully received by gNB, UE still needs to perform PDCCH monitoring which may waste the power consumption.  We don’t think there is a need to introduce some concept of “retransmission period”. The current timer can clearly define the time duration, i.e., drx-RetransmissionTimerDL is running. |
| LG | We are generally okay with the proposal. |
| ZTE, Sanechips | Support.  To reduce the retransmission latency, it is better to allow UE to monitor PDCCH during the PDCCH skipping duration. |
| DOCOMO | We are generally okay with the proposal. |
| MTK | We partially agree the proposal. However, to clarify the ‘retransmission period’, we suggest the following modification  **[High] proposal 4a:**   * + - FFS ‘retransmission period’       * Alt 1: When triggered by DL DCI, the start and end of ‘retransmission period’ is defined as HARQ-ACK condition is satisfied         + FFS HARQ-ACK condition, e.g., the start of ‘retransmission period’ is when the UE transmit NACK       * Alt 2: ‘retransmission period’ consist of roundtrip and retransmission time. The start and end of ‘retransmission period’ is defined as the *drx-HARQ-RTT-TimerDL(UL)* and expiration of *drx-RetransmissionTimerDL(UL)* respectively if DRX is configured.       * others not precluded   And it needs to be FFS what to do if the timer configuration is different from DL and UL. |
| IDCC | Proposal 4-a: We are generally ok with the proposal. |
| Nokia | Proposal 4a: We are fine with the main/first bullet of the proposal. We can further discuss the FFS points. |
| CMCC | We generally support the proposal, but don’t know the relationship among three sub bulltes in the first FFS.  We think the dormant SSSG means “UE performs discontinuously PDCCH monitoring according to the roundtrip and retransmission timers to receive any HARQ retransmissions” , that is if UE doesn’t feedback NACK, UE suspends or stops PDCCH skipping, if UE feedback NACK, UE can monior PDCCH to receive HARQ retransmission and the SS sets are configured in dormant SSSG but not another SSSG. Therefore we think the first subbullet is not needed, and whether UE performs the second sub bullet or three sub bullet is conditional, e.g., feedback NACK. |
| OPPO | We see the retransmission control during the SSSG/skipping is needed. However, we see PDCCH skipping is the main solution as we commented in previous questions. |
| ETRI | Our preference is to make the application time to be after the retransmission period, which may simply resolve the issue at the expense of some application delay. |
| Panasonic | Practically, the impact to new transmission or retransmissions can be handled by gNB implementation, especially if non-scheduling DCI is supported in addition to scheduling DCI. To design detailed mechanism looks DRX duplication to us. Thus we can be open to discuss a unified application delay to address the possible issue if really needed. |
| Fraunhofer | We support the proposal. |
| Ericsson | Not OK. The proposal seems to replicate/interact with the DRX functionality and DRX timers, which should be avoided. The main issue to address is when and how a PDCCH monitoring adaption indication command is applied and if it has any dependency on the HARQ feedback/result for the PDSCH/PUSCH scheduled with the DCI. The proposal 5a (below) seems to already include some options that are considering this issue, so our preference is to focus further on that approach. |
| Lenovo, Motorola Mobility | In principle, we are fine with the proposal. Regarding the definition of “retransmission period”, we support Alt2, which respects gNB’s scheduling decision. |

### Updated proposals (2nd round)

Nordic, Samsung, Qualcomm, Huawei/Hisilicon, LGE, ZTE/Sanechips, DOCOMO, MTK, IDCC, Nokia, CMCC, Fraunhofer, Lenovo/Motorola Mobility thinks the proposal is needed.

While Qualcomm and CMCC thinks for dormant SSSG, i.e., Alt 1-2, HARQ handling is already integrated as a part of the design. Huawei thinks “retransmission period” is not needed to be introduced. The current timer can clearly define the time duration, i.e., drx-RetransmissionTimerDL is running.

Ericsson thinks the proposal seems to replicate/interact with the DRX functionality and DRX timers, which should be avoided.

* ***Recommendations*** : continue working on the specification impact. Companies are encouraged to further revise the proposal.

|  |
| --- |
| **[High] proposal 4a:**   * + After being indicated to skipping PDCCH monitoring and/or switched to ‘dormant’/’empty’ SSSG, the UE still performs PDCCH monitoring for HARQ retransmission at least during a ‘retransmission period’.     - FFS How to perform PDCCH monitoring during the retransmission period, e.g.,       * UE switch to another SSSG, e.g., default SSSG or a SSSG specially configured only for retransmission period .       * UE suspends or stops PDCCH skipping.       * UE performs discontinuously PDCCH monitoring according to the roundtrip and retransmission timers to receive any HARQ retransmissions       * Others not precluded     - FFS ‘retransmission period’       * Alt 1: When triggered by DL DCI, the start and end of ‘retransmission period’ is defined as HARQ-ACK condition is satisfied         + FFS HARQ-ACK condition, e.g., the start of ‘retransmission period’ is when the UE transmit NACK       * Alt 2: the start and end of ‘retransmission period’ is defined as         + Alt 2a: the start of *drx-RetransmissionTimerDL(UL)* and expiration of *drx-RetransmissionTimerDL(UL)* respectively if DRX is configured.         + Alt 2b: the start of *drx-HARQ-RTT-TimerDL(UL)* and expiration of *drx-RetransmissionTimerDL(UL)* respectively if DRX is configured.       * others not precluded       * FFS details |

### Companies views (2nd round)

Companies are encouraged to provide comments in the table below.

|  |  |
| --- | --- |
| **Company** | **Comment** |
| OPPO | We agree to have the combined re-transmssion solution for skipping PDCCH monitoring and/or switched to ‘dormant’/’empty’ SSSG. |
|  |  |
|  |  |

## Issue 5: application delay

### Initial proposals

Before the UE starts to skip PDCCH/ switch SSSG, UE needs time to decode DCI carried the signaling. There were several minimum application delay studied in Rel-16,

* For Rel-16 cross-slot scheduling, the time needed for PDCCH processing was studied when specify the application delay for K0min/K2min indication
* For Rel-16 NRU, a UE can be provided by *searchSpaceSwitchingDelay-r16* a number of symbols where a minimum value of is provided in Table 10.4-1 in TS38.213 for UE processing capability 1 and UE processing capability 2 and SCS configuration . ZTE pointed out that the minimum value of application delay for PDCCH adaptation for μ=3 can be 25 symbols.

Also, several companies express their view that skipping command applies after PUSCH transmission if triggered by UL DCI or skipping commend applies after ACK/NACK transmission.

It is recommended that the application time can be futther finalized after decision of issue 1 and also together discussed with issue 4.

|  |
| --- |
| **[High] proposal 5a:**   * Further consider the following application delay for PDCCH adaptation,   + Option a: the application timelines provided in Table 10.4-1 in TS38.213 for search-space group switching for unlicensed band form is reused.     - FFS: for SCS configuration , FFS X = 25 or 39     - FFS:   + Option b: the application delay needed for PDCCH processing for Rel-16 minimum application delay for K0min/K2min indication is reused/extended.   + Option c: PDCCH skipping command applies after PUSCH transmission if triggered by UL DCI   + Option d: PDCCH skipping command applies after ACK/NACK transmission.   + Option e: after successfully decoding TB.   + Option f: Application delay should be “ZERO” for PDCCH monitoring adaptation. PDCCH monitoring adaptation would be applied after UE receive the additional PDCCH monitoring adaptation control signaling bit(s) in DCI   + Others not precluded. * FFS reference points for the application time * FFS whether the same or different and howapplication delay(s) should be used for SSSG switching and PDCCH skipping functions * FFS whether the same or different and how application delay for PDCCH monitoring adaptation indicated by DCI and timer expiration |

Another issue is that the UE behavior during the application time should be clear between gNB and UE. Hence, it is proposed that the UE should not receive different PDCCH monitoring adaptation indications during the application time, as proposed by some companies[21]. Surely it can be further discussed after a clear definition of the application time.

|  |
| --- |
| **[Medium] proposal 4-2:**  UE should not receive different PDCCH monitoring adaptation indications during the application time |

### Companies views (1st round)

Companies are encouraged to provide comments in the table below.

|  |  |
| --- | --- |
| **Company** | **Comment** |
| Nordic | My general comment is that application delay for Empty SSSG (i.e. all SS-sets are stopped) shall not be different to that of PDCCH skipping, and such application delay may be left up to implementation. For other SSSG (when at least one SS-set is monitored), legacy SSSG switching delay may apply. |
| Apple | We would like to emphasize that for option a, \mu=3, SSSG switching needs to be at least one slot longer comparing to NR-U case, due to 125us slot duration. This is because part of UE processing does not scale with slot duration. |
| Samsung | We are genereally OK with this proposal. We need clarification about the first FFS point, should’t the reference point be the time when UE receives the adaptation indication? Are there any other option? |
| CATT | We are against this proposal since PDCCH skipping does not have application delay. |
| Qualcomm | We are generally fine with the proposal. However, as some companies including Qualcomm mentioned in their contributions, we want to add an FFS:   * FFS whether the same or different and how application delay(s) should be applied when the PDCCH monitoring adaptation is indicated simultaneously with other Rel-16 DCI-based power saving schemes, e.g., minimum scheduling offset restriction and SCell dormancy indication. |
| Intel | We are OK to discuss the options. We think if PDCCH skipping is signaled, adaptation could start right way, in a similar manner as SFI indication. |
| Huawei, HiSilicon | We are open for further discuss the options and any other potential option which is not listed.  For option c and d, our proposal is SSSG switching indication is applied after HARQ-ACK feedback for DCI with DL grant or PUSCH transmitting for DCI with UL grant. We suggest to capture it into proposal 5a, e.g. add option c’ and d’ or update the description of option c and option d. |
| LG | Regarding option d, we doubt PDCCH skipping command is appled after NACK transmission. If UE transmits NACK, UE have to monitor scheduling DCI for retransmission. Therefore, UE should not skip PDCCH monitoring after NACK transmission. We suggest to delete “NACK”.  We beleive SSSG switching behavior can be applied after PUSCH or ACK transmission to avoid SSSGs mismatch caused by DCI miss-detection. For example, a network indicated SSSG switching from index 0 to index 1 and sends a scheduling DCI on SS set in group index 1, however, a UE may not receive a DCI indicating SSSG switching and therefore cannot monitor PDCCH candidates in the SS set in group index 1. If SSSG switching is applied after UE’s UL transmission (PUSCH or ACK) which gives confirmation to the network that the UE successfully received the DCI.  We suggest modification as follows:   * + Option c: PDCCH skipping command and/or SSSG switching applies after PUSCH transmission if triggered by UL DCI   + Option d: PDCCH skipping command and/or SSSG switching applies after ACK~~/NACK~~ transmission. |
| ZTE, Sanechips | Option a in proposal 5a is supported. |
| DOCOMO | We are generally okay with the proposal. |
| MTK | We think option a and option b should be supported together.   * Support option a to extend the application delay in FR2 when using the mechanism of SSSG switching. * Support option b when PDCCH skipping is adopted in Alt 2. |
| IDCC | We think skipping may not have an application delay. |
| Nokia | Proposal 5a: We would be fine to consider futher option a and b. Option c, d and e would seem to relate to the HARQ re-transmisson handling, thus bot approaches are not needed.  Proposal 4-2(5-2?): Should this be bit reworded that ‘UE does not expect to receive… during application delay/time’? |
| CMCC | Fine to support these two proposals. |
| OPPO | This proposal should take care of retransmission issue similar as 4a. |
| ETRI | We are okay to discuss based on Proposal 5a. Our preference is Option d as it seems the simplest approach. |
| Panasonic | On 5a, we see some overlapping with the previous proposal. We suggest to deal with these together and strive for a clear design for application delay. |
| Ericsson | Similar comment as Panasonic.  We would like to add one more Option.   * Option g: Application delay(s) are configured via RRC signaling |
| Lenovo, Motorola Mobility | For proposal 5a, we can only consider option a (for SSSG switching) and option b (for PDCCH skipping). Also, we don’t think the following FFS point is needed:   * FFS whether the same or different and how application delay(s) should be used for SSSG switching and PDCCH skipping functions   For proposal 4-2, the wording can be chaged as follows:  “UE ~~should not~~ doest not exepct to receive different PDCCH monitoring adaptation indications during the application time” |

### Updated proposals (2nd round)

Proposaed changes according to the comments. It is recommended that the application time can be futther finalized after decision of issue 1 and also together discussed with issue 4.

|  |
| --- |
| **[High] proposal 5a:**   * Further consider the following application delay for PDCCH adaptation,   + Option a: the application timelines provided in Table 10.4-1 in TS38.213 for search-space group switching for unlicensed band form is reused.     - FFS: for SCS configuration , FFS X = 25 or 39     - FFS:   + Option b: the application delay needed for PDCCH processing for Rel-16 minimum application delay for K0min/K2min indication is reused/extended.   + Option c: PDCCH skipping command applies after PUSCH transmission if triggered by UL DCI   + Option d: PDCCH skipping command applies after ACK/NACK transmission.   + Option e: after successfully decoding TB.   + Option f: Application delay should be “ZERO” for PDCCH monitoring adaptation. PDCCH monitoring adaptation would be applied after UE receive the additional PDCCH monitoring adaptation control signaling bit(s) in DCI   + Option g: Application delay(s) are configured via RRC signaling   + Others not precluded. * FFS reference points for the application time * FFS whether the same or different and howapplication delay(s) should be used for SSSG switching and PDCCH skipping functions * FFS whether the same or different and how application delay for PDCCH monitoring adaptation indicated by DCI and timer expiration |

|  |
| --- |
| **[Medium] proposal 4-2:**  UE ~~should not~~ doest not exepct to receive different PDCCH monitoring adaptation indications during the application time |

### Companies views (2nd round)

Companies are encouraged to provide comments in the table below.

|  |  |
| --- | --- |
| **Company** | **Comment** |
| Nordic | I would like to add the following to Proposal for completeness:  Option h: For PDCCH skipping/Empty SSSG for a duration application delay is up to UE implementation, UE does not expect to receive DCI in USS and TYPE 3 CSS within the duration after DCI indicating PDCCH skipping/Empty SSSG.  FFS whether different application delay applies for empty SSSG and non-empty SSSG |
| OPPO | We also propose to have application delay jointly considered with other application dalay like the Cross-slot application dalay. Option I. |
| Qualcomm | As we commented in the first round, we’d like to add the following FFS:   * FFS whether the same or different and how application delay(s) should be applied when the PDCCH monitoring adaptation is indicated simultaneously with other Rel-16 DCI-based power saving schemes, e.g., minimum scheduling offset restriction and SCell dormancy indication. |
| Samsung | We need clarification about the first FFS point. I think the reference point should be clear, i.e. the time when UE receives the adaptation indication. Are there any other options? We suggest to provide the alternatives. |
| Huawei, HiSilicon | For option c and d, we suggest modification as follows:   * + Option c: PDCCH skipping command and/or SSSG switching applies after PUSCH transmission if triggered by UL DCI   + Option d: PDCCH skipping command and/or SSSG switching applies after ACK~~/NACK~~ transmission. |

## Issue 6: Others

### Companies views (1st round)

|  |  |
| --- | --- |
| **Company** | **Comment** |
|  |  |
|  |  |
|  |  |

# Void

# Summary of the previous agreements

*RAN1#102-e*

Agreements:

* Reusing power model in TR38.840 for evaluation of DCI-based power saving adaptation schemes.
  + Note: company reporting additional power model for missing state or update is not precluded.

Agreements:

* Company should report assumptions used for periodic measurement activities for the Rel-17 DCI-based power saving adaptation evaluation.
  + The periodic activities defined in TR38.840 can be reused.
  + Measurement for RLM/BFD every C-DRX cycle can be optionally modelled

Agreements:

* The performance metrics described in TR38.840 section 8.2 is reused for power saving evaluation of Rel-17 DCI-based power saving adaptation during ActiveTime.
* The following Rel-15 / 16 features is recommended of the power consumption as reference for baseline. Company can report the feature(s) being used in the baseline.
  + DRX
    - C-DRX cycle 40msec for VoIP
      * 10ms IAT, 8ms On-duration
      * Assume max two packets bundled
    - C-DRX cycle 160msec for FTP
      * Alt 1: 20 msec IAT, 8ms On-duration
      * Alt 2: short DRX
        + 20 ms [or 40ms as optional] IAT, 8ms On-duration
        + 20 ms for short DRX cycle, 4 cycles
      * Note: 100 msec IAT, 8ms On-duration can also be used with sufficient justifications that available Rel-15/16 Techniques being used to reduce UE power saving
  + DCP for DRX adaptation,
    - DCP offset  to DRX ON = 2 ms, other values are not precluded
  + Cross-slot scheduling adaptation
    - Minimum K0 can be adapted from 0 to 1 for FR1, 0 to [4] for FR2
  + BWP switching, including
    - MIMO layer adaptation,
      * Max # of MIMO layer can be adapted from 4 layer to 2 layer for FR1, 2 layer to 1 layer for FR2
    - PDCCH monitoring period adaptation
      * PDCCH monitoring period can be adapted from per slot monitoring to X slot monitoring
        + X = [2] for FR1 and [8] for FR2
    - Bandwidth adaptation
      * Bandwidth can be adapted from 100MHz to 20MHz for FR1,FFS for FR2
    - Note:
      * BWP transition time type 2 is assumed, BWP transition duration is
        + 5 slot @ 30kHz SCS for FR1,
        + 18 slot@120kHz SCS for FR2
        + the slot-average power level for BWP transition duration is according to TR38.840
        + BWP transition time type 1 can be optional modelled
      * BWP switching is Y (ms) after last packet/data burst.
        + Y = [8], other values are not precluded
      * Whether BWP switching is modeled depends on the assumed UE capability and evaluated schemes.
  + Scell dormancy assumption for CA capable UEs
    - FR1 & FR2: SCell dormancy with [160 ms] periodic CSI measurement and reporting
* Other settings
  + CA assumption if configured for CA capable UEs
    - For FR1, FFS
    - For FR2, 4\*100MHz can be considered.
  + Assumptions for scheduler
    - For FR1, no restriction on the beam assumptions being used in each slot
    - For FR2, up to each company, e.g., gNB equally schedule the slots for UEs targeting to different beams.
    - Note: the assumptions does not necessary mean to restrict or precluded any implementation. Other assumptions are not precluded and can be reported by companies.
  + Company to report the used assumption for the interruption and also power savings impact due to presence/absence of interruptions .

Agreements:

Legacy traffic models in TR38.840 can be considered for Rel-17 DCI-based power saving adaptation evaluation, other traffic models can be optionally modelled and company report which traffic model(s) is used.

Draft LS is approved (with generic RAN2 action), with final LS in [R1-2007419](file:///C:/Users/wanshic/OneDrive%20-%20Qualcomm/Documents/Standards/3GPP%20Standards/Meeting%20Documents/TSGR1_102/Docs/R1-2007419.zip).

*RAN1#103-e*

Agreements:

Observation:

* Each of the following schemes is individually shown to be beneficial for UE power saving compared to the baseline.
  + Dynamically switching search space set
  + Dynamically skipping PDCCH monitoring for a certain duration or until next DRX ON
* At least the following Rel-15 and/or Rel-16 power saving solutions have been utilized for baseline,
  + For eMBB traffic,
    - DRX setting(including using short DRX or long DRX with a short IAT or long IAT), Wake-up signal, Cross-slot scheduling, CA/Scell dormancy, MAC-CE skipping, BWP switching
  + For VoIP traffic,
    - DRX setting(only long DRX cycle with a short IAT), Wake-up signal,  Cross-slot scheduling, MAC-CE skipping
  + For IM traffic,
    - DRX setting(long DRX cycle [with a short IAT]), Wake-up signal
  + For intensive eMBB traffic,
    - DRX setting(including using short DRX or long DRX with a short IAT), Wake-up signal, Cross-slot scheduling, [CA/Scell dormancy], MAC-CE skipping, BWP switching
    - Note: intensive eMBB traffic is optional and companies may use FTP model 3 with different packet size and mean data arrival time, e.g., 15ms, 30ms, 50ms or 100ms.
* Note 1: For Search space switching, switching from 1slot monitoring to 2, 4, 8, 10, 16 or 32 slot with 30kHz SCS (FR1) and 120kHz (FR2) is utilized.
* Note 2: For PDCCH skipping , skipping 2ms, 4ms, 5ms, 8ms, 15ms, 16ms, 32ms,  64ms or to next DRX cycle is utilized
* Note 3: the baseline assumed may vary across companies

Agreements:

* **Specify at least one of the following options for Rel-17 dynamic PDCCH adaptation ~~in time-domain~~ for active time,**
  + **Option 1: Search space set group switching,e.g., ~~potential adjustments/enhancements for~~including explicit and implicit search spaceset group switching ~~specified in R16 for NR-U~~**
  + **Option 2: PDCCH skipping for a certain duration / DRX cycle**
* **FFS: which option(s)~~(e.g. taking into account additional gain of option 1 over option 2, or vice-versa)~~**
* **Candidate DCI formats for dynamic PDCCH adaptation include DCI formats 1\_1(including scheduling and non-scheduling DCI), 0\_1, 1\_2, 0\_2, 2\_0, 2\_6.**
* **Note:**
  + **Companies are encouraged to provide analysis on specification impact, power saving benefit and system impact (e.g., packet latency, system overhead)**
* **FFS: other schemes are not precluded for further study**

*RAN1#104-e*

Agreements:

* Strive for a common design for DCI based PDCCH monitoring adaptation in active time for an active BWP to support functionalities inclusive of both SSSG switching and PDCCH skipping for a duration.
  + Details FFS

Agreements:

* Further study whether and how to minimize the impact to data scheduling for new transmissions and retransmissions.
  + FFS details
* Further study the application delay for PDCCH adaptation indication

Agreements:

For DCI based PDCCH skipping in active time for an active BWP (if supported), the following can be further considered,

* Explicit indication of PDCCH adaptation
  + Scheduling DCI
    - Format 1\_1
    - Format 0\_1
    - Format 0\_2/1\_2
  + Non-scheduling DCI
    - Format 2\_6 in active time
    - Format 2\_0
    - Format 1\_1 (SCell dormancy case 2)
  + additional indication mechanism
    - By reusing Rel-16 SCell dormancy indication when CA is configured, FFS details
    - By reusing Rel-16 cross-slot scheduling indication when R16 cross-slot scheduling is configured, FFS detailds
* DCI dynamically indicates a duration/periodic interval for skipping
  + FFS: how to indicate the duration/period interval, e.g., number of slots or skipping current DRX
* PDCCH skipping for a duration indicated by minimum scheduling offset
* Others are not precluded

Agreements

* For DCI based SSSG switching in active time for an active BWP (if supported), the following can be further considered,
  + Explicit indication of PDCCH adaptation
    - Scheduling DCI based
      * Format 1\_1,
      * Format 0\_1,
      * Format 0\_2/1\_2
      * ~~Format 1\_0~~
    - Non-scheduling DCI ~~supported by vivo, Samsung~~
      * Format 2\_6 in active time
      * Format 2\_0
      * ~~Format 1\_0~~
      * Format 1\_1 (SCell dormancy case 2)
    - additional indication mechanism
      * By reusing Rel-16 SCell dormancy indication when CA is configured, FFS details
      * By associating Rel-16 cross-slot scheduling indication when R16 cross-slot scheduling is configured, FFS detailds
    - DCI dynamically indicates a duration ~~period~~ for the switched SSSG, UE switch back to previous/default SSSG after duration ends~~timer expried~~
  + Timer-based SSSG switching, including RRC configured a timer, UE switch back after timer expired.
  + SSSG activation/deactivation
  + FFS: Implicit SSSG switching
    - SSSG switching triggered by SR
    - SSSG switching triggered by RACH
    - Default SSSG that a UE monitors when coming out of DRX to monitor an ON duration.
* FFS: whether/how to support SSSG switching for multiple groups of cell(s).
* FFS: whether/how to support SSSG switching in active time with DCP outside active time
* FFS: whether / how to support more than 2 SSSGs,
  + FFS: number of SSSGs
* FFS: a search space set group to emulate PDCCH skipping
* Others are not precluded

Agreements:

* The following alternatives can be considered for DCI based PDCCH monitoring adaptation in active time for an active BWP for power saving
  + Alt 1: Enhancement of Rel-16 SSSG switching to support PDCCH monitoring adaptation including skipping for a duration
  + Alt 2a: Enhancement of DCI(s) utilized for Rel-16 power saving adaptation for supporting both skipping PDCCH monitoring for a duration and SSSG switching
  + ~~Alt 2b: Enhancement of DCI(s) utilized for Rel-16 power saving adaptation for supporting both skipping PDCCH monitoring for a duration and PDCCH monitoring periodicity adaptation~~
  + Others not precluded

*RAN1#105-e*

Agreement:

* PDCCH schedules data and also indicates PDCCH monitoring adaptation by SSSG switching and PDCCH skipping for a duration is supported.
  + At least DCI format(s) 1-1, 0-1, 1-2 and 0-2 can be used for the indication(s)

Agreement:

* ~~At least~~ one of  Alt 1 and Alt 2 is supported, to be decided in RAN1#106,
* Alt 1: Supporting SSSG  switching to emulate PDCCH skipping functionality,
  + Alt 1-1: by an ‘empty’ SSSG which no SS set(s) is configured for the ‘empty’ SSSG, UE does not monitoring PDCCH on the ‘empty’  SSSG,
  + Alt1-2: by a ‘dormant SSSG’ which may have associated SS sets, and monitored conditionally (e.g., depending on HARQ NACK or RTT/ReTx timers)
* Alt 2: PDCCH schedules data and also indicates PDCCH monitoring adaptation by PDCCH skipping for a duration is supported.
  + FFS details, including
    - e.g., joint / separate indication of SSSG switching and PDCCH skipping
    - Determination of the duration(s) for PDCCH skipping, e.g.,
      * by RRC signaling,
      * by DCI indication
      * Implicitly, to the end of C-DRX active time

Agreement:

At least SSSG#0 and SSSG#1 switching is supported for Rel-17 SSSG switching indicated by PDCCH scheduling data and/or timer.

* FFS: support of more than 2 SSSGs

# Proposals from companies’ submitted contributions

## Huawei, HiSilicon

1. **R1-2106481 Extensions to Rel-16 DCI-based power saving adaptation for an active BWP Huawei,** **HiSilicon**

***Observation 1: The existing specification does not support to configure empty SSSG.***

***Observation 2:*** ***It is complicated for UE implementation and for specification to introduce dormant SSSG.***

***Observation 3: If the number of SSSG is more than 2 by Alt 1, the complicated state transitions should be discussed and specified.***

***Observation 4: Alt 2 provides the flexibility for gNB to indicate both SSSG switching and PDCCH skipping depending on configuration, meanwhile Alt 1 needs to rely on more than two SSSGs and complicated state transitions to achieve the same purpose.***

***Observation 5: Alt 2 provides good forward compatibility, e.g., combination of PDCCH skipping and Rel-18 network energy saving.***

***Observation 6：PDCCH monitoring adaptation indicated by group common DCI format is beneficial，e.g., DCI format 2\_6 inside DRX active time, if there is no data transmission.***

***Observation 7: Two groups of SS sets is enough to support both PDCCH skipping and SSSG switching.***

***Proposal 1: Specify directly indicating PDCCH skipping for a duration, i.e., do not use SSSG switching to emulate PDCCH skipping functionality.***

***Proposal 2: Reuse/extend dormancy indication field in scheduling DCI to indicate PDCCH monitoring adaptation.***

***Proposal 3: Extend MCS/NDI/RV/HARQ process number/antenna port/DMRS sequence initialization field used for SCell dormancy case 2 to indicate PDCCH monitoring adaptation.***

***Proposal 4: Support group common DCI, e.g. DCI format 2\_6 inside DRX active time, to indicate PDCCH skipping and SSSG switching.***

***Proposal 5: Support different application delay for SSSG switching and PDCCH skipping：***

* *If DCI indicates the UE switching to another SSSG, UE applies the DCI after HARQ-ACK feedback for DCI with DL grant or PUSCH transmitting for DCI with UL grant;*
* *If DCI indicates the UE to skip PDCCH monitoring, the application delay is max(applicable K0min, Z), after which the UE stops monitoring PDCCH in a duration.*

***Proposal 6: Explicitly configure the duration(s) of PDCCH skipping by RRC signaling.***

***Proposal 7: After being indicated to skipping PDCCH monitoring and/or switching to ‘empty’ SSSG, if the HARQ feedback for PDSCH is NACK, the UE still performs PDCCH monitoring for HARQ retransmission when drx-RetransmissionTimerDL is running.***

***Proposal 8: For different SSSG, the skipped duration(s) may be different to match the PDCCH monitoring periodicity.***

***Proposal 9: Support SSSG switching or stop PDCCH skipping triggered by SR and RACH.***

## ZTE, Sanechips

1. **R1-2106524 Extension to Rel-16 DCI-based power saving adaptation during DRX Active Time ZTE, Sanechips**

**Observation 1: The emulation of an implicit PDCCH skipping via SSSG switching by pure network implementation cannot give gNB a straightforward information of a proper power saving configuration.**

**Observation 2: For Alt 1, there will be low probability to deploy the implicit PDCCH skipping function if the UE power saving gain from the common design is degraded.**

**Observation 3: According to Rel-16 specification, the latency of data processing will be increased if DCI-based indication of SSSG switching is not supported with Alt 1.**

**Observation 4: For Alt 2, UE can perform PDCCH skipping right after the triggering DCI with data scheduling and the UE can fall in a deep sleep during the skipping duration.**

**Observation 5: For PDCCH adaptation, the processing time for responding DL SPS PDSCH release needs to be considered.**

**Observation 6: When cross-slot scheduling is applied for the UE, the delay for applying the PDCCH adaptation does not need to consider the minimum scheduling offset.**

**Proposal 1: Alt 2 should be supported for the common design of SSSG switching and PDCCH skipping for the following advantages:**

* **explicit PDCCH skipping function for UE and gNB;**
* **more flexibility;**
* **better power saving gain;**
* **less latency.**

**Proposal 2: Supporting two SSSGs for Rel-17 SSSG switching.**

**Proposal 3: It should support to configure multiple candidate values of skipping duration by RRC signaling and use DCI to dynamically indicate one of the configured skipping durations.**

**Proposal 4: For SSSG switching of Alt 2, timer-based triggering mechanism for SSSG switching can be reused to simplify the specification work.**

**Proposal 5: A skipping timer used for PDCCH adaptation from SSSG monitoring to PDCCH skipping should be considered in the cases of PDCCH adaptation without DCI indication.**

**Proposal 6: UE should switch to a default SSSG after the end of PDCCH skipping duration, wherein the default SSSG should be configured by RRC signaling to adapt to various traffic models.**

**Proposal 7: The UE should monitor PDCCH for retransmission data, but it does not monitor PDCCH for an initial transmission data during the PDCCH skipping period.**

**Proposal 8: The UE should monitor PDCCH according to all of search space sets configured in the DL active BWP or search space sets in a default SSSG when the following events occur during a skipping duration.**

* **SR indicated by the UE,**
* **beam failure detection, or**
* **random access procedure in RRC connected mode due to out-of sync, etc.**

**Proposal 9: The application delay for PDCCH adaptation for μ=0/1/2 can reuse that of SSSG switching in Rel-16. The minimum value of application delay for PDCCH adaptation for μ=3 can be 25 symbols.**

## vivo

1. R1-2106610 Discussion on DCI-based power saving adaptation in connected mode vivo

**Proposal 1: a new ‘empty’ SSSG group can be configured for scheduling DCI based SSSG switching.**

**Proposal 2**

* **If Alt 1 (i.e. Supporting SSSG switching to emulate PDCCH skipping functionality) is supported, maximum 3 SSSGs should be supported for Rel-17 SSSG switching in the active BWP.**
* **If Alt 2 (i.e. PDCCH schedules data and also indicates PDCCH monitoring adaptation by PDCCH skipping for a duration.) is supported, more than 2 SSSGs is NOT supported.**

**Proposal 3: Type0/0A/1/2-PDCCH CSS monitoring is not impacted by Rel-17 PDCCH monitoring adaptation**

**Proposal 4, Rel-17 supports the following mechnisms for SSSG swithing**

* **Scheme 1: for scheduling DCI triggered SSSG switching**
  + **SSGS bit(s) in a UE specific DCI (such as DCI format x\_1/x\_2)** 
    - **‘0’: starts monitoring PDCCH according to SSSG#0 and stop monitoring SSSG#1**
    - **‘1’: starts monitoring PDCCH according to SSSG#1 and stop monitoring SSSG#0**
    - **FFS: more bits for extending more than 2 SS set groups**
* **Scheme 2: A duration indicated by scheduling DCI**
  + **UE switch from SSSG#1 to SSSG#0 after a last symbol of a remaining duration from timer1 indicated by scheduling DCI**
* **Scheme 3: RRC configured timer for switching**
  + **Timer0 for switching from SSSG#0 to SSSG#1**
    - **UE switches from SSSG#0 to SSSG#1 at the beginning of the first slot that is at least symbols after a slot where the timer0 expires** 
      * **timer1 is configured by RRC**
  + **Timer 1 for switching from SSSG#1 to SSSG#0**
    - **UE switches from SSSG1 to SSSG0 at the beginning of the first slot that is at least symbols after a slot where the timer1 expires** 
      * **timer1 is configured by RRC**
* **Scheme 4: Non-scheduling DCI triggered SSSG switching, e.g.**
  + **Any non-scheduling DCIs with C-RNTI scrambled.**
  + **Format 1\_1 (SCell dormancy case 2)**

**Proposal 5, the following additional mechanisms is supported for PDCCH switching/skipping when interaction with HARQ in case UE receives the PDCCH in SSSG 0 indicating SSSG switching from 0 to 1,**

* **UE switches to SSSG#1 after decoding the PDCCH,**
* **UE switches to SSSG#0 (from SSSG1), if** 
  + **Alt 1-1: UE Tx NACK,**
  + **Alt 1-2: *k* slot after UE Tx NACK**
  + **Alt 2: after *drx-RetransmissionTimer* starts**

**And after UE successfully complete retransmission,**

* **UE switches to SSSG#1 (from SSSG0),**
  + **Alt 1: UE Tx an ACK which corresponds to the PDCCH indicating SSSG switching from 0 to 1**
  + **Alt 2: after *drx-RetransmissionTimer* expires**

## Spreadtrum Communications

1. R1-2106710 Discussion on power saving techniques for connected-mode UEs Spreadtrum Communications

Rel-17 SSSG switching technique

***Proposal 1: The wakeup indication for Rel-17 SSSG switching technique should be down prioritized.***

***Proposal 2: The state machine for Rel-17 SSSG switching technique should be down prioritized, or simplified as much as possible.***

***Proposal 3: The application delay for Rel-17 SSSG switching technique can be defined.***

Rel-17 PDCCH skipping technique

***Proposal 4: The wakeup indication for Rel-17 PDCCH skipping technique is not supported.***

***Proposal 5: The state machine for Rel-17 PDCCH skipping technique is not supported.***

***Proposal 6: The application delay for Rel-17 PDCCH skipping technique is zero.***

Scheduling DCI

***Proposal 7: Select Alt 2 to support PDCCH skipping technique, i.e. PDCCH schedules data and also indicates PDCCH monitoring adaptation by PDCCH skipping is supported.***

***Proposal 8: Consider the following DCI fields for PDCCH that schedules data and also indicates PDCCH monitoring adaptation by SSSG switching and PDCCH skipping.***

* ***New DCI field***
* ***Reusing the Rel-16 indication of cross-slot scheduling***
* ***Reusing the Rel-16 Indication of SCell dormancy***

Non-scheduling DCI with C-RNTI scrambling

***Proposal 9: Support the non-scheduling DCI with C-RNTI scrambling that indicates PDCCH monitoring adaptation by SSSG switching and PDCCH skipping.***

## Samsung

1. R1-2106901 Discussion on DCI-based power saving techniques Samsung

**Observation 1: One PDCCH skipping state associated with a PDCCH skipping time duration is sufficient to achieve UE power saving from PDCCH skipping.**

**Proposal 1: Support more than 2 SSSG for PDCCH monitoring adaptation in a finer adaptation granularity and wider adaptation aspects.**

**Proposal 2: Support an “empty” SSSG associated with a RRC configured time duration for PDCCH skipping.**

**Proposal 3: Support application delay for PDCCH monitoring adaptation triggered by scheduling DCI format, based on one of the following alternatives:**

* **Alt1: configured by higher layer**
* **Alt2: after HARQ-ACK feedback**

**Propose 4: Support UE assistance information for PDCCH monitoring adaptation, including**

* **preferred search space set group,**
* **PDCCH skipping duration.**

**Proposal 5: Support PDCCH skipping for a duration indicated by minimum scheduling offset.**

## CATT

1. R1-2106986 PDCCH monitoring adaptation CATT

***Observation 1: SSSG switching has the additional application delay during search space switching, which can decrease the power saving gain.***

***Observation 2: For SSSG switching, the flexible PDCCH adaptation would need large Search Space Set overhead.***

***Observation 3: Although the SSSG switching is enhanced by using UE-specific indication, e.g. scheduling DCI, the catastrophe could be created when DCI is miss-detected and large search space set overhead.***

***Observation 4: The PDCCH skipping can obtain the higher power saving gain than the SSSG switching.***

***Proposal 1: The SSSG switching cannot create the exact function of the PDCCH skipping to achieve UE power saving for its non-negligible drawbacks, especially for the additional switching delay, miss detection of HARQ-ACK and large search space set overhead.***

***Proposal 2: The Alt 2: PDCCH schedules data and also indicates PDCCH monitoring adaptation by PDCCH skipping for a duration should be supported.***

***Proposal 3: The PDCCH monitoring adaptation can dynamically indicate UE to reduce the PDCCH monitoring without any changes of SearchSpace configuration.***

## NEC

1. R1-2107017 Discussion on DCI-based power saving adaptation NEC

**Proposal 1: Support more than 2 SSSGs for PDCCH monitoring adaptation by SSSG switching.**

**Proposal 2: Maximum number of simultaneously monitored search space set groups may be configurable.**

**Proposal 3: Introduce a bitmap in DCI for dynamic indication of SSSG switching.**

## Nordic Semiconductor ASA

1. R1-2107046 On PDCCH monitoring adaptation Nordic Semiconductor ASA

***Observation-1:*** *PDCCH skipping can be achieved according to R16 specification by configuration of an empty SSG#1*

***Proposal-1:*** *To enable user-specific PDCCH skipping for a duration, add SS group switching field to DCI format 1\_1 and 1\_2. Support Alt 1.*

* *Adopt the correspond TP in Appendix*

***Observation-2:*** *SS group switching with additional re-transmission behavior may achieve the same as PDCCH skipping with less DL control overhead.*

***Proposal-2:*** *Support a dormancy SSS group, a SS group configured with sparse MOs, where monitoring of all or subset of group’s search-space sets is conditional on a pending re-transmission.*

***Observation-3:*** *Support of 3rd SS group does not result in large specification changes.*

***Proposal-3:*** *Consider support of 3rd SS group*

***Proposal-4:*** *If more than one timer values is configured, DCI format 1\_1,1\_2 or 2\_0 contains additional field indicating which timer value applies.*

* *Initialize timer to value X*
* *Initialize timer to value Y*
* *…..*

***Proposal-5:*** *Reuse the existing timelines for search-space group switching form sub-clause 10.4 of TS 38.213. For switching to and from empty SSG, timelines are up to UE implementation.*

## Lenovo, Motorola Mobility

1. R1-2107184 Enhanced DCI based power saving adaptation Lenovo, Motorola Mobility

**Observation 1: Rel-16 search space set group configuration may result in an unnecessary high signalling overhead.**

**Proposal 1: In Rel-17, support configuring more than one value for a subset of search space configuration parameters in a given search space configuration.**

**Proposal 2: Rel-17 NR supports search space set adaptation when starting an ON duration timer in every DRX cycle based on DCI format 2\_6. Further, Rel-17 NR supports small-scale search space set adaptation within a DRX cycle based on scheduling DCI.**

**Proposal 3: Support scheduling-DCI based PDCCH skipping indication. Reuse the Rel-16 application delay for K0,min/K2,min indication as an application delay for PDCCH skipping.**

**Proposal 4: If there is no active UE-specific search space set excluding a UE-specific search space set indicated by an PDCCH skipping indication, UE applies PDCCH skipping for the indicated UE-specific search space set as follows:**

* **not earlier than the application delay after the end of PDCCH including the indication, and**
* **for DL DCI format(s) of the UE-specific search space set, upon expiration of *drx-RetransmissionTimerDL* if *drx-HARQ-RTT-TimerDL* or *drx-RetransmissionTimerDL* is running, and**
* **for UL DCI format(s) of the UE-specific search space set, upon expiration of *drx-RetransmissionTimerUL* if *drx-HARQ-RTT-TimerUL* or *drx-RetransmissionTimerUL* is running.**

## OPPO

1. R1-2107255 DCI-based power saving adaptation solutions OPPO

***Proposal 1: No further DCI format other than 1\_1, 0\_1, 1\_2 and 0\_2 is used for triggering PDCCH monitoring adaptation.***

***Proposal 2: Directly indicating skipping of PDCCH monitoring occasions is supported as PDCCH monitoring adaptation:***

***PDCCH skipping is based on number of slots.***

***2bits indication in DCI format is introduced to support for non-skipping, 4-slot skipping, 8-slot skipping, 16-slot skipping.***

***Proposal 3: Introduce a delay window in the PDCCH skipping indication, which is based on PDCCH-PDSCH-HARQ-ACK timing and re-scheduling timing.***

***Proposal 4: In the delay window for retransmission, PDCCH monitoring can be only after PDCCH-PDSCH-HARQ-ACK timing and in few consecutive monitoring occasions.***

***Proposal 5: Indicate PDCCH search space group sets by the PDCCH skipping indication bits.***

***Proposal 6: When multiple PDCCH search space groups are switchable, autonomous PDCCH monitoring adaptation is triggered by timer.***

***Proposal 7: The search space group switching indication states in the DCI can also trigger cross-slot scheduling states.***

***The application delay can be also applicable to the search space group switching.***

## Qualcomm Incorporated

1. R1-2107358 DCI-based power saving adaptation during DRX ActiveTime Qualcomm Incorporated

Proposal 1: For the common design of PDCCH monitoring adaptation in Rel-17, Alt 1 (RAN1 #105-e agreement) is adopted.

Proposal 2: To emulate PDCCH skipping with search space group switching (Alt 1), a dormant search space set group is introduced (Alt 1-2):

* To enable HARQ retransmissions during the dormant search space set group, only discontinuous PDCCH monitoring according to RTT and Retransmission timers is allowed, if configured.
* The UE switches back to a non-dormant search space set group when a dormancy timer associated with the dormant SSSG expires.

Proposal 3: For the common design of PDCCH monitoring adaptation based on SSSG switching (Alt 1), the maximum number of configured SSSGs larger than two is considered.

Proposal 4: For explicit indication of PDCCH monitoring adaptation, in addition to scheduling DCI formats 0\_1/1\_1/0\_2/1\_2, non-scheduling DCI formats are also considered:

* DCI format 1\_1 (similar to Case 2 SCell dormancy indication) and DCI format 2\_6 (outside active time).

Proposal 5: For implicit indication of PDCCH monitoring adaptation, the following candidates are considered:

* **Configured timer: per-non-default SSSG (including dormant and non-dormant SSSGs), if more than two SSSGs are supported,**
* **Transmission of SR and PRACH: transition from a dormant SSSG to a non-dormant SSSG after transmitting a scheduling request or a PRACH preamble.**

Proposal 6: For the application delay of PDCCH monitoring adaptation, combination the application delays of Rel-16 minimum scheduling offset restriction and Rel-16 SSSG switching is considered:

* **Different application delays are used for indication types (explicit or implicit) and SSSG types (dormant and non-dormant).**
* **Different application delays are used depending on whether the PDCCH monitoring adaptation is jointly configured with Rel-16 minimum scheduling offset restriction or not.**

Proposal 7: In the CA scenario, for the joint adaptation across CCs, carrier-group-based PDCCH monitoring adaptation is considered.

Observation 1: In terms of codepoint mapping, Alt 1 is homogeneous, and Alt 2 is heterogeneous. Heterogeneous codepoint mapping is not found in Rel-15 and Rel-16 standards.

Observation 2: During the application delay, the UE does not expect to receive another indication of power saving adaptation different from the previous indication.

## CMCC

1. R1-2107416Discussion on PDCCH monitoring reduction during DRX active time CMCC

**Proposal 1: Alt 1 is adopted for R17 PDCCH monitoring adaptation, that is supporting SSSG switching to emulate PDCCH skipping functionality.**

**Proposal 2: Alt1-2 can be supported for SSSG switching, e.g. to emulate PDCCH skipping functionality by a ‘dormant SSSG’ which may have associated SS sets, and monitored conditionally (e.g., depending on HARQ NACK or RTT/ReTx timers).**

**Proposal 3: More than 2 SSSGs can be supported for Rel-17 SSSG switching and the PDCCH skipping duration of dormant SSSG(s) are configured by RRC.**

**Proposal 4: UE should switch to the last non-dormant SSSG used before current dormant SSSG when the PDCCH skipping duration expires.**

**Proposal 5: A default SSSG can be configured and applied for the following cases,**

* **SSSG switching triggered by SR**
* **SSSG switching triggered by RACH**

**Proposal 6: Format 1\_1 (SCell dormancy case 2) is supported as non-scheduling DCI indication for SSSG switching in active time for an active BWP.**

## LG Electronics

1. R1-2107455 Discussion on DCI-based power saving adaptation during DRX ActiveTime LG Electronics

***Proposal 1: Discuss explicit definition of PDCCH skipping with regard to a DRX functionality, especially on Type0/0A/1/2-PDCCH CSS sets.***

***Observation 1: Supporting SSSG switching to emulate PDCCH skipping functionality, i.e. Alt 1, cannot be possible if PDCCH skipping shut down UE’s monitoring PDCCH candidates for a DCI with CRC scrambled by RNTIs controlled by a DRX functionality.***

***Proposal 2: Support Alt 2 for a common design for DCI-based monitoring adaptation.***

***Proposal 3: Consider supporting the following design for DCI-based PDCCH monitoring adaptation:***

* + - ***1-bit flag distinguishing between PDCCH skipping and SSSG switching***
    - ***UE behavior corresponding to each state configured by RRC signaling***
      * ***FFS: details including the number of bits required.***

***Proposal 4: Consider PDCCH monitoring adaptation indicated by a DCI format 2\_6 inside/outside DRX Active Time.***

* + - ***Discuss whether and how to define the monitoring window for DCI format 2\_6 inside DRX Active Time.***

***Proposal 5: Study how to handle missing case of DCI indicating SSSG switching.***

***Proposal 6: Discuss different application delay for two cases:***

* + - ***Command applied after UE’s UL transmission (PUSCH or ACK) if monitoring adaptation is indicated by a DCI with scheduling information.***
    - ***Time-based application delay if monitoring adaptation is indicated by a DCI without scheduling information.***

***Proposal 7: Consider supporting implicit PDCCH monitoring adaptation triggered by SR and RACH***

* + - ***Discuss whether and how to define a monitoring window for a UL grant regarding SR***

## ETRI

1. R1-2107476 DCI-based power saving adaptation during DRX active time ETRI

**Observation 1: Noting that Alt. 2 needs a new DCI field to indicate PDCCH skipping and also noting that Alt. 2 may have an issue addressed in Table 1, taking Alt. 1 may result in less specification change.**

**Proposal 1: Support Alt. 1, i.e., SSSG switching to emulate PDCCH skipping functionality.**

**Proposal 2: For Alt. 1, support up to three SSSGs.**

**Proposal 3: PDCCH which does not schedule data and indicate SSSG switching or PDCCH skipping for an active BWP in active time is supported by DCI Format 1\_1 (SCell dormancy case 2 like)**

**Proposal 4: DCI format 2\_6 is supported to indicate SSSG switching or PDCCH skipping for an active BWP in active time when DRX is configured.**

**Proposal 5: For UE configured with DRX, higher layer signaling can configure SSSG that a UE monitors when coming out of DRX to monitor an ON duration.**

**Proposal 6: At least for the case where ACK/NACK for the DCI is present, application time of SSSG switching or PDCCH skipping is configured to be after the HARQ-ACK transmission timing + some margin for gNB’s HARQ-ACK decoding processing time.**

**Proposal 7: Application time of SSSG switching or PDCCH skipping is configured to be after the potential retransmission period, i.e., time period while DRX retransmission timer for PDSCH/PUSCH is running.**

## MediaTek Inc.

1. R1-2107521On enhancements to DCI-based UE power saving during DRX active time MediaTek Inc.

**Observation 1: At least three PDCCH monitoring behaviours are needed for Rel-17 power saving adaptation.**

* **Per-slot monitoring: The default monitoring behaviour during scheduling of data packets**
* **PDCCH skipping for a duration: Switch to this behaviour after the last TB scheduling**
* **Periodical PDCCH monitoring: Switch to this behaviour when there is potential timing critical data scheduling (e.g., for AR/VR UL traffic)**

****

**Figure 1. The Rel-17 PDCCH adaptation for reducing PDCCH monitoring**

**Observation 2: In Alt 1, to support at least 3 monitoring behaviours, at least 1 additional SSSG is needed. However, there will also introduce more complicate state transition design and error handling.**

****

**Figure 2. The state machine of Alt 1 becomes complex after adding one SSSG**

**Observation 3: Alt 2 requires minimum extension to Rel-16 SSSG switching and addition of simple PDCCH skipping behaviour.**

**Proposal 1: Support Alt 2 for reusing 2 SSSG framework and skipping for one duration.**

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**Figure 3. The state machines of Alt 2**

**Proposal 2: To avoid blocking retransmission scheduling during the skipping duration, UE should resume a short term PDCCH monitoring during a configured duration, depending on the HARQ processing outcome.**

****

**Figure 5. Illustration of UE power saving adaptation for retransmission handling**

**Proposal 3: The configured time duration for DL/UL can be set to the sum of RTT and retransmission timer of DRX configuration.**

* ***drx-HARQ-RTT-TimerDL* + *drx-RetransmissionTimerDL* for downlink scheduling.**
* ***drx-HARQ-RTT-TimerUL* + *drx-RetransmissionTimerUL* for uplink scheduling.**
* **Note: UE is not required to monitoring PDCCH during the first RTT timer**
* **FFS: UE behaviour when receiving DL and UL indications of different configured time durations**

**Proposal 4: Application delay is only one slot for PDCCH skipping, aiming for the minimum specification change.**

**Proposal 5: Application delay for SSSG switching triggered by scheduling DCI should be further discussed since the scheduling DCI can also trigger cross-slot scheduling adaptation.**

## Fraunhofer HHI, Fraunhofer IIS

1. R1-2107533DCI-based Power Saving Enhancements Fraunhofer HHI, Fraunhofer IIS

**Proposal 1: Use the SSG switching mechanism as a starting point for a unified design.**

**Proposal 2: Adopt SSG switching using the minimum offset signaling to trigger a switch.**

**Proposal 3: Support PDCCH skipping by enabling empty or dormant SSGs which stay active for a configured time duration.**

**Proposal 4: The PDSCH processing time shall be adaptable based on certain parameters, e.g., the minimum scheduling offset or the currently active SS group.**

## Intel Corporation

1. R1-2107601 Discussion on DCI-based UE Power Saving Schemes during active time Intel Corporation

**Observation 1:**

* **PDCCH monitoring adaptation can be potentially triggered by both scheduling and non-scheduling DCI formats and it is expected that unified approach is taken regarding start of the adaptation**
* **Impact to HARQ retransmission due to PDCCH monitoring adaptation can be avoided by gNB implementation**

**Proposal 1: Support Alt 2: PDCCH schedules data and also indicates PDCCH monitoring adaptation by PDCCH skipping for a duration is supported.**

* **Duration for PDCCH skipping can be RRC signaled or indicated by DCI**
* **FFS: joint / separate indication of SSSG switching and PDCCH skipping**

**Proposal 2: Support indication of PDCCH monitoring adaptation by following ways.**

* **Use explicit bit field in DCI formats 1-1, 1-2, 0-1, 0-2 when they schedule data**
* **DCI Format 1\_1 (SCell dormancy case 2) when not scheduling data.**
* **DCI Format 2\_6 during active time.**

**Proposal 3: PDCCH monitoring adaptation should not be dependent on HARQ outcome or PUSCH transmission**

* **Monitoring adaptation does not start before Z slots from the slot where DCI is received, and Z is given by (1, 1, 2, 2) for DL SCS of (15, 30, 60, 120) KHz, respectively.**

## Panasonic

1. R1-2107624 Potential extension(s) to Rel-16 DCI-based power saving adaptation during DRX ActiveTime Panasonic

Proposal 1: Support Alt 2 to specify a unified framework supporting SSSG switching and PDCCH skipping by RRC configuration + DCI indication. Following design is considered:

* gNB configures by RRC a list of PDCCH monitoring adaptation behaviours, including which search space to be monitored with/without timer. DCI indicates which index in the list the UE should follow.

## Apple

1. R1-2107752 Enhanced DCI-based power saving adaptation Apple

***Proposal 1: Alt 2 should be supported to enable PDCCH monitoring adaptation.***

***Proposal 2: When DCI format 0-1, 0-2, 1-1 and 1-2 is used to trigger PDCCH monitoring adaptation, the adaptation is applied to all CCs within a CC group.***

***Proposal 3: For PDCCH based adaptation using non-scheduling DCI, enable DCI format 1-1 with triggering bits per cell group.***

***Proposal 4: For PDCCH based adaptation using non-scheduling DCI, enable DCI format 2-6 monitoring in DRX-ON duration.***

***Proposal 5: When PDCCH monitoring adaptation is triggered by non-scheduling DCI, application delay for SSSG switching is 25 OFDM symbols for , and 39 OFDM symbols for .***

***Proposal 6: When PDCCH monitoring adaptation is triggered by non-scheduling DCI, application delay for PDCCH skipping is 11 OFDM symbols for , and 25 OFDM symbols for .***

***Proposal 7: When PDCCH monitoring adaptation is triggered by DCI format 1-1 and 1-2, application delay applies after the last OFDM symbol of ACK transmission. Application delay can 1 slot considering gNB ACK decoding time and UE processing time to apply the new configuration.***

***Proposal 8: When PDCCH monitoring adaptation is triggered by DCI format 0-1 and 0-2, application delay applies after the last OFDM symbol of PUSCH transmission when drx-RetransmissionTimerUL is not configured or longer than a threshold.***

***Proposal 9: When PDCCH monitoring adaptation is triggered by DCI format 0-1 and 0-2, application delay applies after drx-RetransmissionTimerUL expires if drx-RetransmissionTimerUL is configured and less than a threshold.***

## InterDigital, INC.

1. R1-2107808 PDCCH monitoring reduction in Active Time InterDigital, Inc.

**Proposal 1: Support configuration of codepoints per SSSG to indicate SSSG switching and/or PDCCH skipping.**

**Proposal 2: Support indication of skipping a subset of SSs in a SSSG.**

**Proposal 3: PDCCH skipping indication (including monitoring the PDCCH according to a null SSSG) is not applied in an interval when the DL retransmission timer is running.**

**Proposal 4: PDCCH skipping indication (including monitoring the PDCCH according to a null SSSG) is not applied in an interval when the UL retransmission timer is running.**

## NTT DOCOMO, INC.

1. R1-2107871 Discussion on extension to DCI-based power saving adaptation NTT DOCOMO, INC.

**Observation 1: If ‘empty’ SSSG which no SS set(s) is configured, only when switching to ‘empty’ SSSG, the application delay can be as short as possible to cover PDCCH processing time.**

**Proposal 1: For PDCCH skipping functionality, SSSG switching should be used.**

**Proposal 2: More than 2 SSSGs should be considered for flexible monitoring periodicity and PDCCH skipping functionality.**

**Observation 2: PDCCH skipping along with cross-slot scheduling can maximize the benefit of cross-slot scheduling.**

**Proposal 3: It should be considered that the duration of PDCCH skipping is equal to or longer than the applicable minimum scheduling offset.**

## Ericsson

1. R1-2108001 Design of active time power savings mechanisms Ericsson

[Observation 1 Allowing NW to have control on which SSSG the UE needs to monitor PDCCH after the skipping duration ends can be beneficial.](#_Toc79165169)

[Observation 2 UE PDCCH monitoring behavior during PDCCH monitoring adaptation application delay should be clear to avoid different understanding between NW and UE.](#_Toc79165170)

[Proposal 1 Support following as a common solution for SSSG switching and PDCCH skipping:](#_Toc79168505)

[- DCI indicates one of the following states to the UE](#_Toc79168506)

[i. switch to SSSG0](#_Toc79168507)

[ii. switch to SSSG1](#_Toc79168508)

[iii. skip PDCCH monitoring for duration X (X configured by RRC)](#_Toc79168509)

[iv. no change to PDCCH monitoring](#_Toc79168510)

[Proposal 2 Use the baseline application delay from Rel. 16 SSSG-switching feature.](#_Toc79168511)

[- FFS: the baseline application delay for 120 kHz SCS .](#_Toc79168512)

[Proposal 3 HARQ retransmissions should not be delayed due to PDCCH monitoring adaptation and mechanisms to avoid this should be supported e.g., a configurable timer-based application delay or HARQ ACK-based application of the PDCCH monitoring adaptation command.](#_Toc79168513)

[Proposal 4 PDCCH monitoring adaptation for Rel. 17 should not entail an interruption to UE transmission/reception on any serving cell.](#_Toc79168514)

[Proposal 5 For UE configured with DRX, higher layer signaling can configure SSSG that a UE monitors when coming out of DRX to monitor an ON duration.](#_Toc79168515)

[Proposal 6 UL scheduling request can be used as a trigger to stop skipping and/or to switch between SS-set groups. The SS-set group that UE monitors after transmitting an UL scheduling request is configurable by NW.](#_Toc79168516)

[Proposal 7 For a transition between SSSG1 and SSSG0, a similar mechanism with Rel. 16 SSSG-switching timer-based feature is adopted.](#_Toc79168517)

[Proposal 8 The SSSG that a UE monitors after skipping duration ends is explicitly configured by RRC or is indicated by the PDCCH monitoring adaptation bitfield in the DCI.](#_Toc79168518)

[Proposal 9 Indication for PDCCH monitoring adaptation (by SSSG switching and PDCCH skipping for a duration) is supported only via DCI formats 1-1/1-2/0-1/1-1.](#_Toc79168519)

[Proposal 10 For self-scheduling, PCell’s scheduling DCI format 1\_1/0\_1/1\_2/0\_2 can indicate SSSG-switching/skipping for the primary cell.](#_Toc79168520)

[Proposal 11 For self-scheduling, an SCell’s scheduling DCI format 1\_1/0\_1/1\_2/0\_2 can indicate SSSG-switching/skipping for the SCell.](#_Toc79168521)

[Proposal 12 Study further how to support SSSG-switching/skipping for multiple groups of cell(s). Details including number of groups FFS.](#_Toc79168522)

## ITRI

1. R1-2108014 Discussion on DCI-based power saving adaptation ITRI

**Observation:**

SSSG switching may impact on PDCCH monitoring after UE transmit SR or PRACH.

**Proposal 1:**

Supporting SSSG switching to emulate PDCCH skipping functionality by an ‘empty’ SSSG which no SS set is configured for the ‘empty’ SSSG, UE does not monitoring PDCCH on the ‘empty’ SSSG.

**Proposal 2:**

Further study the SSSG switch impacts over multi-cell operation and cross-carrier scheduling.

**Proposal 3:**

Further study the SSSG switch impacts over PDCCH monitoring.

## ASUSTeK

1. R1-2108048 A common framework for SSSG switching and PDCCH skipping ASUSTeK

**Observation1: Rel-16 SSSG switch is well-specified in Rel-16 and could provide a good frame work for both Rel-17 SSSG switch as well as PDCCH skipping.**

**Proposal 1: RAN1 considers Rel-16 SSSG switch as a starting point for power saving adaptation during Active Time and makes further required adjustment which fits needs of Rel-17 power saving better.**

**Observation 2: time duration could be variant, e.g. indicating by DCI to fit the needs of power saving.**

**Observation 3: SSSG activation deactivation may not fit the case of PDCCH skipping.**

**Proposal 2: RAN1 considers the following adjustment to Rel-16 SSSG switch to support both Rel-17 SSSG switch and PDCCH skipping:**

* **Variant time duration indicated by DCI**
* **DCI format triggering the SSSG switch**
* **More than two SSSGs**

## Nokia, Nokia Shanghai Bell

1. R1-2108124 UE power saving enhancements for Active Time Nokia, Nokia Shanghai Bell

In Section 2 were present the evaluation of signalling overhead related to the PDCCH monitoring adaptation considering SSSG switching and PDCCH skipping:-

**Observation:** *SSSG switching has lower signalling overhead than PDCCH skipping for most of the evaluated traffic scenarios.*

In the continued discussion in RAN1#105e it was agreed that SSSG switching at least with 2 SSSGs wil be supported in Rel-17 for PDCCH monitoring adaptation and the method to introduce stopping the PDCCH monitoring, will be either introduced as a part of the SSSG frame work or as a separate functionality. Based on this we made following observations and proposals in Section 3:-

**Observation**: A field indicating the target SSSG index could be added to the scheduling DCI.

**Proposal:** Support Rel-17 SSSG switching, in addition to scheduling DCIs also for DCI format 2\_0. For multi cell operation consider introducing similar mechanism as for Scell dormancy e.g. via DCI format 1\_1 (SCell dormancy case 2).

**Proposal:** Increase the number of SS set groups at least from 2 to 3.

**Proposal:** Support timer-based UE autonomous SS set group switching for active time power saving.

**Observation:** Through use of timer based SSSG switching and proper configuration of SS set stopping of PDCCH monitoring for a duration can be achieved.

**Proposal:** Supporting SSSG switching with ‘empty’ SSSG to embed PDCCH skipping functionality to SSSG framework.

**Proposal:** Procedures such as SR transmission, BSR or beam failure recovery should result UE to stop PDCCH monitoring adaptation and resume normal PDCCH monitoring.

**Observation:** In case of C-DRX, timer-based SSSG switching could be applied during the inactivity and SSSG would be switched if UE is scheduled during On Duration.

**Proposal:** Support configuring a default SS set group that is applied during On Duration, at least when DCP is configured.

**Proposal:** PDCCH monitoring adaptation should not be applied to Type0/0A/1 or 2 PDCCH CSS. For Type3-PDCCH CSS adaptation could be considered, but the monitoring should not be stopped completely.

**Proposal:** Use the application delay timeline introduced in Rel-16 for SSSG switching.

**Observation:** For PDCCH monitoring adaptation case, where UE still continues to monitor PDCCH, albeit at reduced rate, there may not be any need to have special handling of HARQ re-transmissions scheduling, but scheduling can follow the applied SS set(s).

**Observation:** Special handling of HARQ re-transmissions is only needed when UE stops the PDCCH monitoring for extended time.

**Proposal:** For stopping PDCCH monitoring based on {empty} SSSG or PDCCH skipping, define timers similarly as in C-DRX operation to enable configuring time windows for handling the open re-transmissions.

**Observation:** Associating minimum cross-slot scheduling restriction to certain SSSGs could be considered.

# Void

# Work Item Description

*NR\_UE\_pow\_sav-Core; WID in* [*RP-200938*](http://www.3gpp.org/ftp/tsg_ran/TSG_RAN/TSGR_88e/Docs/RP-200938.zip)*. The objectives are as follows*

|  |
| --- |
| 1. Specify enhancements for idle/inactive-mode UE power saving, considering system performance aspects [RAN2, RAN1]    1. Study and specify paging enhancement(s) to reduce unnecessary UE paging receptions, subject to no impact to legacy UEs [RAN2, RAN1]  * NOTE: RAN1 to check and update, if needed, evaluation methodology in RAN1 #102-e meeting   1. Specify means to provide potential TRS/CSI-RS occasion(s) available in connected mode to idle/inactive-mode UEs, minimizing system overhead impact [RAN1] * NOTE: Always-on TRS/CSI-RS transmission by gNodeB is not required  1. Study and specify, if agreed, enhancements on power saving techniques for connected-mode UE, subject to minimized system performance impact [RAN1, RAN4]    1. Study and specify, if agreed, extension(s) to Rel-16 DCI-based power saving adaptation during DRX Active Time for an active BWP, including PDCCH monitoring reduction when C-DRX is configured [RAN1]  * NOTE: Rel-15 and Rel-16 available power saving solutions should be supported by the UE and included in the evaluation. RAN1 will ask the confirmation from RAN2 that Rel-15 and Rel-16 available power saving solutions are properly utilized.   1. Study the feasibility and performance impact of relaxing UE measurements for RLM and/or BFD, particularly for low mobility UE with short DRX periodicity/cycle, and specify, if agreed, relaxation in the corresponding requirements [RAN4] * NOTE: Supplementary RAN2 work, if needed, can be triggered by RAN4 LS |

# Reference

**The following contributions are submitted in RAN1#106-E in AI 8.7.2,**

1. R1-2106481 Extensions to Rel-16 DCI-based power saving adaptation for an active BWP Huawei, HiSilicon
2. R1-2106524 Extension to Rel-16 DCI-based power saving adaptation during DRX Active Time ZTE, Sanechips
3. R1-2106610 Discussion on DCI-based power saving adaptation in connected mode vivo
4. R1-2106710 Discussion on power saving techniques for connected-mode UE Spreadtrum Communications
5. R1-2106901 Discussion on DCI-based power saving techniques Samsung
6. R1-2106986 PDCCH monitoring adaptation CATT
7. R1-2107017 Discussion on DCI-based power saving adaptation NEC
8. R1-2107046 On PDCCH monitoring adaptation Nordic Semiconductor ASA
9. R1-2107184 Enhanced DCI based power saving adaptation Lenovo, Motorola Mobility
10. R1-2107255 DCI-based power saving adaptation solutions OPPO
11. R1-2107358 DCI-based power saving adaptation during DRX ActiveTime Qualcomm Incorporated
12. R1-2107416 Discussion on PDCCH monitoring reduction during DRX active time CMCC
13. R1-2107455 Discussion on DCI-based power saving adaptation during DRX ActiveTime LG Electronics
14. R1-2107476 DCI-based power saving adaptation during DRX active time ETRI
15. R1-2107521 On enhancements to DCI-based UE power saving during DRX active time MediaTek Inc.
16. R1-2107533 DCI-based Power Saving Enhancements Fraunhofer HHI, Fraunhofer IIS
17. R1-2107601 On PDCCH Monitoring Adaptation during DRX active time Intel Corporation
18. R1-2107624 Potential extension(s) to Rel-16 DCI-based power saving adaptation during DRX ActiveTime Panasonic
19. R1-2107752 Enhanced DCI-based power saving adaptation Apple
20. R1-2107808 PDCCH monitoring reduction in Active Time InterDigital, Inc.
21. R1-2107871 Discussion on extension to DCI-based power saving adaptation NTT DOCOMO, INC.
22. R1-2108001 Design of active time power savings mechanisms Ericsson
23. R1-2108014 Discussion on DCI-based power saving adaptation ITRI
24. R1-2108048 A common framework for SSSG switching and PDCCH skipping ASUSTeK
25. R1-2108124 UE power saving enhancements for Active Time Nokia, Nokia Shanghai Bell

**Other references:**

1. RP-200938, “Revised WID: UE Power Saving Enhancements for NR”, MediaTek Inc., RAN#88-e

# History

1. R1-2007065 FL summary of potential extension(s) to Rel-16 DCI-based power saving adaptation during DRX ActiveTime RAN1#102-E Moderator (vivo)
2. R1-2007117 FL summary#2 of potential extension(s) to Rel-16 DCI-based power saving adaptation during DRX ActiveTime RAN1#102-E Moderator (vivo)
3. R1-2007225 FL summary#3 of potential extension(s) to Rel-16 DCI-based power saving adaptation during DRX ActiveTime RAN1#102-E Moderator (vivo)
4. R1-2007400 FL summary#4 of potential extension(s) to Rel-16 DCI-based power saving adaptation during DRX ActiveTime RAN1#102-E Moderator (vivo)
5. R1-2009501 FL summary#1 of power saving for Active Time RAN1#103-E Moderator (vivo)
6. R1-2009655 FL summary#2 of power saving for Active Time RAN1#103-E Moderator (vivo)
7. R1-2009656 FL summary#3 of power saving for Active Time RAN1#103-E Moderator (vivo)
8. R1-2009804 FL summary#4 of power saving for Active Time RAN1#103-E Moderator (vivo)
9. R1-2101893 FL summary#1 of power saving for Active Time RAN1#104-E Moderator (vivo)
10. R1-2101894 FL summary#2 of power saving for Active Time RAN1#104-E Moderator (vivo)
11. R1-2106040 FL summary#1 of power saving for Active Time RAN1#105-E Moderator (vivo)
12. R1-2106041 FL summary#2 of power saving for Active Time RAN1#105-E Moderator (vivo)
13. R1-2108224 FL summary#1 of power saving for Active Time RAN1#106-E Moderator (vivo)