3GPP TSG-RAN WG1 Meeting #106-e R1-21xxxxx

e-Meeting, 16th – 27th August, 2021

Agenda Item: 8.2.3

Source: Moderator (Ericsson)

Title: FL Summary #3 for [106-e-NR-52-71GHz-03] Email discussion/approval on enhancements for PUCCH formats 0/1/4

Document for: Discussion, Decision

# 1 Introduction

This document summarizes the contributions made under the “Enhancements for PUCCH Formats 0/1/4” agenda item of the Rel-17 work item "Supporting NR from 52.6GHz to 71 GHz."

The following email thread is assigned for discussion of this topic:

[106-e-NR-52-71GHz-03] Email discussion/approval on enhancements for PUCCH formats 0/1/4 with checkpoints for agreements on August 19, 24, 27 – Steve (Ericsson)

The following is an outline of the summary:

2 Maximum Number of RBs for Enhanced PF0/1/4 AGREEMENT

3 Configuration of Number of RBs AgreementS

4 Sequence Construction for Enhanced PF0/1 AGREEMENT

5 RE Mapping for Enhanced PF0/1/4 for 120 kHz SCS AGREEMENTs

6 Payload Limitation and Rate Matching for PF4

6.1 Maximum UCI Payload for PF4 Conclusion

6.2 Rate Matching for PF4 Agreement

7 PUCCH Resource Set Prior to RRC Configuration

7.1 Indication of Number of RBs Agreement

7.2 PUCCH Resource Set Construction Proposal

# 2 Maximum Number of RBs for Enhanced PF0/1/4

The following agreements were made in RAN1#104bis-e:

Agreement:

* The maximum values for the configured number of RBs, NRB, for enhanced PF0/1/4 are at least:
  + 12 RBs for 120 kHz SCS
  + 3 RBs for 480 kHz SCS
  + 2 RBs for 960 kHz SCS
* FFS: Whether or not the above values need to be revised to support larger values (and any associated signaling impact), e.g., to support lower UE Tx beamforming gain and/or larger UE EIRP and conducted power limits for different UE power classes, different from those in the agreed evaluation assumptions

Agreement:

For addressing the FFS from the prior agreement in RAN1#104bis-e on the maximum values for the configured number RBs, send an LS to RAN4 asking for feasible maximum values for UE\_EIRP and UE\_P for operation in 52.6-71 GHz.

RAN4 has sent reply LS that summarizes the discussion that has occurred so far within RAN4 [4] on UE power classes. RAN4 has provided the following answer to RAN1's question in the original LS [1]:

**Answer**

RAN4 can confirm that the current regulatory limits, i.e. max EIRP and max TRP, are higher than the above values**.** We further note that for the 52.6 to 71 GHz frequency range, regulations in some regions also specify a maximum spectral power density (EIRP).

Regarding what minimum peak EIRP value RAN4 will specify for a power class in this frequency range, it is premature to answer at this stage. RAN4 will continue discussing the power classes, reference UE type, antenna array size, and design considerations to make this assessment. While power class performance is TBD, an FWA UE is expected to yield the highest minimum peak EIRP, and it may be specified around 25 dBm or higher. However, further study is needed to confirm this and provide an exact minimum peak EIRP value. A power ranging from minimum peak EIRP to below the regulatory maximum EIRP limit, is technically valid for the UE to transmit out.

For additional context, different radiated output powers in 38.101-2 are summarized in the table below. Please note that there are no conducted output power requirements defined for FR2 in RAN4 specifications.

**Table 1.** FR2 minimum peak EIRP requirements

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Power class** | **Max TRP**  **[dBm]** | **FR2 band** | **Min peak EIRP**  **[dBm]** | **Max EIRP**  **[dBm]** |
| Power class 1  Fixed wireless access UE | 35 | n257/n258/n261 | 40.0 | 55 |
| n260 | 38.0 |
| Power class 2  Vehicular UE | 23 | n257/n258/n261 | 29.0 | 43 |
| Power class 3  Handheld UE | 23 | n257/n258/n261 | 22.4 | 43 |
| n260 | 20.6 |
| n259 | 18.7 |
| n262 | 16.0 |
| Power class 4  High-power non-handheld UE | 23 | n257/n258/n261 | 34.0 | 43 |
| n260 | 31.0 |
| Power class 5  Fixed wireless access UE | 23 | n257 | 30.0 | 43 |
| n258 | 30.4 |

TRP: Total **Radiated** Power

EIRP: Effective Isotropic **Radiated** Power

Considering RAN4 is in the early stages of our discussions, this is the information we can provide at this time. Further guidance will be provided as power class discussions progress in RAN4.

The main open issue is whether or not the maximum number of RBs should be increased beyond the agreed values of 12/3/2 for 120/480/960 kHz SCS accounting for the above feedback from RAN4.

The following table provides a summary of company proposals on this topic.

|  |  |
| --- | --- |
| **Company** | **Company Proposals** |
| Intel | **Proposal 1: The maximum number of PRBs over which a PUCCH format 0/1/4 may span is increased based on either option 1 or option 2:**   * **Option 1: The maximum number of PRB is at least 40 RBs, 18 RBs, 8 RBs for 120, 480, and 960 kHz SCS, respectively.** * **Option 2: The maximum number of PRB is at least 20 RBs, 12 RBs, 4 RBs for 120, 480, and 960 kHz SCS, respectively.**   **Proposal 3: RAN1 should discuss a proper framework to implicitly or explicitly indicate the UE’s beamforming gain to the gNB.** |
| Futurewei | ***Proposal 1. Support 22RBs/6RBs/3RBs as the maximally allowed numbers for 120kHz/480kHz/960kHz for enhanced PF0/1/4 of B52/FR2-2, taken the RAN4 feedback further into account.*** |
| vivo | **Proposal 1: When considering the revision of maximum number of RBs, the coverage (maximum isotropic loss (MIL)) should be mainly considered, including the RE mapping method, sequence type, UE power class.** |
| CATT | **Proposal 1 Two sets of maximum values for enhanced PF0/1/4 can be used. The smaller set can be used to reduce the bandwidth, while the larger set can be used to improve the coverage.** |
| ZTE | **Proposal 1: The maximum number of PRBs can be 32 according to the regulation requirements of majority regions with compatibility.** |
| NTT DOCOMO | ***Proposal 1:*** *For the evaluation assumption of enhanced PUCCH format 0/1/4, larger maximum UE conducted power/EIRP should be considered as additional assumption in RAN1.* |
| Nokia | ***Proposal 1:*** *The maximum values for the configured number of RBs for enhanced PF0/1/4 are either the agreed 12/3/2 RBs for 120/480/960 kHz SCS or are extended to 16 RBs and 4 RBs for 120 and 480 kHz SCS, respectively.* |
| Apple | ***Proposal 1: T****he maximum value of N\_RB should be based on the currently agreed values of 12, 3 and 2 for 120 kHz SCS, 480 kHz SCS and 960 kHz SCS respectively.* |
| LGE | **Proposal #4: Although UE types with larger EIRP in RAN4 LS reply are considered, it is not necessary to increase the NRB values larger than 12/3/2 for 120/480/960 kHz SCS, respectively.** |
| OPPO | **Proposal 1: When EU regulatory power limit is applied, the maximum values for the configured number of RBs, NRB, for enhanced PF0/1/4 are:**   * **32 RBs for 120 kHz SCS** * **8 RBs for 480 kHz SCS** * **4 RBs for 960 kHz SCS**   **Proposal 2: When US regulatory power limit is applied, the maximum values for the configured number of RBs, NRB, for enhanced PF0/1/4 are:**   * **32 RBs for 120 kHz SCS** * **8 RBs for 480 kHz SCS** * **4 RBs for 960 kHz SCS**   **Proposal 3: When South Korea regulatory power limit is applied, the maximum values for the configured number of RBs, NRB, for enhanced PF0/1/4 are:**   * **16 RBs for 120 kHz SCS** * **4 RBs for 480 kHz SCS** * **2 RBs for 960 kHz SCS** |
| Samsung | **Proposal 1: RAN1 can consider up to 16 PRBs as maximum number of PRBs, considering UE\_P can be larger than 21dBm but much smaller than 25 dBm min peak EIRP provided by RAN4.** |
| Huawei | ***Proposal 1: The maximum number of PRBs for the PUCCH is:***   * ***For 120 kHz SCS: 32*** * ***For 480 kHz SCS: 8*** * ***For 960 kHz SCS: 4*** |
| Interdigital | ***Proposal 1:*** *It is preferred to hold the discussion on max(NRB) until receiving RAN4’s response on the LS.*  Moderator note: Note that RAN4 has provided a response (see [1]); however, given that discussions in RAN4 are still at an early stage, the LS reply is not conclusive on all questions asked by RAN1. |
| Ericsson | **Proposal 7 RAN1 should wait for further feedback from RAN4 on feasible pairs of (UE\_EIRP, U\_P) values for the 52.6 – 71 GHz band before concluding on whether or not to increase the maximum number of RBs beyond 12 / 3 / 2.** |

The following extract from [8] summarizes the the reply LS from RAN4 quite succinctly (the moderator has done some light editing to provide context):

According to reply LS from RAN4 [1], RAN4 can confirm the regulatory limits max EIRP and max TRP can be higher than the values quoted in the RAN1 LS (i.e., UE\_EIRP = 25 dBm and UE\_P = 21 dBm), and min peak EIRP for FWA UE can be around 25dBm. A power ranging from minimum peak EIRP to below the regulatory maximum EIRP limit, is technically valid for the UE to transmit out. Therefore, UE\_EIRP can be larger than 25dBm. Regarding UE\_P, RAN4 does not specify requirement for conducted power, but TRP may be used as a reference for determining UE\_P. min peak EIRP is typically much larger than TRP due to antenna gain. Therefore, with 25dBm min peak EIRP for FWA UE in 60GHz, TRP is most likely smaller than 25dBm, though it can be higher than 21dBm.

Maximum transmission power is limited by the minimum of (UE\_EIRP-Tx BF gain) and (UE\_P-CM), the transmission power is limited by UE\_P rather than UE\_EIRP when UE\_EIRP is above a threshold, e.g. > 25dBm. As analysed above, UE\_P may be slightly larger than 21 dBm.

To add to this, some companies have observed that the required number of RBs scales inversely with the Tx beamforming gain (TxBF), and thus RAN1 should use TxBF = 0 dBi (instead of 6 dBi) as a "worst case" for defining the number of RBs. So, if either TxBF = 0 dBi and/or RAN4 agrees on a min Peak EIRP > 25 dBm, it will most likely be the conducted power UE\_P that is the limiting factor, not EIRP.

Based on the analytical expressions for the required PUCCH bandwidth discussed in RAN1#104bis-e (see FL summary [2]), if EIRP is not limiting then the number of RBs as a function of UE\_P and CM is given as follows (table extracted from [11] where CM = 2 is assumed), where the values in red are what have been agreed so far in RAN1:

Table 5: Maximum number of RBs as a function of

|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **x** | **19** | **20** | **21** | **22** | **23** | **24** | **25** | **26** | **27** |
| **120 kHz** | 12 | 14 | 18 | 22 | 28 | 35 | 44 | 56 | 70 |
| **480 kHz** | 3 | 4 | 5 | 6 | 7 | 9 | 11 | 14 | 18 |
| **960 kHz** | 2 | 2 | 3 | 3 | 4 | 5 | 6 | 7 | 9 |

Hence, the open issue to discuss is whether or not RAN1 should consider a value of UE\_P larger than 21 dBm (x = 19 assuming CM = 2) for defining the maximum number of RBs. Some companies observe that further feedback from RAN4 on power classes (including TRP/EIRP definitions) may come too late in the WI for RAN1 to do anything about it, hence RAN1 should decide on a reasonable value now.

Multiple companies have also pointed out that it is the US regulatory region that requires the largest number of RBs, and the above table assumes this.

Based on various combinations of the above observations, companies have provided the following candidate values for the maximum number of RBs:

* 40 / 18 / 8 (Intel, Option 1)
* 32 / 8 / 4 (OPPO, Huawei)
* 32 / ? / ? (ZTE)
* 28 / 7 / 4 (CATT, assuming CM = 2 dB)
* 22 / 6 / 3 (Futurewei)
* 20 / 12 / 4 (Intel, Option 2)
* 16 / 4 / 2 (LGE)
* 16 / 4 / ? (Nokia)
* 16 / ? / ? (Samsung)
* 12 / 3 / 2 (Apple, LGE)

Given the rather wide spread of proposals, clearly further discussion is needed.

**Proposal 1 Further discuss potentially increasing the maximum number of RBs above the current agreed values 12 / 3 / 2 for 120 / 480 / 960 kHz SCS, respectively.**

## 2.1 <1st Round Comments>

Please provide your company view on Proposal 1 including answering the following questions to help focus the discussion.

**Question 1**: Based on the RAN4 LS reply, is it your understanding that a limit on UE\_P will primarily determine the maximum number of RBs rather than a limit on UE\_EIRP?

**Question 2**: For determining the maximum number of RBs, should RAN1 consider an additional combination of (UE\_EIRP, TxBF) values other than what has been considered so far (25 dBm, 6 dBi)? If yes, then what combination of values should be considered?

**Question 3**: For determining the maximum number of RBs, should RAN1 consider an additional value of UE\_P > 21 dBm? If yes, then what value should be considered?

**Question 4**: Should RAN1 try to conclude now on a maximum values for N\_RB or wait for further RAN4 feedback?

|  |  |
| --- | --- |
| **Company** | **View/Position** |
| Nokia, NSB | Although increasing the number of PRBs in principle increases PUCCH coverage, we are not convinced that the coverage of PUCCH formats 0/1/4, which are used for transmitting only a limited number of bits, is necessarily the bottle neck for system coverage at 60 GHz spectrum. Therefore one should be cautious in increasing the number of PRBs beyond of what is already agreed. |
| vivo | We are okay with proposal 1.  Q1: In the FL summary, it mentioned that “some companies have observed that the required number of RBs scales inversely with the Tx beamforming gain (TxBF), and thus RAN1 should use TxBF = 0 dBi (instead of 6 dBi) as a "worst-case" for defining the number of RBs.” We don’t agree with this argument as it may work for some region(s). The regulation power limits by region are listed in Tables 3 in Section 2.3 of R1-2102127 in the RAN1 104-e meeting. In the US, the TxBF affects the Pmax\_EIRP; while in South Korea, the TxBF affects the Pmax\_EIRP and Pmax\_PSD. Specifically, the number of RBs scales in direct ratio with TxBF in South Korea when the UE\_EIRP is above a threshold. For example, when UE\_EIRP = 25dBm, UE\_P = 21dBm, TxBF = 6dBi, CM = 2, the maximum number of RBs is 12 for SCS 120kHz, while 14 with TxBF changes to 7dBi in the South Korea and no change in the US. However, the maximum number of RBs would not change if the TxBF changes to 0dBi without any other assumption change in all regions.  So, it is not correct to always assume the UE\_P will primarily determine the maximum number of RBs rather than a limit on UE\_EIRP without a specific value.  Q2 and Q3: in our opinion, the additional combination of (UE\_EIRP, TxBF, UE\_P) values can be (43, 6, 23) at least as in existing FR2 if we do not want to wait for RAN4’s further reply.  Q4: we prefer not to wait for further RAN4 feedback if later than the next meeting to make a decision. |
| ZTE, Sanechips | We are fine with Proposal 1.  A1: Yes. We share similar understanding with FL on RAN4’s reply LS.  A2: Yes. TxBF should be 0dBm.  A3: Yes. 25 dBm could be considered.  A4. RAN1 should try to make a consensus on additional value set of {UE\_EIRP, UE\_P, TxBF}, if no consensus, RAN1 could wait for further RAN4 feedback. |
| Huawei/HiSilicon | We think a decision can be made in RAN1 given the current status of RAN4. The impact of different number of PRBs to the design of the PUCCH is not critical, since the PUCCH formats are/will be scalable in bandwidth. Thus, the same signal structure could be used regardless of the selected maximum values for the PRBs. RAN1 should choose a set of values (larger than 12/3/2), e.g., 32/8/4, that allows sufficient flexibility for the deployment and to guarantee future proofness. |
| Lenovo, Motoroloa Mobility | We tend to agree with Nokia that the number of PRBs should not be increased beyond what is already agreed.  We are also okay to wait for RAN4 feedback before making the final conclusion |
| Apple | We are fine with Proposal 1. Given that TRP can be used as a proxy for UE\_P and given the values of TRP indicated by RAN4, we can consider using the values of TRP in the estimation of N\_RB.  Q1: Yes. Agree with the FL  Q2: RAN1 should consider additional values of UE\_EIRP to account for different UE power classes. From our analysis, at least for the United States, the maximum number of RBs is invariant with a change in the TxBF. We can use 0 dB as a reference.  Q3: RAN1 can consider additional values of UE\_P. Given the use of TRP as a proxy for UE\_P, we can set it to 23 dBm.  Q4: RAN1 should make a decision given that we have only a few meetings left to complete the design. There are some decisions that are contingent on this one. |
| Intel | * Q1: Our understanding is indeed that UE\_P may be dominat on UE\_EIRP to determine the number of PRBs * Q2: As shown by the analysis conducted by companies, the maximum number of PRBs increases as the UE’s TX beamforming gain decreases, so RAN1 should consider 0 dBi for UE’s TX beamforming gain as worse case. As for the UE\_EIRP, since FR2-1 would be used as a baseline for RAN4 to define new UE power classes, the highest EIRP supported in FR2-1 should be supported which corresponds to 55 dBm for UE power class 1, which is devoted to fixed wireless, which should be also supported for above 52.6 GHz. So to answer the initial questions (0 dBi, 55 dBm) should be considered. * Q3: In our opinion 21 dBm is very limitative as UE’s output power, and a larger value should be supported as a worse case scenario used to identify the highest number of PRBs to support. As indicated above, our understanding is that RAN4 would use FR2-1 as a baseline to define power classes for FR2-2. For this reason the highest supported TRP should be considered, which correspond to 35 dBm for UE power class 1. * Q4: our understanding from RAN4 reply is that it may be quite challenging for RAN4 to conclude on the definition of power classes for above 52.6 GHz carrier frequency soon, and it is more likely that RAN4 will conclude after the conclusion of this WI. Since this topic may be a bottleneck for progress within this WI, we believe that RAN1 should try to convey assuming the worse case scenarios and FR2-1 as a baseline. |
| CATT1 | For Q1 we don’t think a limit on UE\_P will primarily determine the maximum number of RBs rather than a limit on UE\_EIRP.  Q2: additional combination is needed  Q3:additional value is needed  Q4:we can always try to reach some consensus in ran1. If failed then may be we need to wait for ran4. |
| Sony | We are okay with proposal 1.  Q1: We have similar views to Vivo, i.e., both UE\_P and UE\_EIRP can be the limiting factor that determines the maximum number of RBs, depending on the region and assumed values of UE\_EIRP, UE\_P and TxBF.  Q2: We are open to consider new values of (UE\_EIRP, TxBF). However, given that RAN4 has not suggested specific values in its reply to RAN1 LS, it is not clear how new values of (UE\_EIRP, TxBF) should be selected. For example, it does not seem reasonable to set TxBF=0 dB as a worst case. The value TxBF=6 dBi already accounts for gain losses, compared to the # antenna elements expected at 60 GHz.  Q3: Again, it is not clear from RAN4’s response how a new value of UE\_P should be selected.  Q4: Consensus on maximum values of N\_RB should be attempted. If not reached, we are okay to wait for the conclusion of ongoing RAN4 discussions on power classes for the 52.6 to 71 GHz frequency range. |
| NTT DOCOMO | Question 1: It depends on the assumed value of TxBF. For example, when UE\_EIRP is much larger than UE\_P, UE\_P can be the limitation to deterrmine the maximum number of RBs even when considering TxBF. As discussed above, if TxBF=0 dBi is assumed as “the worst case” and assuming that minimum peak EIRP is around 25 dBm, actual transmit power is limited to UE\_P. On the other hand, as shown in the reply from RAN4, both minimum and maximum value are specified for EIRP, while only maximum value for TRP, for FR2. Assuming similar specification will be constructed for FR2-2, we believe it would be necessary to consider power ranging regarding EIRP as well as regional regulation. With that, we think UE\_EIRP may need to be considered to determine the maximum number of RBs.  Question 2: If UE\_P is regarded as the limiting factor, additional values for (UE\_EIRP, TxBF) are not needed.  Question 3: Yes. 25 dBm can be the candidate value of additional assumption for UE\_P.  Question 4: Yes. Considering the remaining RAN1 meetings, RAN1 should discuss the additional assumption. And then, if RAN1 gets additional information for UE\_P and UE\_EIRP of 52.6-71 GHz band from RAN4, these values would be considered. |
| Qualcomm | We are fine with proposal 1. Given the reply from RAN4, while there are no concrent final numbers for the band, we need to discuss to further increase the maximum number of RBs.  For questions listed by FL, please see our response below:  A1: Yes, we share same view as FL  A2&A3: Yes, additional (UE\_EIRP,TxBF, UE\_P) should be considered, like proposed optional combination (40,6, 23)  A4: If companies may agree on an additional sets of EIRP/TxBF/UE\_P during this meeting, then we may decide the maximum value of RBs during this meeting, or RAN1 may wait until next meeting to make the decision. |
| Samsung | Q1: Yes, we share same view as FL.  Q2 & Q3: We’re open to additional value, if companies can prove the suggested value is a reasonable range which can be implementated. We don't think it is feasible to assume the product really achieves maximum values which is defined according to the regulation, actually, companies make great effort to achieve the minimum value, e.g. minmum EIRP in FR2 in Rel-15/16, and TRP achieved by the product is also smaller than maximum TRP.  Q4: We can try to reach some consensus in RAN1 first. |
| OPPO | Q1: From our understanding, the max number of RBs is determined by the maximum allowed transmission power, which is further determined by max UE\_P and max UE\_EIRP. The max UE\_P shoud at least be in line with max TRP given by RAN4. The max UE\_EIRP should follow regulation rules.  Q2: at least for UE\_EIRP, the value should not be limited to 25dBm. It should follow regulation allowed value.  Q3: Yes, UE\_P should follow the max TRP value provided by RAN4, which is 35dBm.  Q4: from RAN4 LS, it is clear that there is no technical evidence to support (UE\_EIRP = 25 dBm and UE\_P = 21 dBm). |
| LG Electronics | We are fine with Proposal 1 and we added our preferred candidate values (16/4/2 for 120/480/960kHz SCS) for the maximum number of RBs in the above summary. It may be not necessary to increase the NRB values larger than 12/3/2 considering the UE types with larger EIRP in RAN4 LS reply. However, both UE\_P and UE\_EIRP can be the limiting factor, and TxBF and CM should also be considered to determine the maximum number of RBs. Therefore, 16 RBs can be seen as an upper limit for the enhanced PUCCH format 0/1/4 with 120 kHz SCS since up to 16 RBs can be allocated for PUCCH format 2 and format 3. |
| Futurewei | We are ok with Proposal 1.  Q1: Yes. Given the RAN4 feedback, UE\_P will primarily determine the maximal RB.  Q2&Q3: Additional values can be considered for (UE\_EIRP, TxBF) and UE\_P. However, since additional values will only lead to more feasible sets of RBs, it is more important to think about which of these values should be taken as the primary value that determines the maximal number of RB for possible convergence.  Q4. Since RAN4 feedback is ready and this issue has been discussed for a couple of meetings. It is better to conclude the issue by this meeting. |

## 2.2 <Summary of 1st Round>

There seems to strong support for RAN1 to make a decision now, since it could take too long for RAN4 to provide additional responses on UE power class definitions. The moderator observes that there is still a wide range of proposals on what UE\_EIRP, TxBF, UE\_P values to assume, and a wide range in proposals on potential values for the number of RBs. This is a bit problematic, since there are other decisions that can depend on the number of RBs. On the one hand some companies prefer to stay with the existing number of RBs that has been ageed so far (12 / 3 /2). On the other hand, some companies propose supporting a very large number of RBs by assuming, e.g., (UE\_EIRP, TxBF, UE\_P) = (55, 6, 35). The moderator points out that UE\_P = 35 dBm exceeds the maximum 27 dBm value in at least the US regulatory region.

It seems some compromise will be needed, and values somewhere in the middle would be the most likely compromise. For the sake of progress, perhaps it is better to try to agree directly on the number of RBs. To try and facilitate some convergence, the following proposal is made, and hopefully we can converge on one of the alternatives in this meeting. The intention is to provide 3 options, one at the lower end, one at the higher end (not too high), and one in the middle. For the higher end option (Alt-3), very large number of RBs is not considered since it seems questionable why the coverage should be extended so much for PF0/1/4, while PF2/3 are not touched.

**Proposal 1a**

Down select to one of the following alternatives for the maximum configured number of RBs, NRB, for enhanced PF 0/1/4:

* Alt-1 (No change to what has been agreed so far)
  + 12 RBs for 120 kHz SCS
  + 3 RBs for 480 kHz SCS
  + 2 RBs for 960 kHz SCS
* Alt-2 (Maximum value for 120 kHz aligned with the maximum number of RBs for PF2/3 in Rel-16)
  + 16 RBs for 120 kHz SCS
  + 4 RBs for 480 kHz SCS
  + 2 RBs for 960 kHz SCS
* Alt-3 (Higher end option)
  + 22 RBs for 120 kHz SCS
  + 6 RBs for 480 kHz SCS
  + 3 RBs for 960 kHz SCS

## 2.3 < 2nd Round Comments>

Please provide your company view on Proposal 1a, and which alternative you prefer (1st choice and 2nd choice). The moderator strongly encourages that we down-select to one of the 3 alternatives in this meeting to unblock progress on other items.

|  |  |
| --- | --- |
| **Company** | **View/Position** |
| Moderator | Recommend to down-select to one of the alternatives during this meeting |
| Intel | Many thanks for the FL for yet another great summary.  While as discussed during the GTW we beleive that we should consider support for UE power class 1, we want to point out a few things based on our evaluations, which are based on thorogh study and simulations that we have conducted rather than analtical analysis, which neglects many important factors:   * the maximum number of PRBs would not increase further if the UE\_P is increased more than 27 dBm, since as the FL highlighed in US we are contrained up that maximum output power; * the EIRP has a negligible effect on the maximum number of PRBs, and after 30 dBm EIRP the maximum number of PRBs remains the same. * the beamforming gain is another factor that influences the number of PRBs, and the number of PRBs needed increases as the beamforming gain descreases.   With that said, we do not beleive we are very far off from each other in terms of assumptions, and we do not need to necessarily assume extreme cases for the evaluation of the maximum number of PRBs to support UE power class 1, and actually (UE\_EIRP, TxBF, UE\_P) = (30, 0, 27) should be OK with us.  Furthermore, we totally understand the concern of other companies that do not want to increase the number of PRBs much, and this is also one of our concerns as well.  However we want to point out two things:   1. the 16 PRBs constrain many companies are posing seems to be related to the maximum number of PRBs that PF 2/3 in Rel.16 supports, which is uncorrelated with the issue that we are solving in this WI and specifically in this AI since the KPI are very different here. Our understanding is that: a) we are not enhancing PFs 2/3, and b) we are enhancing because the transmit power is contrained due to regulatory restrictions, which we beleive are different goals than those set in Rel.16. 2. we notice that if we apply a 1% backoff on MIL a saving of nearly half the BW is possible, and this is why we proposed a compromised solution of 20, 12, 4 for 120, 480 and 960 Khz, which according to our evaluations (copied below) allows to support UE power class 1 and would not require us to update later on our decisions based on RAN4’s future discussions, when UE power class 1 is likely to be defined.  |  |  | | --- | --- | | **120 kHz SCS** | A picture containing LEGO, toy  Description automatically generated | | **480 kHz SCS** | Diagram  Description automatically generated | | **960 kHz SCS** | Chart, diagram  Description automatically generated |   With that said, we would be very glad if companies would consider the following set of values:   * 20 RBs for 120 kHz SCS * 12 RBs for 480 kHz SCS * 4 RBs for 960 kHz SCS   As for the alternatives proposed by the FL, none of them would be OK based on our evaluation, and even Alt.3 would fall short for 480 KHz SCS since at minimum 12 RBs would be needed. |
| Nokia, NSB | From our point of view we are ok with either Alt-1 or Alt-2. As for comparision with PF 2/3, we note that the same restrictions wrt TX power apply there too, and therefore it is logical to align the max number of PRBs (and TX power). |
| Futurewei | We support Alt-3. Companies seem to have different power values in mind and/or evaluation methods, but we think that at this stage of the WI, it is better to follow the analytical expressions for the required PUCCH bandwidth discussed in RAN1#104bis-e and focus on RB values given in table 5 in this summary. The other value in the middle of table 5, (28, 7, 4), is also acceptable besides Alt-3. |
| vivo | We support Alt-1. |
| Lenovo, Motoroloa Mobility | We prefer Alt-1, but we are fine with Alt-2 if majority of companies agree. |
| Huawei/HiSilicon | We prefer Alt-2 or Alt-3. |
| LG Electronics | We support Alt-2. As we mentioned before, 16/4/2 RBs for 120/480/960 kHz SCS can be adopted as an upper limit for the enhanced PF0/1/4 considering the maximum number of RB for PF2/3 in Rel-16. |
| NTT DOCOMO | Our preference is Alt-2 or Alt-3. |
| Samsung | We prefer Alt-1 or Alt-2 with the following reasons:   1. For transmission power, we don't think UE\_P for 60GHz can be up to 27dBm. For Power class 1 defined in FR2, maximum TRP is much smaller than minimum EIRP. The same relation is hold for FR2-2. Therefore, maximum TRP is much smaller than27 dBm. Futhermore, maximum TRP and maximum EIRP is defined according to the regulation for maximum power and UL co-channel interference constraint. But the feasible values for TRP and EIRP is typically much smaller. For example, feasible TRP is similar to Total conducted power defined in 38.817 TABLE 7.2.1.1.1-1 for power class 1 in FR2. It can be seen Total conducted power is used to derive minimum EIRP, and it is much smaller than minimum EIRP. Therefore, we believe Total conducted power and feasible TRP (which can be a proxy for UE\_P in RAN1) for 60GHz is much smaller than 25dBm. 2. For the relation between PF2/3 and PF 0/1/4, We share similar view with Nokia. |
| ZTE, Sanechips | We prefer Alt3, and we can also accept Alt2 as a compromise. |
| Qualcomm | We prefer Alt-2 as we share similar view with Samsung and Nokia on the relation between PF2/3 and PF 0/1/4. |
| Sony | We prefer Alt1 or Alt2. |
| Apple | We are fine with Alt-1 or Alt-2 |

## 2.4 <Summary of 2nd Round>

The following is a summary of company support for the 3 alternatives in Proposal 1a.

* Alt-1: (12 / 3 / 2)
  + Nokia/NSB, vivo, Lenovo/MotMob(1st choice), Samsung, Sony, Apple
* Alt-2: (16 / 4 / 2)
  + Nokia/NSB, Lenovo/MotMob(2nd choice), Huawei/HiSilicon, LGE, NTT DOCOMO, Samsung, ZTE(2nd choice), Qualcomm, Sony, Ericsson, Apple
* Alt-3: (22 / 6 / 3)
  + Futurewei, Huawei/HiSilicon, NTT DOCOMO, ZTE (1st choice), Ericsson
* Other:
  + Intel (20 / 12 / 4)
  + Futurewei (28 / 7 / 4)

The moderator thanks companies that have compromised. It apeears as though Alt-2 has the most support and it includes companies that have a 1st choice for a lower number of RBs and companies that have a 1st choice for a larger number of RBs. Taking these compromise views into account, the summary of support looks like the following, assuming that Futurewei is okay with Alt-3.

* Alt-1: (12 / 3 / 2)
  + vivo
* Alt-2: (16 / 4 / 2)
  + Nokia/NSB, Lenovo/MotMob, Huawei/HiSilicon, LGE, NTT DOCOMO, Samsung, ZTE, Qualcomm, Sony, Ericsson, Apple
* Alt-3: (22 / 6 / 3)
  + Futurewei
* Other:
  + Intel (20 / 12 / 4)

Several companies have observed that Alt-2 makes sense from the perspective of trying to align with the maximum number of RBs for PF2/3. Based on this observation, and based on the technical analysis that has shown that Alt-2 (and even Alt-1) already offers a large improvement in coverage for PF0/1/4 compared to Rel-15/16, it seems as though Alt-2 should be the way forward considering the majority view. This issue has been discussed for a long time, and a decision is needed to unblock other issues.

**Proposal 1b**

The maximum configured number of RBs, N\_RB, for enhanced PF 0/1/4 is given by the following

* 16 RBs for 120 kHz SCS
* 4 RBs for 480 kHz SCS
* 2 RBs for 960 kHz SCS

## 2.5 <3rd Round Comments>

Please provide your company view on Proposal 1b

|  |  |
| --- | --- |
| **Company** | **View/Position** |
| Intel | We are not OK with the proposal. As mentioned, and as our detailed analysis shows, these values would be quite restrictive and won’t be enough to support UE power class 1 for higher SCSs, and specifically we are challenging the 4 RBs and 2 RBs for 480 and 960. Basically, we do not wish to artificially limit potential coverage and performance of both eMBB and fixed wireless services simply because companies did not have a chance to account for them in the study. All the functionality to support various PRB sizes will be there. These values will be used for common PUCCH as well, which will not be possible to update in future releases. So we think it is extremely important that we are not too conservative with this numbers.  For the proponents of Alt 2, we would like to ask what aspect our analysis provided in our Tdoc, R1-2107579, which is thought to be incorrect or maybe with wrong assumptions. We think it would be important to first understand why companies come to different conclusions. For example, it could be that some companies did not consider other UE power class intended for fixed wireless systems or something else. If we can be convinced that various UE power classes that may be supported for this band is not negatively impacted, we would be ok to accept. At the moment, we were not convinced that this was the case.  With that said, if companies are feeling strong about smaller numbers, as a potential compromise, we would be ok to accept the number for 120kHz, but we strongly suggest to keep higher numbers for 480kHz and 960kHz, i.e. (16, 12, 4) |
| vivo | We are okay with proposal 1b |
| LG Electronics | We agree with Moderator and support Proposal 1b. We can tentatively decide on the maximum configured number of RBs as 16/4/2 to move forward with other relevant discussions, and then we'll revisit if the need for a larger values is identified. |
| NTT DOCOMO | We are fine with Proposal 1b. |
| Nokia, NSB | We support Proposal 1b |
| Lenovo, Motoroloa Mobility | We support Proposal 1b. |
| OPPO | We agree with Intel. |
| Apple | We are fine with the proposal. |
| Qualcomm | We are OK with the proposal |
| Sony | We can support proposal 1b. |
| Huawei/HiSilicon | We are fine with Proposal 1b. |
| ZTE, Sanechips | We are fine with the proposal. |
| Futurewei | We are ok with Proposal 1b if not to revisit larger value is the best decision taken into all technical considerations. We appreciate that the FL had captured the recommendation we have with our contribution R1-2107099 into one alternative in Proposal 1a for down-selection.  The main reason that we slightly prefer one middle value in table 5 of the summary over the values 16/4/2 for PF2/3 is that although 16/4/2 RBs can be a convenient choice by reusing the legacy values, table 5 already has good sets of reference values to choose from to achieve better coverage. |
| CATT | We are OK with the proposal |
| Samsung | We support Proposal 1b. |

## 2.6 <Summary of 3rd Round>

* Support Proposal 1b
  + vivo, LGE, NTT DOCOMO, Nokia/NSB, Lenovo/MotMob, Apple, Qualcomm, Sony, Huawei/HiSilicon, ZTE/Sanechips, Futurewei, CATT, Samsung
* Do not support Proposal 1b
  + Intel, OPPO

The moderator appreciates the good technical discussion on this topic. While there is very strong support for Propsal 1b, at least one company has very strong concerns about this proposal and would like to see that larger values are considered. Already quite a few companies have compromised by supporting Proposal 1b when they would have preferred smaller values. The moderator's recommendation is to discuss a bit more on-line, but a decision is needed, preferably in this meeting to unblock other issues. Hence let's discuss Proposal 1c in the GTW.

In Proposal 1c, Alt-2 is a compromise from Intel that keeps the same maximum value for 120 kHz as Alt-1 but increases the values for 480/960 kHz. For the case of 480 kHz, the moderator has placed two options in square brackets [6 and 12] where the proposed value from Intel is 12. The value 6 is the value from Alt-3 which is now removed. The reason for the square brackets is due to the moderator's concern that the number for 480 kHz seems abnormally large. Intel explained that there can be an issue if the PUCCH bandwidth exceeds the coherence bandwidth of the channel; thus, the detection performance suffers, and more RBs are needed. However, if this was the explanation, it seems that both 120 kHz and 960 kHz would be affected as well. Moreover, as Intel described, the evaluation assumption is 5 ns delay spread, which should lead to approximately 200 MHz coherence bandwidth. 12 RBs at 480 kHz is only 69 MHz, i.e., far less than the coherence bandwidth.

**Proposal 1c**

The maximum configured number of RBs, N\_RB, for enhanced PF 0/1/4 is given by the following (down-select this meeting to one alternative):

* Alt-A
  + 16 RBs for 120 kHz SCS
  + 4 RBs for 480 kHz SCS
  + 2 RBs for 960 kHz SCS
* Alt-B
  + 16 RBs for 120 kHz SCS
  + [6 or 12] RBs for 480 kHz SCS
  + 4 RBs for 960 kHz SCS
* Note:
  + Alt-A corresponds to the previous Alt-2
  + Alt-B corresponds to the previous Alt-3 and Intel compromise proposal

Thank-you for the good discussion. The following was agreed in the GTW:

Agreement:

The maximum configured number of RBs, N\_RB, for enhanced PF 0/1/4 is given by 16 RBs for 120 kHz SCS

This still leaves open the maximum number of RBs for 480 and 960 kHz. The following alternatives have been discussed, and maybe we can try to narrow down a bit by the next check point. The moderator's recommendation is to try hard to conclude on this topic during this meeting.

The main difference between the proposals is for the case of 480 kHz. At least to the moderator, it is not clear why such a large number of RBs is needed for 480 kHz for Alt-3, but not for 120 and 960 kHz. If PUCCH detection performance due to narrow coherence bandwidth was the culprit, it seems like a large number of RBs would also be needed for both 120 and 960 kHz.

**Proposal 1d**

The maximum configured number of RBs, N\_RB, for enhanced PF 0/1/4 for 480 and 960 kHz is given by the following (down-select to one alternative in this meeting):

* Alt-A
  + 4 RBs for 480 kHz SCS
  + 2 RBs for 960 kHz SCS
* Alt-B
  + 6 RBs for 480 kHz SCS
  + 4 RBs for 960 kHz SCS
* Alt-C
  + 12 RBs for 480 kHz SCS
  + 4 RBs for 960 kHz SCS

## 2.7 <4th Round Comments>

Please provide your company view on Proposal 1d, and which alternative you prefer (1st choice and 2nd choice). Please also indicate if there is any alternative that you cannot accept. The moderator strongly encourages that we down-select to one of the 3 alternatives in this meeting, and also points out that even with a compromise solution, all 3 options still provide increased PUCCH coverage, even for larger power class UEs. At this point, the focus should maybe be "good enough."

|  |  |
| --- | --- |
| **Company** | **View/Position** |
| Intel | While Alt-C is preferred, we would also be OK to select for 480 KHz SCS any value between 10 and 12, and we would be OK to compromise to the following:   * + 10 RBs for 480 kHz SCS   + 4 RBs for 960 kHz SCS   Regarding the rationale why we need larger vales for 480 KHz and 960 KHz SCS, as explained during the GTW and over email, this is the result of our simulation study (R1-2107579), which accounts for a lot of factors which are neglected in the analytical evolution, which wrongly assumes that the required SNR and noise power effect would always cancel out between each other. As previously explained, while the detection algorithm is one of the components that plays a role in the need of larger number of PRBs there are many other factors which are not accounted within the analytical evaluation that play equally an important role (e.g., tradeoff among regulatory restrictions and UE power contrains, relationship between CM and BW, and relationship of the noise power with the BW).  The value of 12 PRB for 480kHz, was already a value that did NOT try to optimize MIL in the most extent possible, but it was a relaxed value that will met the maximum MIL within 1.45 dB. By supporting only 6 PRB will result in additional maximum MIL loss on top of this, which we think can potentially limit various use cases (eMBB and non-eMBB alike) and deployment scenarios for 60GHz.  Once again, we have requested more clarifications on what assumptions have been used by other companies to deduce that smaller values would not contrain UE power class intended for fixed wireless system, but so far we haven’t received any replies or clarifications. With that said, at the moment, we are still not convinced that this is the case, but we are willing to compromise to reasonable values which in our opinion may not be optimal, but may not greatly limit UEs in power class intended for fixed wireless system. |
| LG Electronics | We support Alt-A.  It is noted that Alt-A is supported by the majority of companies including the companies that showed flexibility. Moreover, if we adopt 16/4/2 as the maximum number of RBs for each subcarrier spacing, the same transmission power can be achieved considering the requirements of power spectral density and provide enough coverage.  If there is no consensus on Alt-A (i.e., 16/4/2), we prefer to support 16 RBs for all the subcarrier spacing (i.e., 16/16/16 RBs for 120/480/960kHz SCS) rather than Alt-B or Alt-C. |
| Nokia, NSB | We support Alt-A, which results in the same bandwidth for all SCS. We are not convinced that in any of the deployment scenarios of interest, transmitting HARQ-ACK of just a few bits is a bottle neck for the system operation, as the targeted data rates are expected to be fairly large. |
| NTT DOCOMO | We are open for any of the alternatives above. It seems Alt C could be a super set of the others. We would like to understand what is the technical issue on increaseing the number of RBs for 480/960 kHz SCS, i.e., why Alt-A or Alt-B should be selected. |
| ZTE, Sanechips | We prefer Alt A since it ensures the same bandwidth for different SCSs. We also accept that if we only define a maximum RB number(i.e. 16) which can be applied to all SCS similar as Format 2/3. |
| Apple | At this point, we feel that the focus should be on down-selecting to a set of numbers that can be explained in some way so either Alt-A (same BW) or settting all the SCSs to 16 may be good alteratives. This is under the assumption that ultimately, the gNB and UE will negotiate for the best values of N\_RB for the specific deployment scenarios, UE power class etc. |
| OPPO | Since the recent couple of meetings, the FP4 design becomes much clearer. And we found that the current design is very much similar to FP3. For this reason, we would like to ask whether we still need to set different max RB for different SCS. The reason why we raised this question (also in the last GTW) is that it seems that a UE shall still support FP3 for number RB up to 16 for all SCS. While for FP4 the UE will need different UE capability to support different Max RB for different SCS. Such UE capability constrain is not meaningful to us. Could any company please explain why a UE can support FP3 up to 16 but can only support FP4 up to N, where N<16, given that the design of FP4 is almost identical to FP3.  With our analysis, we believe even there is a UE capability for FP4, it should not be on the issue of number of RBs, but something else.  For the proposal, we think that a more reasonable wayforward is to support max RB=16 for any SCS. |
| Lenovo, Motoroloa Mobility | Although, we prefer Alt-A, but we agree with others that for the sake of progress, we can define a maximum number, e.g.,16 for all SCS. |
| Huawei/HiSilicon | Alt-A or Alt-B are reasonable to us. Having a single value, i.e., 16 RBs, for all subcarrier spacings is also possible. |
| Qualcomm | We share same view as Nokia and prefer Alt-A. 16/4/2 leads to same maximal bandwidth and same max tx power. |
| Futurewei | Similar to Intel, we think among the three alternatives, Alt B/Alt C can be seen as one alternative, which is slightly different from legacy; but for 480 kHz SCS, it doesn’t have to be 6RBs or 12RBs, since 8RBs or 10RBs would also work. Therefore, it may rather be a down-selection between two alternatives.  We are also seeking progress on this issue, especially after extensive simulation effort from many companies during several meetings, provided with the reference set of values in RAN1 and feedback from RAN4. It might have brought a little more of a sense of accomplishment (beyond just progress) at least for some of us if we do not have to be agreeing that we have to rely on the legacy values for PF1/3 when we have better values at hands, although it seems that reusing the legacy maximum hardly evitable at this stage of discussion.  Based on the thinking, we find LG’s idea to support 16 RBs for all the subcarrier spacing (i.e., 16/16/16 RBs for 120/480/960kHz SCS) as constructive advice, as agreed by plenty of other companies. While if PF1/3 has to be enhanced for FR2-2 in the future, there can be separate discussions over there and they might not even find 16RBs as the most appropriate maximum anymore given separate evaluation results by then. Therefore, it does not seem that the legacy is always the most convincing choice after accumulating sufficient technical materials, even if it remains the most convenient one. However, by adopting 16/16/16 RBs instead, we at least find some further room for MIL increment for 480kHz and 960kHz. |
| vivo | We only support Alt-A.  We have a comment regrading the proposal to adopt 16 for all SCS. Some companies argued that a large number is required for coverage (MIL) for 480 kHz and 960 kHz. First of all, we disagree with the stetament that increasing RB number will already improve the coverage. Actually, many contributions showed it’s not the case. During SI phase, there’s a lot of debate on the typical usage scenario for an SCS. It is well understood that 120 kHz being advocate as the one for coverage purpose. Now if we were to improve coverage for 480 and 960 kHz, why only 16 for 120 kHz? Shouldn’t we improve 120 kHz even more if following that logic?  On the argument that PF2/3 support 16, our understanding is that different PUCCH formats are designed to cover different scenario. The system has the flexibility selecting the bset format with consideration on all aspects. However, we don’t see why the same RB number should be kept among different PUCCH formats. The goal of this discussion is not to enhance PF4 to be same as PF2/3 so that one format can be removed. |
| Samsung | We prefer Alt-A.  We’re also ok with 16 PRBs for all SCS as suggested by many companies., with the understanding that gNB can configure a proper number between 1 to maximum (16) for difererent scenarios. |

## 2.8 <Summary of 4th Round>

A summary of company views is as follows:

* Alt-A
  + LGE (1st preference), NTT DOCOMO, ZTE/Sanechips, Apple, Lenovo/MotMob, Huawei/HiSilicon, Qualcomm, vivo (only Alt-A is acceptable), Samsung
* Alt-B
  + NTT DOCOMO, Huawei/HiSilicon
* Alt-C or compromise by supporting 10 RBs instead of 12 for 480 kHz
  + Intel, NTT DOCOMO,
* Alt-D: New alternative to support maximum 16 RBs for all SCSs (120 / 480 / 960 kHz)
  + LGE (if no consensus on other alternatives), ZTE/Sanchips, Apple, OPPO, Futurewei, Samsung, Lenovo/Motoroloa Mobility
* Rationale for Alt-A
  + Same maximum PUCCH bandwidth for all SCSs
  + PF0/1 for HARQ-ACK of 1 or 2 bits is not a coverage bottleneck
* Rationale for Alt-C
  + Support high power class (e.g., Power Class 1)
* Rationale for Alt-D
  + Consistent with maximum number of RBs as for PF2/3

Regarding Alt-B, it seems as though this alternative can be dropped. The two companies supporting it also support Alt-A. A new alternative (Alt-D) has emerged, based on some discussion in the GTW where 16 RBs is adopted as the maximum for all SCSs. I assume Intel would be supportive of Alt-D.

Hence, the moderator makes the following proposal:

**Proposal 1e**

The maximum configured number of RBs, N\_RB, for enhanced PF 0/1/4 is given by the following (down-select to one alternative in this meeting):

* Alt-A
  + 4 RBs for 480 kHz SCS
  + 2 RBs for 960 kHz SCS
* Alt-D
  + 16 RBs for 480 and 960 kHz SCS (same as for 120 kHz SCS)

## 2.9 <5th Round Comments>

Please provide your view on Proposal 1e including the following:

* Your 1st and 2nd preference
* If there is an alternative you cannot accept

The moderator's intention is to bring this up in the next GTW to see if we can make some progress.

|  |  |
| --- | --- |
| **Company** | **View/Position** |
| LG Electronic | As mentioned before, our 1st preference is Alt-A and 2nd preference is Alt-D. |
| Nokia, NSB | As commented before, we support Alt A |
| NTT DOCOMO | Our 1st preference is Alt-D and 2nd preference is Alt-A. |
| ZTE, Sanechips | Our 1st preference is Alt-D and 2nd preference is Alt-A. |
| Sony | Our first preference is Alt-A. Our second preference is Alt-D. |
| OPPO | Our first preference is Alt-D.  To make a progress on this topic, may I suggest another Alt-E  Alt-E:  For PF0/1: Alt-A  For PF4: Alt-D.  Our concern as explained in the 4th round discussion is on PF4, according to the current agreements, the design is very much similar to PF3. We are not convinced why PF3 can be flexibly configured by the network up to 16. While PF4 is has restrictions w.r.t. different SCS. This is quite weird. |
| Lenovo, Motoroloa Mobility | We prefer Alt-A, but are also fine with Alt-D for the sake of consensus. |
| Huawei/HiSilicon | First preference is Alt-D, second preference is Alt-A. |
| vivo | We support Alt-A.  We are not convinced on the argument to support Alt-D. PF4 and PF3 are designed for different purpose from the beginning of Rel-15 NR. Other than arbitrarily select the same number of RB 16 for PF4 as in PF3, what is the technical benefit by doing so?  The whole reason to increase the number of RB for PF0/1/4 as scoped by the WID is for coverage. Many companies provided evalution results on this topic and I believe that is the base to reach our previous agreement 12/3/2 RB for 120/480/960 kHz SCS.  It’s totally illogic to derail from all the study/investigation and at the end arbitrarily select a number simply because that numbe is used for some other PFs. |
| Futurewei | We prefer Alt-D, but are also OK with Alt-A for the sake of consensus. Also OK if more time is needed for this issue to be decided later, given that good progress/number of agreements have already been made in this AI during this meeting for many issues. |
| Intel | We would be OK with Alt-D, but our preference is still for Alt-C.  As for Alt-A, as noted in prior comments, in our contribution we have provided a thorough analysis why we technically object to this option. Our main concerns are lack of considerations for fixed wireless and potentially other UE power classes with Alt-A. Once again, we do want to artificially limit potential coverage and performance because companies did not have a chance to account for these factors, given that this decision would be irrivertable and will not be possible to update in future releases. So we think it is extremely important that we are not too conservative with this numbers. |
| **Ericsson** | We have generated some new results trying to match Intel's evaluation scenario. We provide these with a hope that it can help with the decision on which alternative to select. The goal was to at least verify how MIL depends on the number of RBs for 480/960 kHz with larger numbers than we have assumed before. The evaluation scenario is as follows:   * (UE\_EIRP, TxBF, UE\_P) = (30, 0, 27) * TDL-A, 3 km/h, 5 ns DS * PF0, 2 symbols, frequency hopping on   + 1 and 2 bit payloads   + Alt-1 sequence construction   **480 kHz SCS:**    Alt-C  **960 kHz SCS:**    Alt-C  Note: apologies that our simulations are not finished for >10 RBs, but the trend is clearly visible. |
| Qualcomm | Our preference is Alt-A. We should proceed based on agreed evaluation methodology. |
| Samsung | Our first preference is Alt-A, second preference is Alt-D. |
| CATT | Our preference is Alt-D. |
| Moderator | * Alt-A (4/2 RBs)   + Support     - LGE, Nokia/NSB, NTT DOCOMO (2nd), ZTE/Sanechips (2nd), Sony, Lenovo/MotMob, Huawei/HiSilicon, vivo, Futurewei (2nd), Qualcomm, Samsung   + Object     - Intel * Alt-D (16/16 RBs)   + Support     - LGE (2nd), NTT DOCOMO, ZTE/Sanechips, Sony (2nd), OPPO, Lenovo/MotMob (2nd), Huawei/HiSilicon, Futurewei, Intel, Samsung (2nd), CATT   + Object     - vivo   OPPO suggests an alternative (Alt-E):   * Alt-A for PF0/1 * Alt-D for PF4   Intel prefers the following:   * Alt-C   + 12 RBs for 480 kHz SCS   + 4 RBs for 960 kHz SCS   Ericsson results show that maximum MIL achieved for   * 12 RBs for 480 kHz SCS * 8 RBs for 960 kHz SCS |

## 2.10 <Summary of 5th Round>

As can be seen in the table above, there is a roughly even split of companies supporting Alt-1 and Alt-D and one company ojecting to each. This is a tough situation. The moderator observes that under the assumption of (UE\_EIRP = 30 dBm, TxBF = 0 dBi, UE\_P = 27 dBm), the MIL is maximized for the following number of RBs which is roughly in the middle between Alt-A and Alt-D, and close to Intel's original proposal Alt-C

Alt-C':

* 12 RBs for 480 kHz SCS
* 8 RBs for 960 kHz SCS

OPPO suggests yet another alternative (Alt-E)

* Alt-A for PF0/1 for 480/960 kHz
* Alt-D for PF4 to align with the maximum for PF2/3

But the moderator also observes that supporting 16 RBs for all SCSs (Alt-D) is a superset of all alternatives. Clearly compromise is needed.

The moderator's intention is to discuss Proposal 1e in the GTW, and depending on the temperature, we could also discuss Alt-C/C' and Alt-E.

**Proposal 1e**

The maximum configured number of RBs, N\_RB, for enhanced PF 0/1/4 is given by the following (down-select to one alternative in this meeting):

* Alt-A
  + 4 RBs for 480 kHz SCS
  + 2 RBs for 960 kHz SCS
* Alt-D
  + 16 RBs for 480 and 960 kHz SCS (same as for 120 kHz SCS)

**Proposal 1f (Alt-C/C')**

The maximum configured number of RBs, N\_RB, for enhanced PF 0/1/4 is given by the following (resolve square brackets in this meeting):

* 12 RBs for 480 kHz SCS
* [4 or 8] RBs for 960 kHz SCS

**Proposal 1g (Alt-E)**

The maximum configured number of RBs, N\_RB, is given by the following:

* PF0/1
  + 4 RBs for 480 kHz SCS
  + 2 RBs for 960 kHz SCS
* PF4
  + 16 RBs for 480 and 960 kHz SCS (same as for 120 kHz SCS)

Thank-you for the good discussion. The following was agreed in the GTW:

Agreement:

The maximum configured number of RBs, N\_RB, for enhanced PF 0/1/4 is given by 16 RBs for 480 and 960 kHz SCS (same as for 120 kHz SCS).

# 3 Configuration of Number of RBs

The following agreement was made in RAN1#104 on the configuration of the number of RBs for enhanced PF0/1/4 by dedicated ignalling:

Agreement:

* The configured number of RBs for enhanced PF 0/1/4 is denoted NRB
  + The minimum value of NRB is 1 for PF 0/1/4 for all subcarrier spacings
  + The maximum value of NRB depends on subcarrier spacing
    - FFS: maximum value for each SCS and each of PF0/1/4
  + FFS: Allowed values of NRB within the [min/max] range
  + FFS: Details of indication of NRB by cell-specific (for PF0/1) and dedicated ignalling (PF0/1/4)
  + FFS: Whether or not multiplexing of users with misaligned RB allocations is supported, where “misaligned” also includes users with different # of RBs.
  + For PF4:
    - The actual number of RBs used for a PUCCH transmission is equal to NRB, i.e., the actual number of RBs does not vary dynamically based on PUCCH payload
    - NRB fulfils the following: where is a set of non-negative integers
* Note: if frequency hopping is enabled, NRB is the number of RBs per hop
* Note: decisions on the maximum value of NRB for each SCS and PUCCH format shall take into account link budgets based at least on the agreed evaluation assumptions

The following table provides a summary of company proposals regarding the open issue marked in red:

|  |  |
| --- | --- |
| **Company** | **Company Proposals** |
| vivo | Proposal 5: The number of RBs for enhanced PF0/1/4 and RE mapping structure are indicated by dedicated RRC ignalling. |
| CATT | **Proposal 5 The number of RBs for PUCCH format0/1/4 can be cell specific or UE specific configured.**  **Proposal 6 For RRC connected Ues, the Gnb could use RRC configuration or DCI to indicate UE the configured number of RBs.** |
| NTT DOCOMO | ***Proposal 2:*** *For the dedicated PUCCH resources, the number of RBs for PUCCH format 0/1/4 should be indicated via UE dedicated RRC signaling.* |
| LGE | **Proposal #3: The values of NRB after the RRC connection can be configured based on the allowed values of NRB defined in the specification for each PUCCH format/resource by the Gnb (UE-dedicated RRC signalling).** |

The following agreement was made in RAN1#104bis-e on the configuration granularity for the number of RBs:

Agreement:

Down select to one of the following two alternatives for the configuration of the number of RBs, , for enhanced PUCCH formats 0/1/4:

* Alt-1:
  + For enhanced PF0/1
    - Support configuration of all integer values in the range [1 .. max()] for each SCS
  + For enhanced PF4
    - Support configuration of all integer values in the range [1 .. max()] for each SCS that iscus the requirement where is a set of non-negative integers.
* Alt-2:
  + Same as Alt-1, but with coarser granularity, i.e., not all integer values of can be configured
  + FFS: Which values of are supported values in the range [1 .. max()]

The following table provides a summary of company proposals on the open issue marked in red:

|  |  |
| --- | --- |
| **Company** | **Company Proposals** |
| Intel | **Proposal 2: For enhanced PUCCH format 0/1 and 4, support configuration a coarse set of integer values within the range [1.. max()] for each SCS. In particular, for PUCCH format 4 the supported values must fulfill the requirement where is a set of non-negative integers. FFS: on the specific values.** |
| Vivo | **Proposal 2: For the configuration of the number of RBs, , for enhanced PUCCH formats 0/1/4, the alternative 1 is supported if the maximum RB number is not more than 16.** |
| CATT | **Proposal 4 The configurable RB granularity is preferred for the configuration of the number of RBs.** |
| ZTE | **Proposal 2: The allowed values of N\_RB within the range [1, ..., max()]** **can be flexible, Alt-1 (support configuration of all integer values in the range fulfill the requirement) is preferred in PRB number configuration.** |
| NTT DOCOMO | ***Proposal 3:*** *All integer values for PUCCH format 0/1 and all integer values that iscus the requirement where is a set of non-negative integers for PUCCH format 4 (Alt-1 in RAN1#104bis-e agreement) should be supported.* |
| Nokia | ***Proposal 5:*** *In case of dedicated PUCCH resource configuration, Alt-1 is supported for the configuration of the number of RBs.* |
| Apple | ***Proposal 2:*** *For enhanced PUCCH formats 0/1/4 and the granularity of the configured values should be based on Alt-1 i.e.,* *For enhanced PF0/1, support configuration of all integer values in the range [1 .. max( )] for each SCS. For enhanced PF4, support configuration of all integer values in the range [1 .. max( )] for each SCS that fulfil the requirement where is a set of non-negative integers.* |
| LGE | **Proposal #2: For the allowed values of NRB, the positive integer values between the min/max NRB can be used for PF0/1 while the allowed values of NRB between the min/max NRB for PF4 can be obtained by applying the DFT constraint.** |
| OPPO | **Proposal 6: Support Alt-1 for configuration granularity on number of RBs for UE dedicated RRC configuration.** |
| Samsung | **Proposal 2: Support configuration of all integer values in the range of [1 .. max()] per SCS, for PUCCH format 0/1. Support configuration of all integer values in the range [1 .. max()] for each SCS that iscuss the requirement where is a set of non-negative integers for PUCCH format 4.** |
| Huawei | ***Proposal 2: Adopt Alt. 1 for the granularity of the configuration of the number of RBs, , for enhanced PUCCH formats 0/1/4.*** |
| Qualcomm | ***Proposal 2:*** *All integer values for PUCCH format 0/1 and all integer values which fulfill the requirement for PUCCH format 4 (Alt-1) are supported* |
| Spreadtrum | ***Proposal 1: Support the configuration of all integer values in the range of [1…max(NRB)] for the numbers of contiguous RBs for enhanced PUCCH format 0/1/4 for 120/480/960 kHz SCS.*** |
| Ericsson | **Proposal 8 Support Alt-1 in the agreement from RAN1#104bis-e on the granularity of the configuration of the number of RBs, i.e.,**   * **For enhanced PF0/1**   + **Support configuration of all integer values in the range [1 .. max()] for each SCS** * **For enhanced PF4**   + **Support configuration of all integer values in the range [1 .. max()] for each SCS that iscus the requirement where is a set of non-negative integers.** |

There seems to be consensus that dedicated ignalling is needed for the configuration of the number of RBs. On the issue of configuration granularity, here is a summary of the support for the two alternatives:

* Alt-1
  + vivo, ZTE, NTT DOCOMO, Nokia, Apple, LGE, OPPO, Samsung, Huawei, Qualcomm, Spreadtrum
* Alt-2
  + Intel, vivo (if N\_RB > 16)

Since the rapporteur will start collecting needed RRC parameters for this WI after RAN1#106-e, it makes sense to agree on what parameters are needed for enhanced (multi-RB) PUCCH formats 0/1/4. In Rel-16, the moderator points out that for PUCCH formats 2 and 3 which support multiple RBs, the number of RBs for a PUCCH resource is configured within the IE *PUCCH-Config* which is used to configure UE specific PUCCH parameters (per BWP).

As pointed out by some companies, the RRC overhead savings from trying to optimize the granularity of N\_RB is quite small. For example, the difference in overhead required to signal 12 values and 32 values is only 1 bit. Considering that there is flexibility needed to configure the bandwidth of a PUCCH resource depending on the regulatory region, SCS, and the deployment scenario, it does not seem worth it to try to save 1 bit which would limit the deployment flexibility.

Based on this, the moderator makes the following two proposals:

**Proposal 6 Agree to the following:**

* Support an RRC parameter to configure the number of RBs for a PUCCH resource for each of enhanced PUCCH formats 0, 1, and 4
* The parameter is provided by dedicated ignalling (per UE) per BWP

**Proposal 7 Agree to the following:**

* For an RRC parameter that configures the number of RBs for a PUCCH resource for each of enhanced PUCCH formats 0, 1, and 4, support a value range of 1 .. N\_RB\_Max in steps of 1 RB where N\_RB\_Max is the maximum number of RBs per SCS value
* FFS: N\_RB\_Max for each SCS value (120, 480, and 960 kHz)

## 3.1 <1st Round Comments>

Please provide your company view on Proposals 6 and 7.

|  |  |
| --- | --- |
| **Company** | **View/Position** |
| Nokia, NSB | We support Proposal 6 and Proposal 7. |
| Vivo | We support proposal 7. |
| ZTE, Sanechips | We agree with Proposal 6.  For Proposal 7, we are generally fine. It might be better to separately consider PF 4 due to the requirement where is a set of non-negative integers. |
| Huawei/HiSilicon | We are fine with Proposal 6 and 7. We would also like to remark that optimization of RRC signalling has never been an issue for RAN1. |
| Lenovo, Motoroloa Mobility | We support both Proposals. |
| Apple | We are fine with both proposals |
| Intel | We support proposal 6, but we are not OK with proposal 7.    For proposal 7, we would rather prefer to wait until we conclude the discussion related to the maximum number of PRBs to support. If RAN1 agrees to increase the number of PRBs to values larger than those currently agreed, some of the larger values would never be used, and we iscuss Alt. 2 would be more suitable. As we noticed from our LLS evaluations, as the number of PRBs increases the MIL tends saturates, and after some PRB value changing the value of PRB would have marginal effect in coverage, so effectively some PRB values would have same effect, and therefore should not be considered.  As for the specific text of proposal 7, we believe that this is missing the restriction for PF4 according with only values that iscus the requirement ,where is a set of non-negative integers, should be considered. |
| CATT | For P7 we think the step (granuality) should be configurable. |
| Sony | We support Proposal 6 and Proposal 7. |
| NTT DOCOMO | We agree with both Proposal 6 and Proposal 7. |
| Qualcomm | We support proposal 6  For proposal 7, we are generally fine with it if the final N\_RB\_max is not too much bigger. If N\_RB\_max is increased significiantly, we think it is unnecessary to support granularity of 1RB |
| Samsung | We support Proposal 6 and 7. |
| OPPO | In general we are fine with the proposals. But it would be better to make it clear that these proposals are applied for UE in connected phase, as there are also proposals to enable UE-dedicated N\_RB configuration in initial access phase, which needs to be discussed separately. |
| LG Electronics | We are fine with Proposal 6 with the understanding that the number of RBs for each PUCCH resource for each enhanced PUCCH format 0/1/4 can be different. We are also fine with Proposal 7. |
| Futurewei | We support Proposal 6.  For Proposal 7, we look forward to a coarser granularity option be added to the proposal as we raised during the GTW. |

## 3.2 <Summary of 1st Round>

The following agreement was made in the GTW regarding Proposal 6.

Agreement:

* Support an RRC parameter to configure the number of RBs for a PUCCH resource for each of enhanced PUCCH formats 0, 1, and 4
* The parameter is provided by dedicated ignalling (per UE) per BWP

Regarding Proposal 7, many companies have commented that the DFT restriction for PF4 needs to be taken into account. Three companies have concerns that if N\_RB\_Max is too large, then a coarser granularity should be adopted; however, some companies point out that optimizing RRC overhead (saving 1 or at most 2 bits) is not a concern.

**Proposal 7a Agree to the following:**

* For the agreed RRC parameter that configures the number of RBs for a PUCCH resource, the value range is given by the following, where N\_RB\_Max is the maximum number of RBs per SCS value
  + For enhanced PF0/1
    - All integer values in the range [1 .. N\_RB\_Max]
  + For enhanced PF4
    - All integer values in the range [1 .. N\_RB\_Max] that iscus the requirement where is a set of non-negative integers
* FFS: N\_RB\_Max for each SCS value (120, 480, and 960 kHz)

## 3.3 < 2nd Round Comments>

Please provide your company view on Proposal 7a. The moderator’s intention is that we first try to make progress on the maximum number of RBs in this meeting (see Proposal 1a ins Section 2.2) and then come back to Proposal 7a once progress is made later in the meeting. However the moderator urges that companies try to compromise on Proposal 7a to avoid long discussions on how coarse the parameter granularity should be only to save a small amount of RRC overhead. Moreover, each region and deployment will have different requirements on the number of RBs, so it seems there would be no one answer on what is an appropriate reduced granularity. In that sense having integer granularity covers all cases.

|  |  |
| --- | --- |
| **Company** | **View/Position** |
| Moderator | Recommend to support Proposal 7a |
| Intel | As previously mentioned, we would be more confortable to conclude on the number of PRBs first, before making this agreement. We believe that this is the detail that can be discussed/concluded at a later time. |
| Nokia, NSB | We support the Proposal 7a |
| Futurewei | We also think it is better to first conclude the maximum numbers of PRBs first, which serve as a prerequisite for or at least serve to simplify decision of several other proposals including this one, such that at least the FFS is no longer needed for Proposal 7a. |
| InterDigital | We support proposal 7a. |
| Vivo | Support proposal 7a. |
| Lenovo, Motoroloa Mobility | We support Proposal 7a, but we also agree with Intel and Futurewei to first conclude the maximum number of PRBs. |
| Huawei/HiSilicon | We support Proposal 7a. Optimization of RRC signaling is not a valid argument in RAN1. |
| LG Electronics | We agree with the Moderator and support Proposal 7a. |
| NTT DOCOMO | We support Proposal 7a but we are OK to conclude after the maximum number of RBs for each SCS is agreed. |
| Samsung | We support Proposal 7a. |
| ZTE, Sanechips | We support Proposal 7a. |
| Qualcomm | We share similar view with Lenovo that we may come back to this after N\_RB\_max is decided.  Our position of supporting coarser granularity is not about RRC signaling overhead, rather on the modem chip testing effort. With granularity of 1 for a large N\_RB\_max, we as chip vender need to test all configurable RBs.  During the email iscussion of 105e, we accepted the Alt-1 as a compromise because for that Alt-1 N\_RB is limited to <=16. In this proposal, the limit is removed. |
| Sony | We support proposal 7a, but also see Intel and Futurewei´s point of concluding first on the maximum number of RBs for each SCS and are okay with that. |
| Apple | Given the possible increase in N\_RB under discussion, it may be a good idea to wait until it is decided. |

## 3.3 < Summary of 2nd Round>

Several companies have suggested that the maximum number of RBs should be decided first.

## 3.4 <3rd Round Comments>

Please provide your view on the following question that could help with moving forward. To be clear, the moderator’s intention is to agree on the maximum number of RBs first, but it is helpful to have an extra temperature check on Proposal 7a.

**Question**: If Proposal 1b in Section 2.4 is agreed, do you support Proposal 7a above?

|  |  |
| --- | --- |
| **Company** | **View/Position** |
| Intel | We are OK to conclude on the discussion related to proposal 1b first and the come back to this proposal later.  If we were to make progress on this topic without making progress on proposal 1b, we recommend taking a working assumption for 7b with a note that states that if N\_RB\_Max is determined to be large than a certain value, e.g. above 25, RAN1 may revisit the RB allocation restriction. |
| Vivo | Yes. If proposal 1b is agreed, we support the 1st bullet of proposal 7a.  One question. If proposal 1b is agreed, do we still need FFS bullet in proposal 7a? Is the intention that N\_RB\_MAX for each SCS in RRC can be different from the numbers in proposal 1b? |
| LG Electronics | We support both Proposal 1b and Proposal 7a. |
| NTT DOCOMO | Yes, we agree Proposal 7a and FFS in the proposal should be removed. |
| Nokia, NSB | Yes, we support Proposal 7a |
| Lenovo, Motoroloa Mobility | We support Proposal 7a and also agree with vivo to remove FFS, if Proposal 1b is agreed. |
| OPPO | Yes, we support Proposal 7a. |
| Apple | Given 1b is agreed, we are fine with 7a. |
| Qualcomm | Yes, we can accept proposal 7a if 1b is agreed. FFS should be removed if 1b is agreed. |
| Sony | Yes, if proposal 1b is agreed, then we can support proposal 7a. In such case, the FFS in proposal 7a is not needed. |
| Huawei/HiSilicon | Yes, we support Proposal 7a. |
| ZTE, Sanechips | We are fine with Proposal 7a. |
| CATT | we can accept proposal 7a if 1b is agreed |
| Samsung | Yes, we support Proposal 7a. |

## 3.5 <Summary of 3rd Round>

It seems there is consensus to support Proposal 7a if Proposal 1b in Section 2.4 is agreed

* Support Proposal 7a
  + Intel (WA if N\_RB\_Max > 25), vivo (remove FFS), LGE, NTT DOCOMO (remove FFS), Nokia/NSB, Lenovo/MotMob (remove FFS), OPPO, Apple, Qualcomm (remove FFS), Sony (remove FFS), Huawei/HiSilicon, ZTE, CATT, Samsung
* Do not support Proposal 7a

While the whole of Proposal 1b was not agreed, at least the value of N\_RB\_Max for 120 kHz was agreed (16 RBs) – see agreement from Chairman’s notes in Sedtion 2.6. Since N\_RB\_Max for 480/960 kHz will be less than 16, it seems it is now safe to agree on the configuration granularity since it is the maximum value (16) that primarily determines the RRC overhead. A number of companies recommened to remove the FFS since that is being discussed in Section 2 already. Based on this the moderator makes the following updated proposal.

**Proposal 7b Agree to the following:**

* For the agreed RRC parameter that configures the number of RBs for a PUCCH resource, the value range is given by the following, where N\_RB\_Max is the maximum number of RBs per SCS value
  + For enhanced PF0/1
    - All integer values in the range [1 .. N\_RB\_Max]
  + For enhanced PF4
    - All integer values in the range [1 .. N\_RB\_Max] that fulfil the requirement where is a set of non-negative integers

## 3.6 <4th Round Comments>

Please provide your view on Proposal 7b

|  |  |
| --- | --- |
| **Company** | **View/Position** |
| Intel | Given that we have agreed on the upper limit of the maximum number of PRBs supported, we are OK with the proposal. |
| LG Electronics | We support Proposal 7b. |
| Nokia, NSB | We support the proposal 7b |
| NTT DOCOMO | We support Proposal 7b. |
| ZTE, Sanechips | We support Proposal 7b |
| InterDigital | We support Proposal 7b |
| Apple | We are fine with the proposal |
| Lenovo, Motoroloa Mobility | We support Proposal 7b. |
| Huawei/HiSilicon | We support Proposal 7b. |
| Qualcomm | We support Proposal 7b. |
| vivo | Support proposal 7b. |
| Samsung | We support Proposal 7b. |
| Moderator | It seems that there is consensus on Proposal 7b hence it will be reported for email approval. |

Thank-you for the good discussion. The following was endorsed over email:

Agreement:

For the agreed RRC parameter that configures the number of RBs for a PUCCH resource, the value range is given by the following, where N\_RB\_Max is the maximum number of RBs per SCS value

* For enhanced PF0/1
  + All integer values in the range [1 .. N\_RB\_Max]
* For enhanced PF4
  + All integer values in the range [1 .. N\_RB\_Max] that fulfil the requirement where is a set of non-negative integers

# 4 Sequence Construction for Enhanced PF0/1

The following agreements were made in RAN1#104-e and RAN1#104bis-e:

Agreement:

* For enhanced PF0/1, support Type-1 low PAPR sequences. Further study and strive to select one of the following alternatives:
  + Alt-1: A single sequence of length equal to the total number of mapped Res of of the PUCCH resource is used. Cyclic shifts for PF0/1 are defined in the same way as Rel-16 for the case that *useInterlacePUCCH-PUSCH* is not configured.
  + Alt-2: A single sequence of length equal to the number of mapped Res per RB of the PUCCH resource is used, and the sequence is repeated in each RB. At least the following scheme is considered for PAPR/CM reduction:
    - Cycling of cyclic shifts across RBs in a similar way as for Rel-16 for PF0/1 for the case that *useInterlacePUCCH-PUSCH* is configured
* At least the following aspects should be considered in the study
  + Coverage (maximum isotropic loss (MIL)), including
    - Required SNR to fulfil PUCCH detection criterion
    - PAPR/CM as a function of N\_RB
  + Specification impact

For the PF0/1 sequence, the main open issue is which sequence construction method should be supported:

* Alt-1: A single long sequence
* Alt-2: Sequence repeated in each RB + cyclic shift cycling for PAPR/CM mitigation

The following table provides a summary of company proposals on this topic.

|  |  |
| --- | --- |
| **Company** | **Company Proposals** |
| Intel | **Proposal 6: For PUCCH format 0 and 1, the sequence is generated by using a Type-1 low PAPR sequence**  **of length equal to the number of subcarriers over which the PUCCH spans across.** |
| Futurewei | ***Proposal 11: For cases with N\_RB < 12, only Alt-1 one is supported, and consider Alt-2 as the only supported alternative for cases with N\_RB ≥ 12, unless it is strongly favored by the majority that only one alternative should be supported for all N\_RB values, in which case we slightly lean towards supporting Alt-1 only.*** |
| Vivo | **Proposal 6:** **For enhanced PUCCH format 0/1, the alt 1 of a single sequence of length equal to the total number of mapped Res of the PUCCH resource is preferred.**  **Proposal 7：For a single sequence of length equal to the total number of mapped Res of the PUCCH resource, the cyclic shift should be adapted with the length of the sequence.** |
| CATT | **Proposal 2 The method to reduce the PAPR should be supported if repetitive sequences are adopted.**  **Proposal 3 For enhanced PUCCH format 0/1 sequence, Alt1 (long sequences) is preferred to keep similar CM for sequences with different lengths.** |
| Lenovo/Motorola Mobility | ***Proposal 2: For NR operation between 52.6 GHz and 71 GHz, PUCCH format 0 transmitted with multiple number of (same) base sequences with different phase shifts should be supported for mapping to multiple RBs***  ***Proposal 3: For NR operation between 52.6 GHz and 71 GHz, Rel 15 based long sequence should be considered for PUCCH formats 0/1 for mapping to multiple RBs***  ***Proposal 4: For NR operation between 52.6 GHz and 71 GHz, PUCCH format 0 should be enhanced to support 2-bit transmission with 1 symbol by mapping to 2 RBs***  ***Proposal 5: For NR operation between 52.6 GHz and 71 GHz, a combination of repetition and longs sequence could be supported for mapping on multiple RBs for PUCCH format 0/1*** |
| ZTE | **Proposal 6: Regarding the PUCCH format 0/1 sequence type selection, Alt1 (a single long sequence) is preferred.** |
| NTT DOCOMO | ***Proposal 5:*** *Alt.1 (a single sequence of length equal to the total number of mapped Res of the PUCCH resource) should be supported for enhanced PUCCH format 0/1.* |
| Nokia | ***Proposal 2:*** *Support Alt-1 sequence construction: a single sequence of length equal to the total number of mapped Res for PUCCH Format 0/1 resources* |
| Sony | **Proposal 1: Since the MIL criterion alone cannot be used to down select between Alt-1 and Alt-2 enhanced PF0/1 sequences, consider UE multiplexing for down selection between Alt-1 and Alt-2.**  **Proposal 2. Given that in practice, Alt-1 and Alt-2 display the very similar performance in terms of MIL, support Alt-2 to enable efficient multiplexing of Ues with different configured values of .** |
| Apple | ***Proposal 4:*** *For enhanced PF0/1, RAN1 should support Alt 1 i.e. a single Type-1 low PAPR sequence of length equal to the total number of mapped Res of the PUCCH resource. Cyclic shifts for PF0/1 are defined in the same way as Rel-16 for the case that useInterlacePUCCH-PUSCH is not configured.*  ***Proposal 5:*** *RAN1 should use the increased resources used for PF0/1 PUCCH transmission for coverage enhancement and not for a payload increase.* |
| LGE | **Proposal #5: Considering better MIL performance and improved coverage of multi-PRB based initial PUCCH for the specific RB range (e.g., NRB around 12-16), support Alt-2 (a single sequence of length equal to the number of mapped Res per RB with the step size ∆ = 5 for the cycling of cyclic shifts across RBs) for the sequence type for enhanced PUCCH format 0/1 in 60 GHz.** |
| Qualcomm | **Proposal 1: Support Alt-2 for base sequence type when PUCCH format 0/1 occupies more than one RB.** |
| OPPO | **Proposal 7: Adopt long sequence for PUCCH format 0 and format 1 when N\_RB>1.** |
| Samsung | **Proposal 3: Support Alt-2 (Rel-16 NR-U short sequence with repetition) for PUCCH format 0/1.** |
| Huawei | ***Observation 6: Using a repeated DMRS sequence of length-12 with phase ramp for CM/PAPR reduction offers better UE multiplexing than a single sequence. The 95-percentile CM is at most 2 Db worse for allocations below 9 PRBs.***  ***Observation 7: When considering the regional limitations, there is no or very small (< 1 Db) difference in maximum transmit power between using a single sequence or a repeated sequence of length-12 with phase ramp for CM/PAPR reduction.***  Moderator note: Corresponding proposal is missing; however, the moderator assumes that Huawei proposes Alt-2.  Huawei: We see merits of both proposals. |
| Interdigital | ***Proposal 3:*** *It is preferred to support a single sequence of length equal to the total number of mapped Res of the PUCCH resource (Alt-1) for PUCCH format 0/1.* |
| WILUS | * *Proposal 2: For low PAPR sequence for enhanced PUCCH format 0/1 (PF0/1), we support Alt-2 that a single sequence of length equal to the number of mapped Res per RB of the PUCCH resource is used, and the sequence is repeated in each RB with cycling of cyclic shifts across RBs in a similar way as for Rel-16 for PF0/1.* |
| MediaTek | Proposal 1: Alternative 1 should be adopted as the base sequence design for enhanced PUCCH format 0/1. |
| Spreadtrum | *Proposal 3: For enhanced PF0/1, Alt -2 should be supported in order to reduce the impact of the specification.* |
| Ericsson | Proposal 9 In the agreement from RAN1#104-e on sequence construction for enhanced PF0/1, support Alt-1, i.e., reuse the Rel-15 rules to select base sequences based on Low-PAPR sequence Type-1 defined in 38.211 Section 5.2.2. Do not support repeated sequences with cyclic shift cycling (Alt-2). |

In the previous meeting, it was decided to wait until there is further input from RAN4 on the maximum number of RBs. As discussed above, at least some feedback has now been received.

The following is a high level summary of company evaluations comparing Alt-1 vs. Alt-2.

|  |  |
| --- | --- |
| **Company** | **Evaluation summary** |
| Intel | * Alt-1 performance in terms of MIL meets or exceeds Alt-2 performance considering a wide range of RBs (1 – 40) * For 480/960 kHz the gain for Alt-1 vs. Alt-2 is larger than for 120 kHz |
| vivo | * Alt-1 performance in terms of MIL meets or exceeds Alt-2 performance for 3 combinations of (UE\_EIRP, UE\_P) considering up to 18 RBs   + (25, 21) dBm   + (40, 21) dBm   + (43, 23) dBm * For 480/960 kHz the gain for Alt-1 vs. Alt-2 is larger than for 120 kHz |
| Lenovo | * Alt-1 and Alt-2 performance in terms of MIL are comparable considering up to 4 RBs and (UE\_EIRP, UE\_P, TxBF) = (40 dBm, 25 dBm, 0 dBi) |
| ZTE | * Alt-1 and Alt-2 have comparable MIL performance for 120 kHz considering 12 RB * Alt-1 has larger MIL than Alt-2 for 480/960 kHz   + 1.5 Db gain for 3 RBs for 480 kHz   + 1 Db gain for 2 RBs for 960 kHz |
| Nokia | * Alt-1 performance meets or exceeds Alt-1 performance considering up to 16/5/4 RBs for 120/480/960 kHz SCS and UE\_EIRP = 25 dBm   + 0.3 – 0.9 Db gain for Alt-1 for 2 and 4 RBs in Europe for 120 kHz and in all regions for 480/960 kHz |
| Sony | * With (UE\_EIRP, UE\_P, TxBF) = (25 dBm, 21 dBm, 6 dBi):   + 120 kHz     - Larger transmit power achievable for Alt-1 compared to Atl-2 for PUCCH bandwidth up to 100 MHz, except for 15 – 25 MHz bandwidth where Alt-2 allows up to 1 Db larger transmit power   + 480 kHz     - Larger transmit power achievable for Alt-1 compared to Alt-2 for all PUCCH bandwidths up to 60 MHz. For 60 – 100 MHz bandwidth, Alt-2 allows up to 1 Db larger transmit power   + 960 kHz     - Larger transmit power achievable for Alt-1 compared to Alt-2 for all PUCCH bandwidths up to 100 MHz |
| Qualcomm | * With (UE\_EIRP, UE\_P, TxBF) = (25 dBm, 21 dBm, 6 dBi)   + 120 kHz:     - Comparable transmit power between Alt-1 and Alt-2 up to 20 RBs, except for 11 – 16 RBs where Alt-2 allows up to 0.3 Db larger transmit power   + 480 kHz:     - Alt-1 can achieve 1.5 Db higher power for 3 RBs (comparable power for 1,2 RBs)   + 960 kHz:     - Alt-1 can achieve 1 Db Db higher power for 2 RBs (comparable power for 1 RB) * With (UE\_EIRP, UE\_P, TxBF) = (40 dBm, 21 dBm, 6 dBi)   + 120 kHz:     - Comparable transmit power between Alt-1 and Alt-2 up to 20 RBs, except for 11 – 17 RBs where Alt-2 allows 0.3 – 1.5 Db larger transmit power |
| OPPO | * 120 kHz (Considered 12 and 32 RBs)   + For 12 RBs: comparable MIL for DS = 10, 20 ns. Alt-2 has 0.5 Db gain for 5 ns   + For 32 RBs: Alt-1 has 0.5 – 1.5 Db gain depending on DS * 480 kHz (Considered 3 and 8 RBs)   + Alt-1 has 0.5 – 1.5 Db gain depending on OS and number of RBs * 960 kHz (Considered 2 and 4 RBs)   + Alt-1 has 1 – 1.5 Db gain depending on OS and number of RBs |
| Huawei | * MIL comparison for 120 kHz considers 4 and 8 RBs   + USA     - Comparable MIL   + EU     - Alt-1 has 0.4 – 1.4 Db gain compared to Alt-2 depending on number of RBs |
| Ericsson | * MIL comparison for 480kHz considers up to 3 RBs   + US/SK: Alt-1 has 1.5 Db (US) larger MIL for 3 RBs; comparable MIL for 1,2 RBs   + Europe: Alt-1 has 0.8 – 1.3 Db (Europe) larger MIL for 2 and 3 RBs; comparable MIL for 1 RB |

Moderator observations based on contributions and reported evaluations:

* Spec complexity
  + Both Alt-1 and Alt-2 can be seen as extensions of Rel-15 or 16, so no real difference in spec complexity
  + Alt-1: Used for DMRS of PF3 in Rel-15/16
  + Alt-2: Used for PF0/1 in Rel-16 when interlacing configured
* MIL performance
  + 120 kHz
    - MIL for Alt-1 is either comparable or exceeds MIL for Alt-2 for a wide range of N\_RB values (up to 40 RBs)
      * The exception is for the case of N\_RB in the range 12 – 16 RBs where Alt-2 can exceed the MIL of Alt-1 if UE\_EIRP is increased
    - In all cases, the difference in MIL between Alt-1 and Alt-2 is within approximately 1.5 Db
  + 480/960 kHz:
    - MIL for Alt-1 exceeds MIL for Alt-2 over all practical values for N\_RB
    - The difference in MIL between Alt-1 and Alt-2 is within 1.5 Db
* Multiplexing of users with misaligned RB allocations
  + Some companies observe that Alt-2 offers better opportunities for multiplexing users with misaligned RB allocations, where “misaligned” also includes users with different number of RBs.
  + Other companies state that user multiplexing is not important in the 52.6 – 71 GHz band and refer to the agreement from RAN1#104bisi-e that user-multiplexing has lower priority as a design criterion compared to MIL

Discussion Point

It seems that the decision point on Alt-1 vs. Alt-2 comes down to a trade-off coverage vs. multiplexing of users with misaligned RB allocations.

* Alt-1:
  + Better coverage for 480, 960 kHz SCS
  + Potentially better coverage for 120 kHz for N\_RB less than 12 depending on regulatory region
  + Degraded coverage for 120 kHz for N\_RB = 12 .. 16 RBs if UE\_EIRP does not limit transmit power
  + Cannot multiplex users with mialigned RB allocations
* Alt-2:
  + Can multiplex users with misaligned RB allocations
  + Better coverage for 120 kHz for N\_RB = 12 .. 16 RBs if UE\_EIRP does not limit transmit power
  + Degraded coverage for 480, 960 kHz SCS
  + Potentially degraded coverage for 120 kHz for N\_RB less than 12 depending on regulatory region

The following is a summary of support for Alt-1 and Alt-2

* Alt-1:
  + Intel, Futurewei (if only 1 alternative selected), vivo, CATT, Lenovo(?), ZTE, NTT DOCOMO, Nokia, Apple, OPPO, Interdigital, MediaTek, Ericsson
* Alt-2:
  + Futurewei (if both alternatives selected), Lenovo(?), Sony, LGE, Qualcomm, Samsung, ~~Huawei~~, WILUS, Spreadtrum

**Proposal 2 Further discuss down-selection to one of Alt-1 and Alt-2**

## 4.1 <1st Round Comments>

Please provide your company view on Proposal 2, particularly with respect to the above Discussion Point on trade-off between coverage and multiplexing of users with misaligned RB allocations.

|  |  |
| --- | --- |
| **Company** | **View/Position** |
| Nokia, NSB | As discussed in our contribution, since the number of Ues served simultaneously via the same beam can be rather limited, we do not see a need for supporting multiplexing of users with misaligned RB allocations with enhanced PUCCH formats 0/1. |
| Vivo | We still support alt1.  As summaried by FL, alt 1 has better coverage for 480, 960 kHz SCS than alt 2.  Regarding 120kHz SCS, similar MIL is observed for alt 1 and alt 2. The debate is on multiplexing capability or multiplexing users with misaligned RB allocations.  As a step forward of proposal 2, we propose to first agree with support alt 1 for 480kHz and 960kHz SCS. Down-select for 120kHz, once we know more about the maximum of RBs. |
| ZTE, Sanechips | We agree with Proposal 2. There is no need to define 2 kinds of sequence types for above 52.6GHz which requires larger spec efforts. Among the 2 alternatives, we support Alt1 considering the better coverage in most cases. |
| Huawei/HiSilicon | We see merits with both proposals but prefer that just one of them is selected. |
| Lenovo, Motoroloa Mobility | We prefer Alt1, as it has been shown in our contribution that simple frequency domain repetition shows significant increase of PAPR and CM comparing to long sequence-based approach which results in different link budget. However, we are also fine with Alt 2, if a combination of repeitition and long sequence is supported. For example, the long sequence is used for multiple RBs (but not total RBs) and then repetiting the long sequence to occupy total RB allocation. |
| Apple | We also think that there should be a down-selection. We prefer Alt 1 |
| Intel | We support Alt-1, and we share the same view as Nokia regarding the need of multiplexing, which has been already agreed should be considered with lower priority compared to MIL when down-selecting among options:    Agreement:  User-multiplexing can be considered but as lower priority compared to maximum isotropic loss for PUCCH as a design criterion. |
| CATT | We still support alt1. No need for optimization of multiplexing user. |
| Sony | As pointed out in the moderator summary, both Alt-1 and Alt-2 are already part of Rel-15 or Rel-16; hence, there is no added complexity if both alternatives are kept. Given that it has proven difficult to reach a consensus on this issue, we are open to keeping both sequence constructions in Rel-17, if this would facilitate an agreement. On the other hand, if the majority of companies prefers to downselect to only one alternative, then we prefer Alt-2. As discussed in our contribution, both Alt-1 and Alt-2 offer similar performance in terms of coverage (i.e., MIL), but only Alt-2 can multiplex Ues with misaligned RB allocations. |
| NTT DOCOMO | We support Alt.1. As we agreed at RAN1 #104-bis-e meeting, coverage performances should be pripritized compared to the user-multiplexing capacity considering the use of narrower beam which will accommodate limited number of Ues. In addition, especially for PF0/1, PF0/1 is used during initial access procedure and the number of RBs for the PUCCH resource mey not be large enough for some region to achieve maximum allowed transmission power as it has been deiscuused on RB shortage issue. Hence, we should focus on the MIL performances to decide the sequence design for PF0/1. |
| Qualcomm | We still support Alt2. Alt-2 shows better CM properties for 12-16RB ranges for 120khz SCS. While for 1-11RBs, CM different doesnot affect MIL. We also argue that from coerage point of view, 120kHz SCS is more suitable than 480/960kHz SCS. |
| Samsung | We still support Alt-2. Because MIL is similar for Alt-1 and Alt 2(in some cases, Alt-1 outperforms Alt-2, while in other cases, Alt-2 outperforms Alt-1), but Alt-2 provides better UE multiplexing. |
| OPPO | We support Alt-1.  The UE multiplexing gain has been discussed extensively in RAN1#105-e meeting for PF0/1 full PRB vs. sub PRB, and in the last GTW we had already reached common understanding UE multiplexing gain is not important. |
| LG Electronics | It is important that the multi-PRB (i.e., NRB larger than 1) based PUCCH should be supported for the initial PUCCH resource considering the coverage of PUCCH format 0/1. Moreover, at least for 120 kHz SCS, the PAPR/CM performance of Alt-2 for the specific RB range (e.g., NRB around 12-16) is better than that of Alt-1. In this regard, we prefer to support Alt-2. However, if it is hard to downselect to one of Alt-1 and Alt-2, it may also be considered to configure both sequences and use one of sequence types according to the number of RB or the PUCCH resources. |
| Futurewei | We suggest to first agree to support Alt-1, and focus on Alt-2 once the maximal number of RB is determined. |
| Moderator | Please continue to discuss. We wil come back to this issue when we make some progress on the maximum number of RBs (hopefully this meeting – see Proposal 1a in Section 2.2). |
| InterDigital | We support Alt 1. Given that narrow beam, probability of UE multiplexing with same beam should be very limitied. |
|  |  |

## 4.2 <Summary of 1st Round>

Amonsgst companies that responded on coverage vs. user multiplexing, all but one comment that the decision point on Alt-1 vs. Alt-2 should be based on coverage. Several companies have suggested that only one sequence construction method (Alt-1 or Alt-2) should be supported, while some companies indicated that if a consensus cannot be achieve, then both should be supported. Some companies suggest that the decision on Alt-1 and Alt-2 should be made after the maximum number of RBs is decided.

The moderator’s view is that we should prioritize a working system and avoid unneeded complexity, hence it would be better to down-select to only one alternative. Furthermore, given the (almost consensus) view, the decision point on Alt-1 vs. Alt-2 should be based on coverage considerations only, not optimization for user multiplexing.

As a small step forward, the following updated proposal is made based on the original agreement in RAN1#104. The update states that down-selection to one alternative is done, and only coverage is considered.

**Proposal 2a Agree to the following**

* For enhanced PF0/1, down-select to one of the following alternatives
  + Alt-1: A single sequence of length equal to the total number of mapped Res of of the PUCCH resource is used. Cyclic shifts for PF0/1 are defined in the same way as Rel-16 for the case that *useInterlacePUCCH-PUSCH* is not configured.
  + Alt-2: A single sequence of length equal to the number of mapped Res per RB of the PUCCH resource is used, and the sequence is repeated in each RB. Cyclic shifts are cycled across RBs in a similar way as for Rel-16 for PF0/1 for the case that *useInterlacePUCCH-PUSCH* is configured
* The decision on down-selection shall be made considering coverage only, i.e., not user-multiplexing

|  |  |
| --- | --- |
| LG Electronics | Q1: We prefer to open to support both Alt-1 and Alt-2 rather than the down-select to one of the alternatives.  Q2: We prefer Alt-2. Because, at least for 120 kHz SCS, the PAPR/CM performance of Alt-2 for the specific RB range (e.g., NRB around 12-16) is better than that of Alt-1. However, if it is hard to down-select to one of Alt-1 and Alt-2, it may also be considered to configure both sequences and use one of sequence types according to the number of RB or the PUCCH resources.  Meanwhile, in the case of Alt-1, it seems too early to define the cyclic shifts for PF0/1 in the same way as Rel-16. For example, the performance may be degraded if the same cyclic shifts for PF0/1 are used for the enhanced (multi-RB) PF0/1. Therefore, it needs further discussion and we suggest the following modification on Alt-1:   * + Alt-1: A single sequence of length equal to the total number of mapped Res of of the PUCCH resource is used. Cyclic shifts for PF0/1 are defined in a similar way as Rel-16 for the case that *useInterlacePUCCH-PUSCH* is not configured. |

## 4.3 <2nd Round Comments>

Please provide answers to the following questions that can help with movin forward. To be clear, the moderator’s intention is to agree on the maximum number of RBs first, but it is helpful to have a temperature check in case Proposal 1b in Section 2.4 is agreed.

* **Question 1**: Do you support Proposal 2a?
* **Question 2**: If the answer to Q1 is yes, and if Proposal 1b in Section 2.4 is agreed, which alternative to you support, Alt-1 or Alt-2?

|  |  |
| --- | --- |
| **Company** | **View/Position** |
| Moderator reccomendation | Q1: Support Proposal 2a to avoid complexity of supporting two schemes. A workding system avoiding extra complexity should be prioritized at this point in the WI. |
| Intel | Q1: We support the proposal  Q2: Alt-1. |
| Vivo | Q1: yes  Q2: Alt-1 |
| LG Electronics | Q1: We prefer to open to support both Alt-1 and Alt-2 rather than the down-select to one of the alternatives.  Q2: We prefer Alt-2. Because, at least for 120 kHz SCS, the PAPR/CM performance of Alt-2 for the specific RB range (e.g., NRB around 12-16) is better than that of Alt-1. However, if it is hard to down-select to one of Alt-1 and Alt-2, it may also be considered to configure both sequences and use one of sequence types according to the number of RB or the PUCCH resources. |
| NTT DOCOMO | Q1: Yes.  Q2: Alt-1. |
| Lenovo, Motoroloa Mobility | Q1: Yes.  Q2: Alt-1 (first preference), Alt-2 (second preference)  Also, we would prefer to have combination of both the alternatives |
| OPPO | Q1: Yes.  Q2: Alt-1. |
| Apple | Q1: Yes  Q2: Alt-1 |
| Qualcomm | Q1: No. We do not agree that only coverage is considered to down-select.  We can only accept Proposal 2a if “The decision on down-selection shall be made considering coverage only, i.e., not user-multiplexing” is removed. |
| Sony | Q1: We are okay to downselect to one alternative. However, if consensus cannot be reached, we also can support both alternatives, i.e., no downselection.  Q2: Our view is similar to LG and support Alt-2. If consensus cannot be reached, we also can support both alternatives, i.e., no downselection. |
| ZTE, Sanechips | Q1: Yes  Q2: Alt-1 |
| Futurewei | Q1: Yes  Q2: Alt-1 |
| InterDigital | Q1: Yes  Q2: Alt-1 |
| CATT | Q1: Yes  Q2: Alt-1 |
| Samsung | Q1: We support to down-select one option. But we still feel it is more reasaonble to also consider UE multiplexing, when coverage is comparable for both options.  Q2: Alt-2. For the sake of progress, if we’re the only company supporting Alt-2, we can compromise to Alt-1. |

## 4.4 <Summary of 2nd Round>

With respect to Question 1, there appears to be a large majority of companies supportive of down-selection to only one alternative:

* Select only one of Alt-1 and Alt-2
  + Intel, vivo, NTT DOCOMO, Lenovo/MotMob, OPPO, Apple, Qualcomm (if 2nd bullet is removed), Sony, ZTE/Sanechips, Furturewei, Interdigital, CATT, Samsung (consider UE multiplexing), Ericsson
* Support both Alt-1 and Alt-2
  + LGE, Sony (if consensus cannot be achieved)

With respect to Question 2, the following is the company support of Alt-1 vs. Alt-2

* Support Alt-1
  + Intel, vivo, NTT DOCOMO, Lenovo/MotMob (1st preference), OPPO, Apple, ZTE/Sanechips, Futurewei, Interdigital, CATT, Samsung (2nd preference)
* Support Alt-2
  + LGE, Lenovo/MotMob (2nd preference), Qualcomm (consider UE multiplexing), Sony, Samsung (1st preference; consider UE multiplexing)

Some companies prefer that UE multiplexing be considered in the down-selection; however, the moderator points out the following agreement from the GTW:

Agreement:

In the following, Alt-1 and Alt-2 refer to the RE mapping agreement for 120 kHz from RAN1#105-e:

* For enhanced PF0/1, for PUCCH resources after RRC configuration, Alt-2 (sub-PRB interlaced mapping) is not supported.
* For DMRS of enhanced PF4, only Alt-1 is supported (all Res within each RB are mapped).
* Note: optimization of user multiplexing for enhanced PUCCH format 0/1/4 is not considered in Rel-17.

Based on this it seems the 2nd bullet of Proposal 2a is not needed anymore, since it is effectively covered by the highlighted text in the above agreement. Baed on this the following update proposal is made (2nd bullet of Proposal 2a removed).

**Proposal 2b Agree to the following**

For enhanced PF0/1, down-select to one of the following alternatives:

* Alt-1: A single sequence of length equal to the total number of mapped Res of of the PUCCH resource is used. Cyclic shifts for PF0/1 are defined in the same way as Rel-16 for the case that *useInterlacePUCCH-PUSCH* is not configured.
* Alt-2: A single sequence of length equal to the number of mapped Res per RB of the PUCCH resource is used, and the sequence is repeated in each RB. Cyclic shifts are cycled across RBs in a similar way as for Rel-16 for PF0/1 for the case that *useInterlacePUCCH-PUSCH* is configured

## 4.5 <3rd Round Summary>

Please provide your company view on Proposal 2b. Based on the strong majority to down-select to only one of Alt-1 and Alt-2, the moderator strongly encourages companies to at least support Proposal 2b. If possible, it would be ignaling to down-select in this meeting.

|  |  |
| --- | --- |
| **Company** | **View/Position** |
| FL Recommendation | Support Proposal 2b  Try to down-select this meeting if possible |
| Intel | As mentioned before, we do not see any need of enhancing multiplexing capabilities for above 60 GHz, and as done for many other topics, emphasis should be provided to enhance coverage.  With that said, based on our evaluations Alt-1 provides almost for all number of PRBs clear advantage in terms of MIL compared to Alt-2, and therefore Alt-1 is preferred. |
| LG Electronics | Unfortunately, we still prefer to open to support both Alt-1 and Alt-2 rather than the down-select to one of the alternatives considering there are some supportive view on Alt-2 in the previous round comments. |
| Nokia, NSB | We are ok with the proposal and support Alt-1. As discussed before, we do not see the multiplexing capacity as a limiting factor in the scenarios of interest. However, rather than agreeing to continue discussion, we should decide on this issue as it has been open for a long time already. |
| ZTE, Sanechips | We still support Alt 1 since it shows better MIL performance than Alt 2 in most cases. |
| InterDigital | We support Alt-1 as previously mentioned several times. We don’t see the strong need to consider UE multiplexing in above 52.6 GHz. In addition, we also believe that supporting both options would make Gnb and UE implementation complex. |
| Apple | We are fine with the proposal and think that a decision should be made. We support Alt-1. |
| Lenovo, Motoroloa Mobility | We support proposal 2b with Alt 1 as our 1st preference, but we would prefer to combine both Alts, or even keep both Alts. |
| Huawei/HiSilicon | We support selecting only one of the alternatives. |
| Qualcomm | Our interpretation of “optimization of user multiplexing for enhanced PUCCH format 0/1/4 is not considered in Rel-17” is that we don’t need to further enhance/optimize user multiplexing, NOT that user multiplexing can not be considered for design choices selection.  With that said, we accept down-select but still support Alt-2. For 120kHz SCS, we have shown that for RB number from 1-10, both Alts lead to same max tx power, while for 11-16, Alt-2 leads to larger max tx power, and thus better coverage.  We understand that for 480/960kHz, Alt-1 shows better CM. But our question is that if coverage is a concern, why should NW choose 480/960kHz SCS over 120kHz? |
| Vivo | We support Alt-1 and also agree to decide in this meeting.  On the suggestion to support both Alt-1 and Alt-2, we cannot accept that and will object if that’s proposed for the same reason on implementation complexity as companies argued against supporting both Alt-1 and Alt-2 for RE mapping.  Again, we request a fair and consistent decision criterion for all design aspects of enhanced PF0/1/4. |
| Samsung | We support the proposal to down-select one alternative. |

## 4.6 < Summary of 3rd Round >

* Support only one of Alt-1 and Alt-2 (as in Proposal 2b)
  + Intel, Nokia/NSB, ZTE/Sanechips, Interdigital, Apple, Huawei/HiSilicon, Qualcomm, vivo, Ericsson, Samsung
* Support both Alt-1 and Alt-2
  + LGE, Lenovo/MotMob
* Object to supporting both Alt-1 and Alt-2
  + vivo, Interdigital, Ericsson

Given the strong support to down-select to one alternative, and given that there are at least as many objections to supporting both Alt-1 and Alt-2 as there are companies supporting both Alt-1 and Alt-2, it seems that the only way forward is to down-select.

The moderator makes the following update to the proposal to set a time limt for down-selection given that some companies would like to down-selet this meeting.

**Proposal 2c**

For enhanced PF0/1, down-select to one of the following alternatives this meeting or by RAN1#106bis-e at the latest:

* Alt-1: A single sequence of length equal to the total number of mapped Res of of the PUCCH resource is used. Cyclic shifts for PF0/1 are defined in the same way as Rel-16 for the case that *useInterlacePUCCH-PUSCH* is not configured.
* Alt-2: A single sequence of length equal to the number of mapped Res per RB of the PUCCH resource is used, and the sequence is repeated in each RB. Cyclic shifts are cycled across RBs in a similar way as for Rel-16 for PF0/1 for the case that *useInterlacePUCCH-PUSCH* is configured

## 4.7 <4th Round Comments>

Please provide your view on Proposal 2c as follows (no need to provide your view again if it has not changed)

* If you have strong concerns about Proposal 2c which proposes down-selection
* If you have strong concerns about supporting both Alt-1 and Alt-2

The moderator’s intention is to bring this up in the next GTW to see if we can make some progress.

|  |  |
| --- | --- |
| **Company** | **View/Position** |
| LG Electronics | We prefer to support Alt-2. It is recommended to support both Alt-1 and Alt-2 unless agreed as one alternative, and different sequence configuration methods can be used for each RB range based on performance depending on the number of RBs. |
| Nokia, NSB | We support Alt-1, as explained earlier. We really hope this issue could be concluded n this meeting as it has been dragging on for a long time and there are many other details to cover still. |
| ZTE, Sanechips | We cannot accept supporting both Alt-1 and Alt-2. We still support Alt-1 only. |
| Sony | Our preference is Alt-2. We share similar views with LG that if consensus on down-selecting a single alternative cannot be reached, then both Alt-1 and Alt-2 can be supported. |
| Huawei/HiSilicon | We have strong concerns about supporting both Alt-1 and Alt-2. This is a minor issue, with small differences between the alternatives, and it does not deserve a complicated solution having both. |
| Vivo | We cannot accept to support both Alt-1 and Alt-2.  We have one question w.r.t. “or by RAN1#106bis-e at the latest” in proposal 2c. This issue has been exensively studied. What else aspect(s) do we need to study if we have to conclude in next meeting? |
| Intel | We cannot accept supporting both Alt-1 and Alt-2,and we really do not understand the technical mean of this. This would clearly overcomplicate the design for no purpouses. We still support Alt-1 only, which we believe provides the best performance in terms of coverage, which is the main objective/KPI of this AI. |
| Apple | We have strong concerns in supporting both. |
| Qualcomm | We support down-select and preference is Alt-2. |
| Samsung | We also have strong concerns in supporting both. |
| CATT | We also have strong concerns in supporting both. |
| Moderator | * Support Proposal 2c, i.e., support only one of Alt-1 and Alt-2   + Nokia/NSB, ZTE/Saneships, Sony, Huawei/HiSilicon, vivo, Intel, Apple, Qualcomm, Interdigital, Samsung, Ericsson   + Alt-1:     - Nokia/NSB, ZTE/Sanechips, Intel, Interdigital, Apple, Lenovo/MotMob, vivo, NTT DOCOMO, OPPO, Futurewei, CATT, Ericsson   + Alt-2:     - LGE, Sony, Qualcomm, Samsung * Support both Alt-1 and Alt-2   + LGE, Sony, Lenovo/MotMob * Object to supporting both Alt-1 and Alt-2   + ZTE/Sanechips, Huawei/HiSilicon, vivo, Intel, Apple, Samsung, Interdigital, Ericsson, CATT * Wish to conclude this meeting   + Nokia/NSB, vivo, Ericsson |

## 4.8 <Summary of 4th Round>

Three companies support both Alt-1 and Alt-2, 11 companies support down-selection to one alternative, and 8 companes object to supporting both Alt-1 and Alt-2. Hence, the minimum step forward is Proposal 2c. Amongst the companies proposing down selection, there is a strong majority preferring Alt-1 (12 vs. 4).

There don't appear to be any new technical arguments presented in the discussion. Companies supporting Alt-1 generally prefer the larger MIL for 480 and 960 kHz. Companies supporting Alt-2 generally prefer the increased flexibility for user multiplexing (which has been down prioritized for consideration)

Hence, the moderator makes two proposals that can be discussed during the GTW. If Proposal 2e is not agreeable at this time, then the fallback is Proposal 2d. The Moderator encourages a spirit of compromise so we can close this item.

**Proposal 2d**

For enhanced PF0/1 support a single sequence of length equal to the total number of mapped Res of of the PUCCH resource is used. Cyclic shifts for PF0/1 are defined in the same way as Rel-16 for the case that *useInterlacePUCCH-PUSCH* is not configured.

* Note: this is Alt-1 from the RAN1#104 agreement

**Proposal 2e**

For enhanced PF0/1, down-select to one of the following alternatives by RAN1#106bis-e:

* Alt-1: A single sequence of length equal to the total number of mapped Res of of the PUCCH resource is used. Cyclic shifts for PF0/1 are defined in the same way as Rel-16 for the case that *useInterlacePUCCH-PUSCH* is not configured.
* Alt-2: A single sequence of length equal to the number of mapped Res per RB of the PUCCH resource is used, and the sequence is repeated in each RB. Cyclic shifts are cycled across RBs in a similar way as for Rel-16 for PF0/1 for the case that *useInterlacePUCCH-PUSCH* is configured

Thank-you for the good discussion. The following was endorsed during the GTW:

Agreement:

For enhanced PF0/1 support a single sequence of length equal to the total number of mapped Res of of the PUCCH resource is used. Cyclic shifts for PF0/1 are defined in the same way as Rel-16 for the case that *useInterlacePUCCH-PUSCH* is not configured.

* Note: this is Alt-1 from the RAN1#104 agreement

# 5 RE Mapping for Enhanced PF0/1/4 for 120 kHz SCS

Agreement:

* For 120 kHz SCS:
  + Support at least Alt-1 for enhanced PF0/1 for both PUCCH resources before and after dedicated PUCCH resource configuration
  + FFS: Whether or not Alt-2 is additionally supported for PF0/1 for either or both of the following:
    - PUCCH resources before dedicated PUCCH resource configuration
    - PUCCH resources after dedicated PUCCH resource configuration
  + FFS: Supported RE mapping scheme(s) amongst {Alt-1, Alt-2} for enhanced PF4 including design details
* Notes:
  + Alt-1 = all Res within each RB are mapped
  + Alt-2 = a subset of Res within each RB are mapped (sub-PRB interlaced mapping)
  + Which RE mapping scheme(s) to support for PF0/1/4 to be concluded in RAN1#106
* Note: No further enhancements on RB shortage issue and requency hopping distance issue should be considered for PUCCH resource sets prior to RRC configuration.

The open issues are:

* Decide whether or not to additionally support Alt-2 for PF0/1 before/after dedicated PUCCH resource configuration
* Decide which amongst Alt-1, Alt-2 are supported for DMRS of PF4

The following table provides a summary of company proposals on this topic.

|  |  |
| --- | --- |
| **Company** | **Company Proposals** |
| Intel | **Proposal 4: For the enhanced (multi-RB) PUCCH formats 0/1 for 120 kHz SCS only mapping over all Res within each RB is supported.**  **Proposal 5: For the enhanced (multi-RB) PUCCH formats 4 for 120 kHz SCS all Res within each RB are mapped.** |
| Futurewei | ***Proposal 2. For PF0/1, consider support Alt-2 additionally for 120kHz SCS only if it provides notable MIL gain over Alt-1; for PF4, support one alternative that has better MIL for more cases, and we are inclined to Alt-1 based on the simulation results***  ***Proposal 3. Alt-2 can be considered for before and after dedicated PUCCH resource configurations only if notable MIL gain is observed. If there is only marginal MIL gain or no MIL gain over Alt-1, Alt-2 should not be supported for either before or after PUCCH resource allocation.***  ***Proposal 4. For PF0, sub-PRB resource mapping can provide marginal MIL gains for 120kHz SCS, thus can be considered for both before and after dedicated PUCCH resource configurations***  ***Proposal 5. Support only the full-RE resource mapping for PF1. Sub-PRB resource mapping for PF1 is not considered due to inferior MIL performance.***  ***Proposal 6. Support only the full-RE resource mapping for PF4. Sub-PRB resource mapping for PF4 should not be supported ~~for~~ due to its inferior MIL performance.*** |
| Vivo | **Proposal 8: For enhanced PUCCH format 0/1, for 120 kHz SCS, we additionally support alt 2 for RE mapping for PUCCH resource after dedicated PUCCH resource configuration.**  **Proposal 10: For DMRS of PUCCH format 4, the sub-PRB interlaced mapping should be supported.** |
| ZTE | **Proposal 3: Sub-PRB mapping is not supported for PF0/1.**  **Proposal 4: Sub-PRB mapping is not supported for DMRS of PF4.** |
| NTT DOCOMO | ***Proposal 4:*** *Alt-1 should be supported for enhanced PF0/1/4 for both PUCCH resources before and after dedicated PUCCH resource configuration.* |
| Nokia | ***Proposal 3:*** *For 120 kHz SCS, all Res within each RB are mapped also for enhanced PUCCH format 4 (i.e. Alt-1).*  ***Proposal 4:*** *Multiple RE mapping schemes are not supported for enhanced PUCCH format 0/1/4 .* |
| Apple | ***Proposal 3:*** *To ensure consistent design across all SCSs, for 120 kHz SCS, all Res within each RB are mapped.* |
| LGE | **Proposal #1: Considering the inter-modulation distortion (IMD) issue for the sub-PRB interlaced mapping and the implementation complexity to support two different RE mapping methods, support only Alt-1 (i.e., all Res within each RB are mapped) as the unified RE mapping for all PUCCH format 0/1/4 and for both PUCCH resources before and after dedicated PUCCH resource configuration.** |
| Samsung | **Proposal 3: Support Alt-1 (full-PRB mapping) for PUCCH format 0/1/4.** |
| Huawei | ***Proposal 3: Sub-PRB interlaced mapping is not introduced for 120 kHz SCS.*** |
| Interdigital | ***Proposal 4:*** *It is preferred to support all RE mapping for DMRS of PUCCH format 4 with 120 kHz SCS as well as other enhanced PUCCH formats.* |
| WILUS | * *Proposal 1: The interlaced or sub-PRB interlaced design even for enhanced PF4 seems not necessary to apply to 60GHz unlicensed spectrum from the perspective of power boosting in the new numerologies, i.e., 480kHz, 960kHz, and 120kHz SCS.* * *We support Alt-1 even for enhanced PF4 in addition to support of Alt-1 for PF0/1 which was already agreed at the RAN1#105-e meeting.*   + *Alt-1: All Res within each RB are mapped.*     - *Note: PRB and sub-PRB interlaced mapping is not considered further.* |
| MediaTek | Proposal 2: Support only Alt-1 as the RE mapping scheme for enhanced PUCCH format 4. |
| Spreadtrum | ***Proposal 2: For enhanced (multi-RB) PUCCH Formats 0/1/4 for 120 kHz SCS, support allocation of N\_RB contiguous RBs in which all Res within each RB are mapped. Sub-PRB interlaced mapping is not considered further.*** |
| Ericsson | **Proposal 4 Do not support sub-PRB interlace mapping (Alt-2) for PUCCH Formats 0/1 either before or after RRC configuration.**  **Proposal 5 Do not support sub-PRB interlace mapping (Alt-2) for DMRS of PUCCH Format 4.** |

The following is a high level summary of company evaluations:

|  |  |
| --- | --- |
| **Company** | **Evaluation summary** |
| Intel | * PF0   + MIL evaluated assuming US, Europe, and SK regulations   + Compared Alt-1 vs. Alt-2 (Comb-2 pattern) for two different sequence constructions (single long sequence, repeated sequence + CSC)   + N\_RB ranges from 1 .. 40   + Delay spread 5 ns and 40 ns   + **MIL loss for Alt-2** |
| Futurewei | * MIL evaluated assuming US and SK regulations * Compared Alt-1 vs. Alt-2 (Comb-2, 4, and 6) * N\_RB = 1, 2, 4, 8, 16, 22 * 10 ns Delay spread * PF0   + **MIL gain for Alt-2 ranging from -1.5 .. 2 Db depdending on # of RBs and Comb 2, 4, or 6**   + Gain increases as comb becomes more sparse * PF1   + Comparable MIL between Alt-1 and Alt-2 for N\_RB = 22   + **MIL loss for Alt-2 ranging from 0.5 .. 3 Db depending on # of RBs and Comb 2, 4, or 6**   + Loss increases as the comb becomes more sparse * DMRS of PF4   + **MIL loss for Alt-2 ranging from 0.5 .. 7 Db depending on # of RBs and Comb 2, 4, or 6**   + Loss increases as the comb becomes more sparse |
| vivo | * PF0   + Compared Alt-1 (called Alt 1-2) vs. Alt-2 (called Alt 2-1)   + N\_RB = 2   + Multiplexing of 2 users     - Alt-1: CDM mux (2 users use different cyclic shifts)     - Alt-2: FDM mux (Comb-2 with 1 user on each comb)   + Comparable MIL for Alt-1 and Alt-2 if UE powers are balanced   + **Alt-2 has ~3 Db MIL gain in US/SK if UE receive powers are imbalanced by 3 (?) Db** * DMRS of PF4 * Compared Alt-1 vs. Alt-2 for 3 combinations of (UE\_EIRP, UE\_P) considering up to 18 RBs   + (25, 21) dBm   + (40, 21) dBm   + (43, 23) dBm * 4, 11, 22 bit payload * 14 OFDM symbols * Delay spread 10 ns * **MIL gain for Alt-2 of 0.5 – 2 Db (dependent on payload, delay spread)** |
| ZTE | * PF0   + MIL evaluated assuming SK regulations   + Compared Alt-1 vs. Alt-2 (Comb 2 or 12)   + 5, 10, 20 ns delay spread   + **MIL loss for Alt-2 of ~ 1Db** * DMRS of PF4   + MIL evaluated assuming US, EU, SK regulations   + Compared Alt-1 vs. Alt-2 (Comb 2)   + Considered 0 and 3 Db power boosting for DMRS for Alt-2   + 4, 11, 22 bit payload   + 5, 10, 20 ns delay spread   + **Comparable performance for Alt-1 vs. Alt-2 when 3 Db power boosting is used for DMRS in Alt-2** |
| Ericsson | * PF0   + MIL evaluated assuming US and EU regulations   + Compared Alt-1 vs. Alt-2 (Comb-2)   + 2,4,6,8,10,12 RBs   + 5 ns and 40 ns delay spread   + **Comparable performance between Alt-1 and Alt-2** * PF0 when multiplexing 2 users   + MIL evaluated assuming US and EU regulations   + Multiplexing of 2 users     - Alt-1: CDM mux (2 users use different cyclic shifts)     - Alt-2: FDM mux (Comb-2 with 1 user on each comb)   + Considered balanced and imbalanced (3 Db) Rx powers between UE1 and UE2   + 10 RBs   + 5 and 20 ns delay spread   + **Comparable performance between Alt-1 and Alt-2 for both balanced and imbalanced Rx powers** * DMRS of PF4   + MIL evaluated assuming US and EU regulations   + Compared Alt-1 vs. Alt-2   + Multiplexing of 2 or 4 users     - Comb-2 for DMRS used when OCC2 for UCI is configured       * 2 users multiplexed     - Comb-4 for DMRS used when OCC4 for UCI configured       * 4 users multiplexed   + 3 Db power boosting for DMRS for Alt-2   + 10 RBs   + 4, 11, 22 bit payload   + 5 and 20 ns delay spread   + **Comparable performance between Alt-1 vs. Alt-2** |

In summary:

* For PF0
  + Two companies (vivo, Futurewei) found a MIL gain for Alt-2
    - One company (vivo) found that the gain occurs when the received powers for 2 users are imblanced (no gain for balanced received powers)
  + Two companies (Intel, ZTE) found a MIL loss for Alt-2
  + One company (Ericsson) found comparable MIL for Alt-1 and Alt-2 for both balanced and imbalnced receive powers for 2 users
* For PF1
  + One company (Futurewei) found a MIL loss for Alt-2
* For DMRS of PF4
  + One company (vivo) found a MIL gain for Alt-2
  + Two companies (ZTE, Ericsson) found comparable MIL for Alt-1 and Alt-2 when 3 Db power boosting is used for DMRS

The following is a summary of support for Alt-1 and Alt-2 based on company contributions:

For PF0/1 for PUCCH resources after RRC configuration:

* Alt-1 only:
  + Intel, ZTE, NTT DOCOMO, Nokia, Apple, LGE, Samsung, Huawei, Interdigital, WILUS, Spreadtrum, Ericsson, Futurewei (PF1)
* Alt-1 + Alt-2:
  + vivo, Futurewei (PF0 only)

For PF0/1 for PUCCH resource sets prior to RRC configuration:

* Alt-1 only:
  + Intel, ZTE, NTT DOCOMO, Nokia, Apple, LGE, Samsung, Huawei, Interdigital, WILUS, Spreadtrum, Ericsson, Futurewei (PF4)
* Alt-1 + Alt-2:
  + Futurewei (PF0 only)

For DMRS of PF4:

* Alt-1:
  + Intel, ZTE, NTT DOCOMO, Nokia, Apple, LGE, Samsung, Huawei, Interdigital, WILUS, MediaTek, Spreadtrum, Ericsson
* Alt-2:
  + vivo

**Proposal 3 For PF0/1 for PUCCH resources after RRC configuration, further discuss and decide by end of RAN1#106-e whether or not Alt-2 is supported in addition to Alt-1.**

**Proposal 4 Agree to the following:**

* For PF0/1 for PUCCH resource sets prior to RRC configuration, Alt-2 (sub-PRB interlaced mapping) is not supported.

**Proposal 5 For DMRS of PF4, further discuss and down-select by end of RAN1#106-e one of Alt-1 and Alt-2.**

## 5.1 <1st Round Comments>

Please provide your company view on Proposasl 3, 4, 5:

|  |  |
| --- | --- |
| **Company** | **View/Position** |
| Nokia, NSB | We are ok with the proposals. Ideally we should decide already before the end of the meeting. |
| Vivo | We are okay with proposal 3, 4, and 5.  As we discussed, Alt 2 has the benefit when UE multiplexing is considered. We propose to support it along with Alt 1 to cover all possible senarios.  Espeacially for DMRS for PF4, as summaried by the FL, no evaluation results showed any MIL performance loss of Alt 2. |
| ZTE, Sanechips | We agree with Proposal 3, 4, and 5. Alt2 should be excluded for PF0/1/4 for PUCCH resources prior to/after RRC configuration. |
| Huawei/HiSilicon | We are fine with Proposal 4. Regarding Proposal 3 and 5, while this is a good intention of what RAN1 should do, explicitly making such intermediate proposals has limited value, in our view. |
| Lenovo, Motoroloa Mobility | We agree with Proposal 3,4, and 5. |
| Apple | We are fine with the proposals |
| Intel | We are OK with the FL’s proposals. |
| CATT | Support the proposals |
| Sony | We are ok with P3, P4, P5. |
| NTT DOCOMO | We agree with all of Proposal 3,4 and 5. |
| Qualcomm | We support proposal 3,4, and 5 |
| Samsung | We are ok with Proposal 3, 4, and 5. |
| LG Electronics | We are fine with the above proposals. |
| Futurewei | We agree with Proposal 3, 4, and 5.  We added our standings with PF1 and PF4 into the list, which is Alt-1, as it was not captured by the summary. |

## 5.2 <Summary of 1st Round>

It seems there is no objection to Proposal 4, hence the moderator assumes that this can be agreed on the first deadline for this email thread (8/19).

## 5.3 <2nd Round Comments>

Please continue to discuss the issues in Proposals 3 and 5. Please only comment on Proposal 4 if you have a strong concern.

|  |  |
| --- | --- |
| **Company** | **View/Position** |
| Nokia, NSB | In our view, based on the discussion on Proposal 3 and Proposal 5 so far there should be enough confidence to go with Alt-1 only. |
| InterDigital | We are fine with the proposals. |
| Lenovo, Motoroloa Mobility | We agree with Proposal 3 and 5. |
| NTT DOCOMO | We support Alt-1 for both PF0/1 after RRC configuration and DMRS of PF4. |
| Samsung | We support Proposal 3 and 5. |
| ZTE, Sanechips | We are fine with Proposal 3 and 5 |
| Qualcomm | We support Proposal 3 and 5. |
| Apple | We are fine with Proposals 3 and 5 |
| Intel | We are fine with both proposals |

## 5.4 <Summary of 2nd Round>

On Proposal 4, it seems that there is no objection. Hence the FL recommends the following

**FL Recommendation:** Agree to Proposal 4 (see Section 5 above)

For enhanced PF0/1, given that there is a very strong majority view for supporting Alt-1 only, and that a majority of companies providing performance results found no gain from Alt-2, it is recommended only Alt-1 is supported for enhanced PF0/1 for PUCCH resources after RRC configuration. Hence the moderator recommends to agree to the following proposal.

**Proposal 3a Agree to the following:**

* For enhanced PF0/1, for PUCCH resources after RRC configuration, Alt-2 (sub-PRB interlaced mapping) is not supported.

For DMRS of enhanced PF4, again, there is a very strong majority view for supporting Alt-1 (13 companies for Alt-1; 1 company for Alt-2). Given that two companies found no gain from Alt-2, and that one company found some gain, it does not seem viable to support Alt-2 only. Furthermore, there have been multiple concerns expressed during discussions in previous meetings of supporting two alternatives. Hence, unless the proponent of Alt-2 can convince other companies to support Alt-1 + Alt-2, it feels like the only practical way forward to is to adopt Alt-1 only. At this stage in the WI, it should be prioritized to have a working system without incurring extra complexity. Based on this, the moderator recommends the following

**Proposal 5a Agree to the following**

* For DMRS of enhanced PF4, only Alt-1 is supported (all Res within each RB are mapped). Alt-2 (sub-PRB interlaced mapping) is not discussed further.

## 5.5 <3rd Round Comments>

Please provide your company view on Proposals 3a and 5a. Note: the moderator assumes there is no ojection to Proposal 4 in Section 5.

|  |  |
| --- | --- |
| **Company** | **View/Position** |
| Intel | We support both proposal 3a and 5a. |
| vivo | We strongly believe the benefit of Alt-2 when UE multiplexing is considered. Such design (Alt-1 and Alt-2) would provide flexibility for the system considering different deployment scenarios.  Some commnets:  1. 3GPP is contribution driven. We don’t think the 2nd entence of proposal 5a is needed.  2. We understand we’re minority. For the sake of progress, we will not object proposal 3a, and 5a (with the 2nd entence removed). However, we have a request to be noted about the decision criterion for us to accept these propsoals. Given that companies do not see optimization of user multiplexing as an important design criterion for RE mapping of enhanced PF0/1/4, we request a fair and consistent decision criterion for other designs of enhanced PF0/1/4 as well. The following note is requested.  Note: optimization of user multiplexing for enhanced PUCCH format 0/1/4 is not considered in Rel-17. |
| LG Electronics | We support both Proposal 3a and 5a. |
| NTT DOCOMO | We support both Proposal 3a and Proposal 5a. |
| Nokia, NSB | We support both Proposal 3a and Proposal 5a |
| Lenovo, Motoroloa Mobility | We agree with Proposal 3a and also agree in principle with Proposal 5a.  Also, we are open to further consider Alt 2 in addition to Alt 1. |
| Apple | We support both propsals |
| Qualcomm | We support both proposals |
| Sony | We support proposals 3a and 5a. |
| Huawei/HiSilicon | We support Proposal 3a and Proposal 5a. |
| ZTE, Sanechips | We support Proposal 3a and Proposal 5a. |
| Futurewei | We support both proposals. |
| InterDigital | We support both proposals. |
| CATT | We support both proposals. |
| Samsung | We support both proposals. |

## 5.6 <Summary of 3rd Round >

Thank-you all for the considering these proposals, and thank-you especially to vivo for making a compromise. Based on vivo’s (fair) requests to remove the 2nd sentence from Proposal 5a and add a Note, please see the following updated proposal (merged into one). I plan to raise this proposal in the GTW today.

**Proposal 5b (Merge of 3a and 5a)**

In the following, Alt-1 and Alt-2 refer to the RE mapping agreement for 120 kHz from RAN1#105-e:

* For enhanced PF0/1, for PUCCH resources after RRC configuration, Alt-2 (sub-PRB interlaced mapping) is not supported.
* For DMRS of enhanced PF4, only Alt-1 is supported (all Res within each RB are mapped).
* Note: optimization of user multiplexing for enhanced PUCCH format 0/1/4 is not considered in Rel-17.

Thank-you for the good discussion. The following was agreed in the GTW:

Agreement:

In the following, Alt-1 and Alt-2 refer to the RE mapping agreement for 120 kHz from RAN1#105-e:

* For enhanced PF0/1, for PUCCH resources after RRC configuration, Alt-2 (sub-PRB interlaced mapping) is not supported.
* For DMRS of enhanced PF4, only Alt-1 is supported (all Res within each RB are mapped).
* Note: optimization of user multiplexing for enhanced PUCCH format 0/1/4 is not considered in Rel-17.

# 6 Payload Limitation and Rate Matching for PF4

## 6.1 Maximum UCI Payload for PF4

In 38.214, it is specified that the UE is not expected to report CSI when the total number of UCI + CRC bits larger than 115 when configured with PF4.

A UE is not expected to report CSI with a total number of UCI bits and CRC bits larger than 115 bits when configured with PUCCH format 4. For CSI reports transmitted on a PUCCH, if all CSI reports consist of one part, the UE may omit a portion of CSI reports. Omission of CSI is according to the priority order determined from the Prii,CSI(*y,k,c,s*) value as defined in Clause 5.2.5. CSI report is omitted beginning with the lowest priority level until the CSI report code rate is less or equal to the one configured by the higher layer parameter *maxCodeRate*.

In the last meeting it was discussed whether or not this limitation should be lifted for enhanced (multi-RB) PF4.

The following table provides a summary of company proposals on this topic.

|  |  |
| --- | --- |
| **Company** | **Company Proposals** |
| Intel | **Proposal 7: For enhanced PUCCH format 4, rate matching to N PRBs without changing UCI limitation is supported (Alt-1).** |
| Futurewei | ***Proposal 7: From the standard effort perspective, it is recommended to keep the same restriction (upper limit) on the UCI payload for PF4, and use PF3 for a larger UCI payload, similar to Rel-16.*** |
| ZTE | **Proposal 7: Rate matching can be performed over N PRBs, and the UCI payload limitation can be relaxed.** |
| NTT DOCOMO | ***Proposal 6:*** *The same CSI payloads upper limit as in Rel-15/16 should be supported for PUCCH format 4with multi-PRB allocation.* |
| Apple | ***Proposal 6:*** *For rate matching in enhanced PF4*   * *Support same restriction (upper limit) on the UCI payload as in Rel-15/16 PF4* * *Rate matching to the configured number of RBs N\_RB (similar to Rel-16 rate matching to the fixed number of RBs N = 10/11 of an interlace for PF3)* |
| Qualcomm | We iterate our views that for UCI payload, we support Alt-a, as PUCCH enhancement in this WI is to increase coverage rather than capacity.  Moderator’s note: Alt-a corresponds to “Suppport same restriction for PF4 as in Rel-15/16” |
| OPPO | **Proposal 8: for enhanced PF4, maintain the same UCI payload limitation.** |
| Samsung | **Proposal 4: Support rate matching over all configure RBs with existing UCI upper limit for PUCCH format 4.** |
| Huawei | ***Proposal 5: Increase the UCI payload upper limit and do rate matching across the whole configured PRBs for enhanced PUCCH format 4.*** |
| MediaTek | **Proposal 3: Support same restriction (upper limit) on the UCI payload as in Rel-15/16 for PF4** |
| Ericsson | **Observation 3 It is not necessary to remove the PUCCH payload limitation of 115 bits for enhanced (multi-RB) PF4 since the objective of the WI is to increase coverage for existing payloads.** |

The following two alternatives are identified, and the company support is as follows:

* Alt-1: Maintain same maximum UCI payload for PF4 as in Rel-15/16 (115 bits)
  + Intel, Futurewei, NTT DOCOMO, Apple, Qualcomm, OPPO, Samsung, MediaTek, Ericsson
* Alt-2: Increase the maximum UCI payload for PF4
  + ZTE, Huawei

For those companies preferring Alt-1, the rationale is that the objective is to increase coverage for PF4, and that PF3 can be used for larger payloads. For those companies preferring Alt-2, the rationale is that if multiple RBs are supported, then those RBs can be used to carry larger payload.

**Conclusion 1 Conclude on the following:**

* For enhanced (multi-RB) PF4, maintain the same maximum UCI payload limit as in Rel-15/16 (115 bits).

### 6.1.1 <1st Round Comments>

Please provide your company view on the Conclusion 1. If your have concerns with the conclusion, please indicate what is your proposal for the increased payload size for PF4.

|  |  |
| --- | --- |
| **Company** | **View/Position** |
| Nokia, NSB | We support the Conclusion 1, for maximized commonality with the existing specs. |
| Vivo | We support conclusion 1. |
| ZTE, Sanechips | We are fine with Conclusion 1 considering the majority’s view. |
| Huawe/HiSilicon | The advantage of increasing the maximum UCI payload is more, or better, CSI reports. It appears to be a trivial specification change to achieve that. Increasing the UCI limit may not be compromising coverage since ultimately the Gnb will configure the number of PRBs and the CSI reporting mode. |
| Apple | We are fine with the conclusion |
| Intel | We support the FL’s conclusion, and we do not see any technical merit in lifting the Rel.16 restriction for the UCI payload limit for PF4. In case a larger payload would need to be used PF3 could be used, which was engineered for this purpouse. |
| CATT | Support the conclusion. |
| NTT DOCOMO | We agree with the Conclusion 1. |
| Qualcomm | We support the conclusion 1 |
| Samsung | We support the conclusion. |
| OPPO | Fine to conclusion 1 |
| LG Electronics | We are fine with the Conclusion 1. |
| Futurewei | We support Conclusion 1. The UCI payload limit from Rel-15/16 can be reused for PF4, i.e., for FR2-2 we do not have to use PF4 for larger UCI payload since there is PF3 as an alternative if FR2-2 deicide to reused Rel-15/16 PF3. However, with this AI there has been no discussion on PF1/3, and we think it can be beneficial to include some discussions on PF1/PF3 if useful for certain issue like this one. |

### 6.1.2 <Summary of 1st Round>

The following conclusion was reached at the GTW.

Conclusion:

For enhanced (multi-RB) PF4, maintain the same maximum UCI payload limit as in Rel-15/16 (115 bits).

## 6.2 Rate Matching for PF4

The following agreement was made in RAN1#104-e

Agreement:

* The configured number of RBs for enhanced PF 0/1/4 is denoted NRB
  + The minimum value of NRB is 1 for PF 0/1/4 for all subcarrier spacings
  + The maximum value of NRB depends on subcarrier spacing
    - FFS: maximum value for each SCS and each of PF0/1/4
  + FFS: Allowed values of NRB within the [min/max] range
  + FFS: Details of indication of NRB by cell-specific (for PF0/1) and dedicated ignaling (PF0/1/4)
  + FFS: Whether or not multiplexing of users with misaligned RB allocations is supported, where “misaligned” also includes users with different # of RBs.
  + For PF4:
    - The actual number of RBs used for a PUCCH transmission is equal to NRB, i.e., the actual number of RBs does not vary dynamically based on PUCCH payload
    - NRB fulfils the following: where is a set of non-negative integers
* Note: if frequency hopping is enabled, NRB is the number of RBs per hop

Note: decisions on the maximum value of NRB for each SCS and PUCCH format shall take into account link budgets

According to this agreement, when the UE is configured with NRB > 1 for PF4, the actual number of RBs is always equal to the configured number, i.e., NRB does not vary dynamically based on PUCCH payload as it does for PF3 in Rel-15. This is the same situation as for PF2/3 in Rel-16 for the case when interlacing is configured. For example, when a single interlace is configured for interlaced PF2/3, the number of RBs is fixed at 10 or 11 (see 38.211 Section 6.3.2.6.3). This means that as the PUCCH payload varies, the code rate varies, thus rate matching is performed according to the fixed number of RBs. This is captured in 38.212 Section 6.3.1.4 as follows:

6.3.1.4 Rate matching

For PUCCH formats 2/3/4, the total rate matching output sequence length  is given by Table 6.3.1.4-1, where  , , and  are the number of symbols carrying UCI for PUCCH formats 2/3/4 respectively;  and  are the number of PRBs that are determined by the UE for PUCCH formats 2/3 transmission respectively according to Clause 9.2 of [5, TS38.213]; and , , and  are the spreading factors for PUCCH format 2, PUCCH format 3, and PUCCH format 4, respectively.

* **Table 6.3.1.4-1: Total rate matching output sequence length **

|  |  |  |
| --- | --- | --- |
| ***PUCCH format*** | ***Modulation order*** | |
| QPSK | π/2-BPSK |
| PUCCH format 2 |  | N/A |
| PUCCH format 3 |  |  |
| PUCCH format 4 |  |  |

In the last meeting, primarily two alternatives were discussed for rate matching for multi-RB PF4

* Alt-1: Rate matching to *N*RB RBs, similar as for PF2/3 in Rel-16 when interlacing is configured
* Alt-2: Rate matching to 1 RB as in Rel-15/16, followed by repetition of the coded bits in each of the configured RBs

The following table provides a summary of company proposals on this topic.

|  |  |
| --- | --- |
| **Company** | **Company Proposals** |
| Intel | **Proposal 7: For enhanced PUCCH format 4, rate matching to N PRBs without changing UCI limitation is supported (Alt-1).** |
| Futurewei | ***Proposal 8: Consider repetition as a new rate matching mechanism for the small payload case only if the final agreed value of maximum NRB is larger than 16.***  ***Proposal 9: For standard effort consideration, not to further investigate other rate matching mechanisms except for repetition despite the potentially agreed value of maximum NRB*** |
| vivo | **Proposal 11:** **The rate matching to the configured number of RBs should be supported for enhanced PF4.** |
| ZTE | **Proposal 7: Rate matching can be performed over N PRBs, and the UCI payload limitation can be relaxed.** |
| NTT DOCOMO | ***Proposal 7:*** *Similar rate matching mechanism to NR-U PF3 interlaced mapping with 10/11 RBs, i.e., rate matching to the configured number of RBs, should be supported.* |
| Apple | ***Proposal 6:*** *For rate matching in enhanced PF4*   * *Support same restriction (upper limit) on the UCI payload as in Rel-15/16 PF4* * *Rate matching to the configured number of RBs N\_RB (similar to Rel-16 rate matching to the fixed number of RBs N = 10/11 of an interlace for PF3)* |
| Qualcomm | For rate matching mechanism for enhanced PF4, we support Alt-a, as it is beneficial to leverage the same rate matching mechanism as interlaced PF3, which is already supported in Rel-16  Moderator’s note: Alt-a corresponds to Alt-1 above (rate matching to N\_RB RBs) |
| OPPO | **Proposal 9: for enhanced PF4, add bit level diversity and rate-match over N\_PRB.** |
| Samsung | **Proposal 4: Support rate matching over all configure RBs with existing UCI upper limit for PUCCH format 4.** |
| Huawei | ***Proposal 5: Increase the UCI payload upper limit and do rate matching across the whole configured PRBs for enhanced PUCCH format 4.*** |
| MediaTek | **Proposal 4: Support rate matching to the number of allocated RBs using existing rate matching mechanism for PF4.**  Moderator Note: The moderator assumes that MediaTek’s proposal is Alt-1 due to the following statement prior to Proposal 4: “Rate matching should be done via the existing rate matching mechanisms that rate matches to the number of allocated RBs. It is worth noting that repetition based rate matching schemes should be avoid since they do not provide coding gain in general.” |
| Ericsson | **Proposal 6 For enhanced (multi-RB) PF4, support rate matching to the configured number of RBs NRB.** |
|  |  |

The following is a summary of support for the two alternatives for rate matching for PF4:

* Alt-1: Rate matching to *N*RB RBs, similar as for PF2/3 in Rel-16 when interlacing is configured
  + Intel, Futurewei (if max(N\_RB) <= 16), vivo, ZTE, NTT DOCOMO, Apple, Qualcomm, OPPO (?), Samsung, Huawei, MediaTek, Ericsson
* Alt-2: Rate matching to 1 RB as in Rel-15/16, followed by repetition of the coded bits in each of the configured RBs
  + Futurewei (if max(N\_RB) > 16), OPPO(?)

**Proposal 8 Agree to the following:**

* For enhanced (multi-RB) PF4, the UCI payload is rate matched to the configured number of RBs, N\_RB
* Note: This is analogous to Rel-16 for PF2/3 when interlacing is configured when there is a fixed number of RBs for the configured interlace(s).

### 6.2.1 <1st Round Comments>

Please provide your company view on Proposal 8.

|  |  |
| --- | --- |
| **Company** | **View/Position** |
| Nokia, NSB | We support Proposal 8. |
| Vivo | We support proposal 8. |
| ZTE, Sanechips | We are fine with Proposal 8. |
| Huawei/HiSilicon | We are fine with Proposal 8. |
| Lenovo, Motorola Mobility | We are okay with proposal 8. |
| Apple | We are fine with the proposal |
| Intel | We are OK with proposal 8 |
| NTT DOCOMO | We agree with Proposal 8. |
| Qualcomm | We support proposal 8 |
| Samusng | We support Proposal 8. |
| OPPO | Our proposal is to allow bit level diversity, i.e. repetition at UCI bit level. Then for the coded bit rate-matching, we support rate match over N\_PRB. |
| LG Electronics | We support the proposal 8. |
| Futurewei | We are ok with Proposal 8. |

### 6.2.2 <Summary of 1st Round>

The following agreement was reached at the GTW.

Agreement:

* For enhanced (multi-RB) PF4, the UCI payload is rate matched to the configured number of RBs, N\_RB
* Note: This is analogous to Rel-16 for PF2/3 when interlacing is configured when there is a fixed number of RBs for the configured interlace(s).

# 7 PUCCH Resource Set Prior to RRC Configuration

## 7.1 Indication of Number of RBs

Agreement:

* The configured number of RBs for enhanced PF 0/1/4 is denoted NRB
  + The minimum value of NRB is 1 for PF 0/1/4 for all subcarrier spacings
  + The maximum value of NRB depends on subcarrier spacing
    - FFS: maximum value for each SCS and each of PF0/1/4
  + FFS: Allowed values of NRB within the [min/max] range
  + FFS: Details of indication of NRB by cell-specific (for PF0/1) and dedicated ignaling (PF0/1/4)
  + FFS: Whether or not multiplexing of users with misaligned RB allocations is supported, where “misaligned” also includes users with different # of RBs.
  + For PF4:
    - The actual number of RBs used for a PUCCH transmission is equal to NRB, i.e., the actual number of RBs does not vary dynamically based on PUCCH payload
    - NRB fulfils the following: where is a set of non-negative integers
* Note: if frequency hopping is enabled, NRB is the number of RBs per hop
* Note: decisions on the maximum value of NRB for each SCS and PUCCH format shall take into account link budgets based at least on the agreed evaluation assumptions

The following table provides a summary of company proposals on this topic.

|  |  |
| --- | --- |
| **Company** | **Company Proposals** |
| Futurewei | ***Proposal 10: Support the Alt-1 to configure N\_RB through SIB1 prior to RRC configuration.*** |
| Vivo | **Proposal 3: The indication of NRB for common PUCCH format 0/1 can be predefined in the table of PUCCH resource sets before dedicated PUCCH resource configuration.** |
| CATT | **Proposal 5 The number of RBs for PUCCH format0/1/4 can be cell specific or UE specific configured.**  **Proposal 7 The Gnb needs to indicate the UE with the configured number of RBs for PUCCH format0/1/4 during the initial access process.** |
| NTT DOCOMO | ***Proposal 8:*** *For the PUCCH resource table for initial PUCCH resource,* *at least cell-specific and UE-specific PRB offsets should be revisited for multi-PRB allocation.*  ***Proposal 9:*** *The maximum number of RBs for PUCCH resource sets before dedicated PUCCH configuration should be specified considering minimum. CBW, transmission power gain for each number of RBs under the regulations and FDM capacity.*  ***Proposal 11:*** *For the PUCCH resource sets before dedicated PUCCH resource configuration,* the *cell-specific number of RBs for PUCCH format 0/1 before dedicated PUCCH configuration should be indicated by SIB1* |
| Nokia | ***Proposal 6:*** *In case of common PUCCH resource set, Alt-2 is supported for the configuration of the number of RBs.*  Moderator’s note: Alt-2 refers to the agreement from RAN1#104bis-e on configuration granularity  ***Proposal 7:*** *Common PUCCH resource sets prior to dedicated configuration are modified to indicate different number of RBs depending on the BWP SCS value* |
| Apple | ***Proposal 7:*** *For PUCCH Resource Sets prior to RRC configuration the UE should use the value of NRB configured through SIB1.* |
| LGE | **Proposal #7: To determine the value of NRB for the initial PUCCH resource, the following options can be considered:**   * **Opt.1: Directly use the predefined maximum value of NRB for PF 0/1 in the specification.** * **Opt.2: Use the value of NRB configured through RRC ignaling (e.g., SIB1) by Gnb.** * **Opt.3: Calculate the value of NRB based on the size of the initial BWP and the required number of FDM resources for each PUCCH resource set.** |
| Qualcomm | **Proposal 2: RAN1 support different number of RBs for common PUCCH resource by configuring multiple N\_RBs through RRC.**  **Proposal 3: RAN1 should study how to indicate UE’s capability of supporting wide-band PUCCH during initial access.** |
| Samsung | **Proposal 5: Support contiguous multi-PRB PUCCH format 0/1 before RRC connection setup**   * **support different number of multiple PRBs for different scenarios.** * **support different number of multiple PRBs for different Ues.** |
| Ericsson | **Proposal 2 For PUCCH resource sets prior to RRC configuration, support indication via SIB1 of the number of RBs, NRB, for PUCCH format 0/1. If the number of RBs is not indicated, the UE assumes single RB. FFS: supported value(s) of NRB.** |

The following broad alternatives have been identified for indication of the number of RBs, N\_RB:

* Alt-1: N\_RB is signaled via SIB1
  + Futurewei, CATT(?), NTT DOCOMO, Apple, Qualcomm, Ericsson
* Alt-2: N\_RB is predefined by specification for each SCS, and is possibly different for each row of the PUCCH configuration table
  + vivo, Nokia
* Alt-3: Indicated by DCI that schedules Msg4
  + Samsung

Two companies (see [9] and [16]) also raised the possibility to support a mechanism to indicate a different number of RBs for different Ues during initial access.

**Proposal 9 Further discuss how to indicated the number of RBs for PUCCH resources prior to RRC configuration**

### 7.1.1 <1st Round Comments>

Please provide your company view on Proposal 9, including addressing the following questions:

**Question 1**: Which alternative for indication of number of RBs do you support (see Alt-1,2,3 above)?

**Question 2**: Is it needed to support a mechanism to indicate a different number of RBs for different Ues during initial access (e.g., as discussed in [9] and [16])? If so, what are the benefits, and how should the mechanism work?

|  |  |
| --- | --- |
| **Company** | **View/Position** |
| Nokia, NSB | Question 1: We support Alt-2, as it avoids additionals ignaling and follows the same principle as Rel-15 and Rel-16. Since the time domain resource allocation for PUCCH is also predefined, it makes little sense to choose a different solution for the frequency domain.  Question 2: The need for UE specific resource allocation has not been identified in earlier releases, and we do not see that it is needed in this WI either. Such ignaling mechanism would unnecessarily complicate the operation without clear benefits. |
| Vivo | Q1: support Alt 2 for the same reason as Nokia.  Q2: we’re not clear about the benefits of UE-specific indication during initial access. |
| ZTE, Sanechips | We are fine with Proposal 9.  A1: Al1 is preferred due to the better flexibility.  A2: There is no need to do so. |
| Huawei/HiSilicon | Alt. 1 appears to be the most flexible and simple one. |
| Lenovo, Motoroloa Mobility | Question 1: We support Alt1 and Alt2.  Question 2: Similar view as Nokia |
| Apple | We are fine with proposal 9  Alt-1. Simple way of signaling N\_RB to accommodate different UE types |
| Intel | Q1: We support Alt.1, which allows to achieve an higher level of flexibility.  Q2: We believe that for initial access, mechanism to indicate different number of RBs may not be technically needed. However, since UE’s capability ignaling has not been discussed in the summary, we would like to highlight this point here. We believe that a framework to allow a UE to indicate capability ignaling (i.e., beamforming gain) may need to be introduced. As pointed out in our tdoc [19], UE power class and beamfoming gain have high impact in MIL, and if Gnb makes wrong assumption the related loss may be quite substantial. We believe that this point is worth discussion within RAN1. |
| CATT | Q1: We support alt1 and ok with alt3 .  Q2: We think it is beneficial to support a mechanism to indicate a different number of RBs for different Ues during initial access vs after initial access. |
| NTT DOCOMO | Q1: We support Alt-1.  Q2: No. We think the indication of different number of RBs for different Ues is not needed for initial access. |
| Qualcomm | Question 1: we support Alt 1  Question 2: the motivation to support different number of RBs used by different Ues is to more efficiently use the 16 common PUCCH resource. Without this mechanism, if N\_RB is assigned the max N\_RB as discussed in section 2, there may not be enough RBs for total 16 resources, as pointed by some contributions. If we choose some smaller value of N\_RB, it will hurt coverage. |
| Samsung | Q1: can be further discussed after progress for Q 2, i.e. whether support UE-specific number of RBs. If RAN1 only supports Cell-specific configuration, Alt-1 or 2 is sufficient, Alt-1 is more preferred due to more ignaling. If UE-specifci indication is supported, , Alt-3 or Alt 4 (a new alternative not listed above) is beneifical.  Alt-4: N\_RB is predefined by specification for each SCS, and is possibly different for different PUCCH resource within a row of the PUCCH configuration table.  Q2: in our understanding, UE-specific number of PRBs is beneficial. If only cell-specific value is supported, to ensure good coverage for all Ues, Gnb may have to configure the largest number. Considering the maximum power for different power class is very different (resulting in very different number of PRBs), the transmission efficiency would be very low.  Gnb can indicate UE-specific number of PRBs, e.g. by reserved bit field in PDCCH scheduling Msg 4, or by indicating a PRI (if different PUCCH resource index within a row can be associated with different number of PRBs as provided in Alt.4 above). |
| OPPO | Alt-1 and Alt-2 are fine with us. Alt-2 follows R15 principle, while Alt1 gives more flexibility. |
| LG Electronics | Q1: We added Alt-4 for determine the number of RBs for PUCCH resources prior to RRC configuration based on the size of the initial BWP and the required number of FDM resources for each PUCCH resource set.  Q2: Considering the different capabilities and geometry of UE, it can be beneficial to configure PUCCH resources with different numbers of RB. The mechanism to indicate the PUCCH resources with the appropriate number of RBs for the UE requires further discussion with capability signaling during initial access. |
| Futurewei | We are ok with Proposal 9.  Q1: We prefer Alt-1 for better flexibility.  Q2: We assume that UE-specific indication during initial access is beneficial for power consumption. If this is the case, we are fine with the proposals in [9] and [16]. |
| Moderator | Please continue to discuss  Several companies have suggested that the issues in 7.1 and 7.2 should be discussed together. That is fine, and on the next upate of the FL summary, I will try to merge the discussions into one. Until then, please feel free to enter your comments in either table (or both) and I will consolidate. |
| InterDigital | Q1: We support Alt-2.  Q2: We don’t see the need to indicate a different number of PRBs. |
|  |  |

### 7.1.2 <Summary of 1st Round>

The following is a summary of responses to Question 1:

* Alt-1:
  + ZTE/Sanechips, Huawei/HiSilicon, Lenovo/MotMob, Apple, Intel, CATT, NTT DOCOMO, Qualcomm, Samsung (if UE-specific RB indication not supported), OPPO, Futurewei, Ericsson
* Alt-2:
  + Nokia/NSB, vivo, Lenovo/MotMob, OPPO, Interdigital
* Alt-3:
  + CATT
* Alt-4 (see proposed additional alternative in Samsung comment)
  + Samsung
* Alt-5 (see proposed additional alternative in LGE comment)
  + LGE

The following is a summary of Question 2:

* UE specific mechanism not needed/beneficial
  + Nokia/NSB, vivo, ZTE/Sanchips, Lenovo/MotMob, Intel, NTT DOCOMO, Intel\*, Interdigital, Ericsson
* UE specific mechanism needed/beneficial
  + CATT, Qualcomm, Samsung, LGE\*\*

\*Intel suggests discussinig UE capability signaling on beamforming gain [Moderator comment: this seems like a separate issue to be discussed, especially since UE capability exchange occurs after configuration of PUCCH resource sets that are used prior to RRC configuration, e.g., for ACK of Msg4]

\*\*LGE suggests UE capability signaling as the UE specific mechanism. [Moderator comment: lt appears there is a causality issue with LGE’s proposal; PUCCH resource set configuration is required prior to UE capability exchange with the network]

Regarding Question 2 on whether or not a UE specific mechanism is needed, 9 companies view is that it is not needed or technically motivated, whereas 4 companies indicated that it can be beneficial. Given that it is not clear how such a mechanism should work yet, and that it could require quite a lot of discussion to converge on a solution agreeable to all companies, it may not be warranted to pursue this at this stage of the WI. The goal should be for a working system without extra complexity.

Regarding Question 1 on how to indicate the number of RBs, 12 companies support Alt-1, 5 companies support only Alt-2, however, 2 of the 5 also support Alt-1. Many companies have commented that Alt-1 is the most flexible solution, and the moderator observes that it can also avoid potentially long discussions on exactly how many RBs should be supported if the number of RBs is fixed by specification. Based on this, the moderator makes the following proposal to help streamline the work.

**Proposal 9a Agree to the following**

* For PUCCH resource sets prior to RRC configuration, support a parameter in SIB1 that indicates the number of RBs for enhanced (multi-RB) PLUCCH format 0/1
* FFS: Granularity of the values for the parameter. Note: the maximum value is no greater than that for dedicated PUCCH resources used after RRC configuration.

### 7.1.3 <2nd Round Comments>

Please provide your company view on Proposal 9a. The moderator recognizes that several companies suggested discussing Issues 7.1 and 7.2 together; however, given the very strong support for signaling the number of RBs by SIB1, it feels like this this is the higher level issue that should be decided first. Then we can come back to discuss issue 7.2, i.e., details of the PUCCH resource set construction.

|  |  |
| --- | --- |
| **Company** | **View/Position** |
| Intel | We support proposal 9a |
| LG Electronics | We are generally fine with Proposal 9a. However, the mechanism to indicate the PUCCH resources with the appropriate number of RBs for the UE requires further discussion since it can be beneficial to configure PUCCH resources with different numbers of RB. |
| NTT DOCOMO | We support Proposal 9a. |
| Nokia, NSB | We still see no need for high flexibility to signal the RB allocation with new signaling in SIB. It has not been deemed as necessary in earlier releases, and we do not see that the situation has changed here. We should keep the same baseline as before, i.e. keep the PUCCH parameters predefined. |
| Lenovo, Motoroloa Mobility | We support Proposal 9a. |
| OPPO | We share a same view as Nokia, and do not believe high flexibility for RB number configuration in necessary for initial access. It would be more reasonable to follow a same design principle as R15/R16, i.e. PUCCH parameters are pre-defined. |
| Apple | We support the proposal |
| Qualcomm | Let us clarify a little bit with regard to Question 2. What we proposed is that, out of 16 common PUCCH resources, some of these 16 resources may use 1 RB (say legacy format), some of these 16 resources may use 16 RBs (assume proposal 1b is agreed), and some may use some RBs inbetween. This allows the Gnb to control how many RBs a UE can use dynamically in initial access.  This proposal is coming from the following considerations   * For a 100MHz@120KHz minimum UL BWP we have to support, this UL BWP contains <70RBs. If N\_RB=16 for all PUCCH resources, we may not have 16 resources * Typically a Gnb deploys N\_RB=16 for coverage. But for such cell, not all Ues are at cell edge. Do we need a UE in cell center to also transmit N\_RB=16? |
| ZTE, Sanechips | We support Proposal 9a. |
| Futurewei | We support Proposal 9a. |
| Samsung | We share same view with QC for Q2.  No matter UE-specific number of PRBs is supported or not, we have strong concern on Alt-2, which has large restriction on PUCCH resource for initial access. |

### 7.1.4 <Summary of 2nd Round>

* Support Proposal 9a:
  + Intel, LGE, NTT DOCOMO, Lenovo/MotMob, Apple, ZTE/Sanechips, Futurewei, Ericsson, Samsung
  + Support Alt-1 (SIB ignaling) in 1st round discussion:
    - Huawei/HiSilicon, CATT, Qualcomm, Samsung (if UE-specific RB indication not supported), OPPO
* Do not support Proposal 9a:
  + Nokia/NSB, OPPO
  + Support Alt-2 (fixed in spec) in 1st round discussion:
    - vivo, IDCC

There is a majority who support SIB1 signaling of the number of RBs for PUCCH format 0/1 for PUCCH resource sets prior to RRC configuration. Some ignaling do not see the need for flexibility and would prefer a number of RBs that is fixed by specification.

Qualcomm suggests that out of the 16 PUCCH resources in a PUCCH resource set, that there is a variable number of RBs to overcome if there are not enough RBs available (RB shortage). However, the moderator points out the following Note in the agreement from last meeting:

* Note: No further enhancements on RB shortage issue and frequency hopping distance issue should be considered for PUCCH resource sets prior to RRC configuration.

Given this, the moderator’s understanding is that optimizations to handle potential RB shortage are out of scope. Question to Qualcomm: do you still support SIB ignaling of the number of RBs?

Question companies not supporting Proposal 9a: would you be willing to compromise to support SIB1 signaling, given that it may be difficult to converge on fixed value or value(s) to use for the number of RBs given different regional regulations and deployment scenarios?

**FL Recommendation**: Support Proposal 9a

Let’s discuss further in the GTW later today.

Thank-you for the good discussion. The following was agreed in the GTW:

Agreement:

* For PUCCH resource sets prior to RRC configuration, support a parameter in SIB1 that indicates the number of RBs for enhanced (multi-RB) PUCCH format 0/1

## 7.2 PUCCH Resource Set Construction

The following table provides a summary of company proposals on details of the construction of the PUCCH resource set prior to RRC configuration.

|  |  |
| --- | --- |
| **Company** | **Company Proposals** |
| Intel | **Proposal 8: RAN1 should further discuss possible enhancements to PUCCH resource sets before dedicated PUCCH resource configuration to support at least same number of orthogonal resources as the legacy design.** |
| Vivo | **Proposal 4: The PUCCH frequency resource and the first PRB index are dependent on the NRB.** |
| ZTE | **Proposal 8: The similar solution in NR-U in rel-16 can be reused for Rel-17 PUCCH enhancement before RRC connected.** |
| NTT DOCOMO | ***Proposal 10:*** *For the PUCCH resource sets before dedicated PUCCH configuration, the following enhancement can be considered to transmit with larger band width.*   * *The cell-specific resources can be overlapped.* * *Frequency hopping can be not supported for 60 GHz unlicensed band operation.* |
| LGE | **Proposal #6: A number of RBs greater than 1 should be supported even for the initial PUCCH resource and the PRB offset value also needs to be scaled by NRB.** |
| LGE | **Proposal #8: To address the potential shortage of PUCCH resources for the initial PUCCH resource set resulting from using multi-PRB to transmit PUCCH formats 0 and 1, consider the following alternatives:**   * **Alt. 1: Use only valid resources in the frequency domain** * **Alt. 2: Support additional starting symbol and OCC index**   **Proposal #9: Considering the available number of RBs in the initial BWP and more than 1 RB allocated for an initial PUCCH resource, discuss how to configure the hopping distance to obtain hopping gain equally for each initial PUCCH resource.** |
| OPPO | **Proposal 4: The potential RB shortage issue prior to RRC configuration can be handled by Gnb implementation.** |
| Ericsson | **Proposal 3 Assuming that the number of RBs is configurable in SIB1, RAN1 should use the Rel-15 PUCCH configuration table 9.2.1-1 as a starting point for discussion on configuration of PUCCH resource sets prior to RRC configuration in combination with an updated procedure on the starting RB indices of the multi-RB PUCCH resources in a set.** |

As pointed out by several companies, the following note is contained in the RAN1#105-e agreement shown in Section 4:

* Note: No further enhancements on RB shortage issue and frequecy hopping distance issue should be considered for PUCCH resource sets prior to RRC configuration.

It is the moderator’s understanding that this means that the following enhancements are out-of-scope for construction of the PUCCH resource set prior to RRC configuration:

* Introduction of additional time domain starting positions and/or additional OCCs
* Support of a different RE mapping scheme (e.g., sub-PRB interlaced mapping)
* Equalization of hopping distance for the PUCCH resources within a set

With that in mind, the construction of PUCCH resource sets prior to RRC configuration becomes simpler; however, there is still a dependence on how the number of RBs should be indicated (see the alternatives Alt-1,2,3 in the previous Section 6.1), and whether or not the number of RBs should be different for each row of the PUCCH configuration Table 9.2.1-1 from 38.213:

**Table 9.2.1-1: PUCCH resource sets before dedicated PUCCH resource configuration**

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **Index** | **PUCCH format** | **First symbol** | **Number of symbols** | **PRB offset** | **Set of initial CS indexes** |
| 0 | 0 | 12 | 2 | 0 | {0, 3} |
| 1 | 0 | 12 | 2 | 0 | {0, 4, 8} |
| 2 | 0 | 12 | 2 | 3 | {0, 4, 8} |
| 3 | 1 | 10 | 4 | 0 | {0, 6} |
| 4 | 1 | 10 | 4 | 0 | {0, 3, 6, 9} |
| 5 | 1 | 10 | 4 | 2 | {0, 3, 6, 9} |
| 6 | 1 | 10 | 4 | 4 | {0, 3, 6, 9} |
| 7 | 1 | 4 | 10 | 0 | {0, 6} |
| 8 | 1 | 4 | 10 | 0 | {0, 3, 6, 9} |
| 9 | 1 | 4 | 10 | 2 | {0, 3, 6, 9} |
| 10 | 1 | 4 | 10 | 4 | {0, 3, 6, 9} |
| 11 | 1 | 0 | 14 | 0 | {0, 6} |
| 12 | 1 | 0 | 14 | 0 | {0, 3, 6, 9} |
| 13 | 1 | 0 | 14 | 2 | {0, 3, 6, 9} |
| 14 | 1 | 0 | 14 | 4 | {0, 3, 6, 9} |
| 15 | 1 | 0 | 14 |  | {0, 3, 6, 9} |

**Example Construction 1 (same N\_RB for each row)**:

Assuming that N\_RB is indicated to the UE somehow (see Alt-1, 2, or 3 in prior Section 6.1), the UE assumes that the indicated number of RBs is the same for whatever row of the PUCCH configuration table is indicated in SIB1. In this case, Table 9.2.1-1 could be used “as is.” It is also assumed that by implementation, the Gnb ensures that whatever row of the table is indicated, that the indicated N\_RB and initial UL BWP size are compatible to ensure that 16 PUCCH resources can be constructed as per Rel-15/16.

Once N\_RB is known (either ignaling or hardwired by specification), the UE could then determine the PRB indices and the initial cyclic shift indices for the PUCCH resources in the set based on the following simple modification of the text in 38.213 Section 9.2.1 where N\_RB is provided according to Alt-1, 2, or 3. This would preserve the frequency hopping pattern used in Rel-15/16, and further ensure that the multi-RB PUCCH resources in the set do not overlap each other (see illustration in [12]):

If and a UE is provided a PUCCH resource by *pucch-ResourceCommon* and is not provided *useInterlacePUCCH-PUSCH* in *BWP-UplinkCommon*

- the UE determines the lowest PRB index of the PUCCH transmission in the first hop as and the lowest PRB index of the PUCCH transmission in the second hop as , where is the total number of initial cyclic shift indexes in the set of initial cyclic shift indexes

- the UE determines the initial cyclic shift index in the set of initial cyclic shift indexes as

If and a UE is provided a PUCCH resource by *pucch-ResourceCommon* and is not provided *useInterlacePUCCH-PUSCH* in *BWP-UplinkCommon*

- the UE determines the lowest PRB index of the PUCCH transmission in the first hop as and the lowest PRB index of the PUCCH transmission in the second hop as

- the UE determines the initial cyclic shift index in the set of initial cyclic shift indexes as 

**Example Construction 2 (different N\_RB for each row)**:

In this example, N\_RB is indicated to the UE according to Alt-2 and is different for each row of the PUCCH configuration table. In one example (see [14]) N\_RB could be hardwired as shown in the highlighted column below. Clearly, further discussion would be required on what value of N\_RB to support for each row.

Like for Example 1, once N\_RB is known for whatever row is indicated by SIB1, the same modification of the text in 38.213 Section 9.2.1 as shown for Example 1 could be used by the UE for determining the PRB indices and initial cyclic shift indices for the PUCCH resources in the set.

Table 1. Proposed modifications to the common PUCCH resource sets

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| Index | PUCCH format | First symbol | Number of symbols | PRB offset | Set of initial CS indexes | **PRBs for 120/ 480/960 kHz SCS** |
| 0 | 0 | 12 | 2 | 0 | {0, 3} | **1/1/1** |
| 1 | 0 | 12 | 2 | 0 | {0, 4, 8} | **12 / 3 / 2** |
| 2 | 0 | 12 | 2 | 3 | {0, 4, 8} | **4 / 1 / 1** |
| 3 | 1 | 10 | 4 | 0 | {0, 6} | **1/1/1** |
| 4 | 1 | 10 | 4 | 0 | {0, 3, 6, 9} | **1/1/1** |
| 5 | 1 | 10 | 4 | 2 | {0, 3, 6, 9} | **1/1/1** |
| 6 | 1 | 10 | 4 | 4 | {0, 3, 6, 9} | **1/1/1** |
| 7 | 1 | 4 | 10 | 0 | {0, 6} | **1/1/1** |
| 8 | 1 | 4 | 10 | 0 | {0, 3, 6, 9} | **12 / 3 / 2** |
| 9 | 1 | 4 | 10 | 2 | {0, 3, 6, 9} | **4 / 1 / 1** |
| 10 | 1 | 4 | 10 | 4 | {0, 3, 6, 9} | **1/1/1** |
| 11 | 1 | 0 | 14 | 0 | {0, 6} | **1/1/1** |
| 12 | 1 | 0 | 14 | 0 | {0, 3, 6, 9} | **1/1/1** |
| 13 | 1 | 0 | 14 | 2 | {0, 3, 6, 9} | **12 / 3 / 2** |
| 14 | 1 | 0 | 14 | 4 | {0, 3, 6, 9} | **4 / 1 / 1** |
| 15 | 1 | 0 | 14 |  | {0, 3, 6, 9} | **1/1/1** |

**Proposal 10 Further discuss the construction of the PUCCH resource set prior to RRC configuration**

### 7.2.1 <1st Round Comments>

Please provide your company view on Proposal 10 including addressing the following questions.

**Question 1**: Which of the following two alternatives do you support:

* Alt-1: Indicated value of N\_RB (either by signaling or hardwired by specification) is the same, regardless of which row of Table 9.2.1-1 is used, **e.g.,** **Example Construction 1**
* Alt-2: Value of N\_RB is hardwired by specification and is different for each row of Table 9.2.1-1, **e.g., Example Construction 2**

**Question 2**: Do you have a different view on how the PUCCH resource set should be constructed? Please elaborate.

|  |  |
| --- | --- |
| **Company** | **View/Position** |
| Nokia, NSB | Question 1: We support Alt-2. Simularly as the other parameters, the number of PRBs may also vary depending on the row of the table, and the SCS applied. We are open to discuss the exact values further. |
| Vivo | We prefer alt-2. We think this issue should be discussed together with indication of RB number in 7.1. |
| ZTE, Sanechips | We are fine with Proposal 10.  A1: Alt1 is preferred due to the better flexibility.  A2: We share similar view with Moderator. |
| Huawei/HiSilicon | We are fine with Proposal 10. |
| Lenovo, Motoroloa Mobility | We support Alt 2. |
| Apple | We are fine with the proposal  As Vivo has said, this should be jointly discussed with the issue in 7.1. |
| Intel | Q1: we support Alt-1, since we should prefer to configure the number of PRBs through RRC ignaling, which may offer more flexibility than hardcoding some values in the spec.  Q2: Regarding the RB shortage issue our view is a bit different: Our understanding is that the note and conclusion made last time on this topic was under the assumption made so far and based on the max PRB values agreed, and the intention was to not enhance multiplexing capability behind what is supported in Rel.16. However, if the maximum number of PRBs are increased, this topic should be discussed further since for many combination of numerologies, initial BW part and PUCCH resources configurations, frequency partitioning would not be possible as the available number of PRBs would not be sufficient, and multiplexing capability would be extremely constrained compared to legacy. |
| CATT | We support Alt 2. |
| Sony | We are okay with the proposal. As pointed out by others, 7.1 and 7.2 can better be discussed together. |
| NTT DOCOMO | Question 1: We support Alt.1. |
| Qualcomm | For Question 1: we support N\_RB indicated through RRC for its flexibility.  For example contruction 1, Fl mentioned that “ It is also assumed that by implementation, the Gnb ensures that whatever row of the table is indicated, that the indicated N\_RB and initial UL BWP size are compatible to ensure that 16 PUCCH resources can be constructed as per Rel-15/16”. As we mentioned in 7.1, if N\_RB is chosen to make sure 16 resource may fit into intilal UL BWP, coverage may be scarifed. And common PUCCH may become bottle neck for coverage due to limited UL BWP size. |
| Samsung | Share similar view with Sony that 7.1 and 7.2 can better be discussed together. |
| OPPO | It seems that 7.2 is a next step of 7.1, so maybe we could first naildown 7.1. |
| LG Electronics | We prefer Alt 1 between Alt 1 and Alt 2. The value of NRB can be configured by Gnb rather than hardwired by the specification. Moreover, the PRB offset value in the Table is needed to be scaled considering the value of NRB. |
| Futurewei | We prefer Alt-1 for better flexibility. Besides, if Alt-2 is selected, the existing Table 9.2.1-1 needs enhancement, which takes more standard effort. |
| Moderator | Please continue to discuss.  Several companies have suggested that the issues in 7.1 and 7.2 should be discussed together. That is fine, and on the next upate of the FL summary, I will try to merge the discussions into one. Until then, please feel free to enter your comments in either table (or both) and I will consolidate. |
| InterDigital | We support Alt-1 for flexible implementation. |

### 7.2.1 <Summary of 1st Round>

The following is a summary of responses to Question 1:

* Alt-1:
  + ZTE/Sanchips, Intel, NTT DOCOMO, LGE, Futurewei, Ericsson
* Alt-2:
  + Nokia/NSB, vivo, Lenovo/MotMob, CATT, OPPO

The support is somewhat mixed. The moderator recommends that we try to agree on Proposal 9a in Section 7.1.2 first since that is a high level detail. We can revisit issue 7.2 once there is more clarity on the indication of the number of RBs. However, if you have further comments, please feel free to express them in the table below.

### 7.2.2 <2nd Round Comments>

|  |  |
| --- | --- |
| **Company** | **View/Position** |
| Intel | We are fine with FL’s recommendation. |
| LG Electronics | We are fine with FL’s recommendation. |
| NTT DOCOMO | We are fine with FL’s recommendation. |
| Lenovo, Motoroloa Mobility | We are fine with FL’s recommendation. |
| OPPO | Agree with FL’s recommendation, also we add our preference between Alt-1 vs. Alt-2. |
| Apple | Okay with FL’s recommendation |
| Qualcomm | We are fine with the recommendation |
| Sony | We are OK with the FL’s recommendation. |
| ZTE, Sanechips | We are fine with FL’s recommendation. |
| Futurewei | We support the FL’s recommendation. |
| InterDigital | We are fine with FL’s recommendation. |
| Moderator | As shown in Section 7.1.4, the following agreement as made in the GTW:  Agreement:   * For PUCCH resource sets prior to RRC configuration, support a parameter in SIB1 that indicates the number of RBs for enhanced (multi-RB) PUCCH format 0/1   Please continue to discuss your views on how the PUCCH resource sets should be constructed, taking this agreement into account. If some convergence can be achieved in this meeting, we can try to make an agreement later this week. If not, the discussion here will provide a useful starting point for next meeting.  **Question 1**: Is the general approach shown in Example Contruction 1 above feasible, given that N\_RB is signaled in SIB1?  **Question 2**: Are there other issues that should be considered? |
| Intel | Given the agreement made during the last GTW, we are OK with the approach used in Example 1. |
| LG Electronics | Q1: We are fine with Example Construction 1 above.  Q2: The PRB offset value (i.e., ) in the Table 9.2.2-1 in TS 38.213 is needed to be scaled by NRB that is signalled in SIB1 for inter-cell frequency division multiplexing. |
| Nokia, NSB | Construction 1 may be a feasible baseline, but further details related to e.g. PRB offset will need to be considered further. We would rather agree on a complete solution than make intermediate agreements. |
| NTT DOCOMO | Q1: We are generally fine with the approach shown in Example 1.  Q2: We share the same view with LGE that PRB offset shown in the Table 9.2.1-1 of TS 38.213 should be enhanced for multi-PRB allocation. |
| ZTE, Sanechips | We are fine with the Example Construction 1. |
| InterDigital | We are fine with the example construction 1. |
| Apple | We are fine with Example construction 1. |
| Lenovo, Motoroloa Mobility | We are fine with Example Construction 1. |
| Huawei/HiSilicon | We are fine with Example Construction 1. |
| Qualcomm | While the whole WI is to increase the coverage, we think that the example 1 proposal may make the common PUCCH 0/1 the bottleneck for the coverage.  While we generally agree with the construction procedure in example 1, we disagree that “the indicated N\_RB and initial UL BWP size are compatible to ensure that 16 PUCCH resources can be constructed as per Rel-15/16”. As we mentioned several times, this limits the cell coverage as Gnb may need to choose some smaller N\_RB for small UL BWP size.  As some companies also mentioned, we think Gnb should be allowed to choose any number between 1 and 16 ( or whatever N\_RB\_max for the SCS) as it is deemed proper and then follow the procedure in example 1. In other words, UL BWP size should not be an factor in decide N\_RB.  As we handcuffed ourselves not to address RB shortage issue, We may live with the fact that 16 common PUCCH resource will overlap and let Gnb try to avoid using overlapping resource in the same time. |
| Samsung | We are fine with Example 1  Regarding , it seems the equation in example 1 is alreadly sufficient, e.g. |
| Sony | We are OK with Example Construction 1. |
| OPPO | We are ok with example construction 1 |

### 7.2.3 <Summary of 2nd Round>

There seems to be strong support for building the PUCCH resource sets based on reusing Table 9.2.1-1 and using Example Construction 1 based on the signaled value of N\_RB in SIB1.

Qualcomm has a preference that the indicated value of N\_RB in SIB1 does not necessarily need to be small enough to guarantee that the full set of 16 PUCCH resources are non-overlapping. If a large value of N\_RB is desired in a deployment, then the gNB can still signal the large value (e.g., 16 RBs) and by implementation, simply avoid using the overlapping PUCCH resources since it has direct control of signaling the value of r\_PUCCH (PUCCH resource indicator). This seems feasible, and is still within the framework of the Examaple Construction 1.

The moderator makes the following high level proposal is specific to 120 and 480 kHz, since those are the two SCSs supported for initial access.

**Proposal 10**

For 120 and 480 kHz SCS, reuse the Rel-15 PUCCH configuration table 9.2.1-1 for configuration of PUCCH resource sets prior to RRC configuration for enhanced (multi-RB) PUCCH formats 0/1

* As previously agreed, the number of RBs for each PUCCH resource in a set is N\_RB which is signaled in SIB1
* The lowest-indexed RB for each PUCCH resource is a function of the N\_RB and the RB offset provided by Table 9.2.1-1
  + FFS: Further details

### 7.2.4 <3rd Round Comments>

Please provide any comments you may have on Proposal 10. If we can converge in this meeting, great. If not then, it is a good starting point for next meeting.

|  |  |
| --- | --- |
| **Company** | **View/Position** |
| NTT DOCOMO | We support Proposal 10. |
| Intel | We support in principle the proposal. However, we would like companies to please clarify their understanding of the following agreed note:   * Note: No further enhancements on RB shortage issue and frequecy hopping distance issue should be considered for PUCCH resource sets prior to RRC configuration.   Our understanding is that this note was agreed under the assumption that the maximum number of PRBs used for enhanced PUCCH would remain the same as per initial agreement (12/3/2 for 120, 480 and 960 KHz SCS), and in that scenario we indeed thought that there was indeed no technical motivation to enhance the resources sets before RRC configuration. However, since the number of PRBs could be potentially configured up to 16 for 480 KHz SCS, and as shown 12 PRBs is actually a realistic value that could be indeed used, we have concerns that if we leave the resources sets before RRC configuration as in legacy, the bandwidth available would not be enough to support frequency domain partitioning, and multiplexing capability will be highly constrained even for feasiblle and frequenclty used values. We feel that coverage for initial access signals/channels is equally really important and should be enhanced appropriately, and our aim is no keep Rel.16 capabilities in place. |
| Moderator | Response to Intel:  I think the important point is that N\_RB is configurable by SIB1. Hence, the network can configure an appropriate value depending on the deployment scenario, e.g., to prioritize coverage. If that enables use of all 16 PUCCH resources in the set, then no problem. But even if it doesn't, then as Qualcomm points out, the gNB by implementation can simply indcate r\_PUCCH values that avoid PUCCH resources that may overlap. In the view of the moderator, that conforms with "No further enhancements on RB shortage ..." |
| Qualcomm | We share same view as intel. We are open to re-visit RB shortage issue if all companies agree to. Or, just as moderator points out, we live with the fact that there may not 16 common pucch resource, and leave solution to gNB implementation. |

# References

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5. R1-2106693 Discussion on enhancements for PUCCH formats 0/1/4 for above 52.6GHz Spreadtrum Communications
6. R1-2106768 Discussions on enhancements for PUCCH formats 0/1/4 InterDigital, Inc.
7. R1-2106797 More considerations on PUCCH enhancements for PUCCH formats 0/1/4 Sony
8. R1-2106833 Enhancements to PUCCH formats 0/1/4 for NR from 52.6 GHz to 71GHz Lenovo, Motorola Mobility
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10. R1-2106958 Enhancements for PUCCH formats for up to 71GHz operation CATT
11. R1-2107002 Discussion on the PUCCH enhancements for 52.6 to 71GHz ZTE, Sanechips
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22. R1-2108149 Discussion on PUCCH enhancement for PUCCH format 0/1/4 WILUS Inc.