Discussion RRC parameters for Rel-17 inter-cell mTRP.

Initial list can be found in the excel sheet in the draft folder, please provide your comments/questions in table below.

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| Company | Comments |
| Apple | * AdditionalPCIInfo…: Although we have sent LS to RAN2, we suggest we mentioned this includes SSB time domain location, SSB periodicity and SSB transmission power.
* NumberOfAdditionalPCI: In our view, this parameter is not needed, but gNB can just configure a list of additionalPCIInfo, and the size of the list means the NumberOfAdditionalPCI.
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| OPPO | Generally we agree with Apple. For AdditionalPCIInfo, if we mention the parameters to be included in this IE, maybe PCI should also be included considering it would not be directly indicated in TCI state.  |
| NTT DOCOMO | * AdditionalPCIInfo…: Agree with Apple to include SSB time domain location, SSB periodicity and SSB transmission power. Also, it should include an actual PCI. And gNB can configure a list of additionalPCIInfo.
* A new RRC indicator/signalling (e.g., re-index the different PCI cells) should be introduced for QCL/TCI configuration.
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| Futurewei | Generally agree with Apple / OPPO / NTT DOCOMO. We could provide an example design:AdditionalPCIInfo::={  PCI,  SSB configuration,  RS configuration,  etc. } |
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