**3GPP TSG RAN WG1 Meeting #106-e R1-210xxxx**

**E-meeting, Aug 16th – 27th, 2021**

**Agenda Item: 7.2.4**

**Source: Moderator (Huawei)**

**Title: Summary of [106-e-NR-5G\_V2X-09]**

**Document for: Discussion and Decision**

# Introduction

This document provides proposals and summary of discussions of the following email discussion:

[106-e-NR-5G\_V2X-09] Discussion on [R1-2106476](file:///D:\Mix-Local\001-Mix%20Working%20Folder\Docs\R1-2106476.zip): Correction on mode 2 resource reservation period by August 18 – Xiang (Huawei)

# Discussion

## 2.1 Issue

As pointed out by the draft CR [1], there was an agreement made in RAN1#101-e:

Agreement:

* A UE is expected to be (pre-)configured with a set *sl-ResourceReservePeriod* containing value of 0 ms

The purpose of this agreement is to allow UE to set “Resource reservation period” = 0 for aperiodic traffic. However, the agreement is not currently captured in any specification, which means that aperiodic transmission has to provide periodic reservation whenever *sl-ResourceReservePeriod* does not contain a value = 0 ms.

Therefore, the draft CR [1] proposes to agree the following TP, i.e., adding UE expectation that *sl-ResourceReservePeriod* contains value of 0 ms. Otherwise, the specification does not align to agreements, and a periodic reservation is forced even for aperiodic traffic.

* Note: the moderator appends “*List*” to “*sl-ResourceReservePeriod*” in the following TP to align with the latest parameter name, which was also pointed out by some companies during the preparation phase.

**TP:**

|  |
| --- |
| **--------------------------- Text starts (TS 38.213, clause 16.4)-----------------------------**  **<Unchanged parts omitted>** 16.4 UE procedure for transmitting PSCCH A UE can be provided a number of symbols in a resource pool, by *sl-TimeResourcePSCCH*, starting from a second symbol that is available for SL transmissions in a slot, and a number of PRBs in the resource pool, by *sl-FreqResourcePSCCH*, starting from the lowest PRB of the lowest sub-channel of the associated PSSCH, for a PSCCH transmission with a SCI format 1-A.  A UE that transmits a PSCCH with SCI format 1-A using sidelink resource allocation mode 2 [6, TS 38.214] sets  - "Resource reservation period" as an index in *sl-ResourceReservePeriodList* corresponding to a reservation period provided by higher layers [11, TS 38.321], if the UE is provided *sl-MultiReserveResource*  - A UE expects that *sl-ResourceReservePeriodList* contains value of 0 ms.  **<Unchanged parts omitted>**  **--------------------------- Text ends (TS 38.213, clause 16.4)-----------------------------** |

Meanwhile, during the preparation week, some companies commented that this issue can be handled in RAN2.

Therefore, the moderator proposes the following alternatives to check companies’ views:

* **Alt 1: Agree on the text proposal in Section 2.1**
* **Alt 2: Send LS to RAN2 about this issue**
  + Draft LS content: RAN1 has identified a potential issue that the following agreement has not been implemented. RAN1 requests RAN2 to inform RAN1 how or if RAN2 specification already captures it and update if necessary

Agreement:

* A UE is expected to be (pre-)configured with a set *sl-ResourceReservePeriod* containing value of 0 ms
* **Alt 3: Agree on the text proposal in Section 2.1, and send LS to RAN2 about this issue**
  + Draft LS content: RAN1 has identified an issue that the following agreement has not been implemented. RAN1 has agreed the attached CR and requests RAN2 to make any necessary updates in their specifications

Agreement:

* A UE is expected to be (pre-)configured with a set *sl-ResourceReservePeriod* containing value of 0 ms

## 2.2 Company views

**Q1: Which alternative in Section 2.1 do you support?**

* Note: if you support Alt 2 or 3, you can also comment on the draft LS content.

|  |  |  |
| --- | --- | --- |
| **Company** | **Which Alt do you support?** | **Comments** |
|  |  |  |
|  |  |  |

# Conclusions

TBD

# References

1. R1-2106476 Correction on Mode 2 resource reservation period Huawei, HiSilicon