**3GPP TSG RAN WG1 Meeting #106-E R1-210xxxx**

**e-Meeting, August 16th – 27th, 2021**

**Source: Moderator (Intel Corporation)**

**Title: Summary of discussion on PDSCH processing time per Capability 2 and DCI format 1\_0**

**Agenda item: 7.1**

**Document for:** **Discussion and Decision**

# Introduction

This document summarizes the following discussion thread, relevant to Rel-15/16 NR specifications:

[106-e-NR-7.1CRs-04] Issue#6: PDSCH processing time per Capability 2 and DCI format 1\_0 by August 20 – Debdeep (Intel)

[R1-2106684](file:///C:\Users\dchatt2\OneDrive%20-%20Intel%20Corporation\Documents\work\3gpp\RAN1\Contribution%20reviews\RAN1_106e_review\allTdocs_R1-106e\R1-2106684.zip) PDSCH processing time per Capability 2 and DCI format 1\_0 Spreadtrum Communications

[R1-2107568](file:///C:\Users\dchatt2\OneDrive%20-%20Intel%20Corporation\Documents\work\3gpp\RAN1\Contribution%20reviews\RAN1_106e_review\allTdocs_R1-106e\R1-2107568.zip) On UE processing capability #2 and PDSCH scheduled by DCI 1\_0 Intel Corporation

[R1-2107677](file:///C:\Users\dchatt2\OneDrive%20-%20Intel%20Corporation\Documents\work\3gpp\RAN1\Contribution%20reviews\RAN1_106e_review\allTdocs_R1-106e\R1-2107677.zip) Processing time for PDSCH scheduled by DCI format 1\_0 Huawei, HiSilicon

[R1-2107995](file:///C:\Users\dchatt2\OneDrive%20-%20Intel%20Corporation\Documents\work\3gpp\RAN1\Contribution%20reviews\RAN1_106e_review\allTdocs_R1-106e\R1-2107995.zip) Capability 2 and fallback DCI format 1-0 PDSCH handling Ericsson

# Background and summary of submitted tdocs

During RAN1 #105-e meeting, the issue of determining minimum UE processing time for processing unicast PDSCH scheduled by DCI format 1\_0 with greater than 7 symbols or 4 symbols for PDSCH mapping types A and B respectively in a serving cell configured with Cap #2 PDSCH processing timeline was discussed, and various possible interpretations and solutions to address the shortcomings in current specifications were identified [1].

The fundamental issue at hand is that PDSCHs, scheduled by DCI format 1\_0 and of durations longer than certain thresholds, are expected to have additional DMRS symbols that the UE is expected to process (in addition to the front-loaded (FL) DMRS symbol), but this makes satisfying Cap #2 processing timeline practically infeasible for UE implementation.

Accordingly, for this scenario, the possible interpretations on PDSCH processing times and DMRS assumptions can be summarized as below [1].

|  |
| --- |
| * If *processingType2Enabled* in *PDSCH-ServingCellConfig* is configured for the cell and set to ‘enable’, and if *dmrs-AdditionalPosition = ‘pos0’* in *DMRS-DownlinkConfig* in both of *dmrs-DownlinkForPDSCH-MappingTypeA, dmrs-DownlinkForPDSCH-MappingTypeB,* then, for a PDSCH scheduled by DCI format 1\_0 with *ld > 7* or *ld > 4* symbols for mapping types A or B respectively, one of the following for min UE processing times and DMRS assumptions respectively apply:   + **Proc\_Time\_Alt 1:** PDSCH processing time per Cap #2 applies   + **Proc\_Time\_Alt 2**: PDSCH processing time per Cap #1 with additional DMRS applies   + **Proc\_Time\_Alt 3**: PDSCH processing time per Cap #1 without additional DMRS applies   + **Proc\_Time\_Alt 4**: UE is not expected to provide a valid HARQ feedback   + **DMRS\_Alt 1:** PDSCH DMRS as per ‘pos2’ at least for PDSCH RE mapping but UE is not expected to process additional DMRS symbols for PDSCH reception (i.e. per ‘pos0’ for channel estimation for PDSCH reception)   + **DMRS\_Alt 2:** PDSCH DMRS as per ‘pos2’ for both PDSCH RE mapping and channel estimation for PDSCH reception   + **DMRS\_Alt 3:** PDSCH DMRS as per ‘pos0’ for both PDSCH RE mapping and channel estimation for PDSCH reception |

The candidate conclusions/spec-updates and their implications that were identified during RAN1 #105-e are detailed in the Appendix in Table A-1 ([1]). Descriptions for the different options are listed below in Table 1 for convenience.

**Table 1. Identified Options for defining UE behavior and assumptions for the issue identified during RAN1 #105-e on PDSCH processing time per Capability 2 and DCI format 1\_0.**

|  |  |
| --- | --- |
| **Option** | **Description** |
| A | * For a UE configured with Cap #2 in a DL cell and NOT configured with additional DMRS by higher layers:   + ***All PDSCHs subject to HARQ-ACK feedback (“unicast PDSCHs”) that may be scheduled in the DL cell are expected to satisfy conditions for Cap #2 timeline (i.e., w/o additional DMRS)***   + For a unicast PDSCH scheduled by DCI 1\_0, DMRS\_Alt 2 (PDSCH DMRS as per ‘pos2’ for both PDSCH RE mapping and channel estimation for PDSCH reception) applies.   + UE is not expected to provide valid HARQ-ACK feedback in response to a unicast PDSCH scheduled by DCI format 1\_0 with *ld > 7* or *ld > 4* symbols for mapping types A or B respectively in the cell. |
| A1 | * For a UE configured with Cap #2 in a DL cell and NOT configured with additional DMRS by higher layers:   + ***All PDSCHs subject to HARQ-ACK feedback (“unicast PDSCHs”) that may be scheduled in the DL cell are expected to satisfy conditions for Cap #2 timeline (i.e., with additional DMRS per ‘pos0’ only)***   + For a unicast PDSCH scheduled by DCI 1\_0, DMRS\_Alt 2 (PDSCH DMRS as per ‘pos2’ for both PDSCH RE mapping and channel estimation for PDSCH reception) applies   + UE is not expected to provide valid HARQ-ACK feedback in response to a unicast PDSCH scheduled by DCI format 1\_0 ~~with~~ *~~l~~~~d~~ ~~> 7~~* ~~or~~ *~~l~~~~d~~ ~~> 4~~* ~~symbols for mapping types A or B respectively~~ in the cell. |
| B | * For a UE configured with Cap #2 in a DL cell and NOT configured with additional DMRS by higher layers:   + ***For all PDSCHs, including PDSCH scheduled by DCI format 1\_0 with ld > 7 or ld > 4 symbols for mapping types A or B respectively, subject to HARQ-ACK feedback (“unicast PDSCHs”) are to follow Cap #2 timeline***   + For a unicast PDSCH scheduled by DCI 1\_0, DMRS\_Alt 2 (PDSCH DMRS as per ‘pos2’ for both PDSCH RE mapping and channel estimation for PDSCH reception) applies   + UE is expected to satisfy Cap #2 processing times as per Table 5.3-2, regardless of a unicast PDSCH actually containing additional DMRS that the UE is expected to process. |
| B1 | * For a UE configured with Cap #2 in a DL cell and NOT configured with additional DMRS by higher layers:   + ***For all PDSCHs, including PDSCH scheduled by DCI format 1\_0 with ld > 7 or ld > 4 symbols for mapping types A or B respectively, subject to HARQ-ACK feedback (“unicast PDSCHs”) are to follow Cap #2 timeline***   + For a unicast PDSCH scheduled by DCI 1\_0, DMRS\_Alt 1 (PDSCH DMRS as per ‘pos2’ for PDSCH RE mapping but UE is not expected to process additional DMRS symbols for PDSCH reception) applies.   + UE is expected to satisfy Cap #2 processing times as per Table 5.3-2, regardless of a unicast PDSCH scheduled by DCI format 1\_0 actually having additional DMRS or not.     - UE is NOT expected to process any additional DMRS.     - Existing DMRS mapping and rules are maintained. |
| B2 | * For a UE configured with Cap #2 in a DL cell and NOT configured with additional DMRS by higher layers:   + ***For all PDSCHs, including PDSCH scheduled by DCI format 1\_0 with ld > 7 or ld > 4 symbols for mapping types A or B respectively, subject to HARQ-ACK feedback (“unicast PDSCHs”) are to follow Cap #2 timeline***   + For a unicast PDSCH scheduled by DCI 1\_0, DMRS\_Alt 3 (PDSCH DMRS as per ‘pos0’ for both PDSCH RE mapping and channel estimation for PDSCH reception) applies   + UE is expected to satisfy Cap #2 processing times as per Table 5.3-2, regardless of a unicast PDSCH scheduled by DCI format 1\_0 actually having additional DMRS or not.     - UE is NOT expected to process any additional DMRS.     - DMRS mapping rule is updated – if Cap #2 is configured and additional DMRS is not configured, then UE assumes DMRS per ‘pos0’ for PDSCH scheduled by DCI 1\_0 subject to HARQ-ACK feedback. |
| C | * For a UE configured with Cap #2 in a DL cell and NOT configured with additional DMRS by higher layers:   + ***For a PDSCH scheduled by DCI format 1\_0 with ld > 7 or ld > 4 symbols for mapping types A or B respectively, subject to HARQ-ACK feedback (“unicast PDSCH”), the UE may fall back to Cap #1 timeline; other PDSCHs are to follow Cap #2 timeline, subject to any other applicable constraints (e.g., Cap 2 with 136 PRBs constraint for 30 kHz)***   + For a unicast PDSCH scheduled by DCI 1\_0, DMRS\_Alt 2 (PDSCH DMRS as per ‘pos2’ for both PDSCH RE mapping and channel estimation for PDSCH reception) applies.   + All PDSCH durations can be scheduled by DCI format 1\_0 for unicast PDSCH. |

However, RAN1 failed to arrive at a consensus on the way forward for Rel-15 and Rel-16 and the discussion was recommended to be continued during RAN1 #106-e.

As indicated in the Introduction section, four companies shared their views in tdocs submitted to RAN1 #106-e meeting. These views are summarized below in Table 2.

**Table 2. Summary of company proposals to RAN1 #106-e to the issue identified during RAN1 #105-e on PDSCH processing time per Capability 2 and DCI format 1\_0.**

|  |  |  |  |
| --- | --- | --- | --- |
| **Company** | **Proposal for Rel-15** | **Proposal for Rel-16** | **Moderator’s comments** |
| Spreadtrum | Options A or A1 | Options A or A1 or C | * Options B1, B2 seem to have not been considered in this tdoc. * Option A1 is even more restrictive than necessary (while Option A is already seen as restrictive to some) and could even be deemed NBC (and need spec updates) since for PDSCH durations with *ld > 7* or *ld > 4* symbols for mapping types A or B respectively, there is no issue. * ***Note:*** For Option C, some UE behavior to allow UE to drop certain PDSCH(s) to avoid overlapping processing pipelines as for the case of *pdsch-ProcessingType2-Limited* would be necessary. |
| Intel | Option A | Option B1 |  |
| HW-HiSi | ***Proposal: The solution handling the case when cap#2 is configured and a unicast PDSCH is scheduled by DCI format 1\_0 and then followed by a unicast PDSCH scheduled by DCI format 1\_1, should take both the feasibility of UE implementation and restriction of gNB scheduling into account.*** | | * Although not explicitly proposed, Options C and B1 are indicated as possibly reasonable options considering trade-off between UE implementation and gNB restrictions. * Not clear if the proposal is for both Rel-15 and Rel-16 or only for Rel-16. * ***Note:*** For Option C, some UE behavior to allow UE to drop certain PDSCH(s) to avoid overlapping processing pipelines as for the case of *pdsch-ProcessingType2-Limited* would be necessary. |
| Ericsson | Option C with scheduling restriction to avoid overlapping processing pipelines as for the case of *pdsch-ProcessingType2-Limited.* | | Not clear if the proposal is for both Rel-15 and Rel-16 or only for Rel-16. |

# Discussion

# For Rel-15

As expressed by multiple companies, it would be prudent to try to avoid spec impact to Rel-15 specifications.

Option A1 precludes any unicast PDSCH scheduled by DCI 1\_0 in a DL cell configured with Cap 2 and no additional DMRS. However, this would be excessively constraining, since, as per current specifications, for the concerned scenario, there is no issue/ambiguity for shorter unicast PDSCH durations (with *ld ≤ 7 or ld ≤ 4* for PDSCH mapping types A and B respectively) without additional DMRS symbols to follow Cap #2 timing.

As also observed in company contributions, Option B lends itself infeasible to a typical UE implementation consistent with NR designs and considerations till date.

The other identified options (Options B1, B2, and C) all would require some spec update, and thus, may not be desirable at this stage for Rel-15.

Thus, Option A could be an option to conclude on (w/o spec impact) for Rel-15. Depending on existing implementations (or the lack thereof), the scheduling restrictions from Option A can be lived with (if already implemented) or avoided (if not implemented yet).

Further, it should be noted that a RAN1 conclusion would be necessary for Rel-15 to ensure common understanding considering the clear evidence of different interpretations across companies as expressed since RAN1 #105-e meeting. Considering the above, the following is proposed for Rel-15 (note that this is essentially same as Moderator Proposal 1 from RAN1 #105-e with some editorial updates).

## Moderator Proposal 1

* ***Conclusion:*** *For Rel-15 specifications,*
  + *For a UE configured with Cap #2 in a DL cell and NOT configured with additional DMRS by higher layers:*
    - ***All PDSCHs subject to HARQ-ACK feedback (“unicast PDSCHs”) that may be scheduled in the DL cell are expected to satisfy conditions for Cap #2 timeline (i.e., w/o additional DMRS)***
    - For a unicast PDSCH scheduled by DCI 1\_0, **DMRS\_Alt 2** (PDSCH DMRS as per ‘pos2’ for both PDSCH RE mapping and channel estimation for PDSCH reception) applies.
    - UE is not expected to provide valid HARQ-ACK feedback in response to a unicast PDSCH scheduled by DCI format 1\_0 with *ld > 7* or *ld > 4* symbols for mapping types A or B respectively in the cell

*Kindly elaborate (w/ justifications) on suitable alternative(s) for Rel-15, including any option(s) not identified so far, if the above is not preferred/acceptable.*

|  |  |  |
| --- | --- | --- |
| **Company** | **Agree? (Y/N)** | **Comments** |
| Qualcomm | No | We do not think any conclusion is necessary. Following is already clear from the spec. The proposed conclusion is not aligned with this spec language.  *Otherwise the UE may not provide a valid HARQ-ACK corresponding to the scheduled PDSCH.* |
| ZTE | No | The conclusion implies that the UE does not expect to be scheduled with a unicast PDSCH by DCI format 1\_0 with *ld > 7* or *ld > 4* symbols for mapping types A or B. This would cause huge scheduling restriction, and it is NBC.  In addition, we don’t agree that Option B1, B2, and C would require spec update while not for Option A and A1. In our view, we could draw a conclusion either without any spec update or with spec update, regardless of which option is chosen.  We would be ok with Option B or Option C. |
| Ericsson | No | We are not OK to add such severe scheduling restriction on TDRA.  We also agree with Qualcomm that this is not consistent with existing spec “*otherwise the UE may not provide a valid HARQ-ACK corresponding to the scheduled PDSCH*” |
| OPPO | No | In our understanding, additional DMRS is expected by UE for DCI format 1\_0 with ld > 7 or ld > 4 symbols. In this case, for a UE configured with Cap #2, the following should be applied:  “*otherwise the UE may not provide a valid HARQ-ACK corresponding to the scheduled PDSCH*”  Then we think a conclusion with different wording from current spec. is not needed. |

# For Rel-16

For Rel-16 specifications, it would not be desirable to maintain the severe restrictions of Options A/A1, especially since Rel-16 specifications may be expected to see some of the first UEs with Cap #2 support and deployments with Cap #2 configurations in the context of URLLC/IIoT use-cases. Restricting unicast PDSCH TDRA options (especially slot-based) for when scheduled by fallback DCI format can be quite a significant restriction to gNB scheduler.

The constraint (e.g., one results from Option A), that a UE, configured with Cap #2 timing for PDSCH in a DL cell, cannot be scheduled with unicast PDSCH with with *ld > 7* or *ld > 4* symbols for mapping types A or B respectively by fallback DCI format during the “RRC ambiguity period”. Here, it is highlighted that, for unicast PDSCH scheduling, specifying that “UE is not expected to provide valid HARQ-ACK feedback” for certain configurations is, for the most part, equivalent to a scheduling restriction (except for the corner cases wherein a HARQ-ACK feedback may not be consequential for a unicast PDSCH, e.g., at the end of the user plane (UP) delay budget for the PDSCH TB).

Thus, it would be reasonable to consider some options that can achieve a better tradeoff between UE implementation challenges and gNB scheduling restrictions and latency/reliability/throughput performance.

Towards this, as a first step, based on the prior discussions and inputs in company contributions to RAN1 #106-e, the following is proposed for Rel-16 specifications, with the aims of final down-selection in the second round of discussions based on received inputs from companies.

Option B1 allows for all unicast PDSCHs, regardless of duration, can follow Cap #2 timing in the DL cell even when scheduled by DCI 1\_0, as was originally intended. At the same time, it avoids impact to PHY channel design and PDSCH DMRS and RE mapping, as against Option B2. This is achieved by simply not expecting the UE to process the additional DMRS symbols for PDSCH demodulation when scheduled by DCI 1\_0. In light of the fact that the UE is configured with Cap #2 without additional DMRS symbols (via higher layers), the demodulation performance for PDSCH scheduled by DCI 1\_0 would be similar to DCI 1\_1 and thus, acceptable for most cases. The PDSCH reliability that may be “lost” due to not processing additional DMRS symbols can be compensated with appropriate MCS selection for the PDSCH.

On the other hand, Option C still expects the UE to process additional DMRS symbols when present in a PDSCH as per Rel-15 specifications, but is now allowed to fall back to Cap #1-based timing for the corresponding HARQ-ACK feedback.

Next, to address impact to UE pipelining architecture, the UE may be allowed to skip decoding one or more unicast PDSCH(s) scheduled by DCI 1\_0 (with additional DMRS symbols) if they are scheduled within a certain window before the start of a unicast PDSCH following Cap #2 timing (e.g., as proposed in [5]):

“… UE may skip decoding a number of PDSCHs with last symbol within N symbols before the start of a PDSCH that is scheduled to follow capability 2 processing time, if any of those PDSCHs are scheduled with DCI 1\_0 following capability 1 processing time, where N = 8 for PDSCH with 15kHZ SCS, N = 10 for PDSCH with 30kHZ SCS, N = 17 for PDSCH with 60kHZ SCS”

## Moderator Proposal 2

* *For Rel-16 specifications,*
  + *For a UE configured with Cap #2 in a DL cell and NOT configured with additional DMRS by higher layers, one of the following Options is supported:* 
    - *Option B1*
    - *Option C, with the associated UE behavior:*
      * *UE may skip decoding one or more unicast PDSCHs with last symbol within N symbols before the start of a PDSCH that is scheduled to follow capability 2 processing time, if any of those PDSCHs are scheduled with DCI 1\_0 following capability 1 processing time, where N = 8 for PDSCH with 15kHZ SCS, N = 10 for PDSCH with 30kHZ SCS, N = 17 for PDSCH with 60kHZ SCS.*
    - *Other option(s)? (please elaborate)*

*Please also indicate your preference between Options B1 and C, with justifications from your side.*

*Also, kindly elaborate (w/ justifications) on suitable alternative(s), including any option(s) not identified so far, for Rel-16 if the above is not preferred/acceptable.*

|  |  |  |
| --- | --- | --- |
| **Company** | **Preferred Option** | **Comments** |
| Qualcomm | Other | First of all, we should first understand that any potential solution requires a new Rel-16 capability signalling.  Option B1 requires a new way of PDSCH process; TB size determination is based on front-loaded DMRS + additional DMRS, while channel estimation/decoding are based on front-loaded DMRS only, and this is only for a unicast PDSCH scheduled by DCI format 1\_0. We think this should be avoided.  With Option C, a UE is required to cancel/drop on-going Cap1 PDSCH process when the UE starts to process Cap2 PDSCH. This is not aligned with the fundamental framework of PDSCH processing capability 2 – i.e., dynamic switch between Cap1 and Cap2 for the given DL cell has not been considered (other than the case with *pdsch-ProcessingType2-Limited*).  A possible compromise is to let UE to support Cap1 PDSCH timeline for a PDSCH with additional DMRS scheduled by a DCI format 1\_0 in a DL cell configured with Cap2, while scheduler ensures that a Cap1 PDSCH process is not interrupted by a Cap2 PDSCH process in the DL cell. This can be realized by following.   * *UE does not expect to receive one or more unicast PDSCHs with last symbol within N symbols before the start of a PDSCH that is scheduled to follow capability 2 processing time, if any of those PDSCHs are scheduled by DCI format 1\_0 following capability 1 processing time* * *N is not smaller than N1 + d1,1 where the values of N1 for SCS 15kHz, 30kHz, and 60kHz are given by the right column of Table 5.3-1 in TS38.214*   + *Exact values to be confirmed* * *For the above, new Rel-16 UE capability signalling is introduced.* |
| ZTE | Option B or Option C | Our preference is Option B, while if there are concerns about overlapping processing pipelines, Option C is also ok for us. |
| Ericsson | Option B or C or other | Our preference is Option B, but we would also be OK with Option C.  We are not OK with B1 which implies (advanced) cap2 UE is allowed to have worse performance than a baseline cap1 UE.  Regarding QC proposal, we are open to introducing a new capability for Rel-16. For the behavior of cap1 followed by cap2, our preference is to align behavior with current spec for cap2 with 136 PRB limitation (simpler for implementation). |
| OPPO | Option A/C | Our preference is option A for backward compatibility. For Rel-16, we can just have a conclusion similar to proposal 1.  We can also accept option C with additional restriction to avoid overlapping processing pipelines for cap#1 and cap#2. The proposal from QC is fine to us. |

# Summary of discussion

…

# References

1. R1-2106026, Summary of discussion on PDSCH processing time per Capability 2 and DCI format 1\_0, Moderator (Intel Corporation).
2. R1-2106684, PDSCH processing time per Capability 2 and DCI format 1\_0, Spreadtrum Communications.
3. R1-2107568, On UE processing capability #2 and PDSCH scheduled by DCI 1\_0, Intel Corporation.
4. R1-2107677, Processing time for PDSCH scheduled by DCI format 1\_0, Huawei, HiSilicon.
5. R1-2107995, Capability 2 and fallback DCI format 1-0 PDSCH handling, Ericsson.

# Appendix

**Table A-1: Summary of Options for Rel-15 and Rel-16 on Cap 2 and DCI format 1\_0 [1]**

|  |  |  |
| --- | --- | --- |
| **Option** | **Description** | **Comments** |
| **Option A** | * For a UE configured with Cap #2 in a DL cell and NOT configured with additional DMRS by higher layers:   + ***All PDSCHs subject to HARQ-ACK feedback (“unicast PDSCHs”) that may be scheduled in the DL cell are expected to satisfy conditions for Cap #2 timeline (i.e., w/o additional DMRS)***   + For a PDSCH scheduled by DCI 1\_0 and subject to HARQ-ACK feedback, DMRS\_Alt 2 (PDSCH DMRS as per ‘pos2’ for both PDSCH RE mapping and channel estimation for PDSCH reception) applies | * For a UE configured with Cap #2 in a DL cell and NOT configured with additional DMRS by higher layers:   + The UE does not expect to be scheduled with a unicast PDSCH by DCI format 1\_0 with *ld > 7* or *ld > 4* symbols for mapping types A or B respectively in the cell   + **No NBC issue \*\***   + **Severe scheduling constraint since unicast PDSCH longer than 7 symbols and 4 symbols for mapping types A and B respectively cannot be supported with DCI 1\_0** |
| **Option A1** | * For a UE configured with Cap #2 in a DL cell and NOT configured with additional DMRS by higher layers:   + ***All PDSCHs subject to HARQ-ACK feedback (“unicast PDSCHs”) that may be scheduled in the DL cell are expected to satisfy conditions for Cap #2 timeline (i.e., with additional DMRS per ‘pos0’ only)***   + For a PDSCH scheduled by DCI 1\_0 and subject to HARQ-ACK feedback, DMRS\_Alt 2 (PDSCH DMRS as per ‘pos2’ for both PDSCH RE mapping and channel estimation for PDSCH reception) applies | * For a UE configured with Cap #2 in a DL cell and NOT configured with additional DMRS by higher layers:   + The UE does not expect to be scheduled with a unicast PDSCH by DCI format 1\_0 ~~with~~ *~~l~~~~d~~ ~~> 7~~* ~~or~~ *~~l~~~~d~~ ~~> 4~~* ~~symbols for mapping types A or B respectively~~ in the cell   + **No NBC issue \*\***   + **Severe scheduling constraint since unicast PDSCH cannot be scheduled using DCI 1\_0** |
| **Option B** | * For a UE configured with Cap #2 in a DL cell and NOT configured with additional DMRS by higher layers:   + ***For all PDSCHs, including PDSCH scheduled by DCI format 1\_0 with ld > 7 or ld > 4 symbols for mapping types A or B respectively, subject to HARQ-ACK feedback (“unicast PDSCHs”) are to follow Cap #2 timeline***   + For a PDSCH scheduled by DCI 1\_0 and subject to HARQ-ACK feedback, DMRS\_Alt 2 (PDSCH DMRS as per ‘pos2’ for both PDSCH RE mapping and channel estimation for PDSCH reception) applies | * For a UE configured with Cap #2 in a DL cell and NOT configured with additional DMRS by higher layers:   + The UE is expected to satisfy Cap #2 processing times as per Table 5.3-2, regardless of ~~the~~ a unicast PDSCH actually containing additional DMRS that the UE is expected to process   + **No NBC issue \*\***   + **Serious feasibility issue at the UE for Cap #2** |
| **Option B1** | * For a UE configured with Cap #2 in a DL cell and NOT configured with additional DMRS by higher layers:   + ***For all PDSCHs, including PDSCH scheduled by DCI format 1\_0 with ld > 7 or ld > 4 symbols for mapping types A or B respectively, subject to HARQ-ACK feedback (“unicast PDSCHs”) are to follow Cap #2 timeline***   + For PDSCH scheduled by DCI 1\_0 and subject to HARQ-ACK feedback, DMRS\_Alt 1 (PDSCH DMRS as per ‘pos2’ for PDSCH RE mapping but UE is not expected to process additional DMRS symbols for PDSCH reception) applies | * For a UE configured with Cap #2 in a DL cell and NOT configured with additional DMRS by higher layers:   + The UE is expected to satisfy Cap #2 processing times as per Table 5.3-2, regardless of ~~the~~ a unicast PDSCH actually containing additional DMRS or not   + The UE is NOT expected to process any additional DMRS   + Existing DMRS mapping and rules are maintained   + **No feasibility issue for UE implementation**   + **UE needs different handling of additional DMRS symbols in general for DMRS per ‘pos2’**   + **This option is NBC to Rel-15 specs** |
| **Option B2** | * For a UE configured with Cap #2 in a DL cell and NOT configured with additional DMRS by higher layers:   + ***For all PDSCHs, including PDSCH scheduled by DCI format 1\_0 with ld > 7 or ld > 4 symbols for mapping types A or B respectively, subject to HARQ-ACK feedback (“unicast PDSCHs”) are to follow Cap #2 timeline***   + For PDSCH scheduled by DCI 1\_0 and subject to HARQ-ACK feedback, DMRS\_Alt 3 (PDSCH DMRS as per ‘pos0’ for both PDSCH RE mapping and channel estimation for PDSCH reception) applies | * For a UE configured with Cap #2 in a DL cell and NOT configured with additional DMRS by higher layers:   + The UE is expected to satisfy Cap #2 processing times as per Table 5.3-2, regardless of ~~the~~ a unicast PDSCH actually containing additional DMRS or not   + The UE is NOT expected to process any additional DMRS   + **DMRS mapping rule is updated – if Cap #2 is configured and additional DMRS is not configured, then UE assumes DMRS per ‘pos0’ for PDSCH scheduled by DCI 1\_0 subject to HARQ-ACK feedback**   + **No feasibility issue for UE implementation**   + **No need for different handling of additional DMRS symbols for a given DMRS time domain configuration**   + **This option is NBC to Rel-15 specs** |
| **Option C** | * For a UE configured with Cap #2 in a DL cell and NOT configured with additional DMRS by higher layers:   + ***For a PDSCH scheduled by DCI format 1\_0 with ld > 7 or ld > 4 symbols for mapping types A or B respectively, subject to HARQ-ACK feedback (“unicast PDSCH”), the UE may fall back to Cap #1 timeline; other PDSCHs are to follow Cap #2 timeline, subject to any other applicable constraints (e.g., Cap 2 with 136 PRBs constraint for 30 kHz)***   + For a PDSCH scheduled by DCI 1\_0 and subject to HARQ-ACK feedback, DMRS\_Alt 2 (PDSCH DMRS as per ‘pos2’ for both PDSCH RE mapping and channel estimation for PDSCH reception) applies | * For a UE configured with Cap #2 in a DL cell and NOT configured with additional DMRS by higher layers:   + All PDSCH durations can be scheduled by DCI format 1\_0 for unicast PDSCH   + **This option is NBC to Rel-15 specs**   + **Feasibility issue for UE implementation due to overlapping of processing timelines in case a “fast PDSCH” follows a “slow PDSCH” in quick succession** |