**3GPP TSG RAN WG1 #104b-e R1-2103217**

**e-Meeting, April 12th – 20th, 2021**

**Agenda item:** 7.2.6

**Source:** Moderator (Samsung)

**Title:** Summary for Rel.16 NR eMIMO maintenance

**Document for:** Discussion and Decision

1. Introduction

The moderator summary of the maintenance-related issues raised in the submitted contributions for Rel.16 NR\_eMIMO maintenance is given below. The listed maintenance issues are under the usual designations:

* LP: low-PAPR RS
* MB: Multi-beam operation
* MT: Multi-TRP
* MU: Type-II enhancement for MU-CSI
* UL: UL full power transmission

An initial assessment on each of the issues is given (but can be revised based on the outcome of the discussion during the preparation week). The assessment will be used as a basis to select four issues (per chairman instruction) for further discussion in the upcoming weeks.

* *High priority (H):* this includes high-priority item (essential, pending issues, broken spec components) and proposed editorial changes that either enhance the clarity of the specs or correct mistakes
	+ *H2:* The proposal can be endorsed without discussion (i.e. unless pointed out otherwise, the moderator will propose to the chair that the proposal be endorsed thereby not counted toward the four-thread quota). It can be merged with any of the assigned threads without any further discussion. This includes TPs associated with previous agreements.
* *Non-essential (N)*: this includes all other purposes such as spec optimization and low priority issues
* *Editorial (E)*: this includes editorial issues that will be handled as editorial CRs (to be communicated to the editors/chairs) and thereby not counted toward the four-thread quota
1. Maintenance issues

The issues are summarized in the following table:

**Table 1 Summary**

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| **#** | **Issue (summary)** | **Companies** | **Initial assessment** | **Company inputs (if any)** |
| MB.1  | RRC parameter misalignment in TS 38.213 and TS 38.214 with regards to *enableDefaultBeamPL-ForPUSCH0-0-r16*FL: needed | CATT, Vivo, Docomo | E | Samsung: We agree with the FL’s assessment.Ericsson: No CRs should be submitted for RRC parameter alignments. Please contact the editor directly.OPPO: This CR is not needed. RAN2 sent an LS which has a guidance that suffix "-r16" should not be used |
| MB.2 | Fixing a typo in clause 7.2.1 of TS 38.213: ‘with with’ 🡪 ‘with’FL: needed | CATT, Docomo | E | Samsung: We agree with the FL’s assessment.Ericsson: Please contact the editor directly.  |
| MB.3 | Current TS38.213 could be misinterpreted that multi-CC simultaneous TCI update cannot be applied to CORESET#0 (i.e. p=0) because CORESET index p starts from 1 in the same paragraph (either 0<p<12 or 0<p<16). TP proposes to include p=0 for the multi-CC simultaneous TCI update to be aligned with the related MAC-CE description. FL: discussed in pre-phase of last several meetings but not treated. Even though current text in TS38.213 does not explicitly preclude p=0, it is true that it could be implicitly precluded by the value range of p described in the same paragraph.  | Vivo | H | Samsung: We agree with the FL’s assessment.Qualcomm: It may be non-essential. The spec does not preclude p=0. So 213 does not conflict with 321 to our understanding. Nokia: ok to clarify. Ericsson: Looking at the section in 38.213, the text on *simultaneousTCI-UpdateList1* does not belong at its current location. The bulleted list explains what a CORESET contains. Adding p>=0 does not improve readability.  |
| MB.4 | 1. For multi-CC simultaneous TCI update, clarify which BWP’s tci-States-ToAddModList is applied to CORESET#0. 2. If TCI state of active BWP is applied for CORESET #0, conclude that UE does not expect to receive TCI state indication for CORESET #0 when the BWP(s) containing CORESET #0 is inactive.FL: discussed in pre-phase of last meeting. According to TS38.321, it seems clear that the TCI list is from active BWP of the CC. Motivation of the second proposal is unclear. | Vivo | N | Samsung: We agree with the FL’s assessment. It is obviously from active BWP of the CC.vivo: The motivation for the second proposal is as following: if CORESET #0 in CC2 is QCL’ed with a TCI state not in the same BWP, the UE behavior is unclear. We would like to preclude such cases.Qualcomm: Not see the issue. For Proposal 1, 321 is already clear to use active BWP. For Proposal 2, no gNB will send TCI indication for CORESET 0 if it is in inactive BWP. 321 already says no monitoring PDCCH on an inactive BWP, so the proposal is a unnecessary concern. If the field of CORESET ID is set to 0, this field indicates a TCI-StateId for a TCI state of the first 64 TCI-states configured by tci-States-ToAddModList and tci-States-ToReleaseList in the PDSCH-Config in the active BWP.1> if a BWP is deactivated:[…]2> not monitor the PDCCH on the BWP;Ericsson: Agree with FL assessment Huawei, HiSilicon: Support to discuss. |
| MB.5 | Paragraph indentation and wording modification (Proposal4 of R1-2102946)FL: Paragraph indentation seems needed while wording modification seems not. | Vivo | E (only paragraph indentation) | Samsung: We agree with the FL’s assessment. Likewise the FL’s assessment, we only agree with aligning the paragraph indentation, but the added wording “that does not include rrc-ConfiguredUplinkGrant” does not need since the following wording “activated, as described in Clause 10.2, by a DCI format that does not include an SRI field” clearly mentioned that this is type 2 configured grant.vivo: Fine with FL proposal.Qualcomm: Same view as FL. Indent is needed but additional wording is not needed, since the paragraph is clearly saying “activated, as described in Clause 10.2, by a DCI format that does not include an SRI field”, which excludes Type1 CG.Ericsson: Please contact the editor directly.  |
| MB.6 | Clarifying PL-RS selection for Type-2 CG-PUSCH based on one of the following interpretations:•Interpretation 1: The PL-RS for Type-2 CG-PUSCH is always based on the one indicated by SRI in activating DCI•Interpretation 2: The PL-RS for Type-2 CG-PUSCH is based on the lasted PL-RS associated with the SRIFL: current spec seems support Interpretation 1 only as the MAC-CE is only for updating ‘mapping’ | Apple, OPPO | N | Samsung: We agree with the FL’s assessment. Our understanding is Interpretation 1 before UE receives a deactivation DCI. Apple: If all the companies share the same view that current specification is interpretation 1, we can quickly reach a conclusion without potential change to the specification.Qualcomm: Not see the issue. The two interpretations may need further clarification. Anyway, the spec rule is clear to us, i.e. using the currently mapped PL RS for the indicated SRI. the UE determines a RS resource index  from a value of PUSCH-PathlossReferenceRS-Id that is mapped to a SRI field value in a DCI format activating the PUSCH transmission.Ericsson: The FL interpretation seems to be the natural interpretation, but interpretation 2 is more aligned with the intent of the functionality. Propose to discuss, Huawei, HiSilicon: Our understanding is Interpretation 1 (assuming ‘lasted’ in Interpretation 2 is a typo and should be ‘latest’).OPPO: support to discuss it as H. We need to discuss and make a conclusion to align the understanding |
| MB.7 | Clarify that SSB cannot be used for BFD (R1-2102374)FL: This was proposed multiple times and suggest at least a conclusion should be made. | OPPO | H | Docomo: Not support. This should be N.Samsung: We agree that some clarification would be needed. However, if we adopt this CR, the TP has impacts on Rel-15 PCell BFR. Hence if we agree with discussion for this issue, the right place would be Rel-15 maintenance.ZTE: We can fine with some discussion. But, alternatively, we support to explicitly specify that the SSB can be used for BFD.Apple: We think some clarification is preferred since this has been lingering issue for long time. However, the outcome could be that SSB can be used for BFDQualcomm: We prefer to clarify in 213 section 6 that SSB can be BFD RS. SSB at least as explicit BFD RS is in 331 and UE capability even from R15. purpose ENUMERATED {beamFailure, rlf, both}, detectionResource CHOICE { ssb-Index SSB-Index,maxNumberSSB-BFDDefines maximal number of different SSBs across all CCs, and across MCG and SCG in case of NR-DC, for UE to monitor PDCCH quality.Ericsson: Discussed multiple times. No point to discuss again. Intel: OK to make conclusion.Huawei, HiSilicon: In our understanding, RAN4 has introduced performance requirement on using SSB for BFD since R15. Still, we have a question on how the gNB can configure the UE to monitor SSB for BFD. In this sense, we support to discuss this. OPPO: we need to make an conclusion to clarify that at least. |
| MB.8 | Clarify SCS for 28 symbols (R1-2102657, TP1/2)FL: Although this was discussed in last meeting without any consensus, it is better to fix this issue with regard to potential ambiguity. | ZTE, Docomo | H | Docomo: Agree with H.Samsung: Our view is interpretation-2. It would be better to clarify this or just a conclusion is also fine.Qualcomm: We support TP1. The spec needs to be aligned with the earlier agreement. **Agreement**The value of K to apply the newly identified beam to all the CORESETs after UE receives response to step 2 MAC CE is 2828 symbols is based on the smallest SCS of the response receiving cell and the failed cellEricsson: ok to clarify.Huawei, HiSilicon: Our understanding is Interpretation 2, and we failed to see how the situation would be different from last meeting.OPPO: Prefer to make an conclusion for this issue, |
| MB.9 | Update CORESETPoolIndex to be 0 after BFR (R1-2102657, TP3)FL: This was proposed multiple times and suggest at least a conclusion should be made. | ZTE | H | Docomo: Not support. This should be N.Samsung: Our view is that this is not essential. We think that rather than reseting CORESETPoolIndex of all CORESETs as 0, restricting monitoring the CORESETs with CORESETPoolIndex = 1 before MAC-CE activation for TCI state of the CORESETs from UE side, or using a single CORESETPoolIndex for a certain time period after beam failure (which is LGE’s last comment) by gNB side is sufficient by implementation.LG: not essential. BFR with two CORESET pools is not of typical case for Rel-16 and is now under working for Rel-17. gNB can configure only one CORESET pool for BFD cell or use one CORESET pool after beam failure by implementation. No need to further optimize this.ZTE: We encourage opponents to clarify the UE behavior when mDCI-mTRP and SCell-BFR are enabled both.Ericsson: The UEs should not autonomously update its RRC configuration. No need to discuss.OPPO: Not essential. Do not support reset the CORESETPoolindex after BFR. |
| MB.10 | Add a condition (when spCell-BFR-CBRA is ‘true’) to apply new beam to PUCCH after CBRA based BFR (R1-2102946)FL: This is based on RAN2’s agreement in last meeting. | vivo | E | Docomo: Not support. This should be N. There is no ambiguity in the current specs.* In TS38.213, PUCCH beam after CBRA-BFR is updated if BFR MAC CE is contained in Msg.3/A.
* In TS38.321, BFR MAC CE is contained in Msg.3/A, **if spCell-BFR-CBRA is set 'true'.**

Samsung: We agree with the FL’s assessment.vivo: This could be editorial. E: Not necessary. The description in the RAN1 specs assume that the feature is configured. The configurability is described in 331, and that is sufficient,Intel: Not needed. Should be clear from RAN2 specOPPO: Not needed. Agree with the comments of Docomo. |
| MB.11 | Define BFD RS selection to avoid ambiguity of BFD RS counting for FG 16-1g (R1-2103084)FL: There is a potential ambiguity for UE FG 16-1g counting if BFD RS selection rule is unclear. | Apple, Docomo | H | Docomo: Agree with H.Samsung: We agree with the FL’s assessment.LG: not positive to open this discussion again at this late stage. This can create NBC issue. Regarding UE capability, we think that one RS difference on counting between UE and gNB would not create critical issue (gNB may assume that UE will use all three RSs for the counting purpose in such case).ZTE: We can fine with some discussion but can NOT support the proposed solution. In our view, the solution should be simplified, e.g., lowest CORESET ID.Qualcomm: Support. Otherwise, 16-1g does not work.Ericsson: important to clarify.Intel: OK to discussSpreadtrum: Fine to discuss, but the current TP seems to be non-backward compatibility and it will influence Rel-15 UE behavior. |
| MB.12 | Add time duration definition for CPU occupation for L1-SINR computation (R1-2103402)FL: It seems the time duration definition for CPU occupation for L1-SINR is missing in current spec. | Huawei/HiSilicon | H | Samsung: We agree with the FL’s assessment.LG: ok to discussZTE: We suggest to mark it as ‘N’. We have no agreement that report quantity = ‘none’ can be applied to L1-SINR measurement.vivo: Support to discuss this issue. Wording update may be necessary.Apple: We do not fully understand the motivation. Why L1-SINR is needed “for a CSI report with higher layer parameter reportQuantity set to 'none' and CSI-RS-ResourceSet with higher layer parameter trs-Info not configured”Nokia: support to discuss this. Ericsson: ok to discuss.Huawei, HiSilicon: Response to ZTE/Apple, our understanding is this case is supported/allowed by current specs, as there is no agreement to preclude it, and it can be used to enable UE Rx beam refinement taken interference into account. |
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| MT.1  | R1-2102596 suggested that the title description of Table 5.1.2.1-2 is not proper for URLLC scheme 4 since scheme 4 uses “RepetitionNumber-r16” but the table title uses “*pdsch-AggregationFactor*”. Thus, it suggested to use a separate table for scheme 4.FL: this was discussed in pre-phase in last meeting, some companies commented that the suggested change does not change the spec interpretation.  | CATT | N | Samsung: We agree with the FL’s assessment.vivo: Agree with FL’s assessment.Nokia: Discussed multiple times. This is not required. Spec interpretation is clear.Ericsson: Agree with FL’s assessment. |
| MT.2 | R1-2102596 explained that in current specification, out-of-order operation for PDSCH to HARQ-ACK can be supported only in slot-level granularity. R1-2102596 proposed to update the text to support out-of-order operation for PDSCH to TDMed HARQ-ACK within a slot.FL: this has been proposed multiple times. Suggest to discuss to at least make an conclusion.  | CATT | H | Samsung: We agree with the FL’s assessment.LG: ok to discussZTE: We are OK to discuss thisvivo: We think the current spec is clear since out-of-order operation for PDSCH to HARQ-ACK is supported when separate HARQ-ACK feedback in a slot is configured and there is no restriction of forbidding such a case in the spec.Apple: We do not prefer to discuss it. It already provides NW with enough flexibility for scheduling inter-slot TDM OOO. We already finished the UE FG design. Nokia: we are fine to discuss the issue. Intel: we support to discuss this issue |
| MT.3 | The issues of default TCI state:* R1-2102658 proposed to extend the default TCI state mapping mechanism specified for TDMSchemeA to all the other single-DCI PDSCH transmission schemes.
* R1-2102947 proposed to specify the mapping between default TCI states and frequency sources of scheme 2a/2b
* R1-2102947 proposes to specify default TCI state for cross-carrier scheduling for mTRP.
* R1-2103218 propose to specify the default TCI state of PDSCH of cross-carrier in S-DCI based mTRP.
* R1-2103673 suggested to discuss the default TCI state of mTRP in cross-carrrier scheduling.

FL: That issue has been discussed multiple time in previous meetings but no conclusion. | ZTE, vivo, Samsung, ASUSTEK | N | Samsung: Since the spec already covers the default TCI state of scheme 3 and 4, AP CSI-RS cases, and a part of cross-carrier scheduling, so for the completeness of the spec, it would be good to have the solutions for other issues related to default TCI state.LG: Agree with FL’s assessment (Not essential)ZTE: We have to emphasize the current spec is not complete, the Rel-16 default TCI is only specified for TDM schemes, not yet for SDM and FDM. We encourage opponents to clarify the technical reason.vivo: Propose changing to “H”. As we have made agreement on the default TCI state of scheme 3 and 4, there is no reason to miss the Scheme 2a/2b part. In addition, default TCI state of MTRP for cross-carrier scheduling also needs to be specified. ASUSTeK: Since the concerned cases of default TCI state is covered by existing spec, while incompleteness or conflict has been identified by the proponents, common understanding on the existing spec should be derived to resolve this issue. (Note that the contribution has indeed been there for couples of meetings while concrete discussion has not started yet)Nokia: Ok to discuss “default TCI state mapping mechanism specified for TDMSchemeA to all the other single-DCI PDSCH transmission schemes”Intel: we are not too eager to discuss this full package (default beam for 2a/2b can be discussed if there is space)OPPO: This issue has been discussed a lot in previous meeting. Considering limited email thread, not support to discuss it again. They are not essential issues. Lenovo/MotM: Propose changing to “H”. Default TCI state of MTRP for cross-carrier scheduling should be specified. |
| MT.4 | R1-2102373 proposes to clarify in 38.214 that the UE does not expect to receive single-DCI mTRP TCI state activation MAC CE when multi-DCI mTRP is configured.FL: it is a good clarification in 214. | OPPO, Docomo, Apple | H | Docomo: Agree with H.Samsung: We don’t think it is needed. It was already captured as a conclusion in RAN1#101-e, that is, Simultaneous reception or dynamic switch of sDCI based mTRP and mDCI based mTRP are not supported. Also, Note 1 in the conclusion also said that this conclusion has no RAN1 specification impact in Rel-16.LG: Ok to discussZTE: This seems non-essential since gNB will not configure SDCI and MDCI based MTRP together.vivo: We don’t have the need to specify anything with the conclusion.Apple: We are fine to discuss this and clarify the specification.QC: Not support. This has been discussed and captured as conclusion. No need to discuss again.**Conclusion**In Rel-16, RAN1 specification do not support the following operations at least within a CC:* Simultaneous reception of single-DCI based multi-TRP and multi-DCI based multi-TRP
* Dynamic switch between single-DCI based multi-TRP and multi-DCI based multi-TRP

Note1: this conclusion has no RAN1 specification impact in Rel-16. Note2: Whether to support the above operation in Rel-17 or beyond is FFS.Nokia: agree with QC, this was discussed and have above conclusion. Ericsson: This doesn’t need to be discussed. As mentioned by others above, we already made a conclusion in RAN1#101-e that sDCI and mDCI cannot be simultaneously received. And there was a clear note in the conclusion that there is no RAN1 specification impact in Rel-16. So, no need to rediscuss this already concluded issue.Intel: No need, this issue is concluded as mentioned above.Huawei, HiSilicon: The issue has discussed before and agreed before as shown in QC’s comment. So, no need to be discussed again.CATT: discussion is not needed. We already have conclusion on this issue in previous meeting.  |
| MT.5 | For multi-DCI based M-TRP transmission, UE needs to determine whether is larger than or not. But the description in 213 does not align with that. According to the description in current 213, reader would understand that the SCS configuration μ only corresponds to the active DL BWPs of the scheduling cells, and the deactivated cells (without active DL BWP) are not counted for the comparison. Proposal 2 in R1-2102373 provided TP to fix that.FL: this is needed. It looks like the current text description in 213 change the UE behavior wrongly. | OPPO | H | Samsung: We agree with the FL’s assessment and if other companies are okay, then it can be rated E.ZTE: We are OK to discuss thisApple: We are supportive to discuss itQC: Not support. The TP is undoing a change made by another Rel. 16 AI (NR-DC/CA for the case of x-carrier scheduling with different numerologies). The TP is not multi-TRP related issue (applies to single-TRP as well). If needed, it can be discussed in AI 7.2.10.Agreements**:*** At least for the case of lower SCS PDCCH scheduling a higher SCS PDSCH the earliest possible starting point for the PDSCH is defined by the end of the PDCCH + 
	+ >0. Detailed value(s) FFS
	+ FFS other factor(s) impacting 
* The limit of BDs/CCEs (per slot in the scheduling CC) for the scheduled CC is determined based on the numerology of the scheduling CC.
	+ Change the definition of NcellsDL, to “the number of configured DL-CCs whose scheduling cell is with active DL BWP having SCS configuration ” as in Section 10.1 of 38.213

Nokia: Ok to discuss. Huawei, HiSilicon: fine to discuss. |
| MT.6 | R1-2102947 proposes TP to specify the case when PUCCH of CSI/SR/LRR does not overlap with two HARQ-ACK PUCCHs.FL: this issue has been proposed by multiple times. We can discuss and make an conclusion  | Vivo, Apple | H | Docomo: can be N and left to gNB implementation.Samsung: It also can be avoided by gNB implementation.LG: it is not essential and should be low priorityvivo: Agree with FL’s assessment. This restriction is needed to make the gNB avoid such overlapping cases. Otherwise, UE behavior is unspecified when PUCCH of CSI/SR/LRR overlaps with two HARQ-ACK PUCCHs.Apple: We think this is very important for UE implementation. gNB always has scheduling flexibility, but the specification is needed for the expected UE behavior Nokia: Not support. Agree also with FL. Ericsson: Agree with DCM and Samsung that this can be handled by gNB implementation. Suggest to change this to N.Intel: no need, similar proposals were discussed multiple times. The common understanding is to leave this to gNB implementation.Huawei, HiSilicon: no need to discuss it again, the issue can be addressed with implementation.OPPO: support to discuss and at least make a conclusion.CATT: agree to discuss and make a conclusion. |
| MT.7 | R1-2102947 proposes to conclude that UE does not expect to be scheduled a PDSCH overlapping with a PDCCH associated to CORESET having different CORESETPoolIndex from the scheduling PDCCHFL: During pre-phase in last meeting, 9 companies thought it is H but 4 companies thought it is N.  | vivo | H | Docomo: can be N.Samsung: It seems not essential and can be rated N.LG: it is not essential and should be low priorityZTE: This should be lower priority since it can be up to implementation. That is, gNB will not configure like that, otherwise, it will be an error case.vivo: To avoid the unclear UE behavior to deal with such overlapping cases, we propose to have the restriction.Apple: We support the discussionQC: Not support as the issue is not clear (PDSCH can be rate matched around other CORESETs). This was discussed not only during pre-phase discussion, but actually also as part of one of allocated Email threads before, and was not agreed.Nokia: Not required as a CR. But, ok to have a discussion (not only this, but also others) to close the discussions by concluding or marking that CRs are rejected (such that repeat of discussions are not coming back again).Ericsson: Similar view as Docomo.Intel: Not needed, resources protected from scheduled PDSCH is protected by gNB using rate-matching-pattern.OPPO: support to discuss and at least make a conclusion.CATT: no need to discuss. This can be avoided by implementation.  |
| MT.8 | The issue of radio link monitoring in mTRP:* Apple (R1-2103085) proposed to enhance the method of UE determining RLM RS in M-DCI mTRP system by adding Lmax = 8.

FL: was proposed multiple time and suggest to discuss it at least for a conclusion | Apple, Docomo | H | Docomo: Agree with H.Samsung: It can be discussed but the condition of mDCI mTRP would be included.LG: it is not essential and should be low priorityZTE: We are OK to discuss this. However, we think RLM is only supported for CORESETs with CORESETPoolIndex = 0.Nokia: same comment as before. Conclusion on the topic would be ok.Huawei, HiSilicon: Not essential to be discussed, which seems a further enhancement. |
| MT.9 | The issue of sub-slot HARQ vs m-DCI mTRP transmission.R1-2103085 proposes to make conclusion on the following two issues:* whether UE supports sub-slot based HARQ-ACK PUCCH when UE is configured with Multi-DCI based Multi-TRP operation.
* whether UE supports two HARQ-ACK codebooks with different priorities when UE is configured with Multi-DCI based Multi-TRP operation

R1-2103433 suggests to limit the number of PUCCH transmission for m-DCI based mTRP HARQ when sub-slot PUCCH or two PUCCH configurations are configured with m-DCI mTRP transmission.FL: The issue of m-DCI mTRP HARQ transmission vs sub-slot based HARQ is important. Suggest to discuss and make conclusion on that | Apple, Nokia  | H | Samsung: We agree with the FL’s assessment.ZTE: Not support. From the current specification, it is hard to support both features. Even without further conclusion, we think it is common understanding that both features can not be configured together.vivo: Agree with FL’s assessment. This issue can be discussed to avoid complexity of the mixed configurations.Apple: If companies believe it cannot be configured together, we can have a conclusion. But the current specification suggests that concurrent configuration is supported Intel: Support to discussSpreadtrum: fine to discuss, prefer to have one conclusionOPPO: support to discuss and at least make some conclusionLenovo/MotM: support to discuss. |
| MT.10 | R1-2103145 proposes to reset the PDSCH beam to qnew during BFR in single-DCI based mTRP systemFL: This was proposed couple of times. Whether to reset the beam of PDSCH to qnew was discussed a lot in rel15/rel16 PCell and SCell.  | Qualcomm | N | Samsung: We agree with the FL’s assessment.LG: Agree with FL’s assessment (Not essential)vivo: Agree with FL’s assessment. This can be discussed in Rel-17 scope.QC: The issue is specific to single-DCI (not related to Rel. 15 discussions). We suggest to discuss and align the understanding. Based on our offline discussions with companies, there are different understanding about default beam after BFR even in exiting spec. For single-DCI, the additional issues is that since the default beams are decoupled from CORESET beams, resetting the CORESET beam (e.g. resetting CORESET0 beam 28 symbols after BFR response in the case of PCell BFR) does not result in changing the default beam at all. Hence, UE cannot recover from BFR if only same-slot scheduling (has to be default beam) is supported. Nokia: we are Ok to discuss this. Ericsson: Ok to discuss. |
| MT.11 | R1-2103145 proposes to specify the BD/CCE limit when NR-DC and multi-DCI mTRP are configuredFL: This was discussed in pre-phase in a few previous meetings. Some companies commented that it is not essential issue. We might discuss it and make a conclusion on that | Qualcomm | H | Samsung: It seems not essential issue.LG: Not essentialZTE: We are OK to discuss itApple: We are okay to discuss itQC: Support as UE does not know how to report capabilities pdcch-BlindDetectionMCG-UE and pdcch-BlindDetectionSCG-UE since the value of them (sum of the two) should satisfy a condition which is a function of whether or not multi-DCI can be configured.Nokia: Ok to discuss. Ericsson: Ok to discuss.Huawei, HiSilicon: Not essential.Spreadtrum: Seems not essential.CATT: Not essential. |
| MT.12 | The issue of SPS PDSCH transmission in mTRP:* R1-2103145 proposed to clarify that the RV sequence used across multiple repetitions in schemes 2b, 3, and 4 is based on setting rvid=0.
* R1-2103218 proposed to Extend the single-DCI M-TRP dynamic grant PDSCH transmission schemes to include SPS PDSCH for enhanced PDSCH reliability for URLLC service types
* R1-2103551 proposed CR to specify the RV values for DL DPSCH SPS in S-DCI mTRP

FL: it was discussed in pre-phase in previous meeting and some companies thought that is it is not essential to rel16 and maybe for later release. In last meeting, 4 companies support it as H but 5 companies suggested this is N | Qualcomm, Samsung, Ericsson | N | Samsung: SPS is supported already in S-DCI based M-TRP. There’s no reason to exclude SPS for M-DCI based M-TRP only. Also overlapping issue should be resolved and without resolving this issue, overlapping SPS PDSCHs may not be received by the UE even though UE may have declared a capability to receive overlapping DG PDSCHs.vivo: Agree with FL’s assessment.QC: Suggest to promote to H for single-DCI case. SPS for single-DCI is already supported, and only requires clarification for RV.Nokia: support the discussion. Ericsson: Suggest to discuss at least the SPS for S-DCI based M-TRP. The support of SPS for S-DCI based M-TRP is not complete as the RV sequence used across multiple repetitions needs to be clarified. So, it would be good to complete the spec for S-DCI based M-TRP.Huawei, HiSilicon: Not essential, it is a further enhancement.Spreadtrum: support the discussion, at least for S-DCI based M-TRP.CATT: Not essential. |
| MT.13 | R1-2103218 proposes to Introduce a parameter X which can be corresponding to or can include a DCI decoding delay time for default TCI states of the single-DCI multi-TRP PDSCH repetition. UE applies the first TCI state to a receive symbols before decoding DCI. The value of X can be specified by one of the following candidatesFL: the threshold *timeDurationForQCL* already consider the DCI decoding latency.  | Samsung | N | Samsung: We think that it can be rated as H. For the case of tdmSchemeA based on default beam, if the second PDSCH occasion starts before finishing the PDCCH decoding, the UE cannot know an appropriate timing for applying the second default TCI. Hence, some clarification would be needed for this case.LG: Current specification is enough.Nokia: nothing seems required on this. Also issue is not clear.Ericsson: Agree with FL’s assessment. Samsung2: We would like to elaborate the issue more. So far the two default beams of tdmSchemeA, the mapping of the TCI states to PDSCH transmission occasions is determined according to clause 5.1.2.1 by replacing the indicated TCI states with the TCI states corresponding to the lowest codepoint among the TCI codepoints containing two different TCI states. However, before DCI is decoded, UE cannot know which time domain resources the UE would apply the first and second default TCI state, respectively. If the second PDSCH occasion starts earlier than the DCI decoding, then what is the UE behavior? Which default TCI the UE applies? The current spec did not clearly mention the UE behavior for this situation. |
| MT.14 | The issue of PT-RS in NC-JT:R1-2103552 suggested that the current PT-RS power allocation for NC-JT does not take the muted RE into account when 2 PT-RS ports are configured. Thus, R1-2103552 proposes to update PT-RS power allocation in Table 4.1-2 of 38.214 v16.4.0 to reflect additional power boosting for NC-JT when 2 PT-RS ports are configured and also proposed a new table for 38.214.FL: suggest to discuss it | Ericsson, Docomo | H | Docomo: Agree with H.Samsung: We agree with the FL’s assessment.LG: Ok to discussZTE: OK to discuss thisQC: We are fine to discuss this in more details.Nokia; Ok to discussEricsson: Support to discussSpreadtrum: agree with FL’s assessmentLenovo/MotM: support to discuss |
|  |
| UL.1 | ***ul-FullPwrMode2-TPMIGroup-r16***Indicates the UE supported TPMI group(s) which delivers full power. The capability signalling comprises the following values:- *twoPorts-r16* indicates a 2-bit bitmap- *fourPortsNonCoherent-r16* indicates one of the TPMI groups {~~G~~g0-3}- *fourPortsPartialCoherent-r16* indicates one of the TPMI groups (~~G~~g0-6)UE indicates support of this feature shall also indicate support of *ul-FullPwrMode2-MaxSRS-ResInSet.*NOTE 1: When a full coherent UE operates in mode 2, it reports TPMIs the same as a partial-coherent UE.NOTE 2: For 4 port partial-coherent or full-coherent UE, UE can only report: 2-port {2-bit bitmap} or/and one of 4-port non-coherent {G0~G3} or/and one of 4-port partial-coherent {G0~G6}For 4 port non-coherent UE, UE can only report: 2-port {2-bit bitmap} or/and one of 4-port non-coherent {G0~G3}For 2 port UE, UE can report: 2-port {2-bit bitmap}}NOTE 3: A UE that supports this feature must report at least one of the values.FL: TP for 38.306, should be discussed in RAN2 and make necessary correction according the previous LS from RAN1 | Samsung | E (RAN2) | Samsung: For clarification of label “E(RAN2)” which one is the right interpretation? 1) Do we agree on this CR and send an LS to RAN2?2) Should this issue be discussed in RAN2?LG: Ok to discuss for clarification.ZTE: We support to discuss thisFL: if RAN1 can agree on proposed correction, RAN1 can send LS to RAN2. My original thinking was it can discussed directly in RAN2. Intel: Not essential issue. There is no need to update RAN2 spec. In addition, the modification in Note 2 changes the agreement reached in UE feature session. The discussion should not be opened again.Ericsson: Agree with the feature lead that this issue should be discussed in RAN2, given the LS in R1-2006081. No need to discuss this in RAN1.QC: We don't think there is an issue in the current 38.306 spec. So we don't see the need to update the RAN2 spec.Huawei, HiSilicon: Agree with FL’s thinking, i.e., the typos can be directly discussed in RAN2. Not necessary for RAN1 discussion and no need to send an LS to RAN2. |
| UL.2 | To correct the maximum value of the j from ρ-1 to ν-1 for the intermediate quanlity in the formulation of DMRS port mapping in 6.4.1.1.3 of TS 38.211. | Huawei, HiSilicon | E | Huawei, HiSilicon: the issue is for UL DMRS port mapping in Rel-16 spec. The mapping between PUSCH and UL DMRS is incorrect captured in current spec, which should be addressed. Ericsson: Thanks for spotting this typo. Since there seems to be no danger of misinterpreting the spec given the remainder of the subclause (and the corresponding equation for PUSCH REs in 6.3.1.5), it should be a category D CR or referred to the editor.  |

1. Discussion and proposal

From the inputs shared by participating companies during the preparation phase, the following **observation** can be made:

* The following issues can be handled as E (a part of editorial CR):
* The following issues can be handled as H2 (editorial TPs that can be agreed without further email discussion, including capturing previous agreements)
* The following issues can be designated as H (requiring discussion and additional agreements/conclusions):

In light of the above observations, the moderator makes the following **proposals**:

* Continue discussion on 4 threads:
	+ Thread 1 (moderator ...) Maintenance and TPs for Multi-beam 1: addressing
	+ Thread 2 (moderator ...) Maintenance for Multi-TRP 1: addressing

# References

|  |  |  |  |
| --- | --- | --- | --- |
| 1 | R1-2102373 | Text proposals for M-TRP transmission | OPPO |
| 2 | R1-2102374 | Corrections for SCell BFR | OPPO |
| 3 | R1-2102595 | Maintenance of Multi-beam enhancement operation | CATT |
| 4 | R1-2102596 | Maintenance of Multi-TRP transmission | CATT |
| 5 | [R1-2102657](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_104b-e/Docs/R1-2102657.zip) | Maintenance of multi-beam operation | ZTE |
| 6 | [R1-2102658](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_104b-e/Docs/R1-2102658.zip) | Maintenance of Multi-TRP enhancements | ZTE |
| 7 | [R1-2102946](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_104b-e/Docs/R1-2102946.zip) | Maintenance on multi beam related issues | vivo |
| 8 | [R1-2102947](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_104b-e/Docs/R1-2102947.zip) | Maintenance on multi TRP | vivo |
| 9 | [R1-2103084](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_104b-e/Docs/R1-2103084.zip) | Remaining issues on Rel-16 beam management enhancement | Apple |
| 10 | [R1-2103085](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_104b-e/Docs/R1-2103085.zip) | Remaining issues on Rel-16 Multi-TRP enhancement | Apple |
| 11 | R1-2103145 | Remaining Issues on Multi-TRP Enhancements | Qualcomm Incorporated |
| 12 | R1-2103218 | On Rel.16 multi-TRP/panel transmission | Samsung |
| 13 | [R1-2103219](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_104b-e/Docs/R1-2103219.zip) | On UL full power transmission and multi-beam | Samsung |
| 14 | [R1-2103395](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_104b-e/Docs/R1-2103395.zip) | Corrections on the precoding for PUSCH | Huawei, HiSilicon |
| 15 | [R1-2103402](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_104b-e/Docs/R1-2103402.zip) | Correction on CPU occupation rules for L1-SINR reporting | Huawei, HiSilicon |
| 16 | [R1-2103433](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_104b-e/Docs/R1-2103433.zip) | Maintenance of Rel-16 Multi-TRP operation  | Nokia, Nokia Shanghai Bell |
| 17 | R1-2103551 | Draft CR on DL SPS based PDSCH repetitions | Ericsson |
| 18 | R1-2103552 | Maintenance for single-DCI based multi-TRP in Rel-16 | Ericsson |
| 19 | R1-2103673 | Interoperation between cross-carrier scheduling and multiple TRPs | ASUSTEK COMPUTER (SHANGHAI) |