

TSG RAN Working Group 1 Meeting #9
Dresden, Germany, November 30 - December 3

TSGR1#9(99)h95

TSG-RAN Working Group 4 (Radio) meeting #8
Sophia Antipolis, France 26-29 October 1999

TSG R4#8(99)762

Source: TSG RAN WG4
To: TSG RAN WG1
Title: Liaison statement on UE timing adjustment issue

TSG RAN WG4 would like to thank TSG RAN WG1 for its liaison statement TSGR1#7(99)e13 on L1 timing issues asking TSG RAN WG4 their guidance on the maximum timing adjustment of the UE DPDCH/DPCCH that can be envisaged. The issue has been investigated during the WG4 #8 meeting and even if a complete agreement has not been reached, WG4 would like to inform WG1 on the current progresses for UTRA/FDD.

Regarding the UE DPDCH/DPCCH TX timing step size, an agreement has been found within TSG RAN WG4; the conclusion is that the UE shall adjust its transmission with step size less than or equal to $\frac{1}{4}$ chip.

Concerning the maximum rate of the adjustment, the value to be retained differs depending on the company and unfortunately no consensus has been reached within WG4 at the time being. WG4 would like to further study impacts on both BS and UE hardware as well as on system performances prior to conclude.

Regarding the UE initial transmit timing error, WG4 would like to inform WG1 that the current assumption is that this value shall be less than or equal $\pm 1/2$ chip, however this has to be confirmed within TSG RAN WG4.

WG4 will insert a section relevant to this issue in TS25.103 stipulating the associated values and the test procedures (based on document TSGR4#8(99)654).