

Agenda Item:

Source:

Title: Proposal for inclusion of the DL/UL timing offset definition
Decision

1. Introduction

After removing Figure 28 “Physical channel timing relation” and relating text from TS 25.211 V2.2.1 (R1-99a71), the definition of the frame timing between DL and UL DPCHs, i.e., T_o , exists only in Appendix A of TS 25.211. Note that T_o is not included in Figure 9 of TS 25.401 V1.1.1. T_o should be defined somewhere in the main body of the TS 25.200 series. We propose to insert the definition into Section 7.1.1 of TS 25.211.

2. Text proposal for TS 25.211

7.1 DPCCH/DPDCH timing relations

7.1.1 Uplink

In uplink the DPCCH and all the DPDCHs transmitted from one UE have the same frame timing.

The uplink DPCCH/DPDCH frame starts 1024 chips (measured at the UE antenna) behind the earliest received path of the downlink DPCH frame. The starting phase of the uplink scrambling code is synchronised with this uplink DPCCH/DPDCH frame timing.