Agenda Item:<br>Source:<br>Title:<br>Document for:

AH 14<br>Golden Bridge Technology<br>CPCH frame format tables<br>Discussion and Approval

## Discussion

It has been proposed that two tables be added to the new section titled 5.2.2.2.5 of $S$ document 25.211. The tables are identical to the ones corresponding to the dedicated uplink physical channels as in section 5.2.1 of S25.211

## Proposed text

Add the following text to the end of section 5.2.2.2.5:
The frame structure is identical to the uplink dedicated physical channel as shown in Figure 1 of section 5.2.1.

The exact number of bits of the different uplink DPCCH fields ( $\mathrm{N}_{\text {pilot }}, \mathrm{N}_{\text {TPC }}, \mathrm{N}_{\text {FBI }}$, and $\mathrm{N}_{\text {TFCI }}$ ) is yet to be determined. The field order is fixed. A limited set of field combinations will be defined.

The values for the number of bits per field are given in Table 1 and Table 2. The channel bit and symbol rates given in Table 1 are the rates immediately before spreading.

Table 1: DPDCH fields

| Channel Bit <br> Rate (kbps) | Channel Symbol <br> Rate (ksps) | SF | Bits/ <br> Frame | Bits/ <br> Slot | $\mathrm{N}_{\text {data }}$ |
| ---: | :---: | ---: | :--- | :--- | :--- |
| 16 | 16 | 256 | 160 | 10 | 10 |
| 32 | 32 | 128 | 320 | 20 | 20 |
| 64 | 64 | 64 | 640 | 40 | 40 |
| 128 | 128 | 32 | 1280 | 80 | 80 |
| 256 | 256 | 16 | 2560 | 160 | 160 |
| 512 | 512 | 8 | 5120 | 320 | 320 |
| 1024 | 1024 | 4 | 10240 | 640 | 640 |

There are two types of Uplink Dedicated Physical Channels; those that include TFCI(e.g. for several simultaneous services) and those that do not include TFCI(e.g. for fixed-rate services). These types are reflected by the duplicated rows of Table 2. The channel bit and symbol rates given in Table 2 are the rates immediately before spreading.

Table 2: DPCCH fields

| Channel Bit <br> Rate (kbps) | Channel Symbol <br> Rate (ksps) | SF | Bits/ <br> Frame | Bits/ <br> Slot | $\mathrm{N}_{\text {pilot }}$ | $\mathrm{N}_{\text {TPC }}$ | $\mathrm{N}_{\text {TFCI }}$ | $\mathrm{N}_{\text {FBI }}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 16 | 16 | 256 | 160 | 10 | 6 | 2 | 2 | 0 |
| 16 | 16 | 256 | 160 | 10 | 8 | 2 | 0 | 0 |
| 16 | 16 | 256 | 160 | 10 | 5 | 2 | 2 | 1 |
| 16 | 16 | 256 | 160 | 10 | 7 | 2 | 0 | 1 |
| 16 | 16 | 256 | 160 | 10 | $[6]$ | $[2]$ | $[0]$ | $[2]$ |
| 16 | 16 | 256 | 160 | 10 | $[5]$ | $[1]$ | $[2]$ | $[2]$ |

Note: The last two rows which correspond to 2 FBI bits might not be needed.

