**3GPP TSG-RAN WG4 Meeting #111  *Draft\_R4-2409968***

**Fukuoka City, Japan, May. 20th - May. 25th,2024**

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| --- |
| *CR-Form-v12.3* |
| **CHANGE REQUEST** |
|  |
|  | **38.101-4** | **CR** | **draftCR** | **rev** | **-** | **Current version:** | **18.3.0** |  |
|  |
| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* |
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|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| ***Proposed change affects:*** | UICC apps |  | ME | **X** | Radio Access Network |  | Core Network |  |

|  |
| --- |
|  |
| ***Title:***  | DraftCR on RMC for Advanced Receivers |
|  |  |
| ***Source to WG:*** | Nokia |
| ***Source to TSG:*** | R4 |
|  |  |
| ***Work item code:*** | NR\_Demod\_enh3-Perf |  | ***Date:*** | 2024-05-13 |
|  |  |  |  |  |
| ***Category:*** | **B** |  | ***Release:*** | Rel-18 |
|  | *Use one of the following categories:****F*** *(correction)****A*** *(mirror corresponding to a change in an earlier release)****B*** *(addition of feature),* ***C*** *(functional modification of feature)****D*** *(editorial modification)*Detailed explanations of the above categories canbe found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | *Use one of the following releases:Rel-8 (Release 8)Rel-9 (Release 9)Rel-10 (Release 10)Rel-11 (Release 11)…Rel-17 (Release 17)Rel-18 (Release 18)Rel-19 (Release 19) Rel-20 (Release 20)* |
|  |  |
| ***Reason for change:*** | Introduction of reference measurement channels for Advance Receivers (R-ML) with MCS17 |
|  |  |
| ***Summary of change:*** | Addition of RMC for MCS17 FDD and TDD with Rank 1 and 2 |
|  |  |
| ***Consequences if not approved:*** | No RMC will be available for Advance Receiver requirements configured with MCS17 |
|  |  |
| ***Clauses affected:*** | A.3.2.1.5 and A3.2.2.7 |
|  |  |
|  | **Y** | **N** |  |  |
| ***Other specs*** |  | **X** |  Other core specifications  |  |
| ***affected:*** | **X** |  |  Test specifications | TS38.521-4 |
| ***(show related CRs)*** |  | **X** |  O&M Specifications |  |
|  |  |
| ***Other comments:*** |  |
|  |  |
| ***This CR's revision history:*** |  |

**<Start of change>**

#### A.3.2.1.5 Reference measurement channels for Intra-cell Inter-UE interference scenario

Table A.3.2.1.5-1: PDSCH Reference Channel for FDD Intra-cell Inter-UE interference scenario

|  |  |  |
| --- | --- | --- |
| Parameter | Unit | Value |
| Reference channel |  | R.PDSCH.5-1.1 FDD | R.PDSCH.5-1.2 FDD | R.PDSCH.5-1.3 FDD | R.PDSCH.5-1.4 FDD |  |
| Channel bandwidth | MHz | 10 | 10 | 10 | 10 |  |
| Subcarrier spacing | kHz | 15 | 15 | 15 | 15 |  |
| Number of allocated resource blocks | PRBs | 52 | 52 | 52 | 52 |  |
| Number of consecutive PDSCH symbols |  | 12 | 12 | 12 | 12 |  |
| Allocated slots per 2 frames | Slots | 19 | 19 | 19 | 19 |  |
| MCS table |  | 64QAM | 64QAM | 64QAM | 64QAM |  |
| MCS index |  | 13 | 13 | 17 | 17 |  |
| Modulation |  | 16QAM | 16QAM | 64QAM | 64QAM |  |
| Target Coding Rate |  | 0.48 | 0.48 | 0.43 | 0.43 |  |
| Number of MIMO layers |  | 1 | 2 | 1 | 2 |  |
| Number of DMRS REs |  | 12 | 24 | 12 | 24 |  |
| Overhead for TBS determination |  | 0 | 0 | 0 | 0 |  |
| Information Bit Payload per Slot  |  |  |  |  |  |  |
|  For Slot i = 0 | Bits | N/A | N/A | N/A | N/A |  |
|  For Slots i = 1,…, 19 | Bits | 13064 | 24072 | 17928 | 32264 |  |
| Transport block CRC per Slot |  |  |  |  |  |  |
|  For Slot i = 0 | Bits | N/A | N/A | N/A | N/A |  |
|  For Slots i = 1,…, 19 | Bits | 24 | 24 | 24 | 24 |  |
| Number of Code Blocks per Slot |  |  |  |  |  |  |
|  For Slot i = 0 | CBs | N/A | N/A | N/A | N/A |  |
|  For Slots i = 1,…, 19 | CBs | 2 | 3 | 3 | 4 |  |
| Binary Channel Bits Per Slot |  |  |  |  |  |  |
|  For Slot i = 0 | Bits | N/A | N/A | N/A | N/A |  |
|  For Slots i = 10, 11 | Bits | 26208 | 47424 | 39312 | 71136 |  |
|  For Slots i = 1,…, 9, 12, …, 19 | Bits | 27456 | 49920 | 41184 | 74880 |  |
| Max. Throughput averaged over 2 frames | Mbps | 12.411 | 22.868 | 34.960 | 62.915 |  |
| Note 1: SS/PBCH block is transmitted in slot #0 with periodicity 20 msNote 2: Slot i is slot index per 2 frames |

**<< Unchanged sections omitted >>**

#### A.3.2.2.7 Reference measurement channels for Intra-cell Inter-UE interference scenario

Table A.3.2.2.7-1: PDSCH Reference Channel for TDD Intra-cell Inter-UE interference scenario

|  |  |  |
| --- | --- | --- |
| Parameter | Unit | Value |
| Reference channel |  | R.PDSCH.7-1.1 TDD | R.PDSCH.7-1.2 TDD | R.PDSCH.7-1.3 TDD | R.PDSCH.7-1.4 TDD |  |
| Channel bandwidth | MHz | 40 | 40 | 40 | 40 |  |
| Subcarrier spacing | kHz | 30 | 30 | 30 | 30 |  |
| Allocated resource blocks | PRBs | 106 | 106 | 106 | 106 |  |
| Number of consecutive PDSCH symbols |  |  |  |  |  |  |
|  For Slot i, if mod(i, 10) = 7 for i from {0,…,39} |  | 4 | 4 | 4 | 4 |  |
|  For Slot i, if mod(i, 10) = {0,1,2,3,4,5,6} for i from {1,…,39} |  | 12 | 12 | 12 | 12 |  |
| Allocated slots per 2 frames |  | 31 | 31 | 31 | 31 |  |
| MCS table |  | 64QAM | 64QAM | 64QAM | 64QAM |  |
| MCS index |  | 13 | 13 | 17 | 17 |  |
| Modulation |  | 16QAM | 16QAM | 64QAM | 64QAM |  |
| Target Coding Rate |  | 0.48 | 0.48 | 0.43 | 0.43 |  |
| Number of MIMO layers |  | 1 | 2 | 1 | 2 |  |
| Number of DMRS REs |  |  |  |  |  |  |
|  For Slot i, if mod(i, 10) = 7 for i from {0,…,39} |  | 6 | 12 | 6 | 12 |  |
|  For Slot i, if mod(i, 10) = {0,1,2,3,4,5,6} for i from {1,…,39} |  | 12 | 24 | 12 | 24 |  |
| Overhead for TBS determination |  | 0 | 0 | 0 | 0 |  |
| Information Bit Payload per Slot  |  |  |  |  |  |  |
|  For Slots 0 and Slot i, if mod(i, 10) = {8,9} for i from {0,…,39} | Bits | N/A | N/A | N/A | N/A |  |
|  For Slot i, if mod(i, 10) = 7 for i from {0,…,39} | Bits | 8456 | 14600 | 11528 | 19464 |  |
|  For Slot i, if mod(i, 10) = {0,1,2,3,4,5,6} for i from {1,…,39} | Bits | 26632 | 49176 | 35856 | 65576 |  |
| Transport block CRC per Slot |  |  |  |  |  |  |
|  For Slots 0 and Slot i, if mod(i, 10) = {8,9} for i from {0,…,39} | Bits | N/A | N/A | N/A | N/A |  |
|  For Slot i, if mod(i, 10) = 7 for i from {0,…,39} | Bits | 24 | 24 | 24 | 24 |  |
|  For Slot i, if mod(i, 10) = {0,1,2,3,4,5,6}for i from {1,…,39} | Bits | 24 | 24 | 24 | 24 |  |
| Number of Code Blocks per Slot |  |  |  |  |  |  |
|  For Slots 0 and Slot i, if mod(i, 10) = {8,9} for i from {0,…,39} | CBs | N/A | N/A | N/A | N/A |  |
|  For Slot i, if mod(i, 10) = 7 for i from {0,…,39} | CBs | 2 | 2 | 2 | 3 |  |
|  For Slot i, if mod(i, 10) = {0,1,2,3,4,5,6} for i from {1,…,39} | CBs | 4 | 6 | 5 | 8 |  |
| Binary Channel Bits Per Slot |  |  |  |  |  |  |
|  For Slots 0 and Slot i, if mod(i, 10) = {8,9} for i from {0,…,39} | Bits | N/A | N/A | N/A | N/A |  |
|  For Slots i = 20, 21 | Bits | 53424 | 96672 | 80136 | 160272 |  |
|  For Slot i, if mod(i, 10) = 7 for i from {0,…,39} | Bits | 17808 | 30528 | 26712 | 53424 |  |
|  For Slot i, if mod(i, 10) = {0,1,2,3,4,5,6} for i from {1,…,19,22,…,39} | Bits | 55968 | 101760 | 83952 | 167904 |  |
| Max. Throughput averaged over 2 frames | Mbps | 37.644 | 69.308 | 57.755 | 104.817 |  |
| Note 1: SS/PBCH block is transmitted in slot #0 with periodicity 20 msNote 2: Slot i is slot index per 2 frames |

**<End of change>**