**3GPP TSG RAN WG1 #117 R1-2404108**

**Fukuoka, Japan, May 20th – 24th, 2024**

**Agenda item:** 9.2.2

**Source:** Moderator (Samsung)

**Title:** Moderator Summary on Rel-19 CSI enhancements

**Document for:** Discussion and Decision

## Introduction

The scope given in the Rel-19 NR MIMO Phase 5 WID pertaining to CSI enhancement is as follows:

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| --- |
| 1. Specify CSI support for up to 128 CSI-RS ports, targeting FR1    1. Type-I codebook refinement supporting up to a total of 128 CSI-RS ports across all resources, assuming legacy CSI-RS resources (with up to 32 CSI-RS ports per resource), based on extension of legacy codebooks    2. Type-II codebook refinement supporting up to a total of 128 CSI-RS ports across all resources, assuming legacy CSI-RS resources (with up to 32 CSI-RS ports per resource), based on extension of legacy codebooks, **without modifying any codebook parameter other than** introducing additional values for the number of ports codebook parameter(s)    3. Extension of CRI(s)-based CSI reporting (CQI/PMI/RI calculated per CRI for ≥1 CRIs) for hybrid beamforming supporting up to a total of 128 CSI-RS ports across all resources, with up to 32 CSI-RS ports per resource, without new codebook design 2. Specify UE reporting enhancement for CJT deployments under non-ideal synchronization and backhaul, targeting FR1, both FDD and TDD 3. Inter-TRP time misalignment and frequency/phase offset measurement and reporting, assuming legacy CSI-RS design, with stand-alone aperiodic reporting on PUSCH |

## Summary of companies’ proposals and views

***Ground rules in sharing your inputs:***

* **Please do NOT input anything in Tables 1A, 2A, and 3A**
  + **Including company names - appreciate your trying to save me some work, but …**
  + **For some reason, most likely due to poor MS Word inter-platform/version compatibility support (if any), the formatting of the FL proposals will change (for the worse) if you do so. This has happened several times in Athens and Changsha ☹**
* **Please input your comments ONLY in Tables 1C, 2C, and 3C, thanks! 😊**

### Issue 1 (WID objective 2a and 2b): Type-I and Type-II codebook refinement for up to 128 CSI-RS ports

Table 1A Summary: issue 1

|  |  |  |
| --- | --- | --- |
| **#** | **Issue/proposal** | **Companies’ views** |
| 1.1.1 | **[116bis] Agreement**  For the Rel-19 Type-I SP codebook refinement for 48, 64, and 128 CSI-RS ports with RI=5-8, decide, by RAN1#117, from the following schemes:   * Scheme1: adding new (N1, N2) values for the Rel-15 Type-I RI=5-8 * Scheme2:   + W1 structure: Independent selection of different ceil(v/2) SD basis vectors for RI = v, where each SD basis vector is applied to two respective layers except that, if v is odd, the last SD basis vector is applied to the orphan layer. Each of the SD basis vectors is freely selected from a group of N1N2 orthogonal SD DFT basis vectors via combinatorial indication     - FFS: mapping between v layers and ceil(v/2) SD basis vectors     - FFS: support of 4 selected SD basis vectors for RI=5-6   + W2 structure:     - For inter-polarization co-phasing, M (e.g., M = 4) codepoints for the orphan layer and M/2 codepoints for two layers sharing a same SD basis vector;     - A fixed  rotation of inter-polarization co-phasing between two layers sharing a same SD basis vector to achieve layer orthogonality. * Scheme3: the 1st beam is freely selected and subsequent 2 beams (RI=5-6) or 3 beams (RI=7-8) are freely selected such that they are orthogonal in at least one dimension (horizontal or vertical). Layers are mapped to the selected SD basis vectors following legacy Rel-15 for RI=5-8. One co-phasing across all layers ∈{1,j} following legacy Rel-15 Type-I RI=5-8 * Scheme4: concatenate two independently calculated RI=1-4 PMIs for RI=5-8 to reduce UE complexity where each PMI is calculated from the agreed RI=1-4 codebook (Scheme-A or Scheme-B) and the CQI for each of the two CWs is derived assuming it is received by one antenna group of 4 antenna ports (FFS: Whether additional mapping between the two PMIs and the two UE antenna groups is needed) * Other schemes are not precluded   **Proposal 1.A.1**: For the Rel-19 Type-I SP codebook refinement for 48, 64, and 128 CSI-RS ports with RI=5-8, support the following schemes:   * The same O1=O2 value(s) as RI=1-4 are supported * Scheme-A (based on Scheme3 described in RAN1#116bis):   + W1 structure:     - The 1st SD basis vector is freely selected and subsequent 2 (RI=5-6) or 3 SD basis vectors (RI=7-8) are freely selected such that they are orthogonal in at least one dimension (horizontal or vertical).     - The v layers are mapped to the selected SD basis vectors following legacy Rel-15 Type-I for RI=5-8.   + W2 structure:     - Following legacy Rel-15 Type-I RI=5-8 * Scheme-B (based on Scheme2 described in RAN1#116bis):   + W1 structure:     - Independent selection of different ceil(*v*/2) SD basis vectors for RI = *v*, where each SD basis vector is applied to two respective layers following legacy Rel-15 Type-I for RI=5-8, except that, if *v* is odd, the last SD basis vector is applied to the orphan layer.       * FFS: mapping between the orphan layer and its selected SD basis vector and, if needed, UE reporting of the selection       * FFS: support of 4 selected SD basis vectors for RI=5-6     - The SD basis vectors are freely selected from a group of N1N2 orthogonal SD DFT basis vectors via combinatorial indication, as well as a layer-common (q1,q2)   + W2 structure:     - For the orphan layer, the inter-polarization co-phasing is selected from {1, j, -1, -j}     - For two layers sharing a same SD basis vector, the inter-polarization co-phasing between two layers is selected from the following pairs {(1, -1), (j, -j)} to achieve inter-layer orthogonality. * A UE can be configured by the NW via higher-layer (RRC) signalling with either Scheme-A (RI=1-4+RI=5-8) or Scheme-B (RI=1-4+RI=5-8)   **FL assessment**: This was discussed OFFLINE [2]. Some wording revision (in change marks) to improve clarity without changing the content of the proposal  All companies are fine with Scheme-B, but a number of companies still prefer Scheme1 for Scheme-A (as opposed to the proposed Scheme3). **However, all available SLS results for Scheme-A show that the scheme in the above proposal (Scheme3) is superior to Scheme1 (cf. Table 1B).**  A small number of companies prefer other schemes (Scheme 4, 5) but there is no empirical evidence to justify their preferences. | **Support/fine**: ZTE, Nokia/NSB, Ericsson, NTT DOCOMO, Intel, Samsung, Huawei/HiSi (ok w/ 1.A.2), Qualcomm, MediaTek, vivo (ok), Xiaomi, NEC, Kyocera, Sharp, CMCC, KDDI, Google, Apple (ok), OPPO (ok), TCL (ok), Spreadtrum (ok), CATT, Fujitsu (ok), Fraunhofer IIS/HHI (ok), IDC, Lenovo/MotM (ok), HONOR (ok)  **Not support (SchA=1, SchB ok)**: CEWiT, Tejas  **Not support (other schemes)**: New H3C (Sch4), LG (Sch1 only) |
| 1.1.2 | **Proposal 1.A.2**: For a UE configured with a total of PSRS=6 or 8 ports across ≥1 SRS resources for antenna switching intended for xT6R or xT8R, respectively, support the following fixed SRS port grouping where (with the PSRS ports indexed in an ascending order according to SRS resource ID and port number within each SRS resource):   * SRS port group 0, corresponding to CW0, comprises the first PSRS/2 out of PSRS ports; and * SRS port group 1, corresponding to CW1, comprises the second PSRS/2 out of PSRS ports   For CQI calculation, UE follows the above grouping assumption.  No other spec enhancement is introduced, e.g. new CW-to-layer mapping, DL resource allocation, DCI format  Note: different SRS ports are associated with different UE antenna ports.  Note: if one single CW is scheduled, both SRS port groups can correspond to the same CW  Note: This feature is a separate UE capability and, for UEs supporting this capability, configured via RRC (FFS details on the extend of RRC configuration)  **FL assessment**: This was discussed OFFLINE [2]. | **Support/fine**: Huawei/HiSi, Samsung, ZTE, Ericsson, Nokia/NSB, Fujitsu, Tejas, Xiaomi, vivo, NTT DOCOMO, NEC, OPPO, TCL, KDDI, Sharp, MediaTek, Google, Apple (ok), New H3C, CATT, HONOR (ok), Spreadtrum,  **Not support**: Qualcomm |
| 1.1.3 | **Proposal 1.A.3**: On the NZP CSI-RS resource aggregation of *K*=2, 3 or 4 legacy NZP CSI-RS resources to attain a total of 48, 64, and 128 ports (for Rel-19 Type-I/II codebook refinement), support to configure a CSI-RS resource set with the *K* CSI-RS resources as the associated NZP CSI-RS for each of the SRS resource set(s) with higher layer parameter usage in *SRS-ResourceSet* set to 'nonCodebook',   * The previously agreed restrictions on the *K* resources for Rel-19 Type-I/II codebook refinement apply * Reuse the legacy approach for triggering of the NZP-CSI-RS resources and the legacy timeline for the NZP-CSI-RS resources and SRS   **FL assessment**: This was discussed OFFLINE [2]. | **Support/fine**: vivo, Samsung, Fujitsu, Xiaomi, Spreadtrum, Tejas, Huawei/HiSi, NTT DOCOMO, TCL Google, CMCC, Nokia/NSB, Ericsson, Sharp, Qualcomm, MediaTek, ZTE, OPPO, Apple (ok), CATT, HONOR (ok)  **Not support**: |
| 1.1.4 | **Proposal 1.A.4:** For the Rel-19 Type-I single-panel (SP) codebook refinement for 48, 64, and 128 CSI-RS ports, for Scheme-A **RI=3-4** only, the legacy mapping of i1,3 to (k1,k2) for (N1=3,N2=2) from Table 5.2.2.2.1-4 of TS 38.214 is used for all of the newly supported (N1,N2) values.   * FFS: whether the i1,3 table (Table 5.2.2.2.1-4 of TS 38.214) needs to be further extended.   **FL assessment**: This is the only solution that reuses legacy spec to allow 3 orthogonal SD basis vectors. There seems no need for additional extension beyond this but this can be kept FFS  cid:image001.jpg@01DAA0DC.7225A550 | **Support/fine**: Samsung, ZTE, Google, Qualcomm, IDC, NTT DOCOMO, OPPO, Fraunhofer IIS/HHI, Huawei/HiSi, Apple, vivo, Intel, Xiaomi, HONOR, Lenovo/MotM, MediaTek, Spreadtrum,  **Not support**: |
| 1.2.1 | **[116bis] Agreement**  For the Rel-19 Type-I single-panel (SP) codebook refinement for 48, 64, and 128 CSI-RS ports, for RI=1-4, support the following:   * … * Scheme-B (based on Scheme2 in RAN1#116 agreement): Adding new (N1, N2) values where 2N1N2 (>32) is the total number of CSI-RS ports across aggregated NZP CSI-RS resources, and   + W1 structure: …   + W2 structure: Layer-specific inter-polarization co-phasing with the alphabet {+1, +j, -1, -j}   **[116bis] Agreement**  For the Rel-19 Type-I SP codebook refinement for 48, 64, and 128 CSI-RS ports, the UCI parameters are captured in the tables below for Scheme-A and Scheme-B:   * Note: The second column includes the location of the parameters when reported with two-part UCI * FFS (RAN1#117): Select between Alt1 and Alt2 for Scheme-B   …  **Scheme-B**   |  |  |  |  | | --- | --- | --- | --- | | Parameter | UCI | Details/description | Status | | … |  |  |  | | SD basis vector selection indicator for each layer | Alt1: Part 1  Alt2: Part 2  Wideband | *v*=1-4:   * Alt1: bit indicator per layer *l=*1*, …, RIMAX* * Alt2: bit indicator per layer *l=*1*, …, v*   *v*=5-8: FFS | Pending | | Inter-pol co-phase selection indicator for each layer | Part 2  Wideband or Subband (\*\*) | *v*=1-4:   * Alt1: QPSK with orthogonality constraints across *v* layers * Alt2: QPSK: 2-bit indicator per layer *l=*1*,…,v*   *v*=5-8: FFS | Pending | | … |  |  |  |   **Proposal 1.B.1**: For the Rel-19 Type-I SP codebook refinement for 48, 64, and 128 CSI-RS ports, regarding UCI parameters for Scheme-B RI=*v*=1-4:   * SD basis vector selection indicator for each layer is in Part 2 (wideband) and bits per layer *l=*1*, …, v* * Inter-pol co-phase selection indicator for each layer is in Part 2 (wideband or subband) and 2 bits (representing {+1, +j, -1, -j}) per layer *l=*1*,…,v*   **FL assessment**: This was discussed OFFLINE [2].  It was observed that Alt1 for co-phase selection is not aligned with the previous agreement.  It was also argued that the overhead reduction from Alt1 (for both) is marginal and highly dependent on UE implementation, while complicating the UE PMI selection algorithm.  Therefore Alt2 (as proposed in 1.B.1) is the outcome. | **Support/fine**: Samsung, OPPO, ZTE, NTT DOCOMO, Qualcomm, Fraunhofer IIS/HHI, Apple, CATT, MediaTek, TCL, CEWiT, Intel, New H3C, Nokia/NSB, Huawei/HiSi, Fujitsu, Xiaomi, Spreadtrum, Tejas, Google, NEC, HONOR, Kyocera, Sharp, OPPO, CMCC, KDDI, Lenovo/MotM, IDC, Ericsson, vivo (ok)  **Not support**: |
| 1.3 | **Proposal 1.C**: For the Rel-19 Type-I SP codebook refinement for 48, 64, and 128 CSI-RS ports, regarding UCI omission, fully reuse the legacy rules for Rel-15 Type-I SP codebook  **FL assessment**: There is no reason to design a different UCI omission rule | **Support/fine**: MediaTek, Intel, Samsung, ZTE, Google, Qualcomm, IDC, Ericsson, NTT DOCOMO, OPPO, Fraunhofer IIS/HHI, Huawei/HiSi, Apple, vivo, New H3C, CATT, Xiaomi, HONOR, Lenovo/MotM, Spreadtrum,  **Not support:** TCL (NES rule) |
| 1.4.1/2 | **[116bis] Agreement**  For the Rel-19 Type-I SP and Type-II codebook refinements for 48, 64, and 128 CSI-RS ports via aggregating K>1 CSI-RS resources, regarding timeline, introduce two UE capabilities:   * Capability 1: Reuse legacy Z/Z’ values * Capability 2: Scale the legacy timeline Z/Z’ by ceil(P/32) where P is the total number of ports across all the K aggregated CSI-RS resources   FFS: CPU occupation and active resource counting  …  **Proposal 1.D.1**: For the Rel-19 Type-I SP and Type-II codebook refinements (except based on Rel-18 Type-II Doppler) for 48, 64, and 128 CSI-RS ports, regarding CPU occupation   * For Capability 1 timeline: OCPU = ceil(P/32) * For Capability 2 timeline: OCPU = 1   **Proposal 1.D.2**: For the Rel-19 Type-I SP and Type-II codebook refinements (expect based on Rel-18 Type-II Doppler) for 48, 64, and 128 CSI-RS ports, active resource counting is:   * For Capability 1 timeline: 1 * For Capability 2 timeline: 1   **FL assessment**: Since Capability 2 is quite (too) relaxed, there is no reason to further relax both OCPU and ARC for Capability 2.  For ARC, since the increase in the total # antenna ports (to up to 128) will be addressed in the ‘triplet’, there doesn’t seem any need to double-book this (mostly relevant to measurement buffering) in ARC (hence 1 should be more fitting, and K is excessive). Hence legacy in FG 2-33 can be interpreted as “Ks=1” (post aggregation) rather than “Ks=K”(pre-aggregation) | **1.D.1:**  **Support/fine:** Huawei/HiSi, Ericsson, Nokia/NSB, Intel, TCL, Samsung, vivo, Google, OPPO, Fraunhofer IIS/HHI, CATT, Qualcomm, MediaTek, Xiaomi, ZTE, HONOR, Lenovo/MotM, Spreadtrum, [NTT DOCOMO], [Apple]  **Not support:**  **1.D.2:**  **Support/fine:** Ericsson, Nokia/NSB, ZTE (ok), Fraunhofer IIS/HHI, Intel, TCL, Samsung, vivo, Google, CATT, Qualcomm, NTT DOCOMO, Huawei/HiSi, Xiaomi, HONOR, Lenovo/MotM, Spreadtrum, [Apple],  **Not support:** OPPO (K), Huawei (cK, c<1) |
| 1.5.1 | **[116bis] Agreement**  For the Rel-19 Type-I multi-panel (MP) codebook refinement for 48, 64, and 128 CSI-RS ports, for RI=1-4, decide, by RAN1#117, whether to support Type-I multi-panel (MP) codebook refinement in Rel-19.  If supported, decide from the following alternatives:   * Scheme1. Based on Rel-15 Type-I MP design directly extended with Ng=K (2, 3, and 4), and new (N1, N2) values * Scheme2. Based on Scheme4/6 as described in the RAN1#116 agreement   + W1 structure: Reuse legacy Rel-15 Type-I SP SD basis selection with L=1 independently for each of the K NZP CSI-RS resources   + W2 structure:     - Legacy Rel-15 Type-I inter-polarization co-phasing rules independently in each resource,     - Layer-common inter-resource M-PSK co-phasing, where M is further down-selected from {2,4}       * FFS: Whether inter-resource co-phasing is wideband or per subband.   If so, decide, by RAN1#117, whether port mapping scheme similar to, e.g. Rel-18 Type-II CJT, needs to be specified.  …  **Proposal 1.E.1**: For the Rel-19 Type-I multi-panel (MP) codebook refinement for 48, 64, and 128 CSI-RS ports, for RI=1-4, support the following (compromise between Scheme1 and Scheme2 described in RAN1#116bis):   * W1 structure: Common SD basis selection across all the Ng=K NZP CSI-RS resources, reusing legacy Rel-15 Type-I SP SD basis selection rules with L=1 for RI=1-4   + Ng = *K* = {2, [3], 4} denotes the number of NZP CSI-RS resources associated with the Ng panels * W2 structure:   + Legacy Rel-15 Type-I inter-polarization co-phasing rules independently in each resource,   + Layer-common sub-band inter-resource QPSK co-phasing   **FL assessment**: This was discussed OFFLINE [2]. The proposal is a compromise between Scheme1 and Scheme2 (resource-common SD basis instead of resource-specific, just as Scheme1).  The majority of companies supporting/ok with 1.E.1 are also supportive of/ok (neutral)with Scheme2, e.g. MediaTek, Ericsson, Samsung, NTT DOCOMO, Nokia/NSB, Huawei/HiSi, vivo, ZTE, Qualcomm, OPPO, CATT, HONOR  Note that **all the available SLS results show that Scheme2 outperforms Scheme1 (cf. Table 1B).** | **Support/fine (panel-common SD basis, compromise between Scheme1 and 2)**: MediaTek, Qualcomm, Ericsson, Nokia/NSB, vivo (ok), Samsung, Tejas (ok), NTT DOCOMO, CMCC, ZTE, Huawei/HiSi, OPPO, CATT, Intel (ok), HONOR  **Prefer resource-specific SD basis, i.e. Scheme2**: Fraunhofer IIS/HHI, CEWiT, New H3C, Fujitsu, NEC, KDDI, IDC  **Prefer Scheme1:** LG,  **No T1 MP**: Apple, TCL, Xiaomi, Spreadtrum, Google, Lenovo/MotM, Fraunhofer IIS/HHI (2nd) |
| 1.6.1 | **[116bis] Agreement**  For the Rel-19 Type-II codebook refinement for 48, 64, and 128 CSI-RS ports, on CBSR, refine the legacy CBSR as follows:   * … * Group-based CBSR granularity where each bit in the CBSR is associated with a set of X1X2 SD basis vectors, where the set includes X1 adjacent SD basis vectors along the N1 direction and/or X2 adjacent SD bases along the N2 direction   + FFS: Value(s) of X1 and X2 and detailed design/spec impact   **[116bis] Agreement**  For the Rel-19 Type-I SP codebook refinement for 48, 64, and 128 CSI-RS ports, regarding CBSR design:   * … * -bit CBSR where each bit in the CBSR is associated with a set of X1X2 SD basis vectors, where the set includes X1 adjacent SD basis vectors along the N1 direction and/or X2 adjacent SD bases along the N2 direction   + FFS: Value(s) of X1 and X2 and detailed design/spec impact   **Proposal 1.F.1**: For the Rel-19 Type-I SP and Type-II codebook refinement for 48, 64, and 128 CSI-RS ports, on CBSR, the value of X1 and X2) are separately NW-configured via higher-layer (RRC) signalling from {1, 2, 4, [8, 16]}   * FFS: Dependence on each supported (X1, X2) value on the configured (N1, N2) value   **FL assessment**: The values of (X1,X2) need to be resolved to complete CBSR design | **Support/fine:** Huawei/HiSi, Spreadtrum, Ericsson, ZTE, vivo, HONOR, NEC, Google, Qualcomm, Fraunhofer IIS/HHI, NTT DOCOMO, IDC, OPPO, Apple, Intel, Xiaomi, [Huawei/HiSi], [TCL], [HONOR]  **Not support:** |
| 1.6.2 | **[116bis] Agreement**  For the Rel-19 Type-I SP codebook refinement for 48, 64, and 128 CSI-RS ports, regarding CBSR design:   * 1-bit hard restriction is supported (analogous to Rel-15 Type-I) * FFS: 3-bit scaling factor for soft restriction with the scaling factor taken into account in CQI/PMI calculation * …   **Proposal 1.F.2**: For the Rel-19 Type-I codebook refinement for 48, 64, and 128 CSI-RS ports, for RI=v=1, support the following:   * for each group of SD basis vectors, a 3-bit scaling factor can be NW-configured via higher-layer (RRC) signalling, where the scaling factors are defined as scalings on the power control offset configured for the associated CSI-RS resources   + The values of and for this feature are separately configured from those for CBSR   + Separate configuration (RRC signalling) from CBSR   + The candidate values of and are the same as those agreed for CBSR * The codepoints of each of the group-specific 3-bit scaling factors are mapped to values of * Note: This feature is a separate UE capability   FFS: Whether this can be extended to RI=v>1 as well as Type-II codebook refinement  **FL assessment**: The proposal is now formulated in terms of scaling factor (not CBSR), but with the same granularity as CBSR. **Ericsson has demonstrated the performance gain within the context of co-existence with NTN (cf. Table 1B).** | **Support/fine:** Ericsson, Huawei/HiSi, ZTE, Samsung, vivo, IDC, Qualcomm, MediaTek, Google, ZTE, Nokia/NSB, OPPO, Fraunhofer IIS/HHI, Apple (ok), NTT DOCOMO, Intel, Xiaomi, HONOR, Lenovo/MotM, Spreadtrum,  **Not support:** |
| 1.7 | **[116bis] Agreement**  For the Rel-19 Type-I and Type-II codebook refinement for 48, 64, and 128 CSI-RS ports, regarding the mapping from CSI-RS resource index/port index per resource and port index to CSI/PMI calculation, support NW to configure UE with one of the following mapping methods via higher-layer (RRC) signaling,   * *Mapping method 1*: Sequential ordering/indexing within (1st resource, 1st polarization), then (2nd resource, 1st polarization), …, then (Kth resource, 1st polarization), then (1st resource, 2nd polarization), then (2nd resource, 2nd polarization), …, then (Kth resource, 2nd polarization) * *Mapping method 2*: Sequential ordering/indexing within (where K\*n2 = N2):   + for the 1st polarization, (1st n2 ports in 1st resource, 1st polarization), (1st n2 ports in 2nd resource, 1st polarization), …, (1st n2 ports in Kth resource, 1st polarization), then (2nd n2 ports in 1st resource, 1st polarization), (2nd n2 ports in 2nd resource, 1st polarization), …, (2nd n2 ports in Kth resource, 1st polarization), … then (N1th n2 ports in 1st resource, 1st polarization), (N1th n2 ports in 2nd resource, 1st polarization), …, (N1th n2 ports in Kth resource, 1st polarization) ,   + and then for the 2nd polarization, (1st n2 ports in 1st resource, 2nd polarization), (1st n2 ports in 2nd resource, 2nd polarization), …, (1st n2 ports in Kth resource, 2nd polarization), then (2nd n2 ports in 1st resource, 2nd polarization), (2nd n2 ports in 2nd resource, 2nd polarization), …, (2nd n2 ports in Kth resource, 2nd polarization), … then (N1th n2 ports in 1st resource, 2nd polarization), (N1th n2 ports in 2nd resource, 2nd polarization), …, (N1th n2 ports in Kth resource, 2nd polarization)   FFS: Exact port indexing within each CSI-RS resource or across K CSI-RS resources  **Proposal 1.G**: For the Rel-19 Type-I and Type-II codebook refinement for 48, 64, and 128 CSI-RS ports, regarding port mapping,   * Following legacy principle, “sequential ordering/indexing within” a group of Q indices {i0, i1, …, iQ-1} is a linearly increasing sequence such that iq < iq+1 (where q=0, 1, …, Q-2 is the port index within a CSI-RS resource, and iq or iq+1 {0, 1,…, KQ-1}). * After resource aggregation, P (=48, 64, or 128) ports are numbered in accordance to Table 7.4.1.5.3-1 from TS 38.211   **FL assessment**: This was discussed in RAN1#116bis but the FL proposal wasn’t well formulated. We decided to postpone to RAN1#117. | **Support/fine:** NTT DOCOMO, Qualcomm, Nokia/NSB, Ericsson, Samsung, Tejas Network, CATT, Lenovo/MotM, OPPO, Fraunhofer IIS/HHI, IDC, Apple, vivo, ZTE, Intel, Xiaomi, HONOR, Spreadtrum, NEC, [Huawei/HiSi], [LG], [Google]  **Not support:** |
| 1.8.1 | **[116] Agreement**  For the Rel-19 Type-I and Type-II codebook refinement for up to 128 CSI-RS ports, regarding NZP CSI-RS resource aggregation to attain 32 < P (or PCSI-RS)≤ 128, support aggregating at least K=2, 3, or 4 legacy NZP CSI-RS resources with equal number of ports   * … * Note: If the supported number of ports does not require aggregation of 3 resources, K=3 can be removed   **Proposal 1.H.1:** For the Rel-19 Type-I and Type-II codebook refinement for 48, 64, and 128 CSI-RS ports, regarding aggregation of K NZP CSI-RS resources to attain 32 < P (or PCSI-RS)≤ 128, support *only* the following combinations of K and P (or PCSI-RS):   * For P (or PCSI-RS) = 48, K = 2 (each resource 24 ports) and 3 (each resource 16 ports) * For P (or PCSI-RS) = 64, K = 2 (each resource 32 ports) and 4 (each resource 16 ports) * For P (or PCSI-RS) = 128, K = 4 (each resource 32 ports)   **FL assessment**: By excluding K=3, the above proposal excludes the cyan aggregation. While there are other aggregations that may not be too useful, there is no need to exclude them (NW implementation and market will determine).   |  |  |  |  | | --- | --- | --- | --- | | **New P** | **New (N1,N2)** | **Legacy resource aggregation** | | | **K** | **Old (N1’,N2’)** | | 48 | (8,3) | 2; 3; | (4,3); (8,1); | | (6,4) | 2/4; | (6,2)/(6,1); | | 64 | (16,2) | 2/4; 2; | (8,2)/(4,2); (16,1); | | (8,4) | 2; 2/4; | (4,4); (8,2)/(8,1); | | 128 | (16,4) | 4; 4; | (4,4); (16,1); | | (8,8) | 4; | (8,2); |   Blue: mapping #1 (along horizontal dimension)  Red: mapping #2 (along vertical dimension) | **Support/fine:** Qualcomm, Google, Samsung, Ericsson, NTT DOCOMO, OPPO, Huawei/HiSi, Apple, vivo, CATT, Xiaomi, HONOR, Lenovo/MptM, MediaTek, Spreadtrum, NEC,  **Not support:** |
| 1.8.2 | **[116bis] Agreement**  For the Rel-19 Type-II codebook refinement for 48, 64, and 128 CSI-RS ports based on the Rel-18 Type-II Doppler codebook, support the following aperiodic CMR configuration:   * A UE can be configured with *KDOPP* = {4, 8, 12} CSI-RS resource groups for the purpose of aperiodic CMR as needed by Type-II Doppler CSI   + The time separation between the first resources from two consecutive groups (=*m*) can be configured from {1, 2}   + FFS: The need for additional restriction in time domain * Each CSI-RS resource group comprises *K* NZP CSI-RS resources (K defined in previous agreements) for aggregation associated with a same CSI-RS resource set assuming the agreed resource set rules for Rel-19 Type-I/II codebooks * All the *KDOPP* CSI-RS resource groups are associated with a same CSI-RS resource set configuration   FFS: the determination of CSI-RS resource group that a CSI-RS resource is associated with  **Proposal 1.H.2**: For the Rel-19 Type-II codebook refinement for 48, 64, and 128 CSI-RS ports based on the Rel-18 Type-II Doppler codebook,   * Ordering the *KDOPPK* CSI-RS resources ascendingly by the CSI-RS resource ID and *kDOPP* ={0,1,…, *KDOPP*  –1}, CSI-RS resources { *kDOPPK*, *kDOPPK* +1, …, (*kDOPP+*1*)K –*1} are associated with the *kDOPP*-th CSI-RS resource group * FFS: If the CSI-RS resources in a resource group span two consecutive slots, *m* is 2. * FFS: If the CSI-RS resources in a resource group are located in one slot, *m* can be configured from {1, 2}]   **FL assessment**: This proposal attempts to resolve the two FFS points. The last bullet point (in brackets) is unclear and needs more discussion. | **Support/fine:** Huawei/HiSi, vivo, CATT, Samsung (1st bullet), Ericsson (1st bullet), Qualcomm, IDC, NTT DOCOMO, OPPO, Fraunhofer IIS/HHI, ZTE, Intel, Xiaomi, HONOR, Lenovo/MptM, MediaTek, Spreadtrum, NEC, [Fujitsu],  **Not support:** |
|  |  |  |

Table 1B SLS results: issue 1

|  |  |  |  |
| --- | --- | --- | --- |
| **Company** | **SLS results** | | |
| **Issue #** | **Metric** | **Observation** |
| IDC | 1.5.1 | Normalized average throughput | From the result above, it is observed that 8-PSK and 4-PSK based co-phasing in Scheme-2 MP with 32 ports on each panel can achieve a reasonable performance. As shown in the result above, Scheme-2 MP achieves higher performance than the legacy Rel-15 Type-I MP with 32 ports albeit having smaller throughput performance than Rel-19 Type-I SP Scheme-A with 64 ports. |
| MediaTek | 1.5.1 | Avg UPT gain vs feedback overhead | It is seen that the compromised proposal 1.E.1 shows 1~2 % UPT degradation compared to Scheme 2. This is because Scheme 2 offers flexibility to compensate the angle difference (however small) between panels due to large array size. In case there is no angle difference between panels, Scheme 2 does not preclude the UE report of common SD bases, thereby offering the best performance in either case. Further, we anticipate that the flexibility of Scheme 2 will be more beneficial in non-co-located/mixed deployments.  Based on the above observations, we support Rel-19 Type I MP codebook enhancement up to 128 ports based on Scheme 2. |
| Huawei/HiSi | 1.1.1 | UPT gain | The simulation results in the figures above show that UEs with rank 6 can have 11% performance gain if 3 or 4 SD bases can be used compared to 3 SD bases only, and UEs with rank 5 with about 6% performance gain. |
| Samsung | 1.1.1 | Avg UPT Gain vs overhead | Scheme 3 yields almost 145%, 127%, 127%, and 100% UPT gains compared to Scheme 1, for the cases of RI=5, 6, 7, and 8 fixed, respectively. |
| on O1,O2 | Avg UPT Gain vs overhead | For Rel-19 Type-I for RI=1-4, it is shown in SLS results that the case of O1=O2=2 incurs 2% UPT loss compared to the case O1=O2=4 for both Scheme A and Scheme B. |
| ZTE | 1.1.1 | Average throughput gain | It is observed in the result above that, both scheme 2(A) and scheme 2(b) can provide over 10% average throughput gain over scheme 1. Besides, by indicating the SD basis applied to the orphan layer, scheme 2(B) further shows approximately 1% throughput gain over scheme 2(A). Note that, the RI is dynamically selected from 1-8 in SLS. For RI = 5 or 7 when the orphan layer occurs, the performance advantage of scheme 2(B) over scheme 2(A) would be more significant. |
| Nokia/NSB | 1.1.1 | Mean UPT gain vs overhead, cell-edge UPT gain vs overhead | **Throughput vs overhead comparison between Scheme 1,2 and 3 for ranks 5-8, with 64 ports and layout (16x2).**  It is observed from SLS results (the above figures are the case of port layout (16x2)) that   * Scheme 3 outperforms Scheme 1 in throughput, by about 167% to 423% depending on number of antenna ports (64 or 128), port layout (8x4), (16x2) or (8x8), RU (30%,50%,70%), reported rank (5 to 8) and cell-average or cell-edge UEs. * Scheme 3 outperforms Scheme 2 in throughput, by up to 243%, for ranks 5,6 and 8, depending on number of antenna ports (64 or 128), port layout (8x4), (16x2) or (8x8), RU (30%,50%,70%), and cell-average or cell-edge UEs. * Lower throughput of Scheme 3 than Scheme 2 is observed for rank 7, because in the legacy Rel15 layer-to-beam mapping, the orphan beam is mapped to beam 2, instead of, which is typically the second strongest beam, instead of weaker beam 4. |
| Ericsson | 1.1.1 | Average user relative throughput vs average overhead | **Average overhead vs throughput gain for (M, N, P) = (8, 8, 2) at 10% resource utilization for the different Type-I codebook schemes in dense urban scenario at 3.5GHz**    **Average overhead vs throughput gain for (M, N, P) = (4, 16, 2) at 10% resource utilization for the different Type-I codebook schemes in dense urban scenario at 3.5GHz**  From the evaluation results above, it is observed that when Scheme 1 is used as the baseline in the simulations (the observations are valid for both 3.5 and 6.5GHz):   * Scheme 2 has the best overall performance albeit at a slightly larger overhead * Scheme 3 is able to achieve slight gains when compared to the baseline (Scheme 1), especially at low resource utilization, and at a slightly higher overhead than the baseline. |
| 1.4 | Mean user relative throughput,  5th percentile user relative throughput | **Average and 5th percentile throughput gain for (M, N, P) = (8, 16, 2) at 20%, 50% and 70% resource utilization for eType-II in dense urban scenario at 3.5GHz with UE speed of 3km/h**  From the evaluation results above, it is observed that when considering 4 slots of CSI feedback delay as the baseline in the simulations, the following performance losses:   * With an increased feedback delay of 8 slots, up to 25% average throughput loss is observed for 64 CSI-RS ports for UEs at 8km/h. * With an increased feedback delay of 12 slots, up to 31% average throughput loss is observed for 128 CSI-RS ports for UEs at 3km/h. |
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Table 1C Additional inputs: issue 1

|  |  |
| --- | --- |
| **Company** | **Input** |
| Mod V0 | **Please share your inputs on each of the issues and, if applicable, proposals in TABLE 1A** |
| Google | Proposal 1.A.1/2/3/4: Support  Proposal 1.B.1: Support  Proposal 1.C: Support  Proposal 1.D.1/2: Support  Proposal 1.E.1: We failed to see the necessity to enhance MP. It seems similar functionality can be achieved by CJT CSI.  Proposal 1.F.1: OK. Probably we can consider to remove (1, 2), (1,4) and (2, 4). Usually, horizontal beams are narrower than vertical beams. The granularity for CBSR for horizontal beams could be larger than vertical beams.  [Mod: I put those values in brackets for further discussion and see what companies think.]  Proposal 1.F.2: We do not think this is needed for high rank case. For high rank, there is already power scaling for each layer. In addition, the actual power scaling factor for each layer should be defined as max(CBSR, sqrt(v)), where CBSR indicates the value configured by the codepoint and v indicates the number of layers. Further, if additional power scaling is applied for some layers, we should allow UE to perform power boosting for some other layers.  [Mod: Noted]  Proposal 1.G: We do not know why this proposal is needed. Port indexing within each CSI-RS resource should remain the same as legacy.  [Mod: Since there were proposals to interleave within group in RAN1#116bis, this proposal says there is no interleaving, i.e. legacy is followed as you said. I added “following legacy”]  Proposal 1.H.1: Support |
| Mod V2 | **Added clarification on 1.G per Google comment** |
| Samsung | We support the following proposals:  - 1.A.1, 2, 3, 4; 1.B.1; 1.C;  - 1.D.1/2 (we are also fine with ACR=1 especially for Capability2 for 1.D.2)  - 1.E.1 (we are also fine with Scheme2, i.e. replacing resource-common with resource-specific in 1.E.1)  - 1.F.1, but (1,4) should be kept (please remove the square brackets)  - 1.F.2; 1.G; 1.H.1;  Regarding 1.H.2: 1) OK only with 1st bullet, 2) 2nd and 3rd bullets are not needed since this can be left to NW implementation. It’s quite obvious that m=1 is not feasible when the aggregated resources are across 2 slots. It’s also obvious that when the aggregated resources span 1 slot, both m=1 and 2 are feasible. 3) 4th bullet is unclear |
| Qualcomm | **Proposal 1.A.2**: Not support   * Firstly, this is a topic that was explicitly excluded in RAN plenary. * Secondly (although we don’t even need to discuss technical details, according to the reason above), anyway some descriptions of this proposal are not clear to us   + For linkage b/w SRS ports and CW, it basically couples the procedure of SRS and PDSCH – in legacy, these two are separate procedures in standard (analogous to CSI feedback and PDSCH: Two decoupled procedures)     - * + Given that for overall rank (i.e. summation of the two SRS port groups) >4, CW0 and CW1 fixedly correspond to the first and last layers respectively, does this mean that the two SRS port groups always need to be derived by network as rank- and rank- (i.e. same rank, or first rank smaller than second rank by 1)?   + Besides, seems this proposal additionally couple CQI and SRS? But CQI is based on PMI and RI, not based on “UE-assumed network utilization of SRS” (which seems to be hinted by this proposal)     **Proposal 1.A.3**:  This seems to be an extension of non-codebook-based PUSCH, from <=32port gNB, to {48,64,128}-port gNB – but should this be out of CSI (9.2.2 agenda)?  [Mod: Not really 😊 this is a legit use case for the CSI-RS enhancement we do for objectives 2a/b, just as we don’t write “timeline, CPU, ACR, UCI omission, CBSR, …” in objectives 2a/b]  **Proposal 1.A.3**: Support  **Proposal 1.C**: Support  **Proposal 1.D.1**: Support in general.  Seems for timeline Capability 1, it is more appropriate to be OCPU = ceil(P/~~16~~32), compared with legacy?  [Mod: Good point, thanks]  **Proposal 1.D.2**: We propose to define active resource counted as “1” (virtual) CSI-RS resource, not K, at least for timeline Capability 2, and the reason is analysed as following:  *For example, a UE may support both Rel-19 Type-I codebook >32port and Rel-15 Type-I codebook <=32port. Then it comes an issue that, UE capability indication mechanism for active CSI-RS resource/port: FG 2-33 (e.g. if Rel-19 has a new version of FG 2-33) – it can’t differentiate b/w the following two cases:*   * *Case A: 1 non-CRI report with 128-port codebook, being configured with K=4 resources each 32 ports;* * *Case B: 4 reports each with 1 CSI-RS resource of 32 ports.*   *In our understanding, UE measuring efforts is: Case A << Case B – a UE signing up to do Case A on a certain CC, does not guarantee to do Case B.*  *To avoid doing Case B, UE may conservatively report a small number of maximum supported active CSI-RS resources/ports, which basically makes 128-port difficult to support.*  *Therefore, counting active CSI-RS resources as “1,” actually makes this feature more likely to be implemented.*  As for timeline Capbility 1, we also prefer to count as “1” due to the same reason above – but we are also open to discuss.  **Proposal 1.F.1**: OK  **Proposal 1.F.2**: We are not sure how Pc ratio (EPRE offset) works for rank>1.  Together with some typo corrections (if we understand the intention correctly), some editorial suggestions:   |  | | --- | | **Proposal 1.F.2**: For the Rel-19 Type-I and Type-II codebook refinement for 48, 64, and 128 CSI-RS ports, in addition to the agreed (hard) CBSR, support the following:   * for each group of SD basis vectors, a 3-bit scaling factor can be NW-configured via higher-layer (RRC) signalling, where the scaling factors are defined as scalings on the power control offset configured for the associated CSI-RS resources, for the case of rank1   + The values of and for this feature are separately configured from those for hard CBSR   + Separate configuration (RRC signalling) from hard CBSR   + The candidate values of and are the same as those agreed for hard CBSR * The codepoints of each of the group-specific 3-bit scaling factors are mapped to values of * Note: This feature is a separate UE capability |   **Proposal 1.G**: OK with the principle, but seems the wording is not clear.  Maybe an example is more clear, we understand this proposal is saying, for 4x32=128 ports, port indices are {0,1,…,127}, rather than #1{0,1,…,31}+#2{0,1,…,31}+#3{0,1,…,31}+#4{0,1,…,31}.  If our understanding is correct, some editorial suggestions:   |  | | --- | | **Proposal 1.G**: For the Rel-19 Type-I and Type-II codebook refinement for 48, 64, and 128 CSI-RS ports, regarding port mapping,   * Following legacy principle, “sequential ordering/indexing within” a group of Q indices {i0, i1, …, iQ-1} is a linearly increasing sequence such that iq < iq+1 (where q=0, 1, …, Q-2 is the port index within a CSI-RS resource, and iq or iq+1 {0,1,…,KQ-1}). * After resource aggregation, P (=48, 64, or 128) ports are numbered in accordance to Table 7.4.1.5.3-1 from TS 38.211 |   [Mod: Got it]  **Proposal 1.H.1**: Support  **Proposal 1.H.2**: Fine, but agree that the last bullet in [ ] needs better description. |
| IDC | **Proposal 1.A.1, Proposal 1.A.4, Proposal 1.B.1, Proposal 1.C, proposal 1.F.1, Proposal 1.G, Proposal 1.H.2:** Support |
| Mod V6 | **Revisions to accommodate comments (also offline)** |
| Ericsson | **Proposal 1.A.3**:  Support  **Proposal 1.B.1**:  Although we think some overhead reduction is possible, we can live with the current proposal.  **Proposal 1.C**:  Support  **Proposal 1.D.2**:  Support, Our preference for active resource counting is 1 for both Capability 1 and Capability 2.  **Proposal 1.F.1**:  ok. but do we need all the combinations? (1,1) doesn’t help with reducing the signalling overhead right?  [Mod: True]  **Proposal 1.F.2**:  We think that the scaling can be useful also for high rank since the same elevation beam can be used for multiple layers. This will counterweight the 1/sqrt(v) scaling so that the expected EIRP can become similar for high and low rank. Since the angular spread in elevation is relatively low, it is not likely that many different elevation beams will be used for high rank.  Having said that, we are ok with FL’s latest revision to focus the proposal on RI=v=1. We can discuss the case of RI=v>1 and Type II applicability in future.  **Proposal 1.H.1:**  Not support. If K=3 is removed, then 48 port needs 24 port CSI-RS resources. Since 48 port may be used in bands where there may be legacy UEs, this may limit CSI-RS resource sharing with legacy UEs using 16 port CSI-RS. Hence, we prefer to keep K=3 also.  **Proposal 1.H.2:**  ok with Only 1st bullet. The remaining bullets need further discussion, so we prefer to make them FFS. Agree with FL that the last bullet is unclear. |
| NTT DOCOMO | **Proposal 1.A.4:**  Support. In addition, maybe companies already have common understanding, but we think it is better to also clarify the i\_1,3 for RI=2 case.  **Proposal 1.C**:  Support  **Proposal 1.D.1**:  Generally support. But we’d like to add ‘except for Type-II doppler CSI refinement’ in the main bullet, because CPU occupation should be discussed separately for Type-II doppler CSI refinement, similar as legacy.  **Proposal 1.D.2**:  Generally support. But we’d like to add ‘except for P/SP CSI-RS resource for Type-II doppler CSI refinement’ in the main bullet, because ARC should be discussed separately for P/SP CSI-RS resource for Type-II doppler CSI refinement, similar as legacy.  In addition, we prefer ARC to be 1 for two UE capabilities.  **Proposal 1.H.1**:  Support  **Proposal 1.H.2**:  OK |
| OPPO | **Proposal 1.A.1**:  Fine.  **Proposal 1.A.2**:  In our original understanding, the association between SRS and PDSCH would only impact PDSCH receiver at UE, That is, UE can select grouping of receive antennae for recovering of two CWs. Now when it is also applied to CQI calculation, there could be two additional issues:   1. RI and CQI are calculated together based on indicated CSI-RS ports, so RI should be considered with CQI. 2. Even when RRC configures the association, the UE receiver is still up to UE implementation. UE can still select the legacy receiver without complexity reduction and performance loss. The RI/CQI calculation assumption should be consistent with the actual UE receiver rather than the indicated association.   We propose not to mention CQI here. How to calculate CQI should be consistent with applied UE receiver which is up to UE implementation. One can never prevent UE to use a receiver with better performance (though with higher complexity).  [Mod: Please check Huawei’s comments]  **Proposal 1.A.3**:  Though we still think it is useless, we can live with it if companies have different thinking.  {mod: Thanks}  **Proposal 1.A.4**/**Proposal 1.B.1**/**Proposal 1.C**/**Proposal 1.D.1**/**Proposal 1.D.1**:  Fine.  **Proposal 1.D.2**:  We prefer K for different timelines.  **Proposal 1.F.1**:  Fine.  **Proposal 1.F.2**:  We could be fine with the proposal though we still think it can be up to gNB implementation.  [Mod: Thanks]  **Proposal 1.H.1**:  Fine.  **Proposal 1.H.2**:  We prefer to define the value of m based on the starting slot of a resource group.  **[Mod: now FFS]** |
| Fraunhofer IIS/HHI | **Proposal 1.A.4**: Support  **Proposal 1.C**: Support  **Proposal 1.D.1**: Support  **Proposal 1.D.2**: Support  **Proposal 1.E.1**: Our first preference is scheme 2 i.e., resource specific SD basis selection for W1. Our second preference is not supporting MP codebook.  **Proposal 1.F.1**: CBSR is an optional feature. Therefore, we think we can limit the number of (X1, X2) combinations to {(1,4) (2,2),(4,1),(4,4)}. To keep it simple, we prefer supporting a single (X1,X2) combination to be associated with a specific (N1,N2) combination.  [Mod: Tend to agree, removed some but kept some]  **Proposal 1.F.2**: Ok with the proposal. The power difference between two consecutive scaling factors is not uniform across the given scaling factors. We are not sure about the need to agree on non-uniform power difference levels.  **Proposal 1.G**: OK  **Proposal 1.H.2**: Support |
| Huawei, HiSilicon | **Proposal 1.A.1**  We are fine with the proposal if complexity issue is addressed by proposal 1.A.2.  For FFS on the supported SD basis vectors for scheme-B, we support 4 SD basis vectors for RI=5-6. As scheme-B is an extension of scheme-B for RI=1-4 targeting better performance, while RI=1-4 has supported up to 4 SD basis vectors, it’s reasonable to support 4 also for RI=5-6.  The simulation results can also show that by supporting 3-4 SD basis vectors, UEs with RI=5-6 can have a performance gain of 5~11%.    **Proposal 1.A.2,**  @QC, for linkage between SRS port grouping and PDSCH CWs, this is needed because gNB and UE need to have the same understanding. The low complexity UEs with two 4R-receivers/port-groups will receive two CWs with two 4R receivers/port-groups separately. Then gNB needs to know which CW is received by which port groups, because gNB needs to determine the percoders derived from a port group should be used for which CW.  @QC @OPPO, for CQI, this is also useful for TDD networks to let gNB know the level CQI, to save the time for MCS scheduling adjustment based on HARQ-ACK. Adjusting MCS based on HARQ-ACK can waste time and reduce performance, especially for burst traffic. For TDD network, there may be a large difference between amplitude of UL/DL channels. Therefore, in addition to SRS, network can usually also configure CQI reporting for gNB to have a rough estimation of CQI. And hence gNB needs to know the CQI of each CW corresponds to which port group, for following scheduling of PDSCH CWs.  **Proposal 1.A.4**: fine with the proposal.  **Proposal 1.C**: fine with the proposal.  **Proposal 1.F.1:** Since 128 ports increases the overhead of CBSR 4 times over legacy 32 ports, to keep the same overhead, we suggest to remove (2,4), (4,2) or (4,4).  **[Mod: The combos you propose to remove save the most overhead, right?]**  **Proposal 1.H.1:**  For simplicity, one combination is enough to support 48/64/128 ports, therefore, 4 16-port CSI-RS resources for 64 ports is not needed. We suggest to remove it.   * For P (or PCSI-RS) = 64, K = 2 (each resource 32 ports) ~~and 4 (each resource 16 ports)~~   **Proposal 1.D.1:**  Suggest to exclude Rel-19 type-II Doppler codebook, which should be discussed separately.  **Proposal 1.D.2:**  Suggest to exclude Rel-19 type-II Doppler codebook, which should be discussed separately.  Counting as 1 is too restricted, while K is too relaxed. We prefer some value between 1 and K, e.g., K, where  is reported by UE.  As the active resources are different for the following two cases, and case 2 is fewer than that of case 1. If 8 8-port CSI-RS resources with CRI are counted as 8 (K=8), then the 2 32-port CSI-RS resources can be counted as a value less than 8.  - Case 1: Multiple CRIs with legacy codebooks with ≤ 32 ports, e.g., 8 8-port CSI-RS resources with CRI  - Case 2: One CRI report with Rel-19 codebooks with >32 ports, e.g., 2 32-port CSI-RS resources without CRI.  **Proposal 1.H.2:**  The last bullet seems not clear to us, suggest to remove it for now. |
| Mod V13 | **Revisions per inputs** |
| Apple | **Proposal 1.A.1:** We are fine even though we prefer Scheme1.  **Proposal 1.A.2:** No strong opinion  **Proposal 1.A.3:** No strong opinion  **Proposal 1.A.4:** We are fine  **Proposal 1.B.1:** Support  **Proposal 1.C:** We are fine  **Proposal 1.D.1:** CPU can use legacy rule, i.e., “the number of CSI-RS resources in the CSI-RS resource set for channel measurement”  **Proposal 1.D.2:** active resource counting can use legacy rule, i.e., each resource is counted interpedently  **Proposal 1.E.1:** We still prefer to deprioritize MP codebook  **Proposal 1.F.1:** We are fine  **Proposal 1.F.2:** We have soft CBSR (2 bit) design in legacy which has no commercial interest. Now we are making  CBSR design a lot more complicated, and it becomes RI specific. We think it is too early to agree on this.  **Proposal 1.G:** We are fine  **Proposal 1.H.1:** We are fine  **Proposal 1.H.2:** Some clarification is needed. It is based on the order of CSI-RS resource configured, or CSI-RS resource ID?  [Mod: ID]  Do we need to ensure sequential order, i.e., the first set of CSI-RS resources corresponding to the >32 ports shows up the first in time domain, and then the second set, etc.?  [Mod: this is taken care of already by the aggregation port mapping rule we agreed] |
| Ericsson | **@Apple regarding Comments on Proposal 1.F.2**  During offline discussions, some companies expressed the view that the scaling factors proposed in 1.F.2 should be separate from CBSR. Hence, in the current formulation of the proposal, it is explicitly stated that the configuration of these scaling factors are separate from CBSR configuration (see, second sub-sub bullet under first sub-bullet). In addition, since this feature is only needed in scenarios where the terrestrial system coexist with fixed satellite systems, we think it is fine to make it optional feature also.  Regarding the comment on making the proposal specific to RI=v=1, this is in response to comments from Qualcomm and Google (QC commented that for RI=v=1, there will only be one beam selected and hence only a single scaling factor which makes it straightforward to apply). We can study further (hence last FFS), whether this feature can be extended to more than 1 layer. |
| vivo | 1.A.4  OK  1.C  OK  1.D.1 & 1.D.2  OK.  Regarding comments on using K as the number of occupied CPUs, it is not reasonable and may cause NW management unnecessarily complex. Specifically, multiple K values may be supported for 48 ports or 64 ports. Using K as O\_CPU will cause multiple possible numbers of CPUs for a single port number, i.e., 48 or 64. Further, for K=3 for 48 ports and K=2 for 64 ports, 48-port even occupies more CPUs than 64-port, which does not make sense.  1.G  OK  1.H.1  OK.  If K=3 is added for 48 ports, the note seems need to be removed.  1.H.2  OK.  Clarification on the last removed bullet: In Rel-18 Doppler CSI, we agreed that **the UE shall assume that the antenna port with the same port index of the aperiodic CSI-RS resources is the same (cf 5.2.1.4.1, TS 38.214)**. For Rel-19 Doppler CSI with K\_Dopp aggregated resources, how the “same port” restriction shall apply need to be determined as it is not accurate to simply say same port index will lead to same antenna port. Since we have multiple mapping methods from the K\_Dopp resource groups to one virtual resource, the removed bullet is the simplest way to extend the Rel-18 rule, i.e., the ports mapped to the same row index in the PMI matrix have the same antenna port. |
| New H3C | We support and live with the following proposals:  - 1.A.2; 1.B.1; 1.C; and 1.E with scheme2 |
| ZTE | **1.A.1:**  Support. Re the FFS on the number of SD bases for RI = 5-6, we didn’t see the necessity to select the 4th SD basis. First, assuming that the first 3 SD bases are stronger, these SD bases should be used as much as possible, there is no reason to select an 4th weaker SD basis. Selecting an 4th SD basis al causes higher overhead (indication of the SD basis and the corresponding co-phase). Besides, we may need to introduce more complicated UCI design (reporting the number of SD basis), and new layer paring/mapping and CW mapping schemes.  **1.A.3:**  Fine.  **1.A.4:**  Support. Further support to extend the i1,3 table to improve the performance, e.g., extending thei1.3 field to 3 bits. Based on previous discussion, with increased number of antenna ports, more flexible SD basis selection is needed due to narrower beam width. Extending i1,3 will not cause significant higher overhead (only 1 more bit is needed.)  **1.B.1:**  Support.  **1.C.1:**  Support.  **1.E.1:**  Fine.  **1.F.1:**  Re Huawei’s comment, we feel confused why the overhead should be same between Rel-15 and Rel-19 CBSR. We agree with Ericsson and Fraunhofer that, (1, 1) and {(1,4), (2,2), (4,1), (4,4)} should be removed, and only one (X1, X2) is needed for one specific (N1, N2). So, we can consider X1/X2 = 2 for N1/N2 < 4, X1/X2 = 4 for N1/N2, respectively.  **1.F.2:**  Fine.  **1.G:**  Fine.  **1.H.1:**  Agree with FL’s assessment that K = 3 for P = 48 and K = 4 for P = 64 should not be excluded. NW implementation and market will determine which combination is useful. There is no need to reduce the flexibility at current stage.  **1.H.2**  Prefer explicit CSI-RS grouping. |
| OPPO1 | Regarding proposal 1.A.2, our comment was that the CQI accuracy would be impacted if the Rx for CQI calculation (based on the association) and the Rx for PDSCH reception (up to UE implementation) is different. For example, if legacy receiver for 8Rx is applied for PDSCH, but 8 Rx is grouped for CQI estimation, the CQI would be lower than expected. If this is not an issue (e.g. could be fixed by OLLA) in companies’ mind, we could be fine to apply the association to CQI calculation. Furthermore, the assumption of Rx should be consistent for RI and CQI calculation |
| CATT | **Proposal 1.A.2:** Support  **Proposal 1.A.3:** Support  **Proposal 1.C:** Support  **Proposal 1.E.1:** We are ok with 1.E.1 now  **Proposal 1.H.1:** OK |
| ZTE2 | **1.F.1:**  Sorry for some mistakes. First, X2 should not be larger than X1, because N2 will not be larger than N1. Second, too complicated design should be avoided, and only one (X1, X2) is needed for one specific (N1, N2). Therefore, we can consider X1/X2 = 2 for N1/N2 <= 4, X1/X2 = 4 for N1/N2>4, respectively. Then only (2, 2), (4, 2), and (4, 4) are needed. |
| Mod V22 | **Minor revision per inputs** |
| Intel | **Proposal 1.A.1**: Support.  **Proposal 1.A.2**: We have several questions with respect to the proposal:   1. Does the proposal apply to any CSI reporting quantity with RI/CQI ('cri-RI-PMI-CQI ', 'cri-RI-i1-CQI', 'cri-RI-CQI', 'cri-RI-LI-PMI-CQI’) and for any number of CSI-RS ports (P > 4 so that 2 CW transmission is possible)? Based on the discussion, our understating that this proposal is beneficial only for 'cri-RI-CQI' given that gNB determines precoder based on SRS for this case. If CQI is based on PMI codebook while PDSCH is using precoder from SRS, change of CQI processing may not be needed given that CQI is not accurate anyway. 2. Is that correct understanding that, according to the 2nd Note, there is no impact on specification for the calculation of CQI for RI < 5 (single CW case)? 3. For the PDSCH processing, the receiver design is not specified in 3GPP. In principle, a UE may use antenna selection or fixed antenna groups for reception as soon as performance requirements set by RAN4 are met. Do you envision work in RAN4 to specify performance requirements for PDSCH with receiver which process CWs using antenna groups?   [Mod: Please check Huawei’s response. Also added “No additional spec impact ..” to partially address your point]  **Proposal 1.A.4**: According to the Proposal 1.A.1, Scheme A Rank 5-8 assumes free selection of 2 or 3 additional SD basis vectors. So, we are not sure this proposal is required given that it introduces constraints for SD basis vector selection.  **Proposal 1.B.1**: Support.  **Proposal 1.C**: Support.  **Proposal 1.D.1** and **Proposal 1.D.2**: We agree with FL assessment that relaxing of CPU rules for Capability 2 timeline is not needed. Furthermore, using ceil(P/32) to determine the number of CPUs is reflecting the complexity increase from larger number of ports for any combination of (K, P).  Thus, we support **Proposal 1.D.1**.  **Proposal 1.E.1**: We are fine to accept the proposal as compromise between scheme 1 and scheme 2.  **Proposal 1.F.1**: Instead of listing the combinations, we prefer separate configuration for X1 = {1,2,4} and  X2 = {1,2,4}.  **Proposal 1.F.2**: Support the proposal.  **Proposal 1.G**: Fine to accept the proposal.  **Proposal 1.H.2**: Support. |
| Xiaomi | **Proposal 1.A.4:**  Support.  **Proposal 1.C**:  Support  **Proposal 1.D.1, Proposal 1.D.2**:  Support. The value of ck include 1 if c=1/K. For conciseness, [1] could be removed.  **Proposal 1.F.1**:  Fine  **Proposal 1.F.1**:  Fine  **Proposal 1.G**:  Support  **Proposal 1.H.1, 1.H.2:**  Support |
| HONOR | **Proposal 1.A.1:** Okay with Scheme-A. However, we prefer Scheme-4 to be combined with Scheme-B. Companies’ argument on Proposal 1.A.2 is that this method can facilitate UE deployment of PDSCH reception more than one CW for TDD system. If this is the case, we propose to have a unified design for FDD and TDD system.  **Proposal 1.A.2:** We’re open to further discuss this issue. However, as commented by Qualcomm, more clarifications are needed:   * The SRS transmission, CQI calculation at network side (transparent to spec) and PDSCH reception are separate procedures in current specification. * Two codewords for PDSCH are supported only when PDSCH layers are larger than 4. If two SRS port groups are configured, the number of first group should be always 4. Otherwise, the mapping between PDSCH layers and codeword should be enhanced.   [Mod: Please check Huawei’s response. Also added “No additional spec impact ..” to address your point]  **Proposal 1.A.3:** No strong view. It seems not in scope.  **Proposal 1.A.4:** Okay to start with the legacy table for i1,3 indication. However, we support to extend the (k1,k2) values. The current Table only support 4 (k1,k2) values, which is too limited when the number of DFT basis is largely increased in Rel-19.  **Proposal 1.B.1**: Support. Prefer to further study whether and how to support subband SD basis selection for Scheme-B.  **Proposal 1.C**: Support.  **Proposal 1.D.1**: Support.  **Proposal 1.D.2**: Support  **Proposal 1.E.1**: Our first preference is to support resource-specific SD basis to increase the performance gain. If companies’ concern is the larger spec impact for Scheme-2. We can accept Proposal 1.E.1as a second preference for progress.  **Proposal 1.F.1:** Prefer to a limited combinations (e.g., only keep (1,4), (4,1), (2,2)) which has comparable CBSR overhead when the number of CSI-RS ports is 32.  **Proposal 1.F.2:** Okay  **Proposal 1.G**: Fine.  **Proposal 1.H.1:** Support  **Proposal 1.H.2:** Support |
| NTT DOCOMO | **Proposal 1.F.2**  Per some offline discussions we understand the need of the soft restriction. We thus support Proposal 1.F.2. |
| Lenovo/ MotM | **Proposal 1.A.1:**  We are fine with the current proposal, although our first preference remains Scheme 1 for Scheme A  **Proposal 1.A.4:**  We are OK, however we agree with Intel’s comment, may not be necessary if Scheme A is tied to Scheme 3 for RI=5-8  **Proposal 1.B.1**:  Support  **Proposal 1.C**:  Support  **Proposal 1.D.1/2**:  Support  **Proposal 1.E.1**:  We do not see Type-I MP a priority for design enhancements in the presence of NCJT CSI based on Type-I CB as well as Type-II CJT CSI framework.  **Proposal 1.F.2:**  Fine  **Proposal 1.G**:  Support  **Proposal 1.H.1/2:**  Fine |
| MediaTek | **Proposal 1.A.4** Support  **Proposal 1.E.1** Our first preference is to support resource specific SD bases as shown by available simulation results. Although for co-located multi-panel deployment, resource specific SD beams do not bring much performance difference, we believe the flexibility offered by it can potentially benefit various deployments in future. For compromise, we can support the current proposal as second preference.  **Proposal 1.H.1** Support. We are of similar view as several companies that K=3 should not be removed.  **Proposal 1.H.2** Support |
| Huawei, HiSilicon | **Proposal 1.A.2:**  @Intel   * + - 1. The reporting of cri/RI/CQI with and without PMI could be both used for a practical TDD network.   In the reporting with PMI, the PDSCH precoder is from SRS measurement, although there may be some mismatch on CQI which is calculated by PMI, it can still provide a reference CQI for following MCS scheduling. The reference CQI is beneficial to reduce the time for MCS adjustment based on HARQ-ACK feedback, especially for burst traffic.  To feedback a useful reference CQI, gNB and UE need to be aligned on which CQI is calculated by which antenna group.  2. Yes, no need to change the calculation of CQI for single CW, which can be up to UE implementation.  3. Since using antenna selection or fixed antenna groups or other algorithms are totally UE implementation, maybe there’s no need to have new PDSCH requirements.  @Honor  1. Since for low complexity UE receiver, each CW is received by one of the antenna groups. Then gNB and UE need to have the same understanding on which CW is received by which antenna group, because gNB needs to determine the percoders derived from a port group should be used for which CW.  2. The legacy CW to layer mapping is reused, the #layer for first CW/first group can be 2 (for rank 5), 3 (for rank 6, 7) and 4 (for rank 8). |
| Spreadtrum | **Proposal 1.A.2**: Regarding the introduction of low complexity UE receiver, We don’t have strong views. We are ok if there’s majority support. Regarding the details, we have one comment: In TDD, SRS for antenna switching together with non-PMI feedback can be used for gNB to determine the DL CSI. So we have similar view as Intel that the report quantity should be configured as ‘cri-RI-CQI’ for CQI calculation and feedback.  [Mod: Agree, but this one can be done via NW configuration/implementation]  **Proposal 1.A.4/ 1.C**/ **1.D.1**/ **1.D.2:** Support.  **Proposal 1.F.2**: We support the intention of this proposal on interference management when co-exist with satellite systems. Considering the importance of this feature, in order to make it easier to be implemented, we think at least configuring one group containing all SD basis vectors shall be supported.  **Proposal 1.G/ 1.H.1/ 1.H.2:** Support. |
| NEC | **Proposal 1.G:** Fine. In addition, it seems the order of CSI-RS resources should also be defined? For example, based on configured order in the CSI-RS resource set.  **Proposal 1.H.1:** Support  **Proposal 1.H.2:** Fine. |
| Ericsson | **Proposal 1.F.1:**  The whole purpose of introducing beam groups (and hence X1 and X2) is to reduce the signaling overhead due to CBSR. Hence, we don’t think (1,1), (1,2), and (2,1) are needed as these don’t help with reducing the overhead much. Instead, we suggest to add (X1, X2) combinations (8,1) and (16,1). These are useful overhead reduction since we have agreed some N1 values of 8 and 16 in the agreed layouts of in Rel-19.  **[Mod: Added in brackets and see what other companies think]** |
| Mod V36 | **Revisions to address comments**  **@Intel/Victor, HONOR/Guozeng, please check Huawei’s response re 1.A.2. I added a sentence per Yubo’s clarification that there isn o additional spec impact on later mapping and DCI** |
| CMCC | **Proposal 1.A.2:** Support to release UE complexity.  **Proposal 1.A.4/1.C:** Support.  **Proposal 1.D.1:** Support.  **Proposal 1.D.2:** Support, ARC as 1 is much proper.  **Proposal 1.F.1:** Support. Although the intension is for CBSR overhead, having more flexible value with RRC configuration, including 1 seems not harmful.  **Proposal 1.F.2/1.G/1.H.1/1.H.2:** Support. |
| Fujitsu | **Proposal 1.A.1:** Fine with proposal  **Proposal 1.A.2:** Support  **Proposal 1.A.3:** Support  **Proposal 1.C:** Support  **Proposal 1.D.1:** For capability 1 without relaxing timeline, at least for type II codebook refinement, CPU for up to 128 ports needs to be further discussed. In Rel-19, the number of ports can be increased by K times, from P=32 to P=48,64,128. In addition, Rel-19 Type II codebook refinement was supported for only increasing (N1,N2) without any enhancement for UE computational complexity. It’s different from R18 CJT because SVD for R18 CJT is still for each resource (32 ports). Therefore, as the dimension of precoder matrix is increased by K times, the complexity of SVD is from O (n^2) to O (n^3). If the number of ports increases by K times, the computational complexity of SVD increases by KX times, where X is from 2 to 3. therefore, for capability 1, OCPU =(ceil(P/32))X, where X can be {2, 2.5, 3} up to UE capability.  **Proposal 1.D.2:** Considering counting RSs/ports is related to UE buffering/memory capability, even for Rel-19, UE also needs to buffer K CSI-RSs for any one CSI report. Thus, we prefer to follow legacy scheme for counting RSs/ports.  **Proposal 1.E.1:** Support  **Proposal 1.H.1:** Support  **Proposal 1.H.2:** Support in principle. Re the consecutive slot(s) for K CSI-RSs in one group, considering fast time-varying channel for medium/high speed, we support all K CSI-RSs in one group should be configured in only one slot (additional restriction for previous agreement), then m can be configured with {1,2} as legacy. |
| Sharp | **Proposal 1.A.1/1.A.2/1.A.3/1.A.4:** Support.  **Proposal 1.B.1:** Support.  **Proposal 1.C:** Support.  **Proposal 1.D.1/1.D.2:** Support.  **Proposal 1.F.1/1.F.2:** Support.  **Proposal 1.G:** Fine. |
| OPPO2 | Regarding proposal 1.A.2, we agree with Intel and spreadtrum that the feature should be applied when the report quantity is configured as ‘cri-RI-CQI’ for DL NCB transmission. For CSI report with RI/PMI, it is unclear whether the RI/PMI should also be estimated based on antenna port grouping. If no, there would be mismatching between RI/PMI and CQI; If yes, the estimated CSI would be inaccurate since the codebook is not designed with a assumption of antenna port grouping (e.g. two low rank CBs for high rank).  Regarding proposal 1.D.2, we could be fine with counting active CSI-RS as 1 for larger than 32 ports. |
| Qualcomm | Some additional feedback.  **Proposal 1.A.1**: Some minor editorial suggestion for UE feature   |  | | --- | | …   * A UE can be configured by the NW via higher-layer (RRC) signalling with either Scheme-A (RI=1-4+RI=5-8) or Scheme-B (RI=1-4+RI=5-8)   + Scheme-A and Scheme-B are two separate UE features, where Scheme-A is a basic UE feature of Rel-19 Type-I SP CSI |   **Proposal 1.A.2**: Haven’t changed our view: Not support  Again, this firstly is a standard procedure issue in RAN (whether enforcing a UE receiving implementation is beneficial e.g. compared to normal 8Rx receiving, is the fundamental motivation of this topic, and should firstly be decided in RAN) – anyway RAN Plenary is near (June), and we should wait for the outcome from there first.  Having said that, we are also providing some technical views here (although we don’t think we need to):   * For CQI calculation, as replied by @Huawei, is a “rough” estimation possibly with mismatch of PMI/RI b/w UE calculation (via CSI-RS) and network calculation (via SRS).   + How could this CQI be useful e.g. UE calculates RI=4 (thus single-CW with all 8-Rx), while network calculates RI=5 (thus 2 port groups of 4-Rx work separately)?     - * + Even if under some less-extreme cases, let’s assume UE and network calculate out same rank, how much would the CQI mismatch be for the different precoder (PMI) assumption?   + What Type of PMI is assumed for UE side for CQI calculation? Type-I or eType-II?     - * + Or, do we assume UE use the same SVD as gNB? With same/similar quantization? (like the two-sided AI/ML model) * Given that there would be no change of CW-to-layer mapping, it means the semi-static configured port group 0 and port group 1 have to be constrained with and layers respectively – constrained rank of 2 separate SVDs are also something new, and need more study   In summary, this is a new topic requiring too many study aspects.  However, there is only one company studying and evaluating this new topic – then based on the single-source, we are specifying the standard? This is very different from our understanding of how we do standard (Em… this comes back to the RAN procedure issue)  **Proposal 1.D.2**: It would be strange that for example, 64-port by 2x32 and 4x16 (where K=2 and 4 respectively) is counted differently due to K.  Therefore, we don’t understand why should # resources is counted depending on K  We think **K should not be mixed with legacy Ks**   * Ks represents more about # CSI processes (which is a standard terminology in LTE) – although Ks is also # resources * But in Rel-19, K is more like an intermediate # ports: P=KQ, and only represent a single CSI process.   Therefore, count K as 1 is following legacy – no conflict with counting Ks as Ks.  **Proposal 1.G**: Some additional editorial suggestion:   |  | | --- | | …   * Following legacy principle, “sequential ordering/indexing within” a group of Q indices {i0, i1, …, iQ-1} is a linearly increasing sequence such that iq < iq+1 (where q=0, 1, …, Q-2 is the port index within a CSI-RS resource, and iq or iq+1 {0, 1,…, KQ-1} is the port index for the codebook, across the K>1 CSI-RS resources).   … | |
| Huawei, HiSilicon | **Proposal 1.A.2**: Regarding comments on reporting quantities of CQI conditioned on PMI, because the searching of RI/PMI at UE side are performed towards the best CQI, as long as the CQI calculation follows the grouping assumption, the RI/PMI will also accommodate the low complexity UE. So the CSI reporting with/without PMI can be up to gNB configuration and UE implementation.  The CQI is used as a reference for gNB for following MCS selection, it’s an estimation of code rate, not an absolute TBS size, no matter Rank-4 or Rank-5, it’s a consistent rough estimation.  **Proposal 1.D.2**, we prefer c.K (c<1) which also include the legacy value of K. This can be easier for UEs to align the complexity of 128-ports and legacy <=32 ports in reporting of FG 2-33 for multiple codebooks.  Regarding Ks, the number of CSI processes seems to be more related to O\_CPU in NR. Following the current NR spec, it’s more proper that the legacy value should K for ARC.  **Proposal 1.H.1**, more combinations will introduce unnecessary complexity, as long as both the mapping methods 1 and 2 can be supported already, there’s no need to support multiple K values for a certain CSI-RS port.   * For P (or PCSI-RS) = 48, K = 2 (each resource 24 ports) [and 3 (each resource 16 ports)] * For P (or PCSI-RS) = 64, K = 2 (each resource 32 ports) [and 4 (each resource 16 ports)] |

### Issue 2 (WID objective 2c): CRI-based CSI for hybrid beamforming (HBF)

Table 2A Summary: issue 2

|  |  |  |
| --- | --- | --- |
| **#** | **Issue** | **Companies’ views** |
| 2.1.1 | **RAN1#116bis discussion + OFFLINE**  **Question 2.F.2:** For the Rel-19 CRI-based CSI refinement for up to 128 CSI-RS ports, for M>1, please share your view on whether the following overhead reduction schemes should be supported:   * CRI/resource-common RI value (indication):   + *Support/fine*: NTT DOCOMO (1st), Xiaomi, TCL, Huawei/HiSi, CATT   + *Not support (CRI/resource-specific RI)*: vivo, Samsung, NEC, Qualcomm, NTT DOCOMO (2nd), Lenovo/MotM, Ericsson, Nokia/NSB, Google, Intel, CMCC, MediaTek, Fujitsu, Sharp, OPPO * Differential WB CQI (the wideband CQI(s) associated with the 2nd, …, M-th CRI(s) is calculated differentially with respect to the 4-bit largest wideband CQI(s) associated with the 1st CRI into Bd<4 bits):   + *Support/fine*: NTT DOCOMO (1st), ZTE, Huawei/HiSi   + *Not support (No differential, Bd=4)*: vivo, Samsung, Qualcomm, Lenovo/MotM, Ericsson, Nokia/NSB, Google, Intel, TCL, CMCC, MediaTek, Fujitsu, Sharp, OPPO, NTT DOCOMO (2nd), * 1-bit differential SB CQIs associated with the 2nd, …, M-th CRI(s), calculated differentially with respect to the 2nd, …, M-th WB CQI(s)   + *Support/fine*: Huawei/HiSi   + *Not support (No differential, legacy 2-bit)*: vivo, Samsung, Lenovo/MotM, Ericsson, Fujitsu Nokia/NSB, Google, Intel, TCL, CMCC, MediaTek, Sharp, OPPO   **Proposal 2.A.1:** For the Rel-19 CRI-based CSI refinement for up to 128 CSI-RS ports, for M>1, support the following:   * Resource-specific RI, i.e. RI is independently calculated and indicated for each of the selected M NZP CSI-RS resources   + FFS: If resource-common RI indication is also supported * 4-bit wideband CQIs are independently calculated and reported across the M selected NZP CSI-RS resources * 2-bit differential SB CQIs are independently calculated across the M selected NZP CSI-RS resource   **FL assessment**: This was already discussed in RAN1#116bis and also OFFLINE [2].   * Whether RI is CRI-common or CRI-specific should be decided first. In this case, the proponents of CRI-common should demonstrate that CRI-common is better than CRI-specific in UPT vs PMI overhead trade-off   + Given the marginal saving in overhead from CRI-common RI, CRI-common RI is justified only if there is practically no loss of UPT relative to CRI-specific RI * If CRI-common is justified, whether differential CQI is supported or not can be decided with the same methodology (UPT vs PMI overhead). Else, the baseline (non-differential) is the natural outcome | **Support/fine**: OPPO, vivo, Samsung, Apple, MediaTek, Intel, CEWiT, Ericsson, NEC, Qualcomm, NTT DOCOMO, Lenovo/MotM, Nokia/NSB, Google, CMCC, Fujitsu, Sharp, Spreadtrum, HONOR, Kyocera, KDDI, Lenovo/MotM, IDC  **Not support**: Huawei/HiSi, ZTE, CATT, Xiaomi (CRI-common RI), TCL (CRI-common RI), |
| 2.1.2 | **[116bis] Agreement**  For the Rel-19 CRI-based CSI refinement for up to 128 CSI-RS ports, for M>1, the M CRIs (each with bits) are separated indicated   * FFS: whether to support NW configuring/requesting the UE to report CRI/RI/PMI/CQI associated with *MR* (<*M*) of *KS* CSI-RS resources, including whether further reduction in the number of hypotheses is supported, i.e. reporting (*M* – *MR*) CRIs (each with bits)   **Proposal 2.A.2**: For the Rel-19 CRI-based CSI refinement for up to 128 CSI-RS ports, for A-CSI only, the NW can configure *MR* (<*M*) of *KS* CSI-RS resources to be selected as part of reporting the *M* “quadruplets”:   * (*M–MR*) CRIs, each with bits are reported, along with the *M* sets of CQI/PMI/RI/(if applicable) LI * The value of *MR* is NW-configured via higher-layer (RRC) signaling * The *MR* selected resources are NW-configured via higher-layer (RRC) signaling   + In addition, the *MR* selected resources can be updated via DCI (as a part of CSI trigger state)   **FL assessment**: The additional trigger-state-based update offers flexibility. | **Support/fine:** MediaTek,Huawei/HiSi, Ericsson, Nokia/NSB, Samsung, CATT, HONOR, Fujitsu, NEC, Google, ZTE, Qualcomm, IDC, Apple (ok), Intel,  **Not support:** Spreadtrum, vivo, NTT DOCOMO, OPPO (no DCI), HONOR (no DCI), Lenovo/MotM |
| 2.1.3 | **Proposal 2.A.3:** For the Rel-19 CRI-based CSI refinement for up to 128 CSI-RS ports, for M=2, when Rel-16 eType-II codebook is configured, FD basis selection and indication are resource-specific (per resource)  **FL assessment**: This is a proposal from Huawei to reduce overhead. Note that the **baseline is resource-specific**, and resource-specific SD basis has been agreed.  **Question 2.A.3:** For the Rel-19 CRI-based CSI refinement for up to 128 CSI-RS ports, for M=2, please share your view on the following proposal:   * When Rel-16 eType-II codebook is configured, support resource-common FD basis selection and indication   **Support/fine:** Huawei/HiSi, Xiaomi (if no performance loss)  **Not support (resource-specific):** Google, Samsung, Qualcomm, Ericsson, NTT DOCOMO, OPPO, Apple, vivo | **Support/fine:** Google, Samsung, Qualcomm, Ericsson, NTT DOCOMO, OPPO, Apple, vivo, Intel, HONOR, Lenovo/MotM, MediaTek, Spreadtrum,  **Not support:** Huawei/HiSi |
| 2.1.4 | **Proposal 2.A.4:** For the Rel-19 CRI-based CSI refinement for up to 128 CSI-RS ports, for M=2, when Rel-16 eType-II codebook is configured, RRC configuration of Parameter Combination is resource-common  **FL assessment**: This needs to be resolved for Rel-16 eType-II based HBF. Analogous to Rel-18 Type-II CJT, **the baseline is Alt2.**  **Question 2.A.4:** For the Rel-19 CRI-based CSI refinement for up to 128 CSI-RS ports, for M=2, when Rel-16 eType-II codebook is configured, please share your preference on the following alternatives:   * Alt1. Resource-specific RRC configuration of Parameter Combination   + Support/fine: Google,   + Not support: Samsung, Qualcomm * Alt2. Resource-common RRC configuration of Parameter Combination   + Support/fine: Samsung, Qualcomm, Ericsson, NTT DOCOMO, OPPO, Apple, vivo, ZTE, CATT, Intel   + Not support: | **Support/fine:** Samsung, Qualcomm, Ericsson, NTT DOCOMO, OPPO, Apple, vivo, ZTE, CATT, Intel, Xiaomi, HONOR, Lenovo/MotM, MediaTek, Spreadtrum,  **Not support:** |
| 2.2 | **Proposal 2.B**: For the Rel-19 CRI-based CSI refinement for up to 128 CSI-RS ports, regarding CBSR, for each of the configured KS NZP CSI-RS resources, reuse per-resource CBSR from the legacy spec as follows:   * Rel-17 Type-I NCJT CBSR when Rel-15 Type-I SP is configured * Rel-18 Type-II CJT CBSR when Rel-16 eType-II is configured   **FL assessment**: No reason not to reuse legacy CBSR | **Support/fine:** MediaTek, ZTE, Samsung, Lenovo/MotM, HONOR, Xiaomi, Google, Qualcomm, IDC, Ericsson, NTT DOCOMO, OPPO, Apple, vivo, CATT, Intel, HONOR, Spreadtrum,  **Not support:** Huawei/HiSi (two-level), ZTE (two-level) |
| 2.4 | **Proposal 2.D**: For the Rel-19 CRI-based CSI refinement for up to 128 CSI-RS ports:   * Active resource counting = KS (following legacy)   **FL assessment**: This proposal is a synthesis between proposals from vivo and Qualcomm. For further discussion on timeline, Qualcomm proposes   * If Ks = 2:   + Z= Q/16 \* Z + (M-1) \* Y, Z’= Q/16 \* Z’ + (M-1) \* Y, * Else if 2 < Ks <= 8:   + Z = Q/8 \* Z + (M-1) \* Y, Z’ = Q/8 \* Z’ + (M-1) \* Y   where Y=2 or 1 symbol should be sufficient.  Vivo proposes   * Multiply legacy Z’ by a factor of M. * Z is increased by (M–1)\*Z’ to match the increase in Z’ | **Support/fine:** vivo, Google, OPPO, Xiaomi, Samsung, Qualcomm, Huawei/HiSi, NTT DOCOMO, Apple, ZTE, Intel, MediaTek  **Not support:** |
| 2.5 | **[116bis] Agreement**  For the Rel-19 CRI-based CSI refinement for up to 128 CSI-RS ports, the following report quantities are supported:   * *‘cri-RI-PMI-CQI ‘* * *‘cri-RI-LI-PMI-CQI’ (only for Type-I)* * *FFS: ‘cri-RI-i1-CQI’ (only for Type-I)* * *FFS: ‘cri-RI-i1’ (only for Type-I)*   **Proposal 2.E**: For the Rel-19 CRI-based CSI refinement for up to 128 CSI-RS ports, the following report quantities are also supported only when Rel-15 Type-I SP codebook is configured [and only for M=1]:   * *‘cri-RI-i1-CQI’* * *‘cri-RI-i1’* * [‘*cri-RI-CSI*’]   **FL assessment**: The restriction M=1 only was proposed by Xiaomi (need to check with companies) | **Support/fine:** IDC, MediaTek, Spreadtrum, CMCC, Xiaomi (M=1), Huawei/HiSi, HONOR  **Not support:** Google, Samsung, Ericsson, Apple |
|  |  |  |

Table 2B SLS results: issue 2

|  |  |  |  |
| --- | --- | --- | --- |
| **Company** | **SLS results** | | |
| **Issue #** | **Metric** | **Observation** |
| Huawei/HiSi | 2.1.2 | Relative Throughput | gNB-assisted reporting beam determination (*M=2, MR*=1*)* provides significant performance gain (~17.5%) over UE-autonomous reporting beam determination(*M=2, MR*=0) |
| 2.1 | Channel Correlation | The normalized power in spatial domain and delay domain of multiple beams are highly correlated |
|  |  |  |  |

Table 2C Additional inputs: issue 2

|  |  |
| --- | --- |
| **Company** | **Input** |
| Mod V0 | **Please share your inputs on each of the issues and, if applicable, proposals in TABLE 2A** |
| Google | Proposal 2.A.1: Support  Proposal 2.A.2: Support  Proposal 2.A.3: We think all the CSI components should be resource-specific.  Q 2.A.3: Support Alt1.  Proposal 2.B: We think CBSR should be configured in resource-specific manner. The digital precoder status can be different on top of different beams.  Proposal 2.D: Support  Proposal 2.E: We failed to see the necessity to support these report quantities. |
| Mod V2 | **No revision** |
| Samsung | We support the following proposals:   * 2.A.1, 2; 2.B; 2.D;   We don’t support proposal 2.E. Although it was supported in legacy Rel-15 CRI-based, we see no use case for the so-called semi-open-loop precoder cycling for HBF. We agree with Google.  Question 2.A.3: Not support. This would complicate UE implementation further when Type-II is configured since now the UE needs to perform joint optimization for FD basis selection across CRIs/resources. The UE can’t assume that the KS panels are heavily correlated and calculate Wf selection only for 1 panel.  Question 2.A.3: Support Alt2. We see no motivation for Alt1. It complicates both UE and NW implementation. |
| Qualcomm | **Proposal 2.A.2**: Not support.  We think this proposal is unnecessary, and the intention is already supported with existing RRC.  To support this functionality, simply configuring separate reports can also work, e.g.  For M=2, and network think 1 certain CRI#x should always be reported;  Then we can just configure 1 non-CRI report associated with the resource#x, and 1 single-CRI report associated with the KS-1 resources.  **Question 2.A.3**: Not support.  We want to remind that PMI FD bases correspond to “delay” components of the precoder, not the measured channel **H**  **Question 2.A.3**: Support the simpler Alt2  **Proposal 2.B**: OK  **Proposal 2.D**: OK with CPU and active resource counting.  Regarding timeline, we have a slightly different view. While we agree “M” would increase timeline, we think # ports per resource should be more significant; Besides, the increased Ks over legacy may also need to consider.  Although a little complicated, we still suggest to consider our proposal by starting from legacy:  *Legacy*:   |  |  |  | | --- | --- | --- | | **KS** | **Maximum # ports per resource** | M | | 2 | 16 | 1 | | Up to 8 | 8 |   *Proposed*: Linear scaling with # ports per resource (Q) over legacy, and consider the increased M-1 (multi-CRI over legacy single-CRI):   * If Ks = 2:   + Z= Q/16 \* Z + (M-1) \* Y, Z’= Q/16 \* Z’ + (M-1) \* Y, * Else if 2 < Ks <= 8:   + Z = Q/8 \* Z + (M-1) \* Y, Z’ = Q/8 \* Z’ + (M-1) \* Y   where Y=2 or 1 symbol should be sufficient. |
| IDC | **Proposal 2.A.2, Proposal 2.B:** Support. |
| Mod V6 | **Revisions to address inputs (including offline)** |
| Ericsson | **Question 2.A.3:**  Since this is for hybrid beamforming M=2, the FS basis selection and indication should be independent for each resource. So, we prefer resource-specific FD basis selection and indication.  **Question 2.A.4**  Fine with Alt 2.  **Proposal 2.B**:  Ok.  **Proposal 2.E**:  Not support. |
| NTT DOCOMO | **Question 2.A.3:**  Not support.  **Question 2.A.4:**  We think it is sufficient to support Alt.2.  **Proposal 2.B**:  OK.  **Proposal 2.D**:  We think *OCPU = Ks* can be considered at least for Type-I based refinement. |
| OPPO | **Proposal 2.A.1:**  Fine.  **Proposal 2.A.2:**  Regarding the MR selected resources, we don’t think update via DCI is necessary. It would impact the scheduling flexibility considering the trigger state is limited.  **Proposal 2.A.3:**  Not need for resource-common FD basis.  **Proposal 2.A.4:**  Prefer Alt.2.  **Proposal 2.B:**  Fine.  **Proposal 2.D:**  For Rel-15 single CRI based CSI report, O\_CPU is equal to Ks. For M-CRI based CSI report in Rel-15, The number of CPU is expected to be increase by M-1. Hence, we think Ks + M -1 may be more accurate. |
| Huawei, HiSilicon | **Proposal 2.A.1/Question 2.A.3:** Given the comparatively high correlation among analog beams as pasted above, we prefer CRI-common RI, differential wideband CQI and common FD basis vectors.  **Proposal 2.A.2:** Support.  This proposal provides gNB with the ability to serve the UE with its preferred analog beam decided based on traffic requirement and UE distribution, which contributes to create more MU MIMO scheduling opportunities and brings apparent performance gain as pasted above.  Regarding the comment raised by QC, ‘simply configuring separate reports can also work’, considering that the UE only support limited number of CSI reports, wasting multiple reports to achieve similar functionality should definitely be avoided, which encumbers the gNB from acquiring sufficient CSI.  **Proposal 2.A.3:** Prefer Alt2.  **Proposal 2.B:** Not support.  As discussed in our contribution, multiple analog beams may partially share similar restricted SD basis vectors regarding inter-cell interference handling under HBF architecture; while they may also have different restricted SD basis vectors regarding intra-cell interference handling given the distinct pairing situation. Taking above analysis into consideration, a two-level CBSR that satisfies the requirement of both inter- and intra-cell interference handling with significant RRC overhead reduction should be considered as below:   * First level: CRI-common group-based restriction via -bit bitmap similar to the already agreed CBSR for 128 CSI-RS ports * Second level: CRI-specific restriction via *X*1·*X*2-bit bitmap for each unrestricted group   **Proposal 2.D**: Not support.  At least one possible approach to conduct CRI-based CSI calculation is based on so-called short-cut principle, i.e., the CSI is only completely calculated for the M selected CRIs, and an additional CRI selection procedure is needed. Based on this assumption, we suggest to consider the following timeline/OCPU/active resource counting:  For the Rel-19 CRI-based CSI refinement for up to 128 CSI-RS ports:   * OCPU = M + 1 * Timeline: Multiply legacy Z/Z’ by a factor of M. * Active resource counting = KS   **Proposal 2.E:** Generally fine.  Since TDD is also an important scenario under HBF architecture, we would like to check with companies whether report quantity *‘cri-RI-CQI’* can be supported as well. |
| Mod V13 | **Revisions per inputs** |
| Apple | **Proposal 2.A.1**: Support.  **Proposal 2.A.2**: Do not have strong option. But to be honest, this seems to be over optimization since the similar design was not adopted for L1-SINR/RSRP and it does not cause much issue in real deployment.  **Question 2.A.3**: We prefer to reduce different codebook types, including different SD/FD basis selection choices, etc. Honestly speaking, Rel-19 CSI becomes another complicated release with many different solutions for a same problem. We strongly doubt it is for the actual commercial deployment.  **Question 2.A.4**: Alt2, resource common  **Question 2.B.**: We are fine  **Question 2.D.**: We are fine  **Question 2.E.**: We prefer not to support non-PMI based CSI |
| vivo | 2.A.2  We think this is not needed as existed configurations can already achieve the same functionality. NW can configure two report settings, where the first report setting is associated with the MR resources and all PMIs/CQIs associated with the MR resources are reported, and the second Ks-MR resources are associated with another report setting for UE to select the best CRIs. Even for the DCI solution, NW can configure multiple resource sets for the MR resources and dynamically trigger one from them. This implementation based solution can achieve same goal as this proposal with same DCI or UCI overhead. This proposal does not optimize anything.  2.A.3  We have agreed in last meeting that the M PMIs are independently calculated and indicated. Hence we shall not discuss this issue.  **Agreement**  For the Rel-19 CRI-based CSI refinement for up to 128 CSI-RS ports,   * **When M>1, the M PMIs are independently calculated and indicated** * with the Rel-16 eType-II codebook and KS={1,2,3,4}, support M=2 with a maximum of 16 ports per resource, R=1 only, and a maximum UCI payload of 1706 bits.   + The value of M={1, 2} is NW-configured via higher-layer (RRC) signalling   + The maximum value of M is subject to UE capability * on the configured KS>1 NZP CSI-RS resources, reuse the legacy IMR rule for the Rel-15 CRI-based reporting for NZP CSI-RS resource for interference measurement, i.e. only 1 NZP CSI-RS resource for interference measurement can be configured   2.A.4  Alt 2   * + - 1. B   OK |
| ZTE | **2.A.1:**  Prefer CRI-common RI and differential wideband CQI, due to the correlation between different channels corresponding to different beams.  **2.A.2:**  To our understanding, the beams required to report corresponding CSI is dynamically changed. So, we didn’t see the necessity of introducing such enhancement for periodic CSI reporting. We are OK if the MR resources are only indicated for AP CSI reporting.  **2.A.3:**  Open to discuss, based on the observation of correlation between different channels corresponding to different beams.  **2.A.4:**  Re parameter combination, prefer Alt2 resource-common configuration.   * + - 1. **B:**   Support to configure one CBSR for each of the resources. Support to further discuss two-level CBSR to reduce the RRC overhead. Since the distribution of interference across different beams is correlated in the horizontal direction, support at least X1 = 1, X2 = N2O2.   * + - 1. **D:**   Support the following timeline, number of occupied CPU, and counted active resources:   * Z/Z’ = Z2/(Z2’+w), where w is a fixed value or depends on the number of CSI-RS resources Ks; * OCPU = Ks; * # of counted active resources = Ks.   + - 1. **E:**   Support. Further support ‘*cri-RI-CQI*’. |
| CATT | **Question 2.A.4:** we prefer Alt. 2  **Proposal 2.B:** ok |
| Mod V22 | **Added proposals 2.A.3 and 2.A.4** |
| Intel | **Proposal 2.A.1**: Support.  **Proposal 2.A.2**: Fine to accept the proposal.  **Question 2.A.3:** Our preference is separate selection of FD basis vectors.  **Question 2.A.4**: We support Alt2 (Resource-common RRC configuration of Parameter Combination).  **Proposal 2.B**: Support legacy CBSR.  **Proposal 2.D**: Our preference is legacy OCPU = Ks. We can accept larger number of CPUs, but it should be at least aligned with legacy if M = 1, i.e., function should be update to OCPU = (M-1) + KS. |
| Xiaomi | **Proposal 2.A.3:**  We are fine to discuss that indication of FD basis are resource common to save feedback overhead if there are no much performance loss  **Proposal 2.A.4:**  Support  **Proposal 2.B**:  Support  **Proposal 2.E**:  We support the proposal for M=1. We could not see the intension of reporting these quantities for M>1. |
| HONOR | **Proposal 2.A.1**: Support.  **Proposal 2.A.2**: RRC configuration only is enough.  In addition, as proposed in our contribution, it’s preferred that UE can send its assistance information about the preferred number of M or MR after UE capability report for power saving purpose. After network receives the assistance, network can either ignore the assistance information or network additionally configures/requests the UE to report CRI/RI/PMI/CQI associated with MR (<M) of KS CSI-RS CSI-RS resources as discussed above. Therefore, we prefer to further study:  *For power saving purpose, support UE to send the preferred number of M in UE assistance information (i.e., UEAssistanceInformation) after UE capability report.*  **Proposal 2.A.3:** Fine with the proposal.  **Proposal 2.A.4**: Support.  **Proposal 2.B:** Support. Propose to further study the method to reduce the CBSR indication overhead (e.g., two-level as proposed by Huawei and ZTE).  **Proposal 2.E:** Open to support. |
| Lenovo/ MotM | **Proposal 2.A.1**:  Support  **Proposal 2.A.2**:  We also see this an over optimization. The NW can configure MR=M CRIs and update the MR beams to implicitly configure the UE with measuring CSI with respect to a different set of analog beams  **Question 2.A.3:**  Support separate FD basis vectors per CRI.  **Proposal 2.A.4**:  Support  **Proposal 2.B**:  Support |
| MediaTek | **Proposal 2.A.3, 2.A.4, 2.D** Support  Regarding timeline, in addition to computation of M CRIs, we think additional time may be needed in Z to account for more CSI-RS resources compared to legacy. |
| Spreadtrum | **Proposal 2.A.3:** Support the proposal. PMI for each resource should be calculated independently.  **Proposal 2.A.4/ 2.B:** Support |
| Mod V36 | **No revision** |
| CMCC | **Proposal 2.A.2**  If it is only for A-CSI, this feature can be implemented by triggering different CSI trigger state, one is R19 Multi-CRI CSI report and the other is legacy CSI report.  In addition, we want to clarify the last bullet, if the *MR* selected resources is configured within CSI trigger state or CSI report config, it seems the *MR* selected resources can be updated implicitly by trigger different CSI trigger state.  **Proposal 2.A.3/2.A.4/2.B/2.D**  Support  **Proposal 2.E**  Support and there seems a typo: ‘cri-RI-CQI’. |
| Fujitsu | **Proposal 2.A.1:** To avoid ambiguity, it’s better to add “and reported ” for 2-bit differential SB CQI   * 2-bit differential SB CQIs are independently calculated and reported across the M selected NZP CSI-RS resource   **Proposal 2.A.2:** We support configuration on *MR*. But for less spec impact and simpler design, we can accept only by RRC in CSI-ReportConfig for both P/SP/AP CSI.  **Proposal 2.A.3/4:** Support  **Proposal 2.B**: Support  **Proposal 2.D**: Support  **Proposal 2.E**: We prefer not to support non-PMI based CSI |
| Sharp | **Proposal 2.A.1:** Support  **Proposal 2.A.2:** We are OK with the proposal. In this proposal, the NW can indicate a measurement on a part of M beams and A-CSI can be used to supplement P-CSI.  **Proposal 2.A.3:** We are fine with the proposal. In our view, difference resources have difference CSIs on the frequency domain.  **Proposal 2.A.4:** We are fine with the proposal.  **Proposal 2.B:** Support.  **Proposal 2.D:** Support.  **Proposal 2.E:** Support. |

### Issue 3 (WID objective 3): CJT calibration reporting for non-ideal synchronization and backhaul

Table 3A Summary: issue 3

|  |  |  |
| --- | --- | --- |
| **#** | **Issue** | **Companies’ views** |
| 3.1.1/2 | **[116bis] Agreement**  For the Rel-19 aperiodic standalone CJT calibration reporting, the dynamic range and resolution parameters for delay offset reporting Dn,offset, i.e. (AD, MD), are NW-configured via higher-layer (RRC) signalling from the following candidate values:   * AD ={0.5CP, 0.75CP, CP, 1.5CP, 2CP, , , } where CP and denote the length of the cyclic prefix according to the current specifications (for normal CP) within a slot and the SCS, respectively   + FFS: Further down-selection of the above candidate values for AD, including the use of a same unit for all supported values * MD ={32, 64}   + FFS: If TDD TX/RX timing misalignment report is supported, whether different set of candidate MD values is needed   …  **[116bis] Agreement**  For the Rel-19 aperiodic standalone CJT calibration reporting, the dynamic range and resolution parameters for frequency offset reporting FOn, i.e. (AFO, MFO), are NW-configured via higher-layer (RRC) signalling from the following candidate values:   * AFO = {0.01ppm, 0.1ppm, 0.2ppm, f, f/2, f/4,f/8, 1/(4t), 1/(8t), 1/(16t), 1/(32t), 1/(512t)} where f and t denote the SCS and duration of one OFDM symbol, respectively   + FFS: Further down-selection of the above candidate values for AFO, including the use of a same unit for all supported values * MFO = {16,32}   …  **Proposal 3.A.1**: For the Rel-19 aperiodic standalone CJT calibration reporting, regarding the dynamic range for delay offset reporting Dn,offset, i.e. AD, at least support the following values: {0.5CP, CP}   * Decide, by RAN1#117, whether any of the following candidate values are supported: {0.75CP, 1.5CP, ,}   **Proposal 3.A.2**: For the Rel-19 aperiodic standalone CJT calibration reporting, regarding the dynamic range for frequency offset reporting FOn, i.e. AFO, at least support the following values: {0.1ppm, 0.2ppm}   * Decide, by RAN1#117, whether any of the following candidate values are supported: {0.025ppm, 0.05ppm, 1/(8t), 1/(16t), 1/(32t)}   **FL assessment**: This was discussed OFFLINE [2] and this is the current situation justifying the above proposals   |  |  |  | | --- | --- | --- | | **Parameter** | **Value** | **Company view** | | AD | 0.5CP | Support/fine: Samsung, OPPO, Qualcomm, Apple, Intel, Ericsson, NTT DOCOMO, Fujitsu, NEC, Google, Xiaomi, Sharp, KDDI, Lenovo/MotM, IDC, vivo  Not support: ZTE, Nokia/NSB, | | 0.75CP | Support/fine: Intel, Ericsson, NTT DOCOMO, Xiaomi, Google, NEC, Sharp, KDDI, IDC  Not support: ZTE, Nokia/NSB, | | CP | Support/fine: Samsung, OPPO, Qualcomm, Apple, Intel, Ericsson, NTT DOCOMO, Fujitsu, Google, Xiaomi, NEC, Kyocera, Sharp, KDDI, , Lenovo/MotM, IDC  Not support: ZTE, Nokia/NSB, | | 1.5CP | Support/fine: Samsung, Ericsson, NTT DOCOMO, Fujitsu, NEC, KDDI  Not support: ZTE, Nokia/NSB, Xiaomi, Google, Sharp, IDC, | | 2CP | Support/fine: NEC,  Not support: ZTE, Nokia/NSB, Xiaomi, Google, Sharp, KDDI, IDC, vivo, | |  | Support/fine: ZTE, Huawei/HiSi, Nokia/NSB, CATT  Not support: Samsung, OPPO, Apple, Intel, Xiaomi, Google, Fujitsu, NEC, Sharp, KDDI, IDC, | |  | Support/fine: ZTE, Huawei/HiSi, Nokia/NSB, CATT  Not support: Samsung, OPPO, Apple, Intel, Xiaomi, Google, Fujitsu, NEC, Sharp, KDDI, IDC, | |  | Support/fine: ZTE, Huawei/HiSi, Qualcomm, Nokia/NSB, vivo, CATT  Not support: Samsung, OPPO, Apple, Intel, Xiaomi, Google, Fujitsu, NEC, Sharp, KDDI, IDC, | | AFO | 0.01ppm | Support/fine: Samsung, Qualcomm, Intel, Ericsson, NTT DOCOMO, Xiaomi, Sharp, IDC, vivo,  Not support: ZTE, Huawei/HiSi, Apple, Nokia/NSB, NEC, Kyocera, | | 0.1ppm (RAN4) | Support/fine: Samsung, OPPO, Qualcomm, Intel, Ericsson, NTT DOCOMO, Fujitsu, Xiaomi, NEC, Kyocera, Sharp, Lenovo/MotM, IDC, vivo  Not support: ZTE, Huawei/HiSi, Apple, Nokia/NSB | | 0.2ppm (RAN4) | Support/fine: Samsung, OPPO, Qualcomm, Intel, Ericsson, NTT DOCOMO, Fujitsu, Xiaomi, NEC, Kyocera, Sharp, Lenovo/MotM, IDC, vivo  Not support: ZTE, Huawei/HiSi, Nokia/NSB | | f | Support/fine:  Not support: ZTE, Huawei/HiSi, Intel, Ericsson, Nokia/NSB, Samsung, Xiaomi, Fujitsu, NEC, IDC, vivo, | | f/2 | Support/fine: Google,  Not support: ZTE. Huawei/HiSi, Intel, Ericsson, Nokia/NSB, Samsung, Xiaomi, Fujitsu, NEC, IDC, vivo, | | f/4 | Support/fine: Google,  Not support: ZTE, Huawei/HiSi, Intel, Ericsson, Nokia/NSB, Samsung, Xiaomi, Fujitsu, NEC, IDC, vivo, | | f/8 | Support/fine: Apple, Google,  Not support: ZTE, Huawei/HiSi, Intel, Ericsson, Nokia/NSB, Samsung, Xiaomi, Fujitsu, NEC, IDC, | | 1/(4t) | Support/fine: Nokia/NSB  Not support: Samsung, Apple, Intel, Ericsson, Xiaomi, Fujitsu, NEC, IDC, vivo, | | 1/(8t) | Support/fine: ZTE, Huawei/HiSi, CATT  Not support: Samsung, Apple, Intel, Ericsson, Xiaomi, Fujitsu, NEC, IDC, | | 1/(16t) | Support/fine: ZTE, Huawei/HiSi, Nokia/NSB, CATT  Not support: Samsung, Apple, Intel, Ericsson, Xiaomi, Fujitsu, IDC, NEC | | 1/(32t) | Support/fine: ZTE, Huawei/HiSi, Nokia/NSB, CATT  Not support: Samsung, Apple, Intel, Ericsson, Xiaomi, Fujitsu, IDC, | | 1/(512t) | Support/fine: Qualcomm, Nokia/NSB, NEC  Not support: Samsung, Apple, Intel, Ericsson, Xiaomi, Fujitsu, IDC, | | **3.A.1:**  **Support/fine**: NTT DOCOMO, Sharp, OPPO, TCL, Sony, KDDI, Lenovo/MotM, IDC, Intel, Samsung, vivo, Xiaomi, NEC, Ericsson, Qualcomm, Google, Apple, CATT  **Not support**:  **3.A.2:**  **Support/fine**: NTT DOCOMO, Sharp, OPPO, TCL, Sony, KDDI, Lenovo/MotM, IDC, Intel, Samsung, vivo, Xiaomi, NEC, Ericsson, Qualcomm, Google, Apple, CATT  **Not support**: Huawei/HiSi, ZTE |
| 3.2.1 | **[116bis] Agreement**  For the Rel-19 aperiodic standalone CJT calibration reporting, given the NTRP configured NZP CSI-RS resources/resource sets and the selected N resources/resource sets, support reporting, in one CSI reporting instance, {n, , n=0, 1, …, N – 1, n≠nref, =0,1,…,-1}, where n, denotes the measured phase offset between the n-th CSI-RS resource/resource set and the reference CSI-RS resource/resource set nref for the -th frequency unit   *  =1 is supported   + FFS: whether >1 (sub-band reporting) is also supported. For this decision, companies are encouraged to evaluate performance loss without the support of >1 due to phase offset induced by TX-RX timing misalignment.   …  **Proposal 3.B.1**: For the Rel-19 aperiodic standalone CJT calibration reporting, when ReportQuantity is ‘cjtc-P’ (DL/UL phase offset), =1 only (agreed in RAN1#116bis) implies that the measured/reported phase offsets {n,, n=0, 1, …, NTRP – 1, n≠nref} are associated with the entire configured CSI reporting band (i.e. ‘wideband’)  **FL assessment**: Needed to clarify what =1 means, and set it apart from >1 (including the slope scheme) | **Support/fine:** Huawei/HiSi, Samsung, Ericsson, Google, Nokia/NSB, ZTE, Qualcomm, vivo, MediaTek, OPPO, Apple, CATT, Intel, Xiaomi, NTT DOCOMO, Lenovo/MotM, NEC, Sony,  **Not support:** |
| 3.2.2 | **[116bis] Agreement**  For the Rel-19 aperiodic standalone CJT calibration reporting, given the NTRP configured NZP CSI-RS resources/resource sets and the selected N resources/resource sets, support reporting, in one CSI reporting instance, {n, , n=0, 1, …, N – 1, n≠nref, =0,1,…,-1}, where n, denotes the measured phase offset between the n-th CSI-RS resource/resource set and the reference CSI-RS resource/resource set nref for the -th frequency unit   *  =1 is supported   + FFS: whether >1 (sub-band reporting) is also supported. For this decision, companies are encouraged to evaluate performance loss without the support of >1 due to phase offset induced by TX-RX timing misalignment.   …  **Proposal 3.B.2**: For the Rel-19 aperiodic standalone CJT calibration reporting, when ReportQuantity is ‘cjtc-P’ (DL/UL phase offset), ***decide***, by RAN1#117, whether to also support >1 (sub-band reporting) as follows:   * A sub-band size is selected from {8,16} PRBs   + FFS: Whether the sub-band size is NW-configured via higher-layer (RRC) signalling or selected (hence reported) by the UE * Denoting the number of sub-bands within the configured CSI reporting band as NSB-P, and the sub-bands are indexed as {0, 1, …, NSB-P –1}, decide, by RAN1#117, from the following reporting options:   + Opt1: {(n,, n), n=0, 1, …, NTRP – 1, n≠nref}, where n,is the phase offset corresponding to sub-band 0 and the phase offset for sub-band  can be calculated as n, + n     - , where {[32], [64], [128], [256]}   + Opt2: = NSB-P, i.e. {(n,, n,, NSB-P), n=0, 1, …, NTRP – 1, n≠nref}     - The alphabet for n, follows the previously agreed alphabet for =1, including the ‘invalid’ state     - FFS: Whether restriction on the maximum payload size is needed   + Note: For all the above reporting options, the UE performs measurement over the entire configured CSI reporting band   **FL assessment**: This was discussed OFFLINE [2]. This is the current situation:  **Support/fine >1**: ZTE (both), Qualcomm (both), CATT (both), Ericsson (both), NTT DOCOMO (Opt1), Samsung (both), Fujitsu, NEC (Opt1), TCL, Sony (Opt1), KDDI, Lenovo/MotM (Opt2), CMCC (Opt2), NICT (Opt1), Sharp (Opt2), MediaTek (both), Nokia/NSB (both), Huawei/HiSi (both)  **Not support >1 (separate D/d+WB PO enough)**: OPPO, Apple, Intel, vivo, Google, | **Support/fine:** ZTE, Qualcomm, CATT, Ericsson, NTT DOCOMO, Samsung, Fujitsu, NEC, TCL, Sony, KDDI, Lenovo/MotM, OPPO, Apple, Intel, vivo, Google, Nokia/NSB, Huawei/HiSi, CMCC, NICT, Sharp  **Not support:** |
| 3.3.1 | **Proposal 3.C.1**: For the Rel-19 aperiodic standalone CJT calibration reporting, when ReportQuantity is ‘cjtc-P’ (DL/UL phase offset),   * For a given phase offset reporting configuration, the UE can be configured (via higher-layer/RRC signaling) with Q associated SRS resource(s) for antenna switching   + FFS: The supported value(s) of Q * The UE antenna port(s) for receiving the CSI-RS configured for phase offset measurement are same as the UE antenna port(s) for transmitting the selected/configured port(s) from the associated SRS resource(s)   + For discussion purposes only (not necessarily for specification), the UE antenna port is referred to as the ‘reference UE antenna port’   **FL assessment**: To ensure no additional impairments and phase misalignment occur, the SRS and CSI-RS used for PO measurement should be linked via UE antenna port (since the SRS used here is SRS for AS) | **Support/fine:** Qualcomm, ZTE, CATT, Ericsson, Nokia/NSB, Huawei/HiSi, Samsung, vivo, MediaTek, CATT, Google, IDC, OPPO (ok), Intel (ok), Xiaomi, NTT DOCOMO, Sony, Apple (ok)  **Not support:** |
| 3.3.2 | **Proposal 3.C.2**: For the Rel-19 aperiodic standalone CJT calibration reporting, when ReportQuantity is ‘cjtc-P’ (DL/UL phase offset), regarding how to determine the SRS port corresponding to the ‘reference UE antenna port’, support the following   * Scheme1. The UE is configured by NW (via higher-layer/RRC signaling) 1 or more SRS port(s) selected from all the port(s) from the configured Q associated SRS resource(s) for phase offset reporting   + FFS: Exact details of configuration mechanism   + FFS: Whether >1 SRS ports can also be selected * Scheme2. The UE selects 1 or more SRS port(s) out of all the ports across Q resources and includes the selection in the phase offset report   + FFS: Whether >1 SRS ports can also be selected   FFS: Whether further restriction(s) to limit the time gap between the received CSI-RS and the transmitted associated SRS are needed  **FL assessment**: This proposal is needed so that the UE and gNB know the exact SRS port(s) used for the linkage in 3.C.1. Scheme2 offers an additional freedom for the UE to select the port(s) according to its implementation, while Scheme1 relies on NW configuration. In some Tdocs it was argued that Scheme2 facilitates NW implementation using non-precoded CSI-RS linked with SRS.  Supporting both schemes facilitates more use cases and deployment scenarios for PO report. | **Support/fine:** Qualcomm, Ericsson, Nokia/NSB, Samsung, vivo, MediaTek, IDC, CATT, NTT DOCOMO, [Google]  **Not support:** OPPO (only sch1 needed), Apple, Intel (only sch1 needed) |
| 3.5 | **Proposal 3.E.1**: For the Rel-19 aperiodic standalone CJT calibration reporting, regarding timeline and OCPU, when ReportQuantity is ‘cjtc-Dd’ (Doffset+d) or cjtc-F’ (frequency offset), fully reuse those from Rel-18 TDCP reporting   * For OCPU, Y denotes the number of reported offset values, i.e. NTRP for each CJT calibration report type   **FL assessment**: This is for a single-type report, either Dd or FO | **Support/fine:** vivo, Samsung, Ericsson, Xiaomi, NTT DOCOMO, ZTE, Apple, Intel, Qualcomm, CATT, Google, IDC, Intel, Xiaomi, Sony  **Not support:** |
| 3.6 | **[116bis] Agreement**  For the Rel-19 aperiodic standalone CJT calibration reporting, an ‘invalid’ quantization state/hypothesis is supported for frequency offset and phase offset CJT calibration reporting   * Note: already supported as ‘out-of-range’ for the (Dn,offset, dn) reporting * FFS (RAN1#117): The need for a condition/event for ‘invalid’ to be specified as a UE procedure e.g. RSRP-based   **Conclusion 3.F**: For the Rel-19 aperiodic standalone CJT calibration reporting, regarding the ‘out of range’ or ‘invalid’ quantization state/hypothesis, there is no consensus on specifying a condition/event for such state.  **FL assessment**: Please share your views  **Question 3.F**: For the Rel-19 aperiodic standalone CJT calibration reporting, regarding the ‘out of range’ or ‘invalid’ quantization state/hypothesis, please share our view whether a condition/event for such state needs to be specified and, if so, please be specific  **Yes (RSRP-based with RRC-configured threshold, e.g. RAN3)**: IDC, CATT, NEC, Google, Nokia/NSB,  **No (UE implementation)**: CMCC, KDDI, Qualcomm, Ericsson, OPPO, Huawei/HiSi, Apple, vivo, ZTE, Intel, Xiaomi, Lenovo/MotM  **Need discussion (need for other metrics? Testing?)**: Samsung | |
| 3.7 | **Proposal 3.G.1**: For the Rel-19 aperiodic standalone CJT calibration reporting, when ReportQuantity is ‘cjtc-Dd’ (Doffset+d), support the following   * UE to compensate the delay offset for CSI-RS resources for CJT CSI reporting (e.g. with Rel-18 Type-II CJT codebook) * Link the report to the CJT CSI reporting (e.g. with Rel-18 Type-II CJT codebook) so that the delay offset values for the transceiver compensation are aligned   **Proposal 3.G.2**: For the Rel-19 aperiodic standalone CJT calibration reporting, when ReportQuantity is ‘cjtc-F’ (frequency offset), support the following:   * link the FO report to the CJT CSI reporting (e.g, with Rel-18 Type-II CJT codebook) so that the FO values for both UE and gNB compensation are aligned   **FL assessment**: Please share your views | **3.G.1**  **Support/fine:** vivo, ZTE, Samsung (discuss), Qualcomm (discuss), Ericsson, OPPO (discuss), CATT, NTT DOCOMO, Lenovo/MotM (discuss), NEC, Sony (discuss),  **Not support:** Google, OPPO, Apple, Intel, Xiaomi (gNB),  **3.G.2**  **Support/fine:** vivo, Samsung (discuss), Qualcomm (discuss), Ericsson, OPPO (discuss), NTT DOCOMO, Lenovo/MotM (discuss), NEC, Sony (discuss),  **Not support:** Google, OPPO, Apple, Intel, Xiaomi (gNB) |
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Table 3B LLS/SLS results: issue 3

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| --- | --- | --- | --- |
| **Company** | **LLS/SLS results** | | |
| **Issue #** | **Metric** | **Observation** |
| Samsung | 3.2.2 | Avg UPT Gain | For CJTC phase-offset reporting, it is identified that Option 1 can perform sufficiently well and nearly achieve the performance of Option 2 and ideal calibration in the scenario with maxTAE=65ns, when only small measurement errors exist (without additional hardware impairments). However, when large measurement errors exist, Option 1 incurs some performance degradation (2% UPT loss) than Option 2, because the large measurement errors affect the underlying assumption of linear phase drift not working well. |
| vivo | 3.1 | SE gain vs maximum payload | It is observed in the result above that when the carrier is 2.2GHz, a frequency error of 0.01 ppm (~ 22Hz) results in a performance loss in the range of 3%, but a frequency error of 0.05 ppm results in a loss of 20% in DU scenarios, which is significant. |
| ZTE | 3.1 | Average throughput gain | The figure above shows the SLS results of average throughput gain for MD = 32, AD = CP (baseline) or . It is shown that, the performance of AD = CP and AD = is very close.  The figure above shows the SLS results of average throughput gain for MFO = 16, AFO = 0.2ppm (baseline) or . It is shown that the performances of AFO = 0.2ppm and AFO = are very close. |
| CATT | 3.2.2 | Mean UPT gain | The SLS results above show that the proposed low feedback overhead based calibration scheme with (UE selects some SBs and reports POs corresponding to the selected SBs 🡺 NW inter/extrapolates missing SBs) can also achieve quite good performance while maintaining a moderate overhead cost.  The SLS results above show that 1) aligned 4 subbands based calibration achieves similar performance to all subbands based calibration, and 2) calibration performance is degraded if misaligned frequency resources in DL and UL are used for phase offset calculation. |
| Sony | 3.2.2 | Average throughput | The LLS simulations show that reporting information about the evolution of the phase offsets in the frequency domain improves the system’s throughput, at least for TAEs of 65 ns or larger. (2% gain over wideband PO reporting) |
| Nokia | 3.6 | Mean cell UPT vs total overhead | A graph with numbers and linesA graph with a line and a chart with numbers   1. (b)   Normalized mean cell UPT (a) without RSRP condition on the invalid state and (b) when an RSRP threshold of 9dB is configured as a condition for invalid state  Configuring an RSRP threshold of 8dB as condition for the invalid state for FO reporting shows average UPT gain of around 5% over not configuring an RSRP threshold. |
| 3.3.1 | Mean spectral efficiency gain | A graph with different colored bars  Description automatically generatedA graph with different colored squares  Description automatically generated  In the left figure, 2 out of 4 antennas at UE side are sounded and the same antennas are used to estimate the phase difference between the CSI-RS signals transmitted by TRP and the reference TRP, and received by antenna , . In the right figure, only 1 SRS antenna port is sounded in UL and all receive antennas are used to compute . We can see how the mismatch between the SRS ports used to compute and the receive antennas used to compute , has impacted the performance greatly as shown in the right figure, where we can see about 60% loss. |
| Qualcomm | 3.2.2 |  | A couple of graphs with lines and numbers  Description automatically generated with medium confidence  As seen in the results above, it is observed that tens of nano seconds can cause nearly 10% UPT loss. Furthermore, it is observed that the UPT loss is still significant, when a small bit, (e.g., 3-to-5 bits, i.e., 8 to 32 quantization levels) is used for TAE quantization. The UPT loss is around 2% to 10%, depending on the exact TAE value – this is due to some TAE value close to certain quantization point by chance. |
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Table 3C Additional inputs: issue 3

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| --- | --- |
| **Company** | **Input** |
| Mod V0 | **Please share your inputs on each of the issues and, if applicable, proposals in TABLE 3A** |
| Google | Proposal 3.A.1: Support  Proposal 3.A.2: OK  Proposal 3.B.1: OK  Proposal 3.B.2: What is the use case for the proposed subband PO report? For both proposals, is it possible for UE to report “invalid” for some subbands?  [Mod: Added clarification. The answer is yes]  Proposal 3.C.1: OK. One minor editorial comment, in the second bullet, “UE antenna port” should be “UE antenna port(s)”, since whether to use one or multiple ports are still open.  [Mod: OK]  Proposal 3.C.2 (Issue 3.3.2): One question, if only one or a subset of UE ports are selected/configured, how about the phase offset for other UE ports?  [Mod: The proposal doesn’t address the other UE antenna ports. This will be left to UE implementation. We just focus on the UE antenna port(s) used for transmitting SRS and receiving CSI-RS for PO measurement/reporting]  Proposal 3.C.2 (Issue 3.5): Support for Ocpu and active resource counting, but the timeline may depend on how we define the condition to report “invalid”. If the “invalid” requires UE to measure SINR/RSRP or something else, more time needs to be considered. In addition, the timeline for PO report also depends on whether UE needs to calculate/compensate the TO/FO.  [Mod: OK, we can discuss timeline in later rounds, I put timeline in brackets for now, waiting for the conclusion if event is needed for ‘invalid’]  Proposal 3.F: We think the condition could follow what has been defined in RAN4 for the condition to determine one resource is measurable, which are as follows:   * RSRP > threshold1 * SINR > threshold2 * Delay offset is between the CSI-RS and the reference CSI-RS the UE has reported is smaller than CP   Proposal 3.G.1/2: Do not support. We think the NW should perform the pre-compensation to transmit the CSI-RS resources. Therefore, the NW can provide the corresponding QCL indication for the CSI-RS resources to tell UE whether pre-compensation has been done or not. Therefore, we propose the followings:  **Support the network to indicate the QCL for the CSI-RS resources configured as CMR for CJT CSI based on one of the following schemes:**   * **Scheme 1: Independent TCI configuration for each CSI-RS resources (Already supported)** * **Scheme 2 (DO pre-compensation): Independent TCI configuration for each CSI-RS resources for QCL indication other than delay offset, and configure a common TCI for all the CSI-RS resources for QCL indication for delay offset** * **Scheme 3 (FO pre-compensation): Independent TCI configuration for each CSI-RS resources for QCL indication other than Doppler shift, and configure a common TCI for all the CSI-RS resources for QCL indication for Doppler shift**   **[Mod: I saw the proposal and already planned to discuss this for later rounds]** |
| Mod V2 | **Minor revision on 3.B.2 and 3.C.2 to address Google’s comments** |
| Samsung | We support the following proposals:   * 3.A.1,2; 3.B.1,2; 3.C.1,2; 3.E (it seems FL mislabeled this as 3.C.2 for issue 3.5)   Question 3.F: RSRP is not the only reason a report should be declared invalid. Another reason is interference (then should we also include L1-SINR which would require IMR support?). This is especially complicated for PO reporting since both SRS and CSI-RS are used. L1-RSRP doesn’t reflect the quality of SRS (there is SRS-RSRP as well, should this be included as well?).   * Without saying whether this should be supported or not, we think this needs to be discussed further. The main question would be, if there is no event/condition specified, does this affect the system performance? This also needs tob e understood together with RAN4 aspect (e.g. how CJT reporting will be tested in RAN4)   Proposal 3.G.1/2: In general, we are supportive of the direction, but further discussion is needed before we can support the proposals at least on the following issues:   * When both CSI-RS (requiring UE-specific CSI-RS) and PDSCH for a UE are compensated, there is no need for an additional spec support since this is NW implementation * When only PDSCH is delay-compensated (but not CSI-RS), the nature of compensation for PDSCH needs to be clarified. Is it digital or analog? * For FO (3.G.2), it is clear this can be done. But what type of linkage is needed? |
| Qualcomm | **Proposal 3.A.2**: We propose to change the FFS value “~~0.01ppm~~” (our own proposed) to 0.025ppm and 0.05ppm  The reason is, FO does not only comprise XO (clock) frequency drift, but also Doppler due to UE velocity.  0.01ppm=2v/c only corresponds to 1.5m/s UE velocity (in a worst case where UE moves from TRP1 towards TRP2) – can work only with walking speed, which may not be very practical.  **Proposal 3.B.1**: Support to clarify, and we think this should be the common understanding.  **Proposal 3.B.2**: Support both options configurable (sorry to repeat)   * Opt1 is beneficial for TDD reciprocity calibration with the method of “CSI-RS precoded by (hUL)\* ” (where phase over frequency is linear due to single delay component: TAE), which should not be precluded (since at least 4 companies propose this method); * Opt2 can be beneficial for   (1) Another TDD reciprocity calibration method with normal non-precoded CSI-RS (where phase over frequency is not linear, due to multi-path channel propagation), as also suggested by @CATT  (2) Hardware calibration (where phase over frequency is not linear due to RF characteristics – also suggested by @CATT)  **Proposal 3.C.2**: Support in-principle to reuse legacy TDCP  Seems timeline and active resource counting is straight-forward; but seems TDCP CPU is a little complicated – will input more in later Round2/3.  **Question 3.F**: No need for RSRP-based.  We think this RSRP-based “reliability” design can be standard-transparent, e.g. network just configure UE to report RSRP in another report – which is already supported with existing RRC.  Actually, this “invalid” codepoint indeed has a use case: When CMR is not available, instead of dropping the report (as in 5.2.2.5 of 38.214), reporting this “invalid” codepoint would not change the UCI payload size and would prevent from UCI error propagation.  **Proposal 3.G.1/2**: Open to discuss the technical perspective, but the design of report linkage looks bad – for example, if we need to also consider Type-II-CJT CQI depending on a recently reported delay/FO, how to define “recent” (or “most recent”, and/or the related timeline)?  We think a cleaner design is to report the delay/FO compensation and Rel-18 Type-II-CJT in a single report – which we are in favor, but we also understand it is out of Rel-19 scope |
| IDC | **Proposal 3.A.1, Proposal 3.A.2, Proposal 3.C.1, Proposal 3.C.2** (issues 3.3.2 and 3.5)**:** Support.  **Question 3.F:** RSRP is the most straightforward threshold that could be specified, but we are open to further discussing other conditions/event that could be specified (e.g., SINR). |
| Mod V6 | **Revisions to address inputs** |
| Ericsson | **Proposal 3.E:**  Does this proposal apply to only FO and TO reporting? For PO reporting, we don’t use TRS, so not sure if we have to reuse the rules from TDCP for PO reporting.  [Mod: Thanks for the good catch]  **Question 3.F**  Ok to leave this to UE implementation  **Proposal 3.G.1:**  Support  **Proposal 3.G.2:**  Support |
| OPPO | **Proposal 3.A.1/3.A.2:**  Fine.  **Proposal 3.B.1:**  Fine.  **Proposal 3.C.1/2:**  In our understanding, it is not needed to restrict the UE receive antenna to receive the CSI-RS. For example, when only part of antennae are used for SRS antenna switching, UE can still use all receive antennae to improve the accuracy of phase offset estimation. There is no such restriction to PDSCH though reciprocity based PDSCH is also associate with SRS. The association between UL/DL antennae should be ensured at gNB side rather than UE side (since it is UL/DL phase imbalance at gNB side).  [Mod: Without this proposal, phase offset measurement will be contaminated by other antenna impairments such as RF mutual coupling due to S12/S21\*, inter-element phase offset, etc. So pPO report will be inaccurate and won’t be useful since UE cannot compensate for them]  **Proposal 3.F:**  Condition/event is not needed. Out of range can naturally indicate that the value is out of the quantization range or the value is not measured by UE.  **Proposal 3.G.1:**  Further study is needed. We think gNB compensation could be sufficient.  **Proposal 3.G.2:**  Further study is needed. We think gNB compensation could be sufficient. |
| Huawei, HiSilicon | **Proposal 3.A.2:**  The range for 0.1ppm, 0.2ppm, 0.01ppm is too small (40~400 Hz for 4GHz carrier), while the range that UE can measure is 1/4∆t (about 3kHz for 15kHz SCS). We support 1/(4∆t), 1/(8t), 1/(16t), 1/(32t) as the range.  **Question 3.F:**  It can be considered as UE implementation. If a condition is to be considered, then the RSRP gap between two TRPs exceeds a threshold, then the TRP with lower RSRP can be considered as invalid, as in this case the TRP with lower RSRP does not bring much performance gain using CJT. |
| Mod V13 | **Minor revision.**  **@OPPO/Wenhong, please check my reply re 3.C.1/2** |
| Apple | **Proposal 3.A.1:** Support, we prefer to use single unit, i.e., CP.  **Proposal 3.A.2:** We are fine, we prefer to use single unit.  **Proposal 3.B.1:** Support  **Proposal 3.B.2:** We slightly prefer not to support subband reporting.  **Proposal 3.C.1:** We do not see the need to have this agreement. In legacy, NW can configure AS SRS, and it is supposedly that UE should use the same antenna port to receive PDSCH for channel reciprocity, or to measurement follow-up CSI, etc. But we do not think it is explicitly specified, i.e., link CSI-RS measurement or PDSCH reception with AS SRS. This can be left for UE implementation.  [Mod: Please check my response to OPPO above]  **Proposal 3.C.2:** We do not see the need to have this agreement. NW can configure AS SRS, not sure why we need the association and what the specification impact is.  **Proposal 3.E:** We are fine.  **Proposal 3.F:** We do not see the need for specific condition or event, “out of range” is just out of range.  **Proposal 3.G.1:** We do not see the need for linkage  **Proposal 3.G.2:** We do not see the need for linkage |
| vivo | 3.B.2  We are fine with the proposal. But again, we don’t see the need to support subband phase. This same functionality can be achieved by the agreed DO reporting, and we are open to discuss the granularity of DO reporting if needed.  3.E  OK in general. And then there is multiple report quantities configured for one report, e.g., DO+FO, Y needs to consider the total number of offset values.  3.F  We prefer to make it UE implementation.  In our view, the RSRP may not be the only influencing factor in determining the ‘out of range’ or ‘invalid’ states. For example, if UE selects the reference TRP with highest SINR, it may be the case that the RSRP is satisfied but its associated FO or DO is negative, i.e., out of quantisation range.  3.G.1 & 3.G.2  In previous meetings, we have agreed that gNB will perform pre-compensation on PDSCH to align timing/FO among TRPs. One typical way to such pre-compensation is done in base band as it needs to be UE specific. The pre-compensation of TO/FO needs to work together with PDSCH precoding based on Rel-18 CJT CSI report to ensure the PDSCH reception performance, i.e., the PDSCH precoding shall match with the TO/FO pre-compensation so that phase shift caused by TO/FO is perfectly canceled out. Thus, CJT CSI report needs to somehow coordinate with CJTC TO/FO report.  One way is gNB to compensate for the TO/FO between the CSI-RS resources associated with the CJT CSI report. However, different UEs may report different TOs/FOs due to different propagation delay/Doppler offsets between TRPs. This usage may require UE-specific CSI-RS resources, which would also imply a significant RS overhead, and it is therefore not recommended. Especially considering for CJT CSI, up to 128 ports will be configured, such large overhead with UE specific CSI-RS is beyond tolerable.  Therefore, we need to let the UE compensate the TO/FO between the CSI-RS resource associated with the CJT CSI report during the calculation of CJT CSI. This approach does not require UE-specific CSI-RS resources, but it does require the gNB and UE to align the values of the TO/FO for UE and gNB compensation. When the gNB uses the CJT CSI reported from the UE, the gNB compensates the TO/FO between TRPs based on the UE-compensated TO/FO values for the PDSCH transmission.  For TO:    For FO:    To conclude, UE needs to know how much it needs to compensate for the CJT CSI reporting so that gNB can pre-compensate phase shift of TO/FO for the PDSCH according to a value aligned with the PDSCH precoding. The simplest way is when UE calculates the CJT CSI, the compensation of TO/FO is performed according to the values reported in the CJTC reporting. Then we need to build linkage between the CJTC reporting and CJT CSI reporting to make the whole compensation accurate. |
| ZTE | **3.A.2:**  Agree with Huawei that, 0.1ppm, 0.2ppm, 0.01ppm is too small (40~400 Hz for 4GHz carrier), while the range that UE can measure is 1/4∆t (about 3kHz for 15kHz SCS). Besides, if further considering the frequency offset caused by Doppler shift, the maximum frequency offset may exceed 0.2ppm.  **3.B.1:**  Support.  **3.B.2:**  Support. In the case where inter-TRP timing difference exists, the phase offset linearly varies across different subbands. So, opt1 is more efficient and economic. The ‘D/d + WB PO’ reporting may not work, because the implementation of calculating wideband PO is up to UE and unclear to gNB. For instance, if UE calculates the POs for two randomly selected subbands and reports the average value as the WB PO, gNB cannot correctly drive the SB POs accordingly.  **3.E:**  Support. When more than one types of CJT calibration reports are multiplexed, support to further extend the number of OCPU, i.e., OCPU = 2\*(Y+1)\*X when D/d and FO are multiplexed.  **3.F:**  Not support introducing a condition for reporting ‘invalid’ status. Whether/when to report ‘invalid’ status can be totally up to UE implementation. If we open this door, the discussion of conditions (RSRP, SINR, delay, …) can be endless.  **3.G.1:**  The discussion is needed.   * Regarding the 1st bullet, compensating the delay offsets for CSI-RS resources is workable, but it leads to UE-specific CSI-RS resources. In real deployment, the huge CSI-RS resource overhead is unacceptable if UE-specific CSI-RS resources are used. So, the realistic implementation is that, gNB transmits normal cell-specific CSI-RS resources, but informs UE the UE-specific delay offsets used for PDSCH transmission. Then UE can take the delay offsets into account when calculating the CJT PMI/CQI. * Regarding the 2nd bullet, linking the DO report with CJT CSI report is one solution, but we agree with QC that the linkage can bring some issues. Besides, the delay offsets reported by UE can be different from those used by gNB in PDSCH transmission. So, the more straightforward solution is to inform the UE the delay offsets that will be used for PDSCH transmission. The method of signalling, e.g., RRC or DCI, can be further discussed. |
| OPPO | **Proposal 3.C.1:**  After offline with proponents, we can interpret the benefit to configure the association with SRS ports. For the second sub-bullet, should it be “The UE antenna port(s) for receiving the CSI-RS configured for phase offset measurement are same as UE antenna port(s) for transmitting the selected/configured port(s) from the associated SRS resource(s)”, since the SRS ports is configured for the phase report.  [Mod: Correct, flipped the order, thanks]  **Proposal 3.C.2:**  Firstly, we think a better solution is to use all the SRS resources/ports rather than a subset of the ports, which can hold the consistency with PDSCH reception. Otherwise, there would be still some phase misalignment during PDSCH reception.  For the two schemes, we think scheme 1 could be sufficient and it is not needed to support both schemes.  **Proposal 3.G.1/2:**  We can be fine to discuss this issue before agreeing on the proposal. Other solutions should not be precoded. Considered how to compensate the delay/frequency offset is up to gNB implementation, association between two UE reports may not be accurate. A straightforward way can be gNB indicates the compensation value to use to UE for CJT CSI calculation. But we are not sure whether this is within the scope since it seems to be CJT CSI enhancement. |
| CATT | **Proposal 3.A.1:**  Ok for the CP-defined AD values. In current 1st bullet, there is only one value for the unit other than CP, i.e., , which makes the inverse of SCS defined range quite broken. We prefer to also add , in the 1st bullet along with .  [Mod: OK]  **Proposal 3.A.2:** Support  **Proposal 3.B.1:** Support  **Proposal 3.B.2:** Support.  Each option can be used under certain conditions. Opt1 has lower overhead, it can be applied when:   1. Inter-TRP phase is linear with frequency ( due to TX-RX time misalignment, which can be either TAE/group delay or residual time synchronization error) 2. Calibration is based on precoded CSI-RS (MRT precoding as mentioned by @Qualcomm)   Opt2 has better performance but potentially more overhead, it can be applied when:   1. Realistic hardware calibration is considered, phase is not linear with frequency due to RF characteristics 2. Calibration is based on non-precoded CSI-RS, with which the measured inter-TRP DL phase includes the DL channel phase component, hence it is not linear with frequency   **Proposal 3.C.2:** Support  **Proposal 3.E:** Ok    **Proposal 3.G.1:** Ok |
| Mod V22 | **Added conclusion 3.F** |
| Intel | **Proposal 3.A.1, 3.A.2**  We are fine. We may also consider to support 1.5 CP for various scenarios.  **Proposal 3.B.1**  We are fine.  **Proposal 3.B.2**:  We are still not convinced to support subband reporting for phase offset. If the delay offset reporting accuracy is sufficient, NW could compensate timing misalignment from delay offset reporting and delay offset measurement from SRS. If NW estimation is inaccurate we expect timing misalignment to be very small.  **Proposal C.1**:  Is the second bullet needed – or how is it specified. What is the use case for Q > 1 ?  [Mod: Will be discussed in later rounds]  **Proposal C.2**:  Is the intention to support both? Our understanding is that Scheme 1 is good enough. It is not clear the benefit of supporting Scheme 2 with UE selection of SRS ports.  [Mod: Correct. The proposal includes both schemes. But before this proposal can be targeted for endorsement a few other things need to be resolved, including Q values and # SRS ports selected]  **Proposal 3.E**  We are fine.  **Question 3.F**  We think this can be up to UE implementation.  **Proposal 3.G.1/G.2:**  It is not clear if this proposal is needed, outer loop link adaptation can be used to adjust CQI due to NW compensation. |
| Xiaomi | **Proposal 3.B.1**  Support  **Proposal 3.C.1**  Support  **Proposal 3.E**  Y should be NTRP -1. Why to preclude delay offset?  [Mod: Resolved offline 😊]  **Question 3.F**  Slightly prefer up to UE implementation  **Proposal 3.G.1/2**  prefer gNB side compensation only. |
| NTT DOCOMO | **Proposal 3.B.1/3.C.1/3.C.2/3.G.1/3.G.2:**  Support. |
| Lenovo/ MotM | **Proposal 3.A.1/2:**  Support  **Proposal 3.B.1:**  Suport  **Proposal 3.B.2**:  Open to discuss. We prefer Alt2  **Cocnclusion 3.F:**  Support  **Proposal 3.G.1/2:**  Open to discuss |
| NEC | **Proposal 3.B.1/3.G.1/3.G.2:**  Support. |
| Sony | **Proposals 3.A.1 and 3.A.2:**  Okay.  **Proposal 3.B.1:**  Support.  **Proposal 3.B.2:**  Support. We prefer Option 1, i.e., phase offset + slope. We do not think that Option 2 is needed. The impairment one would like to calibrate away relates to TX/RX time/phase misalignments of the TRPs engaging in CJT, and not to the multi-path of the radio channel. Only a phase offset and a phase slope are needed to characterize said impairment.  [Mod: One reason the proponents of Opt2 brings in that the effect of multi-path cannot be separated from TAE when non-precoded CSI-RS-based method is used. For BF CSI-RS-based, only the TAE eftect is there so Opt1 works since the multi-path channel effect is compensated by e.g. MRT on BF CSI-RS at each TRP. Please check CATT explanation above]    **Proposals 3.C.1 and 3.E.1:**  Okay.  **Proposal 3.G.1 and 3.G.2:**  We are open to discuss these proposals. |
| Mod V36 | **No revision** |
| Sharp | **Proposal 3.A.1/3.A.2:** Support.  **Proposal 3.B.1/3.B.2**: Support.  **Proposal 3.C.1:** Support.  **Proposal 3.E.1:** Support.  **Proposal 3.G.1/3.G.2:** We are open to discuss. Currently, if the UE compensates for offsets for CJT CSI reporting, we think the offsets does not need to be reported to the NW in advance. |

# References

|  |  |  |  |
| --- | --- | --- | --- |
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| 3 | [R1-2403847](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_117/Docs/R1-2403847.zip) | Discussion on Rel-19 Enhancements of CSI | InterDigital, Inc. |
| 4 | [R1-2403876](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_117/Docs/R1-2403876.zip) | Discussion on Rel-19 CSI enhancements | New H3C Technologies Co., Ltd. |
| 5 | [R1-2403884](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_117/Docs/R1-2403884.zip) | CSI enhancement for NR MIMO Phase 5 | Tejas Networks Limited |
| 6 | R1-2405340 | CSI enhancements to support up to 128 CSI-RS ports | MediaTek Inc. |
| 7 | [R1-2403945](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_117/Docs/R1-2403945.zip) | On 128 CSI-RS ports and UE reporting enhancement | Huawei, HiSilicon |
| 8 | [R1-2403981](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_117/Docs/R1-2403981.zip) | CSI enhancements for MIMO | Intel Corporation |
| 9 | [R1-2404004](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_117/Docs/R1-2404004.zip) | Discussion on Rel-19 CSI enhancements | TCL |
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| 11 | R1-2405365 | Views on Rel-19 CSI enhancements | Samsung |
| 12 | [R1-2404171](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_117/Docs/R1-2404171.zip) | Discussion on Rel-19 CSI enhancements | vivo |
| 13 | [R1-2404240](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_117/Docs/R1-2404240.zip) | Discussion on CSI enhancements | ZTE |
| 14 | [R1-2404278](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_117/Docs/R1-2404278.zip) | Views on R19 MIMO CSI enhancement | Apple |
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| 26 | [R1-2404919](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_117/Docs/R1-2404919.zip) | CSI enhancement for NR MIMO Phase 5 | Nokia |
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| 28 | [R1-2404971](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_117/Docs/R1-2404971.zip) | CSI enhancements | Sharp |
| 29 | [R1-2405005](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_117/Docs/R1-2405005.zip) | CSI enhancements for large antenna arrays and CJT | Ericsson |
| 30 | [R1-2405036](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_117/Docs/R1-2405036.zip) | Discussion on CSI enhancements | NTT DOCOMO, INC. |
| 31 | [R1-2405149](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_117/Docs/R1-2405149.zip) | CSI enhancements for >32 ports and UE-assisted CJT | Qualcomm Incorporated |
| 32 | [R1-2405206](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_117/Docs/R1-2405206.zip) | CSI enhancements | NICT |
| 33 | [R1-2405239](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_117/Docs/R1-2405239.zip) | CSI Enhancements | CEWiT |
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