**3GPP TSG RAN WG1 #117 R1-2404210**

**Fukuoka City, Fukuoka, Japan, May 20th - 24th, 2024**

**Agenda Item: 8.17**

**Source: Moderator (ZTE)**

**Title: Summary on SSB to CG-SDT PUSCH mapping**

**Document for: Discussion**

# Introduction

This document contains the summary of draft CR R1-2404210 for SSB to CG-SDT PUSCH mapping.

# Discussion (round 1)

## Text proposal

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| ***Reason for change:*** | In RAN1#106-e meeting, the SSB to CG PUSCH mapping method was agreed.   |  | | --- | | **Agreement**   * Each N of consecutive SSB indexes associated to one CG configuration are mapped to valid CG PUSCH resources   + first, in increasing order of DMRS resource indexes, where a DMRS resource index *DMRSid* is determined first in an ascending order of a DMRS port index and second in an ascending order of a DMRS sequence index   + second, in increasing order of CG period indexes in the association period * The mapping ratio N is explicitly signalled and the association period is implicitly derived   + FFS candidate value set of mapping ratio, and whether it is configured per CG configuration or per cell   + The SSB to CG PUSCH association period is the duration of multiple of CG periods depending the smallest time duration in the set determined by the CG period such that all SSBs associated with the CG configuration are mapped at least once to CG PUSCH resources.   + An association pattern period includes one or more association periods and is determined so that a pattern between CG PUSCH occasions and SS/PBCH block indexes associated with the CG configuration repeats at most every 640 msec.   Note: The mapping ordering and steps may be revisited if multiple CG PUSCH occasions in one CG period is supported |   In RAN1#108-e meeting, it’s further clarified that non-consecutive SSB indexes are also allowed to be configured in SSB subset for mapping.   |  | | --- | | **Agreement**  Non-consecutive SSB indexes are allowed to be configured in SSB subset for SSB to CG PUSCH mapping. |   Based on the above agreements, each N SSBs will be mapped to one DMRS port, where N is the mapping ratio, however, current spec does not capture the agreement completely, it’s not clear on the order of SSB indexes and how to map SSBs.  For example, SSB#0, 1, 2, 3 are mapped to DMRS port#0 and 1 with mapping ratio 2, based on current spec, there might be 2 different understandings on mapping methods, i.e.   1. SSB#0 and 1 are mapped to DMRS port#0, then SSB#2 and 3 are mapped to DMRS port#1. 2. SSB#0 and 1 are mapped to DMRS port#0 and DMRS port#1 respectively, then SSB#2 and 3 are mapped to DMRS port#0 and DMRS port#1 respectively.   The first understanding aligns with the agreement, but current spec may also be misinterpreted as second understanding, which would result in ambiguous implementation from BS and UE. |
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| ***Summary of change:*** | For SSB to CG-SDT PUSCH mapping, capture the missing information from previous agreements, i.e. “Each *N*, provided by *sdt-SSB-PerCG-PUSCH,* of SS/PBCH block indexes in increasing order are mapped to a valid PUSCH occasion and the associated DMRS resource in the following order” |
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| ***Consequences if not approved:*** | SSB to CG-SDT PUSCH mapping is not clear. |

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| **19.1 Configured-grant based PUSCH transmission**  **< Unchanged text omitted >**  Each *N*, provided by *sdt-SSB-PerCG-PUSCH,* of SS/PBCH block indexes in increasing order are mapped to a valid PUSCH occasion and the associated DMRS resource in the following order  - first, in increasing order of DMRS resource indexes within a PUSCH occasion, where a DMRS resource index is determined first in an ascending order of a DMRS port index and second in an ascending order of a DMRS sequence index [4, TS 38.211]  - second, in increasing order of PUSCH configuration period indexes  A PUSCH occasion is valid if it does not overlap with a valid PRACH occasion as described in clause 8.1.  For unpaired spectrum and for SS/PBCH blocks with indexes provided by *ssb-PositionsInBurst* in *SIB1*  - if a UE is not provided *tdd-UL-DL-ConfigurationCommon*, a PUSCH occasion is valid if the PUSCH occasion  - does not precede a SS/PBCH block in the PUSCH slot, and  - starts at least symbols after a last SS/PBCH block symbol, where is provided in Table 8.1-2  - if a UE is provided *tdd-UL-DL-ConfigurationCommon*, a PUSCH occasion is valid if the PUSCH occasion  - is within UL symbols  - starts at least symbols after a last downlink symbol, and at least symbols after a last SS/PBCH block symbol, where is provided in Table 8.1-2  A UE determines a power of a PUSCH transmission as described in clause 7.1.1, where the UE obtains using a RS resource from an SS/PBCH block with index associated with the PUSCH transmission. |

**< Unchanged text omitted >**

## Companies’ views

Regarding the draft CR above, the following questions are provided:

### Question 1: Do you agree that there is ambiguity on the SSB to CG SDT PUSCH mapping?

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| Company | Comment |
| New H3C | It is fine to clarify SSB to CG SDT PUSCH mapping |
| vivo | There could be some ambiguity. |
| Xiaomi | We are fine to have a further clarification on the SSB to CG SDT PUSCH resources mapping rule. |
| Samsung | No.  For the their first change (Each *N*,  provided by *sdt-SSB-PerCG-PUSCH,* of *N*PUSCHSS/PBCH SS/PBCH block), it is already covered by the following sentence in the specification.  An association period, starting from frame with SFN 0, for mapping 𝑁PUSCHSS/PBCH SS/PBCH block indexes, from the number of SS/PBCH block indexes, to valid PUSCH occasions and associated DM-RS resources is the smallest value in the set determined by the PUSCH configuration period provided by *periodicity* in *ConfiguredGrantConfig* according to Table 19.1-1 such that 𝑁PUSCHSS/PBCH SS/PBCH block indexes are mapped at least once to valid PUSCH occasions and associated DM-RS resources within the association period. **A UE is provided a number of SS/PBCH block indexes associated with a PUSCH occasion and a DM-RS resource by *sdt-SSB-PerCG-PUSCH***. If after an integer number of SS/PBCH block indexes to PUSCH occasions and associated DMRS resources mapping cycles within the association period there is a set of PUSCH occasions and associated DMRS resources that are not mapped to 𝑁PUSCHSS/PBCH SS/PBCH block indexes, no SS/PBCH block indexes are mapped to the set of PUSCH occasions and associated DMRS resources. An association pattern period includes one or more association periods and is determined so that a pattern between PUSCH occasions with associated DMRS resources and SS/PBCH block indexes repeats at most every 640 msec. PUSCH occasions and associated DMRS resources not associated with SS/PBCH block indexes after an integer number of association periods, if any, are not used for PUSCH transmissions.    For the second change (SS/PBCH block indexes in increasing order), it should be naturally understood as increasing order without any clarification. No any ambiguity in current specification. |

Question 2: Do you agree with the text proposal in section 2.1? Or any other wording?

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| Company | Comment |
| New H3C | Because subsequent sub bullet already mentioned “increasing order ”,our suggestion is to remove “in increasing order” as follows  Each *N*, provided by *sdt-SSB-PerCG-PUSCH,* of SS/PBCH block indexes ~~in increasing order~~ are mapped to a valid PUSCH occasion and the associated DMRS resource in the following order |
| vivo | According to the RRC specification, the values of *sdt-SSB-PerCG-PUSCH* could be less than one. Therefore, some additional updates can be made:   |  | | --- | | Each or each *N>1*, provided by *sdt-SSB-PerCG-PUSCH,* of SS/PBCH block indexes in increasing order are mapped to a valid PUSCH occasion and the associated DMRS resource in the following order  - first, in increasing order of DMRS resource indexes within a PUSCH occasion, where a DMRS resource index is determined first in an ascending order of a DMRS port index and second in an ascending order of a DMRS sequence index [4, TS 38.211]  - second, in increasing order of PUSCH configuration period indexes | |
| Xiaomi | Echo to New H3C: In our view, the “in increasing order” mentioned in the subsequent sub bullet actually refers the order of PUSCH resources (including different DMRs ports). Here, the “in increasing order” added by the moderator actually refers to the order of SSB indexes. This is different so we suggest to keep the “in increasing order” in the CR provided by the FL.  Besides, as for the CR, we slightly prefer vivo’s version considering that the value N could be configured less than 1. Furthermore, maybe the following minor modification based on vivo’s version is necessary to address the case of N<1, where one SSB index is associated with 1/N(N<1) PUSCH resources:  Each or each *N>1*, provided by *sdt-SSB-PerCG-PUSCH,* of SS/PBCH block indexes in increasing order are mapped to valid PUSCH occasions and associated DMRS resources in the following order  - first, in increasing order of DMRS resource indexes within a PUSCH occasion, where a DMRS resource index is determined first in an ascending order of a DMRS port index and second in an ascending order of a DMRS sequence index [4, TS 38.211]  - second, in increasing order of PUSCH configuration period indexes |
| Samsung | No, with the above reasons. |

# Summary

The final proposals will be added later.

# References

1. R1-2404210 Correction on SSB to CG-SDT PUSCH mapping ZTE