**3GPP TSG-RAN4 Meeting #112 *R4-2413917***

**Maastricht, Netherlands, 19th Aug 2024 - 23rd Aug 2024**

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| *CR-Form-v12.3* | | | | | | | | |
| **CHANGE REQUEST** | | | | | | | | |
|  | | | | | | | | |
|  | **38.133** | **CR** | **4741** | **rev** | **1** | **Current version:** | **17.14.0** |  |
|  | | | | | | | | |
| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* | | | | | | | | |
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| ***Proposed change affects:*** | UICC apps |  | ME | **x** | Radio Access Network |  | Core Network |  |

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| ***Title:*** | (NR\_HST\_FR2) CR to 38.133 with corrections and missing RRM parameters | | | | | | | | | |
|  |  | | | | | | | | | |
| ***Source to WG:*** | Nokia, Samsung | | | | | | | | | |
| ***Source to TSG:*** | R4 | | | | | | | | | |
|  |  | | | | | | | | | |
| ***Work item code:*** | NR\_HST\_FR2 | | | | |  | ***Date:*** | | | 2024-08-09 |
|  |  | | | |  | |  | | |  |
| ***Category:*** | **F** |  | | | | | ***Release:*** | | | Rel-17 |
|  | *Use one of the following categories:* ***F*** *(correction)* ***A*** *(mirror corresponding to a change in an earlier release)* ***B*** *(addition of feature),* ***C*** *(functional modification of feature)* ***D*** *(editorial modification)*  Detailed explanations of the above categories can be found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | | | | | | | | *Use one of the following releases: Rel-8 (Release 8) Rel-9 (Release 9) Rel-10 (Release 10) Rel-11 (Release 11) … Rel-17 (Release 17) Rel-18 (Release 18) Rel-19 (Release 19)  Rel-20 (Release 20)* | |
|  |  | | | | | | | | | |
| ***Reason for change:*** | | * Requirement paramters Mpss/sss\_sync\_w/o\_gaps, Mpss/sss\_sync\_with\_gaps for PSS/SSS detection and Mmeas\_period\_w/o\_gaps, Mmeas\_period with\_gaps for measurement period for intra-frequency measurements for Power Class 6 UEs are not defined.   + The values were defined in previous TS 38.133 version 17.6.0 in NOTE 3 and were equal to 24, but then the note got voided. * Test cases for HST FR2 are also defiend for re-establishment, i.e., in IDLE state, but corresonding RRM Conditions for RRM requirements applicability in IDLE state are missing. * Test cases for HST FR2 are also defiend for re-establishment, i.e., in IDLE state, but corresonding RRM Conditions for RRM requirements applicability in IDLE state are missing. | | | | | | | | |
|  | |  | | | | | | | | |
| ***Summary of change:*** | | * Clarified the value of Mpss/sss\_sync\_w/o\_gaps, Mpss/sss\_sync\_with\_gaps and Mmeas\_period\_w/o\_gaps, Mmeas\_period with\_gaps for PC6 devices. * Align the number of PRBs to 24 to other TCI state switch test cases (A.7.5.8.1.1 and A.7.5.8.2.1). * Defined the values for Power Class 6 UEs inTable B.1.2-2 for IDLE state. * Removed Z and Y values for PC1 and PC5 from the table B.1.2-2 since values of Z and Y for these PCs are defined and can be calculated directly. | | | | | | | | |
|  | |  | | | | | | | | |
| ***Consequences if not approved:*** | | * Some of the paramters of RRM requirements for HST FR2 scenario are not defined, and the requirement values cannot be calculated * RAN5 test ceses cannot be defined correctly in TS 38.533 | | | | | | | | |
|  | |  | | | | | | | | |
| ***Clauses affected:*** | | 9.2.5, 9.2.6, A.7.5.8.3.1, B.1.2 | | | | | | | | |
|  | |  | | | | | | | | |
|  | | **Y** | **N** |  | | | |  | | |
| ***Other specs*** | |  | **X** | Other core specifications | | | |  | | |
| ***affected:*** | | **X** |  | Test specifications | | | | TS 38.533 | | |
| ***(show related CRs)*** | |  | **X** | O&M Specifications | | | |  | | |
|  | |  | | | | | | | | |
| ***Other comments:*** | |  | | | | | | | | |
|  | |  | | | | | | | | |
| ***This CR's revision history:*** | | Revision of R4-2412019. | | | | | | | | |

## << Start of First change >>

### 9.2.5 Intrafrequency measurements without measurement gaps

#### 9.2.5.1 Intrafrequency cell identification

The UE shall be able to identify a new detectable intra-frequency cell within Tidentify\_intra\_without\_index if the UE is not indicated to report SSB based RRM measurement result with the associated SSB index(*reportQuantityRsIndexes* or *maxNrofRSIndexesToReport* is not configured), or the UE is indicated that the neighbour cell is synchronous with the serving cell (*deriveSSB-IndexFromCell* is enabled). Otherwise UE shall be able to identify a new detectable intra frequency cell within Tidentify\_intra\_with\_index. The UE shall be able to identify a new detectable intra frequency SS block of an already detected cell within Tidentify\_intra\_without\_index. It is assumed that *deriveSSB-IndexFromCell* is always enabled for FR1 TDD and FR2 with SCS smaller or equal to 480 kHz.

Tidentify\_intra\_without\_index = (TPSS/SSS\_sync\_intra + T SSB\_measurement\_period\_intra) ms

Tidentify\_intra\_with\_index = (TPSS/SSS\_sync\_intra + T SSB\_measurement\_period\_intra + TSSB\_time\_index\_intra) ms

Where:

TPSS/SSS\_sync\_intra: it is the time period used in PSS/SSS detection given in table 9.2.5.1-1, 9.2.5.1-2, 9.2.5.1-4 (deactivated SCell) or 9.2.5.1-5 (deactivated SCell) or 9.2.5.1-9 (deactivated SCell) or 9.2.5.1-11 or 9.2.5.1-12 (deactivated PSCell) or 9.2.5.1-13 (deactivated PSCell).

- For UE supporting power class 6 with *highSpeedMeasFlagFR2-r17* configured, if SMTC <= 40ms, TPSS/SSS\_sync\_intra is given in Table 9.2.5.1-11; otherwise, TPSS/SSS\_sync\_intra is given in Table 9.2.5.1-2.

TSSB\_time\_index\_intra: it is the time period used to acquire the index of the SSB being measured given in table 9.2.5.1-3, 9.2.5.1-15 (FR2-2), 9.2.5.1-6 (deactivated SCell), 9.2.5.1-10(deactivated SCell) or 9.2.5.1-14 (deactivated PSCell).

T SSB\_measurement\_period\_intra: equal to a measurement period of SSB based measurement given in table 9.2.5.2-1, table 9.2.5.2-2 table 9.2.5.2-3 (deactivated SCell), 9.2.5.2-4(deactivated SCell), 9.2.5.2-5 or 9.2.5.2-6(deactivated SCell), 9.2.5.2-8(deactivated PSCell) or 9.2.5.2-9(deactivated PSCell).

- For UE supporting power class 6 with *highSpeedMeasFlagFR2-r17* configured, if SMTC <= 40ms, TSSB\_measurement\_period\_intra is given in Table 9.2.5.2-7; otherwise, T SSB\_measurement\_period\_intra is given in Table 9.2.5.2-2.

CSSFintra: it is a carrier specific scaling factor and is determined according to CSSFoutside\_gap,i in clause 9.1.5.1 for measurement conducted outside measurement gaps, i.e. when intra-frequency SMTC is fully non overlapping or partially overlapping with GAP.

For a UE that supports Pre-MG, an SMTC occasion is only considered to be overlapped by Pre-MG if the Pre-MG is activated.

if the high layer in TS 38.331 [2] signalling of *smtc2* is configured, the assumed periodicity of intra-frequency SMTC occasions corresponds to the value of higher layer parameter *smtc2*; Otherwise the assumed periodicity of intra-frequency SMTC occasions corresponds to the value of higher layer parameter *smtc1*.

Mpss/sss\_sync\_w/o\_gaps: For a UE supporting FR2-1 power class 1 or 5, Mpss/sss\_sync\_w/o\_gaps =40. For a UE supporting power class 2, Mpss/sss\_sync\_w/o\_gaps =24. For a UE supporting FR2-1 power class 3, Mpss/sss\_sync\_w/o\_gaps =24. For a UE supporting FR2-1 power class 4, Mpss/sss\_sync\_w/o\_gaps =24. For a UE supporting FR2-1 power class 6, Mpss/sss\_sync\_w/o\_gaps = 24. For a UE supporting FR2-2 power class 1, Mpss/sss\_sync\_w/o\_gaps = 60. For a UE supporting FR2-2 power class 2, Mpss/sss\_sync\_w/o\_gaps = 36. For a UE supporting FR2-2 power class 3, Mpss/sss\_sync\_w/o\_gaps = 36.

Mmeas\_period\_w/o\_gaps: For a UE supporting FR2-1 power class 1 or 5, Mmeas\_period\_w/o\_gaps =40. For a UE supporting FR2-1 power class 2, Mmeas\_period\_w/o\_gaps =24. For a UE supporting FR2-1 power class 3, Mmeas\_period\_w/o\_gaps =24. For a UE supporting power class 4, Mmeas\_period\_w/o\_gaps =24. For a UE supporting FR2-1 power class 6, Mmeas\_period\_w/o\_gaps = 24. For a UE supporting FR2-2 power class 1, Mmeas\_period\_w/o\_gaps = 60. For a UE supporting FR2-2 power class 2, Mmeas\_period\_w/o\_gaps = 36. For a UE supporting FR2-2 power class 3, Mmeas\_period\_w/o\_gaps = 36.

MSSB\_index\_intra: For a UE supporting FR2-2 power class 1, MSSB\_index\_intra = 72 samples. For a UE supporting FR2-2 power class 2, MSSB\_index\_intra = 48 samples. For a UE supporting FR2 power class 3, MSSB\_index\_intra = 48 samples.

When UE supports concurrentMeasGap-r17 and is configured with concurrent measurement gaps,

Kp is the scaling factor for an SSB frequency layer to be measured without measurement gaps. Kp = Ntotal / Navailable, where Navailable and Ntotal are calculated as follows:

- For a window W of duration max(SMTC period, MGRP\_max), where MGRP max is the maximum MGRP across all configured per-UE measurement gap and/or per-FR measurement gap within the same FR as the SSB frequency layer, and starting from the beginning of any SMTC occasion:

- Ntotal is the total number of SMTC occasions within the window, including those overlapped with measurement gap occasions within the window, and

- Navailable is the number of SMTC occasions that are not overlapped with any non-dropped MG occasion within the window W, after accounting for measurement gap collisions by applying the measurement gap collision rule in section 9.1.8.3.

Kp = 1 when Navailable = 0.

Otherwise, when UE is not configured with or UE does not support concurrent measurement gaps:

When intra-frequency SMTC is fully non overlapping with GAP, Kp=1

When intra-frequency SMTC is partially overlapping with measurement gaps, Kp = 1/(1- (SMTC period /MGRP)), where SMTC period < MGRP. When intra-frequency SMTC is partially overlapping with NCSG, Kp = 1/(1- (SMTC period /VIRP)), where SMTC period < VIRP. For calculation of Kp, if the high layer signalling (TS 38.331 [2]) of *smtc2* is configured, for cells indicated in the *pci-List* parameter in *smtc2*, the SMTC periodicity corresponds to the value of higher layer parameter *smtc2*; for the other cells, the SMTC periodicity corresponds to the value of higher layer parameter *smtc1.* If the higher layer signaling in TS38.331 [2] signalling of *smtc2* is present and smtc1 is fully overlapping with measurement gaps and smtc2 is partially overlapping with measurement gaps, requirements are not specified for Tidentify\_intra\_without\_index or Tidentify\_intra\_with\_index.

For FR2,

Klayer1\_measurement=1,

- if all of the reference signals configured for RLM, BFD, CBD or L1-RSRP for beam reporting on any FR2 serving frequency in the same band outside measurement gap are not fully overlapped by intra-frequency SMTC occasions, or

- if all of the reference signal configured for RLM, BFD, CBD or L1-RSRP for beam reporting on any FR2 serving frequency in the same band outside measurement gap and fully-overlapped by intra-frequency SMTC occasions are not overlapped with any of the SSB symbols and the RSSI symbols, and 1 symbol before each consecutive SSB symbols and the RSSI symbols, and 1 symbol after each consecutive SSB symbols and the RSSI symbols, given that *SSB-ToMeasure* and *SS-RSSI-Measurement* are configured, where SSB symbols are indicated by the union set of SSB-ToMeasure from all the configured measurement objects on the same serving carrier which can be merged.and RSSI symbols are indicated by *SS-RSSI-Measurement*;

Klayer1\_measurement=1.5, otherwise.

If the above-mentioned reference signal configured for L1-RSRP measurement is aperiodic CSI-RS resource, longer cell identification delay would be expected.

If MCG DRX is in use, cell identification requirements for intra-frequency measurement in MCG specified in Table 9.2.5.1-1, Table 9.2.5.1-2, Table 9.2.5.1-3, Table 9.2.5.1-4, Table 9.2.5.1-5 and Table 9.2.5.1-6 shall depend on the MCG DRX cycle. If SCG DRX is in use, cell identification requirements for intra-frequency measurement in SCG specified in Table 9.2.5.1-1, Table 9.2.5.1-2, Table 9.2.5.1-3, Table 9.2.5.1-4, Table 9.2.5.1-5, Table 9.2.5.1-6, Table 9.2.5.1-12, Table 9.2.5.1-13 and Table 9.2.5.1-14 shall depend on the SCG DRX cycle. Otherwise, the requirements for when DRX is not in use shall apply.

Table 9.2.5.1-1: Time period for PSS/SSS detection, (Frequency range FR1)

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | max( 600ms, ceil( 5 x Kp) x SMTC period )Note 1 x CSSFintra |
| DRX cycle≤ 320ms | max( 600ms, ceil(M2 Note 2x 5 x Kp) x max(SMTC period,DRX cycle)) x CSSFintra |
| DRX cycle>320ms | ceil(5 x Kp) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identified  NOTE 2: When *highSpeedMeasFlag-r16* is not configured, M2 = 1.5; When *highSpeedMeasFlag-r16* is configured, M2 = 1.5 if SMTC periodicity > 40 ms;,otherwise M2=1.  NOTE 3: When *highSpeedMeasFlag-r16* is configured, the requirements apply only to UE supporting either *measurementEnhancement-r16* or *intraNR-MeasurementEnhancement-r16* on measurements of the primary component carrier and do not apply to measurements of a secondary component carrier with active SCell.  NOTE 4: When *highSpeedMeasCA-Scell-r17* is configured and UE supports *measurementEnhancementCA-r17*, M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2=1. | |

Table 9.2.5.1-2: Time period for PSS/SSS detection, (Frequency range FR2)

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | max(600ms, ceil(Mpss/sss\_sync\_w/o\_gaps x KFR x Kp x Klayer1\_measurement)x SMTC period)Note 1 x CSSFintra |
| DRX cycle≤ 320ms | max(600ms, ceil(1.5 x Mpss/sss\_sync\_w/o\_gaps x KFR x Kp x Klayer1\_measurement)x max(SMTC period,DRX cycle)) x CSSFintra |
| DRX cycle>320ms | ceil(Mpss/sss\_sync\_w/o\_gaps x KFR x Kp x Klayer1\_measurement) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identified  NOTE 2: KFR is a scaling factor depending on the frequency range and the SSB SCS. For FR2-1, KFR = 1. For FR2-2: KFR = 1 if the SCS of the SSB of the cell being detected is 120 kHz, KFR = 2 if the SCS of the SSB of the cell being detected is 480 kHz, and KFR = 3 if the SCS of the SSB of the cell being detected is 960 kHz. | |

Table 9.2.5.1-3: Time period for time index detection (FR1)

|  |  |
| --- | --- |
| DRX cycle | TSSB\_time\_index\_intra |
| No DRX | max(120ms, ceil( 3 x Kp )x SMTC period)Note 1 x CSSFintra |
| DRX cycle≤ 320ms | max(120ms, ceil (M2 Note 2 x 3 x Kp) x max(SMTC period,DRX cycle)) x CSSFintra |
| DRX cycle>320ms | Ceil(3 x Kp) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identified  NOTE 2: When *highSpeedMeasFlag-r16* is not configured, M2 = 1.5; When *highSpeedMeasFlag-r16* is configured, M2 = 1.5 if SMTC periodicity > 40 ms;,otherwise M2=1  NOTE 3: When *highSpeedMeasFlag-r16* is configured, the requirements apply only to UE supporting either *measurementEnhancement-r16* or *intraNR-MeasurementEnhancement-r16* on measurements of the primary component carrier and do not apply to measurements of a secondary component carrier with active SCell.  NOTE 4: When *highSpeedMeasCA-Scell-r17* is configured and UE supports *measurementEnhancementCA-r17*, M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2=1 | |

Table 9.2.5.1-4: Time period for PSS/SSS detection, deactivated SCell (FR1)

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | Ceil(5 x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 320ms | Ceil(5 x Kp) x max(measCycleSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(5 x Kp) x max(measCycleSCell, DRX cycle) x CSSFintra |
| NOTE 1: The requirements also apply to deactivated SCG SCell. | |

Table 9.2.5.1-5: Time period for PSS/SSS detection, deactivated SCell (FR2)

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | Ceil(Mpss/sss\_sync\_w/o\_gaps x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 320ms | Ceil(Mpss/sss\_sync\_w/o\_gaps x Kp) x max(measCycleSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(Mpss/sss\_sync\_w/o\_gaps x Kp) x max(measCycleSCell, DRX cycle) x CSSFintra |
| NOTE 1: The requirements also apply to deactivated SCG SCell. | |

Table 9.2.5.1-6: Time period for time index detection, deactivated SCell (FR1)

|  |  |
| --- | --- |
| DRX cycle | TSSB\_time\_index\_intra |
| No DRX | Ceil(3 x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 320ms | Ceil(3 x Kp) x max(measCycleSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(3 x Kp) x max(measCycleSCell, DRX cycle) x CSSFintra |
| NOTE 1: The requirements also apply to deactivated SCG SCell. | |

Table 9.2.5.1-7: Void

Table 9.2.5.1-8: Void

Table 9.2.5.1-9: Time period for PSS/SSS detection, deactivated SCell (FR1), when *highSpeedMeasCA-Scell-r17* is configured

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | Ceil(5 x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 320ms | Ceil(5 x Kp) x max(measCycleSCell, M2 Note 1xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(5 x Kp) x max(measCycleSCell, DRX cycle) x CSSFintra |
| NOTE 1: M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2=1 | |

Table 9.2.5.1-10: Time period for time index detection, deactivated SCell (FR1)，when *highSpeedMeasCA-Scell-r17* is configured

|  |  |
| --- | --- |
| DRX cycle | TSSB\_time\_index\_intra |
| No DRX | Ceil(3 x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 320ms | Ceil(3 x Kp) x max(measCycleSCell, M2 Note 1xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(3 x Kp)x max(measCycleSCell, DRX cycle) x CSSFintra |
| NOTE 1: M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2=1 | |

Table 9.2.5.1-11: Time period for PSS/SSS detection when *highSpeedMeasFlagFR2-r17* is configured, (Frequency range FR2) when SMTC period <= 40ms

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | max(600ms, ceil(M1Note 2 x Kp x Klayer1\_measurement)x SMTC period)Note 1 x CSSFintra |
| DRX cycle≤ 80ms | max(600ms, ceil(M1Note 2 x Kp x Klayer1\_measurement)x max(SMTC period,DRX cycle)) x CSSFintra |
| 80ms< DRX cycle≤ 320ms | ceil(1.5x Mpss/sss\_sync\_w/o\_gaps Note 3 x Kp x Klayer1\_measurement)x max(SMTC period,DRX cycle) x CSSFintra |
| DRX cycle>320ms | ceil(Mpss/sss\_sync\_w/o\_gaps Note 3 x Kp x Klayer1\_measurement) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identified  NOTE 2: For UE supporting power class 6, M1= 6 if *highSpeedMeasFlagFR2-r17* = set1 or M1= 18 if *highSpeedMeasFlagFR2-r17* = set2  NOTE 3: Void | |

Table 9.2.5.1-12: Time period for PSS/SSS detection, deactivated PSCell (FR1)

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | Ceil(5 x Kp) x measCyclePSCell x CSSFintra |
| DRX cycle≤ 320ms | Ceil(5 x Kp) x max(measCyclePSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(5 x Kp) x max(measCyclePSCell, DRX cycle) x CSSFintra |

Table 9.2.5.1-13: Time period for PSS/SSS detection, deactivated PSCell (FR2)

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | Ceil(Mpss/sss\_sync\_w/o\_gaps x Kp) x measCyclePSCell x CSSFintra |
| DRX cycle≤ 320ms | Ceil(Mpss/sss\_sync\_w/o\_gaps x Kp) x max(measCyclePSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(Mpss/sss\_sync\_w/o\_gaps x Kp) x max(measCyclePSCell, DRX cycle) x CSSFintra |

Table 9.2.5.1-14: Time period for time index detection, deactivated PSCell (FR1)

|  |  |
| --- | --- |
| DRX cycle | TSSB\_time\_index\_intra |
| No DRX | Ceil(3 x Kp) x measCyclePSCell x CSSFintra |
| DRX cycle≤ 320ms | Ceil(3 x Kp) x max(measCyclePSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(3 x Kp) x max(measCyclePSCell, DRX cycle) x CSSFintra |

Table 9.2.5.1-15: Time period for time index detection (Frequency range FR2-2)

|  |  |
| --- | --- |
| DRX cycle | TSSB\_time\_index\_intra |
| No DRX | max(200ms, ceil(MSSB\_index\_intra x Kp x SMTC period) x CSSFintra |
| DRX cycle≤ 320ms | max(200ms, ceil(1.5 x MSSB\_index\_intra x Kp) x max(SMTC period, DRX cycle) x CSSFintra) |
| DRX cycle>320ms | Ceil(MSSB\_index\_intra x Kp )x DRX cycle x CSSFintra |

## << End of First change >>

## << Start of Second change >>

### 9.2.6 Intra-frequency measurements with measurement gaps

#### 9.2.6.1 Void

#### 9.2.6.2 Intra-frequency cell identification

When a measurement gap is provided or an activated Pre-MG is provided without any pre-MG status changed during the measurement period, the UE shall be able to identify a new detectable intra frequency cell within Tidentify\_intra\_without\_index if UE is not indicated to report SSB based RRM measurement result with the associated SSB index (*reportQuantityRsIndexes* or *maxNrofRSIndexesToReport* is not configured), or the UE has been indicated that the neighbour cell is synchronous with the serving cell (*deriveSSB-IndexFromCell* is enabled). Otherwise UE shall be able to identify a new detectable intra frequency cell within Tidentify\_intra\_with\_index. The UE shall be able to identify a new detectable intra frequency SS block of an already detected cell within Tidentify\_intra\_without\_index. It is assumed that *deriveSSB-IndexFromCell* is always enabled for FR1 TDD and FR2 with SCS smaller or equal to 480 kHz.

Tidentify\_intra\_without\_index = TPSS/SSS\_sync\_intra + T SSB\_measurement\_period\_intra ms

Tidentify\_intra\_with\_index = TPSS/SSS\_sync\_ntra + T SSB\_measurement\_period\_intra + TSSB\_time\_index\_intra ms

Where:

TPSS/SSS\_sync\_intra: it is the time period used in PSS/SSS detection given in table 9.2.6.2-1, 9.2.6.2-2 or 9.2.6.2-9.

- For UE supporting power class 6 with *highSpeedMeasFlagFR2-r17* configured, if SMTC <= 40ms, TPSS/SSS\_sync\_intra is given in Table 9.2.6.2-9; otherwise, TPSS/SSS\_sync\_intra is given in Table 9.2.6.2-2.

TSSB\_time\_index\_intra: it is the time period used to acquire the index of the SSB being measured given in table 9.2.6.2-3 or 9.2.6.2-10 (for FR2-2).

T SSB\_measurement\_period\_intra: equal to a measurement period of SSB based measurement given in table 9.2.6.3-1 or 9.2.6.3-2.

- For UE supporting power class 6 with *highSpeedMeasFlagFR2-r17* configured, if SMTC <= 40ms, TSSB\_measurement\_period\_intra is given in Table 9.2.6.3-4; otherwise, T SSB\_measurement\_period\_intra is given in Table 9.2.6.3-2.

CSSFintra: it is a carrier specific scaling factor and is determined according to CSSFwithin\_gap,i in clause 9.1.5.2 for measurement conducted within measurement gaps.

Kgap is the scaling factor for a SSB frequency layer to be measured within an associated measurement gap pattern. Kgap = 1 when the UE is not configured with concurrent measurement gaps. Otherwise, Kgap = Ntotal / Navailable, where Navailable and Ntotal are calculated as follows:

For a window W of duration max(SMTC period, MGRP\_max), where MGRP\_max is the maximum MGRP across all configured per-UE measurement gap and per-FR measurement gap within the same FR as the SSB frequency layer, and starting from the beginning of any SMTC occasion:

- Ntotal is the total number of SMTC occasions that are covered by instances of the associated measurement gap within the window W, including those overlapped with other measurement gap occasions within the window, and

- Navailable is the number of SMTC occasions that are covered by instances of the non-dropped associated measurement gap within the window W after accounting for measurement gap collisions by applying the measurement gap collision rule in section 9.1.8.3.

When concurrent measurement gaps are configured, requirements in this clause do not apply if Navailable =0.

Mpss/sss\_sync\_with\_gaps : For a UE supporting FR2-1 power class 1 or 5, Mpss/sss\_sync with\_gaps=40. For a UE supporting FR2-1 power class 2, Mpss/sss\_sync with\_gaps =24. For a UE supporting FR2-1 power class 3, Mpss/sss\_sync with\_gaps =24. For a UE supporting FR2-1 power class 4, Mpss/sss\_sync with\_gaps =24. For a UE supporting FR2-1 power class 6, Mpss/sss\_sync with\_gaps = 24 For a UE supporting FR2-2 power class 1, Mpss/sss\_sync with\_gaps = 60. For a UE supporting FR2-2 power class 2, Mpss/sss\_sync with\_gaps = 36. For a UE supporting FR2-2 power class 3, Mpss/sss\_sync with\_gaps = 36.

Mmeas\_period\_ with\_gaps: For a UE supporting FR2-1 power class 1 or 5, Mmeas\_period\_ with\_gaps =40. For a UE supporting FR2-1 power class 2, Mmeas\_period\_ with\_gaps =24. For a UE supporting FR2-1 power class 3, Mmeas\_period\_ with\_gaps =24. For a UE supporting FR2-1 power class 4, Mmeas\_period with\_gaps =24. For a UE supporting FR2-1 power class 6, Mmeas\_period with\_gaps = 24. For a UE supporting FR2-2 power class 1, Mmeas\_period\_ with\_gaps = 60. For a UE supporting FR2-2 power class 2, Mmeas\_period\_ with\_gaps = 36. For a UE supporting FR2-2 power class 3, Mmeas\_period\_ with\_gaps = 36.

- MSSB\_index\_intra: For a UE supporting FR2-2 power class 1, MSSB\_index\_intra = 72. For a UE supporting FR2-2 power class 2, MSSB\_index\_intra = 48. For a UE supporting FR2 power class 3, MSSB\_index\_intra = 48.

If the higher layer signaling in TS 38.331 [2] of *smtc2* is present and smtc1 is fully overlapping with measurement gaps and smtc2 is partially overlapping with measurement gaps, requirements are not specified for Tidentify\_intra\_without\_index or Tidentify\_intra\_with\_index.

If MCG DRX is in use, cell identification requirements for intra-frequency measurement in MCG specified in Table 9.2.6.2-1, Table 9.2.6.2-2, and Table 9.2.6.2-3 shall depend on the MCG DRX cycle. If SCG DRX is in use, cell identification requirements for intra-frequency measurement in SCG specified in Table 9.2.6.2-1, Table 9.2.6.2-2, and Table 9.2.6.2-3 shall depend on the SCG DRX cycle. Otherwise, the requirements for when DRX is not in use shall apply.

Table 9.2.6.2-1: Time period for PSS/SSS detection (FR1)

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | max(600ms, 5 x Kgap x max(MGRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 320ms | max(600ms, ceil(M2Note 1x 5 x Kgap) x max(MGRP, SMTC period,DRX cycle)) x CSSFintra |
| DRX cycle>320ms | Ceil( 5 x Kgap ) x max(MGRP, DRX cycle) x CSSFintra |
| NOTE 1: When *highSpeedMeasFlag-r16* is not configured, M2 = 1.5; When *highSpeedMeasFlag-r16* is configured, M2 = 1.5 if SMTC periodicity > 40 ms, otherwise M2=1.  NOTE 2: When *highSpeedMeasFlag-r16* is configured, the requirements apply only to UE supporting either *measurementEnhancement-r16* or *intraNR-MeasurementEnhancement-r16* on measurements of the primary component carrier and do not apply to measurements of a secondary component carrier with active SCell.  NOTE 3: For a UE supporting concurrent measurement gaps, if multiple concurrent gaps are configured, the MGRP is the periodicity of the MG pattern associated to the intra-frequency layer.  NOTE 4: When highSpeedMeasCA-Scell-r17 is configured, the requirements apply to UE on measurements of secondary component carrier with active SCell. | |

**Table 9.2.6.2-2: Time period for PSS/SSS detection (FR2)**

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | max(600ms, Mpss/sss\_sync\_with\_gaps x KFR x Kgap x max(MGRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 320ms | max(600ms, ceil(1.5x Mpss/sss\_sync\_with\_gaps x KFR x Kgap) x max(MGRP, SMTC period, DRX cycle))x CSSFintra |
| DRX cycle>320ms | Ceil(Mpss/sss\_sync\_with\_gaps x KFR x Kgap ) x max(MGRP, DRX cycle) x CSSFintra |
| NOTE 1: For a UE supporting concurrent gaps, if multiple concurrent gaps are configured, the MGRP is the periodicity of the MG pattern associated to the intra-frequency layer.  NOTE 2: KFR is a scaling factor depending on the frequency range and the SSB SCS. For FR2-1, KFR = 1. For FR2-2: KFR = 1 if the SCS of the SSB of the cell being detected is 120 kHz, KFR = 2 if the SCS of the SSB of the cell being detected is 480 kHz, and KFR = 3 if the SCS of the SSB of the cell being detected is 960 kHz. | |

Table 9.2.6.2-3: Time period for time index detection (Frequency range FR1)

|  |  |
| --- | --- |
| DRX cycle | TSSB\_time\_index\_intra |
| No DRX | max(120ms, ceil(3 x Kgap ) x max(MGRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 320ms | max(120ms, ceil(M2Note 1x 3 x Kgap) x max(MGRP, SMTC period,DRX cycle) x CSSFintra) |
| DRX cycle>320ms | Ceil(3 x Kgap )x max(MGRP, DRX cycle) x CSSFintra |
| NOTE 1: When *highSpeedMeasFlag-r16* is not configured, M2 = 1.5; When *highSpeedMeasFlag-r16* is configured, M2 = 1.5 if SMTC periodicity > 40 ms, otherwise M2=1.  NOTE 2: When *highSpeedMeasFlag-r16* is configured, the requirements apply only to UE supporting either *measurementEnhancement-r16* or *intraNR-MeasurementEnhancement-r16* on measurements of the primary component carrier and do not apply to measurements of a secondary component carrier with active SCell.  NOTE 3: For a UE supporting concurrent gaps, if multiple concurrent gaps are configured, the MGRP is the periodicity of the MG pattern associated to the intra-frequency layer.  NOTE 4: When highSpeedMeasCA-Scell-r17 is configured, the requirements apply to UE on measurements of secondary component carrier with active SCell. | |

Table 9.2.6.2-7: Void

Table 9.2.6.2-8: Void

Table 9.2.6.2-8: Void

Table 9.2.6.2-9: Time period for PSS/SSS detection when *highSpeedMeasFlagFR2-r17* is configured, (FR2) when SMTC period <=40ms

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | max(600ms, M1Note 2 x Kgap x max(MGRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 80ms | max(600ms, ceil(M1Note2 x Kgap) x max(MGRP, SMTC period, DRX cycle))x CSSFintra |
| 80ms< DRX cycle≤ 320ms | max(600ms, ceil(Mpss/sss\_sync\_with\_gaps x Kgap) x max(MGRP, SMTC period, DRX cycle))x CSSFintra |
| DRX cycle>320ms | Ceil(Mpss/sss\_sync\_with\_gaps x Kgap ) x max(MGRP, DRX cycle) x CSSFintra |
| NOTE 1: For a UE supporting concurrent gaps, if multiple concurrent gaps are configured, the MGRP is the periodicity of the MG pattern associated to the intra-frequency layer.  NOTE 2: For UE supporting power class 6, M1= 6 if *highSpeedMeasFlagFR2-r17* = set1 or M1= 18 if *highSpeedMeasFlagFR2-r17* = set2  NOTE 3: Void | |

Table 9.2.6.2-10: Time period for time index detection (Frequency range FR2-2)

|  |  |
| --- | --- |
| DRX cycle | TSSB\_time\_index\_intra |
| No DRX | max(200ms, ceil(MSSB\_index\_intra x Kgap x max(MGRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 320ms | max(200ms, ceil(1.5 x MSSB\_index\_intra x Kgap) x max(MGRP, SMTC period, DRX cycle) x CSSFintra) |
| DRX cycle>320ms | Ceil(MSSB\_index\_intra x Kgap)x DRX cycle x CSSFintra |

## << End of Second change >>

## << Start of Third change >>

##### A.7.5.8.3.1 NR PCell FR2 HST active TCI state switch for a known TCI state

###### A.7.5.8.3.1.1 Test Purpose and Environment

The purpose of this test is to verify the active TCI state switch delay requirement defined in clause 8.10.3A for FR2 power class 6 UE. Supported test configuration is shown in Table A.7.5.8.3.1.1-1. Furthermore, the purpose of this test is also to verify the one shot large timing adjustment requirement specified in clause 7.1.2.3 provided *highSpeedMeasFlagFR2-r17* is configured and *highSpeedLargeOneStepUL-TimingFR2-r17* is enabled for UE supporting FR2 power class 6 and *largeOneStepUL-timingFR2-r17* capability.

The test scenario comprises of one NR PCell (Cell 1) as given in Table A.7.5.8.3.1.1-2. Cell-specific parameters of NR PCell are specified in Table A.7.5.8.3.1.1-3 below. The OTA related test parameters for FR2 are shown in Table A.7.5.8.3.1.1-4. During the test, *highSpeedMeasFlagFR2-r17* is configured to be *set2* and broadcast to UE.

PDCCHs indicating new transmissions shall be sent continuously on PCell to ensure that the UE would have ACK/NACK sending.

Before the test starts,

- UE is connected to Cell 1 (PCell) on radio channel 1 (PCC).

- UE is configured with 2 different TCI states for PCell, PDCCH TCI state 0 (QCL’d to SSB0) and TCI state 1 (QCL’d to SSB1), in Cell 1 before starting the test.

- UE is indicated in TCI state 0 as the active PDCCH TCI state

The test consists of two time periods, T1 and T2. Figure A.7.5.8.3.1.1-1 and Figure A.7.5.8.3.1.1-2 show the Time multiplexed (allocation in Frequency is symbolic) downlink transmissions from each Angle of Arrival. During T1 only SSB to which PDCCH-TCI-state0 is QCL’d is transmitted. At the beginning of T2, the SSB corresponding to TCI state 1 starts transmitting, which has the relative timing delay compared to TCI state 0 by the absolute value of the one-way differential propagation time . The UE is configured to provide periodic L1-RSRP reports. In slot n which is within 1280 ms of UE providing L1-RSRP report with results for both SSB0 and SSB1, UE receives a MAC-CE command indicating a switch to TCI state 1. *tci-PresentInDCI* is not configured in the PDSCH configuration, i.e. TCI state for the PDSCH is identical to the PDCCH TCI state. After the TCI state switch, the UE transmit timing accuracy shall be measured by the test equipment by using the SRS defined in Table A.7.5.8.3.1-5.

The test equipment verifies that

- UE can be scheduled on PCell on TCI state 0 till n+ THARQ +3 ms.

- the TCI state switch time in PCell by scheduling the UE on TCI state 1 after slot n + THARQ + 3 ms + Tfirst-SSB + TSSB-proc + Trs + Trs-proc.

- the UE transmission timing immediately after TCI state switch shall follow the requirements as specified in clause 7.1.2.3.

Table A.7.5.8.3.1.1-1: Supported test configurations

|  |  |
| --- | --- |
| Config | Description |
| 1 | NR 120 kHz SSB SCS, 100 MHz bandwidth, TDD duplex mode |

Table A.7.5.8.3.1.1-2: General test parameters for TCI state switch

|  |  |  |  |
| --- | --- | --- | --- |
| Parameter | Unit | Value | Comment |
| NR RF Channel Number |  | 1 | One NR radio channel is used for this test |
| Active PCell |  | Cell 1 | PCell on RF channel number 1. |
| CP length |  | Normal |  |
| DRX |  | OFF |  |
| T1 | s | 0.2 |  |
| T2 | s | 0.2 |  |

Table A.7.5.8.3.1.1-3: NR Cell specific test parameters for TCI state switch

|  |  |  |
| --- | --- | --- |
| Parameter | Unit | Cell 1 |
| Frequency Range |  | FR2 |
| Duplex mode |  | TDD |
| TDD configuration |  | TDDConf.3.1 |
| BWchannel |  | 100 MHz: NRB,c = 66 |
| Data RBs allocated |  | 24 |
| Initial DL BWP Configuration |  | DLBWP.0.2 |
| Dedicated DL BWP Configuration |  | DLBWP.1.1 |
| Initial UL BWP Configuration |  | ULBWP.0.2 |
| Dedicated UL BWP Configuration |  | ULBWP.1.1 |
| PDSCH Reference measurement channel |  | SR.3. 2 TDD |
| RMSI CORESET parameters |  | CR.3.1 TDD |
| Dedicated CORESET parameters |  | CCR.3.1 TDD |
| OCNG Patterns |  | OP. 5 |
| SSB Configuration |  | SSB.1 FR2 |
| SMTC Configuration |  | SMTC.1 |
| TCI State 0 |  | TCI.State.0 |
| TCI State 1 |  | TCI.State.1 |
| TRS Configuration |  | TRS.2.1 TDD |
| Correlation Matrix and Antenna Configuration |  | 1x2 |
| EPRE ratio of PSS to SSS | dB | 0 |
| EPRE ratio of PBCH DMRS to SSS |  |  |
| EPRE ratio of PBCH to PBCH DMRS |  |  |
| EPRE ratio of PDCCH DMRS to SSS |  |  |
| EPRE ratio of PDCCH to PDCCH DMRS |  |  |
| EPRE ratio of PDSCH DMRS to SSS |  |  |
| EPRE ratio of PDSCH to PDSCH |  |  |
| EPRE ratio of OCNG DMRS to SSS(Note 1) |  |  |
| EPRE ratio of OCNG to OCNG DMRS (Note 1) |  |  |
| Propagation Condition |  | AOA1: No external noise (Note 2)  AOA2: No external noise (Note 2) with 9722 Hz frequency offset |
| Note 1: OCNG shall be used such that a constant total transmitted power spectral density is achieved for all OFDM symbols.  Note 2: The downlink connection between the System Simulator and the UE is without Additive White Gaussian Noise, and has no fading or multipath effects as specified in TS 38.521-2 B.0 [38]. | | |

Table A.7.5.8.3.1.1-4: OTA related test parameters for TCI state switch

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| Parameter | Unit | Cell 1 | | | | |
|  |  | SSB0 | | SSB1 | | |
|  |  | T1 | T2 | T1 | | T2 |
| Angle of arrival configuration |  | Setup 3 according to clause A.3.15.3 | | | | |
|  |  | AoA1 | | | AoA2 | |
| Assumption for UE beams Note 6 |  | Rough | | | | |
| Ês | dBm/SCS | -80.6 | -80.6 | -Infinity | | -80.6 |
| SS B\_RP Note 2 | dBm/ SCS | -80.6 | -80.6 | -Infinity | | -80.6 |
| BB Note 7 | dB | 8.3 | 8.3 | -Infinity | | 8.3 |
| IoNote2 | dBm/95.04 MHz Note4 | -55.41 | -55.41 | - Infinity | | -55.41 |
| Note 1: Void  Note 2: SS B\_RP and Io levels have been derived from other parameters for information purposes. They are not settable parameters themselves.  Note 3: Void  Note 4: Equivalent power received by an antenna with 0 dBi gain at the centre of the quiet zone  Note 5: As observed with 0dBi gain antenna at the center of the quiet zone.  Note 6: Information about types of UE beam is given in B.2.1.3 and does not limit UE implementation or test system implementation.  Note 7: Calculation of Es/IotBB includes the effect of UE internal noise up to the value assumed for the associated Refsens requirement in clause 7.3.2 of TS 38.101-2 [19], and an allowance of 1dB for UE multi-band relaxation factor ΔMBP from TS 38.101-2 [19] Table 6.2.1.6-4. | | | | | | |

Table A.7.5.8.3.1.1-5: Sounding Reference Symbol Configuration

|  |  |  |
| --- | --- | --- |
| Field | Value | Comment |
| c-SRS | 16 | Frequency hopping is disabled |
| b-SRS | 0 |  |
| b-hop | 0 |  |
| freqDomainPosition | 0 | Frequency domain position of SRS |
| freqDomainShift | 0 |  |
| groupOrSequenceHopping | neither | No group or sequence hopping |
| SRS-PeriodicityAndOffset | sl5=0 | Once every 5 slots |
| pathlossReferenceRS | ssb-Index=0 | SSB #0 is used for SRS path loss estimation |
| usage | Codebook | Codebook based UL transmission |
| startPosition | 0 | resourceMapping setting. SRS on last symbol of slot, and 1symbols for SRS without repetition. |
| nrofSymbols | n1 |  |
| repetitionFactor | n1 |  |
| combOffset-n2 | 0 | transmissionComb setting |
| cyclicShift-n2 | 0 |  |
| nrofSRS-Ports | port1 | Number of antenna ports used for SRS transmission |
| Note: For further information see clause 6.3.2 in TS 38.331 [2]. | | |



Figure A.7.5.8.3.1.1-1: Time multiplexed downlink transmissions during T1



Figure A.7.5.8.3.1.1-2: Time multiplexed downlink transmissions during T2

###### A.7.5.8.3.1.2 Test Requirements

During T2, UE shall send L1-RSRP report with results for both SSB0 and SSB1.

After the TCI state switch, the UE transmission timing immediately after TCI state switch shall follow the requirements as specified in clause 7.1.2.3.

After receiving MAC-CE command in slot n, UE shall:

- be able to continue to receive on TCI state 0 till n + THARQ + 3 ms

- be able to start receiving on TCI state 1 after n + THARQ +7 ms + Tfirst-SSB + Trs

## << End of Third change >>

## << Start of Forth change >>

## B.1.2 Conditions for measurements on NR intra-frequency cells for cell re-selection

This clause defines the following conditions for NR intra-frequency measurements performed based on SSBs for cell re-selection: SSB\_RP and SSB Ês/Iot, applicable for a corresponding operating band.

The conditions are defined in Table B.1.2-1 for FR1 NR cells.

The conditions are defined in Table B.1.2-2 for FR2 NR cells.

Table B.1.2-1: Conditions for intra-frequency cell re-selection in FR1

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Parameter | NR operating band groups Note1 | Minimum SSB\_RP | | SSB Ês/Iot |
| dBm / SCSSSB | | dB |
| SCSSSB = 15 kHz | SCSSSB = 30 kHz |
| **Conditions** | NR\_FDD\_FR1\_A, NR\_TDD\_FR1\_A | -124 | -121 | ≥ -4 |
| NR\_FDD\_FR1\_B | -123.5 | -120.5 |
| NR\_TDD\_FR1\_C | -123 | -120 |
| NR\_FDD\_FR1\_D, NR\_TDD\_FR1\_D | -122.5 | -119.5 |
| NR\_FDD\_FR1\_E, NR\_TDD\_FR1\_E | -122 | -119 |
| NR\_FDD\_FR1\_F | -121.5 | -118.5 |
| NR\_FDD\_FR1\_G, NR\_TDD\_FR1\_G | -121 | -118 |
| NR\_FDD\_FR1\_H | -120.5 | -117.5 |
| NOTE 1: NR operating band groups are defined in clause 3.5.2. | | | | |

Table B.1.2-2: Conditions for intra-frequency cell re-selection in FR2

|  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| Parameter | Angle of arrival | NR operating bands | Minimum SSB\_RP Note 2, Note 3 | | | | | | | | SSB Ês/Iot |
|  |  |  | dBm / SCSSSB | | | | | | | | dB |
|  |  |  | SCSSSB = 120 kHz | | | | | | SCSSSB = 240 kHz | |  |
|  |  |  | UE Power class | | | | | | UE Power class | |  |
|  |  |  | 1 | 2 | 3 | 4 | 5 | 6 | 1, 2, 3, 4, 5 | |  |
| Conditions | Rx Beam Peak | n257 | -107.3 | -110.8 | -109.1 | -124.8+Y4 | -104.9 | -104.9 | (Value for SCSSSB = 120 kHz) +3dB | | ≥-4 |
|  |  | n258 | -107.3 | -110.8 | -109.1 | -124.8+Y4 | -105.1 | -105.1 |  | |  |
|  |  | n259 |  |  | -105.5 |  | -102.0 |  |  | |  |
|  |  | n260 | -104.3 |  | -106.5 | -122.8+Y4 |  |  |  | |  |
|  |  | n261 | -107.3 | -110.8 | -109.1 | -124.8+Y4 |  | -104.9 |  | |  |
| n262 | -102.3 | -105.6 | -103.6 | -118.8+Y4 |  |  |
|  | Spherical coverage Note 1 | n257 | -99.3 | -99.8 | -98.2 | -115.8+Z4 | -96.9 | -96.9 | (Value for SCSSSB = 120 kHz) +3dB | | ≥-4 |
|  | n259 |  |  | -92.7 |  |  |  |
|  | n258 | -99.3 | -99.8 | -98.2 | -115.8+Z4 | -97.1 | -97.1 |
|  | n260 | -96.3 |  | -93.9 | -110.8+Z4 |  |  |
|  | n261 | -99.3 | -99.8 | -98.2 | -115.8+Z4 |  | -96.9 |
| n262 | -94.1 | -93.7 | -90.5 | -106.7+Z4 |  |  |
| NOTE 1: Values based on EIS spherical coverage as defined in clause 7.3.4 of TS 38.101-2 [19]. Side condition applies for directions in which EIS spherical coverage requirement is met.  NOTE 2: Values specified at the Reference point to give minimum SSB Ês/Iot, with no applied noise.  NOTE 3: For UEs that support multiple FR2 bands, Rx Beam Peak values are increased by ∆MBP,n and Spherical coverage values are increased by ∆MBS,n, the UE multi-band relaxation factor in dB specified in clause 6.2.1 of TS 38.101-2 [19]. | | | | | | | | | | | |

*Editor’s notes for Table B.1.2-2:*

*- The value of Y for Power class 4 is FFS, where Y4 is the rough/fine beam gain difference in Rx beam peak direction for Power class 4*

*- The value of Z for Power class 4 is FFS, where Z4 is the rough/fine beam gain difference in spherical coverage directions for Power class 4.*

## << End of Forth change >>