**3GPP TSG-RAN WG4 Meeting # 112 *R4-2411836***

**Maastricht, Netherlands, August 19 – 23, 2024**

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| *CR-Form-v12.3* | | | | | | | | |
| **CHANGE REQUEST** | | | | | | | | |
|  | | | | | | | | |
|  | **38.101-3** | **CR** | **<CR#>** | **rev** | **-** | **Current version:** | **18.6.0** |  |
|  | | | | | | | | |
| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* | | | | | | | | |
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| ***Proposed change affects:*** | UICC apps |  | ME | **X** | Radio Access Network |  | Core Network |  |

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|  | | | | | | | | | | |
| ***Title:*** |  | | | | | | | | | |
|  |  | | | | | | | | | |
| ***Source to WG:*** | ZTE Corporation, Sanechips | | | | | | | | | |
| ***Source to TSG:*** | R4 | | | | | | | | | |
|  |  | | | | | | | | | |
| ***Work item code:*** | DC\_R19\_xBLTE\_yBNR-Core | | | | |  | ***Date:*** | | | 2024-08-07 |
|  |  | | | |  | |  | | |  |
| ***Category:*** | **B** |  | | | | | ***Release:*** | | | Rel-19 |
|  | *Use one of the following categories:* ***F*** *(correction)* ***A*** *(mirror corresponding to a change in an earlier release)* ***B*** *(addition of feature),* ***C*** *(functional modification of feature)* ***D*** *(editorial modification)*  Detailed explanations of the above categories can be found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | | | | | | | | *Use one of the following releases: Rel-8 (Release 8) Rel-9 (Release 9) Rel-10 (Release 10) Rel-11 (Release 11) … Rel-17 (Release 17) Rel-18 (Release 18) Rel-19 (Release 19)  Rel-20 (Release 20)* | |
|  |  | | | | | | | | | |
| ***Reason for change:*** | | Introduce the following band combination.   * DC\_7C-20A\_n28A   Note that the fallback band combinations have already been supported in the current spec. | | | | | | | | |
|  | |  | | | | | | | | |
| ***Summary of change:*** | | Introduce the inter-band EN-DC configuration within FR1 for DC\_7C-20A\_n28A. | | | | | | | | |
|  | |  | | | | | | | | |
| ***Consequences if not approved:*** | | The mentioned new configuration for three-band EN-DC will not be supported in Rel-19. | | | | | | | | |
|  | |  | | | | | | | | |
| ***Clauses affected:*** | | 5.5B.4.2 | | | | | | | | |
|  | |  | | | | | | | | |
|  | | **Y** | **N** |  | | | |  | | |
| ***Other specs*** | |  | **X** | Other core specifications | | | | TS/TR ... CR ... | | |
| ***affected:*** | | **X** |  | Test specifications | | | | TS/TR ... CR ... 38.521-3 | | |
| ***(show related CRs)*** | |  | **X** | O&M Specifications | | | | TS/TR ... CR ... | | |
|  | |  | | | | | | | | |
| ***Other comments:*** | |  | | | | | | | | |
|  | |  | | | | | | | | |
| ***This CR's revision history:*** | |  | | | | | | | | |

### *<< Start of changes >>*

#### 5.5B.4.2 Inter-band EN-DC configurations within FR1 (three bands)

Table 5.5B.4.2-1: Inter-band EN-DC configurations within FR1 (three bands)

| **EN-DC**  **configuration** | **Uplink EN-DC**  **configuration**  **(NOTE 1)** |
| --- | --- |
| Unchanged configurations omitted | |
| DC\_7A\_n1A-n8A | DC\_7A\_n1A  DC\_7A\_n8A |
| DC\_7A-7A\_n1A-n8A | DC\_7A\_n1A  DC\_7A\_n8A |
| DC\_7A\_n1A-n28A | DC\_7A\_n1A  DC\_7A\_n28A |
| DC\_7C\_n1A-n28A | DC\_7A\_n1A  DC\_7A\_n28A  DC\_7C\_n1A  DC\_7C\_n28A |
| DC\_7A\_n1A-n40A | DC\_7A\_n1A  DC\_7A\_n40A |
| DC\_7A\_n1A-n75A | DC\_7A\_n1A |
| DC\_7A\_n1A-n78A5, 14  DC\_7C\_n1A-n78A5 | DC\_7A\_n1A  DC\_7A\_n78A14  DC\_7C\_n1A  DC\_7C\_n78A |
| DC\_7A\_n1A-n78(2A)5  DC\_7C\_n1A-n78(2A)5 | DC\_7A\_n1A  DC\_7A\_n78A  DC\_7C\_n1A  DC\_7C\_n78A |
| DC\_7A-7A\_n1A-n78A5, 14 | DC\_7A\_n1A  DC\_7A\_n78A14 |
| DC\_7A\_n2A-n66A | DC\_7A\_n2A  DC\_7A\_n66A |
| DC\_7A\_n2A-n71A | DC\_7A\_n2A  DC\_7A\_n71A |
| DC\_7A\_n2A-n77A | DC\_7A\_n2A  DC\_7A\_n77A |
| DC\_7A\_n2A-n78A | DC\_7A\_n2A  DC\_7A\_n78A |
| DC\_7A\_n3A-n78A  DC\_7C\_n3A-n78A | DC\_7A\_n3A  DC\_7A\_n78A  DC\_7C\_n3A  DC\_7C\_n78A |
| DC\_7A\_n3A-n78(2A)  DC\_7C\_n3A-n78(2A) | DC\_7A\_n3A  DC\_7A\_n78A  DC\_7C\_n3A  DC\_7C\_n78A |
| DC\_7A\_n5A-n40A | DC\_7A\_n5A DC\_7A\_n40A |
| DC\_7A\_n5A-n78A14  DC\_7C\_n5A-n78A14 | DC\_7A\_n5A  DC\_7C\_n5A  DC\_7A\_n78A14  DC\_7C\_n78A14 |
| DC\_7A\_n7A-n78A5 | DC\_7A\_n78A  DC\_7A\_n7A2 |
| DC\_7A\_n7A-n78(2A) | DC\_7A\_n78A  DC\_7A\_n7A2 |
| DC\_7A-8A\_n1A  DC\_7A-8B\_n1A | DC\_7A\_n1A  DC\_8A\_n1A |
| DC\_7A-7A-8A\_n1A  DC\_7A-7A-8B\_n1A | DC\_7A\_n1A  DC\_8A\_n1A |
| DC\_7A-8A\_n3A | DC\_7A\_n3A  DC\_8A\_n3A |
| DC\_7A-8A\_n7A | DC\_7A\_n7A  DC\_8A\_n7A |
| DC\_7A-8A\_n20A | DC\_7A\_n20A  DC\_8A\_n20A |
| DC\_7A-8A\_n28A | DC\_7A\_n28A  DC\_8A\_n28A |
| DC\_7A-7A-8A\_n28A | DC\_7A\_n28A  DC\_8A\_n28A |
| DC\_7A-8A\_n40A | DC\_7A\_n40A  DC\_8A\_n40A |
| DC\_7A\_n8A-n40A | DC\_7A\_n8A  DC\_7A\_n40A |
| DC\_7A-8A\_n77A5 | DC\_7A\_n77A  DC\_8A\_n77A |
| DC\_7A-8A\_n78A5, 14 | DC\_7A\_n78A14  DC\_8A\_n78A14 |
| DC\_7A-8A\_n78(2A) | DC\_7A\_n78A  DC\_8A\_n78A |
| DC\_7A-7A-8A\_n78A5, 14 | DC\_7A\_n78A14  DC\_8A\_n78A14 |
| DC\_7A-7A\_n8A-n78A5, 14 | DC\_7A\_n8A  DC\_7A\_n78A14 |
| DC\_7A-8B\_n78A5, 14  DC\_7A-7A-8B\_n78A5, 14 | DC\_7A\_n78A14  DC\_8A\_n78A14  DC\_8B\_n78A |
| DC\_7A\_n8A-n78A5, 14 | DC\_7A\_n8A  DC\_7A\_n78A14 |
| DC\_7A-12A\_n2A | DC\_7A\_n2A  DC\_12A\_n2A |
| DC\_7A-12A\_n2(2A) | DC\_7A\_n2A  DC\_12A\_n2A |
| DC\_7A-12A\_n25A | DC\_7A\_n25A  DC\_12A\_n25A |
| DC\_7A-12A\_n66A | DC\_7A\_n66A  DC\_12A\_n66A |
| DC\_7A-12A\_n77A | DC\_7A\_n77A  DC\_12A\_n77A |
| DC\_7A-12A\_n77(2A) | DC\_7A\_n77A  DC\_12A\_n77A |
| DC\_7A\_n12A-n77A | DC\_7A\_n12A  DC\_7A\_n77A |
| DC\_7A-12A\_n78A | DC\_7A\_n78A  DC\_12A\_n78A |
| DC\_7A-12A\_n78(2A) | DC\_7A\_n78A  DC\_12A\_n78A |
| DC\_7A\_n12A-n78A | DC\_7A\_n12A  DC\_7A\_n78A |
| DC\_7A-13A\_n25A  DC\_7C-13A\_n25A | DC\_7A\_n25A  DC\_13A\_n25A |
| DC\_7A-7A-13A\_n25A | DC\_7A\_n25A  DC\_13A\_n25A |
| DC\_7A-13A\_n66A  DC\_7C-13A\_n66A | DC\_7A\_n66A  DC\_13A\_n66A |
| DC\_7A-7A-13A\_n66A | DC\_7A\_n66A  DC\_13A\_n66A |
| DC\_7A-20A\_n1A  DC\_7C-20A\_n1A | DC\_7A\_n1A  DC\_7C\_n1A  DC\_20A\_n1A |
| DC\_7A-20A\_n3A  DC\_7C-20A\_n3A | DC\_7A\_n3A  DC\_7C\_n3A  DC\_20A\_n3A |
| DC\_7A-20A\_n8A | DC\_7A\_n8A  DC\_20A\_n8A |
| DC\_7A-20A\_n28A16,20  DC\_7C-20A\_n28A16,20 | DC\_7A\_n28A  DC\_20A\_n28A |
| DC\_7A-20A\_n78A5  DC\_7A-20A\_n78C5 | DC\_7A\_n78A  DC\_20A\_n78A |
| DC\_7A-7A-20A\_n78A5 | DC\_7A\_n78A  DC\_20A\_n78A |
| DC\_7A-20A\_n78(2A)5 | DC\_7A\_n78A  DC\_20A\_n78A |
| DC\_7A\_n25A-n66A | DC\_7A\_n25A DC\_7A\_n66A |
| DC\_7A-7A\_n25A-n66A | DC\_7A\_n25A DC\_7A\_n66A |
| DC\_7C\_n25A-n66A | DC\_7A\_n25A DC\_7A\_n66A |
| DC\_7A\_n25A-n71A | DC\_7A\_n25A  DC\_7A\_n71A |
| DC\_7A-25A\_n77A  DC\_7C-25A\_n77A | DC\_7A\_n77A  DC\_25A\_n77A |
| DC\_7A-7A-25A\_n77A | DC\_7A\_n77A  DC\_25A\_n77A |
| DC\_7A-25A-25A\_n77A  DC\_7C-25A-25A\_n77A | DC\_7A\_n77A  DC\_25A\_n77A |
| DC\_7A-7A-25A-25A\_n77A | DC\_7A\_n77A  DC\_25A\_n77A |
| DC\_7A-25A\_n78A  DC\_7C-25A\_n78A | DC\_7A\_n78A  DC\_25A\_n78A |
| DC\_7A-7A-25A\_n78A | DC\_7A\_n78A  DC\_25A\_n78A |
| DC\_7A-25A-25A\_n78A  DC\_7C-25A-25A\_n78A | DC\_7A\_n78A  DC\_25A\_n78A |
| DC\_7A-7A-25A-25A\_n78A | DC\_7A\_n78A  DC\_25A\_n78A |
| DC\_7A-26A\_n78A  DC\_7C-26A\_n78A | DC\_7A\_n78A  DC\_26A\_n78A |
| DC\_7A-26A\_n78(2A)  DC\_7C-26A\_n78(2A) | DC\_7A\_n78A  DC\_26A\_n78A |
| DC\_7A\_n26A-n78A  DC\_7A\_n26A-n78(2A) | DC\_7A\_n26A DC\_7A\_n78A |
| DC\_7C\_n26A-n78A  DC\_7C\_n26A-n78(2A) | DC\_7A\_n26A  DC\_7C\_n26A  DC\_7A\_n78A  DC\_7C\_n78A |
| DC\_7A-28A\_n1A | DC\_28A\_n1A  DC\_7A\_n1A |
| DC\_7A-7A-28A\_n1A | DC\_28A\_n1A  DC\_7A\_n1A |
| DC\_7A-28A\_n2A | DC\_7A\_n2A  DC\_28A\_n2A |
| DC\_7A-28A\_n3A  DC\_7C-28A\_n3A | DC\_7A\_n3A  DC\_7C\_n3A  DC\_28A\_n3A |
| DC\_7A-28A\_n5A6  DC\_7C-28A\_n5A6 | DC\_7A\_n5A  DC\_7C\_n5A  DC\_28A\_n5A |
| DC\_7A-28A\_n7A | DC\_7A\_n7A2  DC\_28A\_n7A |
| DC\_7A-28A\_n20A | DC\_7A\_n20A  DC\_28A\_n20A |
| DC\_7A\_n28A-n40A | DC\_7A\_n28A  DC\_7A\_n40A |
| DC\_7A-28A\_n40A | DC\_7A\_n40A  DC\_28A\_n40A |
| DC\_7A-28A\_n66A  DC\_7C-28A\_n66A | DC\_7A\_n66A  DC\_28A\_n66A |
| DC\_7A-28A\_n78A5,14  DC\_7C-28A\_n78A5,14  DC\_7A-28A\_n78(2A)5,14  DC\_7C-28A\_n78(2A)5,14 | DC\_7A\_n78A14  DC\_7C\_n78A14  DC\_28A\_n78A14 |
| DC\_7A\_n28A-n78A5,14  DC\_7C\_n28A-n78A14 | DC\_7A\_n28A  DC\_7A\_n78A14  DC\_7C\_n28A  DC\_7C\_n78A14 |
| DC\_7A-29A\_n78A  DC\_7C-29A\_n78A | DC\_7A\_n78A |
| DC\_7A-7A-29A\_n78A | DC\_7A\_n78A |
| DC\_7A-32A\_n1A | DC\_7A\_n1A |
| DC\_7A-32A\_n3A  DC\_7C-32A\_n3A | DC\_7A\_n3A |
| DC\_7A-32A\_n8A | DC\_7A\_n8A |
| DC\_7A-32A\_n28A | DC\_7A\_n28A |
| DC\_7A-32A\_n78A | DC\_7A\_n78A |
| DC\_7A-40A\_n1A  DC\_7A-40C\_n1A | DC\_7A\_n1A  DC\_40A\_n1A |
| DC\_7A\_n40A-n77A | DC\_7A\_n40A  DC\_7A\_n77A |
| DC\_7A\_n40A-n77(2A) | DC\_7A\_n40A  DC\_7A\_n77A |
| DC\_7A-7A\_n40A-n77A | DC\_7A\_n40A  DC\_7A\_n77A |
| DC\_7A-7A\_n40A-n77(2A) | DC\_7A\_n40A  DC\_7A\_n77A |
| DC\_7A-40A\_n78A  DC\_7A-40C\_n78A | DC\_7A\_n78A  DC\_40A\_n78A |
| DC\_7A-40A\_n78(2A)  DC\_7A-40C\_n78(2A) | DC\_7A\_n78A  DC\_40A\_n78A |
| DC\_7A\_n40A-n78A  DC\_7A\_n40A-n78C | DC\_7A\_n40A  DC\_7A\_n78A |
| DC\_7A-7A\_n40A-n78A  DC\_7A-7A\_n40A-n78C | DC\_7A\_n40A  DC\_7A\_n78A |
| DC\_7A\_n40A-n105A | DC\_7A\_n40A  DC\_7A\_n105A |
| DC\_7A-46A\_n78A3  DC\_7A-46C\_n78A3  DC\_7A-46D\_n78A3  DC\_7A-46E\_n78A3 | DC\_7A\_n78A |
| DC\_7A-66A\_n2A  DC\_7A-66A\_n2(2A) | DC\_7A\_n2A  DC\_66A\_n2A |
| DC\_7A-66A\_n5A  DC\_7C-66A\_n5A  DC\_7A-66A-66A\_n5A  DC\_7C-66A-66A\_n5A  DC\_7A-7A-66A\_n5A  DC\_7A-7A-66A-66A\_n5A | DC\_7A\_n5A  DC\_66A\_n5A |
| DC\_7A-66A\_n7A | DC\_7A\_n7A2  DC\_66A\_n7A |
| DC\_7A-66A-66A\_n7A | DC\_7A\_n7A2  DC\_66A\_n7A |
| DC\_7A-66A\_n12A | DC\_7A\_n12A  DC\_66A\_n12A |
| DC\_7A-66A\_n25A  DC\_7C-66A\_n25A | DC\_7A\_n25A  DC\_66A\_n25A |
| DC\_7A-7A-66A\_n25A | DC\_7A\_n25A  DC\_66A\_n25A |
| DC\_7A-66A\_n28A | DC\_7A\_n28A  DC\_66A\_n28A |
| DC\_7A-66A\_n66A  DC\_7C-66A\_n66A | DC\_7A\_n66A  DC\_66A\_n66A2 |
| DC\_7A-(n)66AA  DC\_7C-(n)66AA | DC\_7A\_n66A  DC\_(n)66AA2 |
| DC\_7A-7A-(n)66AA | DC\_7A\_n66A  DC\_(n)66AA2 |
| DC\_7A-7A-66A\_n66A | DC\_7A\_n66A  DC\_66A\_n66A2 |
| DC\_7A-66A-66A\_n66A | DC\_7A\_n66A  DC\_66A\_n66A2 |
| DC\_7A-66A-(n)66AA | DC\_7A\_n66A  DC\_(n)66AA2  DC\_66A\_n66A2 |
| DC\_7A-7A-66A-(n)66AA | DC\_7A\_n66A  DC\_(n)66AA2  DC\_66A\_n66A2 |
| DC\_7A-7A-66A-66A\_n66A | DC\_7A\_n66A  DC\_66A\_n66A2 |
| DC\_7A-66A\_n71A | DC\_7A\_n71A  DC\_66A\_n71A |
| DC\_7A-66A-66A\_n71A | DC\_7A\_n71A  DC\_66A\_n71A |
| DC\_7A\_n66A-n71A | DC\_7A\_n66A  DC\_7A\_n71A |
| DC\_7A-66A\_n77A  DC\_7C-66A\_n77A | DC\_7A\_n77A  DC\_66A\_n77A |
| DC\_7A-7A-66A\_n77A | DC\_7A\_n66A  DC\_66A\_n77A |
| DC\_7A-7A-66A\_n77(2A) | DC\_7A\_n66A  DC\_66A\_n77A |
| DC\_7A-66A\_n77(2A)  DC\_7C-66A\_n77(2A) | DC\_7A\_n66A  DC\_66A\_n77A |
| DC\_7A\_n66A-n77A  DC\_7C\_n66A-n77A | DC\_7A\_n66A  DC\_7A\_n77A |
| DC\_7A-7A\_n66A-n77A | DC\_7A\_n66A  DC\_7A\_n77A |
| DC\_7A\_n66A-n78A  DC\_7C\_n66A-n78A | DC\_7A\_n66A  DC\_7A\_n78A |
| DC\_7A-7A\_n66A-n78A | DC\_7A\_n66A  DC\_7A\_n78A |
| DC\_7A-66A\_n78A5,14  DC\_7C-66A\_n78A5,14 | DC\_7A\_n78A14  DC\_7C\_n78A  DC\_66A\_n78A14 |
| DC\_7A-66A\_n78(2A) 5,14  DC\_7C-66A\_n78(2A) 5,14 | DC\_7A\_n78A14  DC\_7C\_n78A  DC\_66A\_n78A14 |
| DC\_7A-7A-66A\_n78A5,14 | DC\_7A\_n78A14  DC\_66A\_n78A14 |
| DC\_7A-7A-66A\_n78(2A)5,14 | DC\_7A\_n78A14  DC\_66A\_n78A14 |
| DC\_7A-7A-66A-66A\_n78A | DC\_7A\_n78A  DC\_66A\_n78A |
| DC\_7A-7A-66A-66A\_n78(2A) | DC\_7A\_n78A  DC\_66A\_n78A |
| DC\_7A-66A-66A\_n78A5,14  DC\_7C-66A-66A\_n78A5,14 | DC\_7A\_n78A14  DC\_7C\_n78A  DC\_66A\_n78A14 |
| DC\_7A-66A-66A\_n78(2A) 5,14  DC\_7C-66A-66A\_n78(2A) 5,14 | DC\_7A\_n78A14  DC\_66A\_n78A14 |
| DC\_7A-71A\_n2A | DC\_7A\_n2A  DC\_71A\_n2A |
| DC\_7A-71A\_n2(2A) | DC\_7A\_n2A  DC\_71A\_n2A |
| DC\_7A-71A\_n12A | DC\_7A\_n12A |
| DC\_7A-71A\_n25A | DC\_7A\_n25A  DC\_71A\_n25A |
| DC\_7A-71A\_n66A | DC\_7A\_n66A  DC\_71A\_n66A |
| DC\_7A-71A\_n77A | DC\_7A\_n77A  DC\_71A\_n77A |
| DC\_7A-71A\_n77(2A) | DC\_7A\_n77A  DC\_71A\_n77A |
| DC\_7A\_n71A-n77A | DC\_7A\_n71A  DC\_7A\_n77A |
| DC\_7A-71A\_n78A | DC\_7A\_n78A  DC\_71A\_n78A |
| DC\_7A-71A\_n78(2A) | DC\_7A\_n78A  DC\_71A\_n78A |
| DC\_7A\_n71A-n78A | DC\_7A\_n71A  DC\_7A\_n78A |
| DC\_7A\_n75A-n78A | DC\_7A\_n78A |
| DC\_7A\_n78A-n79A24  DC\_7A\_n78A-n79C24 | DC\_7A\_n78A  DC\_7A\_n79A |
| DC\_7A-7A\_n78A-n79A24 | DC\_7A\_n78A  DC\_7A\_n79A |
| DC\_7A\_SUL\_n78A-n80A | DC\_7A\_n78A  DC\_7A\_n80A |
| DC\_7A\_n78A-n105A | DC\_7A\_n78A  DC\_7A\_n105A |
| Unchanged configurations omitted | |
| NOTE 1: Uplink EN-DC configurations are the configurations supported by the present release of specifications.  NOTE 2: Only single switched UL is supported  NOTE 3: Restricted to E-UTRA operation when inter-band carrier aggregation is configured. The downlink operating band for Band 46 is paired with the uplink operating band (external E-UTRA band) of the carrier aggregation configuration that is supporting the configured Pcell.  NOTE 4: If a UE is configured with both NR UL and NR SUL carriers in a cell, the switching time between NR UL carrier and NR SUL carrier can be up to 140us and placed in SUL resources.  NOTE 5: Applicable for UE supporting inter-band EN-DC with mandatory simultaneous Rx/Tx capability  NOTE 6: N/A  NOTE 7: Void.  NOTE 8: Void  NOTE 9: Void  NOTE 10: The frequency range in band n1 is restricted for this band combination to 1940 - 1960 MHz for the UL and 2130-2150 MHz for the DL.  NOTE 11: The frequency range in band 3 is restricted for this band combination to 1765 - 1785 MHz for the UL and 1860-1880 MHz for the DL.  NOTE 12: The frequency range in band 42 is restricted for this band combination to 3440 - 3520 MHz.  NOTE 13: The frequency range in band n28 is restricted for this band combination to 728 - 738 MHz for the UL and 783 - 793 MHz for the DL.  NOTE 14: Minimum requirements for PC2 are applicable for this uplink EN-DC configuration in this downlink/uplink EN-DC configuration.  NOTE 15: For UEs not indicating *interBandMRDC-WithOverlapDL-Bands-r16*, the minimum requirements for intra-band non-contiguous EN-DC apply for the Band 42/48 and Band n77/n78 combination and for the Band 2 and Band n25 combinations. For UEs not indicating *interBandMRDC-WithOverlapDL-Bands-r16*, when UE capability *interBandContiguousMRDC* is indicated, the minimum requirements for intra-band-contiguous EN-DC also should be met in addtion to intra-band non-contiguous EN-DC*.*  NOTE 16: For UEs not indicating *interBandMRDC-WithOverlapDL-Bands-r16*, the minimum requirements for inter-band EN-DC apply when the maximum power spectral density imbalance between downlink carriers contained in overlapping or partially overlapping DL bands is within 6 dB.  NOTE 17: Void.  NOTE 18: Void.  NOTE 19: The implementation with 3 low-band antennas is targeted for FWA form factor for this band combination in Release 17.  NOTE 20: For UEs not indicating *interBandMRDC-WithOverlapDL-Bands-r16*, the minimum requirements apply for synchronized DL carriers with a maximum receive time difference ≤ 3 usec between overlapping or partially overlapping DL bands contained in different cell groups.  NOTE 21: The downlink DC\_2\_n2 RESSENS requirements only apply when the band n2 downlink carrier is configured closer to the uplink operating band than the E-UTRA Band 2 downlink carrier.  NOTE 22: The frequency range in band 28 is restricted for this band combination to 703 - 733 MHz for the UL and 758 - 788 MHz for the DL.  NOTE 23: The minimum requirements apply only when there is non-simultaneous Rx/Tx operation between n77-n79 NR carriers. This restriction applies also for these carriers when applicable EN-DC configuration is part of a higher order configuration.  NOTE 24: For UEs supporting band n77, the minimum requirements apply only when there is non-simultaneous Rx/Tx operation between n78-n79 NR carriers. This restriction applies also for these carriers when applicable EN-DC configuration is part of a higher order configuration.  NOTE 25: Only applicable for UE supporting inter-band carrier aggregation without simultaneous Rx/Tx. | |

### *<< End of changes >>*