**3GPP TSG-WG4 Meeting # 111** **R4-2408410**

**Fukuoka, Japan, 20 – 24 May 2024**

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| *CR-Form-v12.3* |
| **CHANGE REQUEST** |
|  |
|  | **38.174** | **CR** | **-** | **rev** | **-** | **Current version:** | **18.4.0** |  |
|  |
| *For* [***HELP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* |
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| ***Proposed change affects:*** | UICC apps |  | ME |  | Radio Access Network | **X** | Core Network |  |

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| ***Title:***  | draftCR to TS 38.174 addition of FRCs for mIAB-MT demod requirements |
|  |  |
| ***Source to WG:*** | Qualcomm Incorporated |
| ***Source to TSG:*** | R4 |
|  |  |
| ***Work item code:*** | NR\_mobile\_IAB-Core |  | ***Date:*** | 2024-05-13 |
|  |  |  |  |  |
| ***Category:*** | **B** |  | ***Release:*** | Rel-18 |
|  | *Use one of the following categories:****F*** *(correction)****A*** *(mirror corresponding to a change in an earlier release)****B*** *(addition of feature),* ***C*** *(functional modification of feature)****D*** *(editorial modification)*Detailed explanations of the above categories canbe found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | *Use one of the following releases:Rel-8 (Release 8)Rel-9 (Release 9)Rel-10 (Release 10)Rel-11 (Release 11)…Rel-17 (Release 17)Rel-18 (Release 18)Rel-19 (Release 19) Rel-20 (Release 20)* |
|  |  |
| ***Reason for change:*** | Add applicability requirements and FRCs in the core spec for mIAB-MT demod requirements based on the agreed test cases in R4-2406058. Endorsed CR R4-2406053 from RAN4#110-bis is included.  |
|  |  |
| ***Summary of change:*** | Addition applicability requirements and FRCs for the new test cases introduced for mIAB-MT demod requirements.  |
|  |  |
| ***Consequences if not approved:*** | Core specification will have incomplete mIAB-MT demod requirements. |
|  |  |
| ***Clauses affected:*** | 4.6, 4.12, 6.3.3, 9.4.3New clauses: 4.6B, A.3B |
|  |  |
|  | **Y** | **N** |  |  |
| ***Other specs*** |  | **x** |  Other core specifications  | TS/TR ... CR ...  |
| ***affected:*** |  | **X** |  Test specifications | TS/TR ... CR ... |
| ***(show related CRs)*** |  | **x** |  O&M Specifications | TS/TR ... CR ...  |
|  |  |
| ***Other comments:*** |  |
|  |  |
| ***This CR's revision history:*** |  |

**< Start of change >**

## 4.6 Applicability of requirements

In table 4.6-1, the requirement applicability for each *requirement set* of IAB-DUs and mIAB-DUs is defined. In table 4.6-2, the requirement applicability for each *requirement set* of IAB-MTs and mIAB-MTs is defined. For each requirement, the applicable requirement clause in the specification is identified. Requirements not included in a *requirement set* is marked not applicable (NA).

Table 4.6-1: *Requirement set* applicability for IAB-DUs

|  |  |  |  |
| --- | --- | --- | --- |
| Requirement | *IAB-DU type 1-H* | *IAB-DU type 1-O* | *IAB-DU type 2-O* |
| Output power | 6.2 | NA | NA |
| Output power dynamics  | 6.3 |  |  |
| Transmit ON/OFF power  | 6.4 |  |  |
| Transmitted signal quality | 6.5 |  |  |
| Occupied bandwidth | 6.6.2 |  |  |
| ACLR | 6.6.3 |  |  |
| Operating band unwantedemissions | 6.6.4 |  |  |
| Transmitter spurious emissions | 6.6.5 |  |  |
| Transmitter intermodulation  | 6.7 |  |  |
| Reference sensitivity level | 7.2 |  |  |
| Dynamic range  | 7.3 |  |  |
| In-band selectivity and blocking  | 7.4 |  |  |
| Out-of-band blocking  | 7.5 |  |  |
| Receiver spurious emissions  | 7.6 |  |  |
| Receiver intermodulation | 7.7 |  |  |
| In-channel selectivity  | 7.8 |  |  |
| Performance requirements | 8 |  |  |
| Radiated transmit power | 9.2 | 9.2 | 9.2 |
| OTA Output power | NA | 9.3 | 9.3 |
| OTA output power dynamics |  | 9.4 | 9.4 |
| OTA transmit ON/OFF power |  | 9.5 | 9.5 |
| OTA transmitted signal quality |  | 9.6 | 9.6 |
| OTA occupied bandwidth |  | 9.7.2 | 9.7.2 |
| OTA ACLR |  | 9.7.3 | 9.7.3 |
| OTA out-of-band emission |  | 9.7.4 | 9.7.4 |
| OTA transmitter spurious emission  |  | 9.7.5 | 9.7.5 |
| OTA transmitter intermodulation  |  | 9.8 | NA |
| OTA sensitivity | 10.2 | 10.2 | NA |
| OTA reference sensitivity level | NA | 10.3 | 10.3 |
| OTA dynamic range |  | 10.4 | NA |
| OTA in-band selectivity and blocking |  | 10.5 | 10.5 |
| OTA out-of-band blocking |  | 10.6 | 10.6 |
| OTA receiver spurious emission  |  | 10.7 | 10.7 |
| OTA receiver intermodulation |  | 10.8 | 10.8 |
| OTA in-channel selectivity |  | 10.9 | 10.9 |
| Radiated performance requirements |  | 11 | 11 |

Table 4.6-2: *Requirement set* applicability for IAB-MTs

|  |  |  |  |
| --- | --- | --- | --- |
| Requirement | *IAB-MT type 1-H* | *IAB-MT type 1-O* | *IAB-MT type 2-O* |
| Output power | 6.2 | NA | NA |
| Output power dynamics  | 6.3 |  |  |
| Transmit ON/OFF power  | 6.4 |  |  |
| Transmitted signal quality | 6.5 |  |  |
| Occupied bandwidth | 6.6.2 |  |  |
| ACLR | 6.6.3 |  |  |
| Operating band unwantedemissions | 6.6.4 |  |  |
| Transmitter spurious emissions | 6.6.5 |  |  |
| Transmitter intermodulation  | 6.7 |  |  |
| Reference sensitivity level | 7.2 |  |  |
| Dynamic range  | NA |  |  |
| In-band selectivity and blocking  | 7.4 |  |  |
| Out-of-band blocking  | 7.5 |  |  |
| Receiver spurious emissions  | 7.6 |  |  |
| Receiver intermodulation | 7.7 |  |  |
| In-channel selectivity  | NA |  |  |
| Performance requirements | 8 |  |  |
| Radiated transmit power | 9.2 | 9.2 | 9.2 |
| OTA Output power | NA | 9.3 | 9.3 |
| OTA output power dynamics |  | 9.4 | 9.4 |
| OTA transmit ON/OFF power |  | 9.5 | 9.5 |
| OTA transmitted signal quality |  | 9.6 | 9.6 |
| OTA occupied bandwidth |  | 9.7.2 | 9.7.2 |
| OTA ACLR |  | 9.7.3 | 9.7.3 |
| OTA out-of-band emission |  | 9.7.4 | 9.7.4 |
| OTA transmitter spurious emission  |  | 9.7.5 | 9.7.5 |
| OTA transmitter intermodulation  |  | 9.8 | NA |
| OTA sensitivity | 10.2 | 10.2 | NA |
| OTA reference sensitivity level | NA | 10.3 | 10.3 |
| OTA dynamic range |  | NA | NA |
| OTA in-band selectivity and blocking |  | 10.5 | 10.5 |
| OTA out-of-band blocking |  | 10.6 | 10.6 |
| OTA receiver spurious emission  |  | 10.7 | 10.7 |
| OTA receiver intermodulation |  | 10.8 | NA |
| OTA in-channel selectivity |  | NA | NA |
| Radiated performance requirements |  | 11 | 11 |

## 4.6B Applicability of performance requirements for mIAB-MT and mIAB-DU

The performance requirements in clauses 8 and 11 for IAB-DU shall apply to mIAB-DU.

The performance requirements in Suffix B in clauses 8 and 11 shall apply to mIAB-MT.

**< Next change >**

## 4.12 Specification suffix information

Unless stated otherwise, the suffix shown in Table 4.12-1 is used for indicating the clause for mobile-IAB node.

Table 4.12-1: Definition of suffixes

|  |  |
| --- | --- |
| Clause suffix | Variant |
| B | Mobile IAB-node |

An IAB-node which supports the mobile feature needs to meet both the general requirements of local area IAB-MT and the additional requirement applicable to the additional clause (suffixes B) in clauses 5, 6, 7, 9, 10, and 12. Where there is a difference in requirement between the general requirements and the additional clause requirements (suffixes B) in clauses 5, 6, 7, 9,10 and 12, the tighter requirements are applicable unless stated otherwise in the additional clause. Requirements given in additional clause (Suffix B) in clauses 8 and 11 are only applicable to mIAB-MTs.

**< Next change >**

## A.3B mIAB-MT Fixed Reference Channels

### A.3B.1 Fixed Reference Channels for PDSCH performance requirements (QPSK)

The parameters for the reference measurement channels are specified in table A.3B.1-1 for FR1 mIAB-MT PDSCH performance requirements.

Table A.3B.1-1: PDSCH Reference Channel for TDD UL-DL pattern FR1.30-1 and FR1.30-1A (QPSK)

|  |  |  |
| --- | --- | --- |
| **Parameter** | **Unit** | **Value** |
| Reference channel |  | R.PDSCH.2-1.1 TDD |
| Channel bandwidth | MHz | 40 |
| Subcarrier spacing | kHz | 30 |
| Allocated resource blocks | PRBs | 106 |
| Number of consecutive PDSCH symbols |  |  |
|  For Slots 0 and Slot i, if mod(i, 10) = {8,9} for i from {0,…,39} |  | N/A |
|  For Slot i, if mod(i, 10) = 7 for i from {0,…,39} |  | 4 |
|  For Slot i, if mod(i, 10) = {0,1,2,3,4,5,6} for i from {1,…,39} |  | 12 |
| Allocated slots per 2 frames |  | 31 |
| MCS table |  | 64QAM |
| MCS index |  | 4 |
| Modulation |  | QPSK |
| Target Coding Rate |  | 0.30 |
| Number of MIMO layers |  | 1 |
| Number of DMRS Res |  |  |
| For Slots 0 and Slot i, if mod(i, 10) = {8,9} for i from {0,…,39} |  | N/A |
|  For Slot i, if mod(i, 10) = 7 for i from {0,…,39} |  | 6 |
|  For Slot i, if mod(i, 10) = {0,1,2,3,4,5,6} for i from {1,…,39} |  | 18 |
| Overhead for TBS determination |  | 0 |
| Information Bit Payload per Slot  |  |  |
|  For Slots 0 and Slot i, if mod(i, 10) = {8,9} for i from {0,…,39} | Bits | N/A |
|  For Slot i, if mod(i, 10) = 7 for i from {0,…,39} | Bits | 2664 |
|  For Slot i, if mod(i, 10) = {0,1,2,3,4,5,6} for i from {1,…,39} | Bits | 8064 |
| Transport block CRC per Slot |  |  |
|  For Slots 0 and Slot i, if mod(i, 10) = {8,9} for i from {0,…,39} | Bits | N/A |
|  For Slot i, if mod(i, 10) = 7 for i from {0,…,39} | Bits | 16 |
|  For Slot i, if mod(i, 10) = {0,1,2,3,4,5,6} for i from {1,…,39} | Bits | 24 |
| Number of Code Blocks per Slot |  |  |
|  For Slots 0 and Slot i, if mod(i, 10) = {8,9} for i from {0,…,39} | CBs | N/A |
|  For Slot i, if mod(i, 10) = 7 for i from {0,…,39} | CBs | 1 |
|  For Slot i, if mod(i, 10) = {0,1,2,3,4,5,6} for i from {1,…,39} | CBs | 1 |
| Binary Channel Bits Per Slot |  |  |
|  For Slots 0 and Slot i, if mod(i, 10) = {8,9} for i from {0,…,39} | Bits | N/A |
|  For Slots i = 20, 21 | Bits | 25440 |
|  For Slot i, if mod(i, 10) = 7 for i from {0,…,39} | Bits | 8904 |
|  For Slot i, if mod(i, 10) = {0,1,2,3,4,5,6} for i from {1,…,19,22,…,39} | Bits | 26712 |
| Max. Throughput averaged over 2 frames | Mbps | 11.419 |
| Note 1: SS/PBCH block is transmitted in slot #0 with periodicity 20 msNote 2: Slot i is slot index per 2 frames |

### A.3B.2 Fixed Reference Channels for PDSCH performance requirements (16QAM)

The parameters for the reference measurement channels are specified in table A.3B.2-1 for FR1 mIAB-MT PDSCH performance requirements.

The parameters for the reference measurement channels are specified in table A.3B.2-2 for FR2-1 mIAB-MT PDSCH performance requirements.

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Table A.3B.2-1: PDSCH Reference Channel for TDD UL-DL pattern FR1.30-1 (16QAM)

|  |  |  |
| --- | --- | --- |
| **Parameter** | **Unit** | **Value** |
| Reference channel |  | R.PDSCH.2-2.1 TDD |
| Channel bandwidth | MHz | 40 |
| Subcarrier spacing | kHz | 30 |
| Allocated resource blocks | PRBs | 106 |
| Number of consecutive PDSCH symbols |  |  |
| For Slots 0 and Slot i, if mod(i, 10) = {8,9} for i from {0,…,39} |  | N/A |
|  For Slot i, if mod(i, 10) = 7 for i from {0,…,39} |  | 4 |
|  For Slot i, if mod(i, 10) = {0,1,2,3,4,5,6} for i from {1,…,39} |  | 12 |
| Allocated slots per 2 frames |  | 31 |
| MCS table |  | 64QAM |
| MCS index |  | 13 |
| Modulation |  | 16QAM |
| Target Coding Rate |  | 0.48 |
| Number of MIMO layers |  | 1 |
| Number of DMRS Res |  |  |
| For Slots 0 and Slot i, if mod(i, 10) = {8,9} for i from {0,…,39} |  | N/A |
|  For Slot i, if mod(i, 10) = 7 for i from {0,…,39} |  | 6 |
|  For Slot i, if mod(i, 10) = {0,1,2,3,4,5,6} for i from {1,…,39} |  | 12 |
| Overhead for TBS determination |  | 0 |
| Information Bit Payload per Slot  |  |  |
|  For Slots 0 and Slot i, if mod(i, 10) = {8,9} for i from {0,…,39} | Bits | N/A |
|  For Slot i, if mod(i, 10) = 7 for i from {0,…,39} | Bits | 8456 |
|  For Slot i, if mod(i, 10) = {0,1,2,3,4,5,6} for i from {1,…,39} | Bits | 26632 |
| Transport block CRC per Slot |  |  |
|  For Slots 0 and Slot i, if mod(i, 10) = {8,9} for i from {0,…,39} | Bits | N/A |
|  For Slot i, if mod(i, 10) = 7 for i from {0,…,39} | Bits | 24 |
|  For Slot i, if mod(i, 10) = {0,1,2,3,4,5,6}for i from {1,…,39} | Bits | 24 |
| Number of Code Blocks per Slot |  |  |
|  For Slots 0 and Slot i, if mod(i, 10) = {8,9} for i from {0,…,39} | CBs | N/A |
|  For Slot i, if mod(i, 10) = 7 for i from {0,…,39} | CBs | 2 |
|  For Slot i, if mod(i, 10) = {0,1,2,3,4,5,6} for i from {1,…,39} | CBs | 4 |
| Binary Channel Bits Per Slot |  |  |
|  For Slots 0 and Slot i, if mod(i, 10) = {8,9} for i from {0,…,39} | Bits | N/A |
|  For Slots i = 20, 21 | Bits | 53424 |
|  For Slot i, if mod(i, 10) = 7 for i from {0,…,39} | Bits | 17808 |
|  For Slot i, if mod(i, 10) = {0,1,2,3,4,5,6} for i from {1,…,19,22,…,39} | Bits | 55968 |
| Max. Throughput averaged over 2 frames | Mbps | 37.644 |
| Note 1: SS/PBCH block is transmitted in slot #0 with periodicity 20 msNote 2: Slot i is slot index per 2 frames |

Table A.3B.2-2: PDSCH Reference Channel for TDD UL-DL pattern FR2.120-1 (16QAM)

|  |  |  |
| --- | --- | --- |
| **Parameter** | **Unit** | **Value** |
| Reference channel |  | R.PDSCH.5-2.2 TDD |
| Channel bandwidth | MHz | 100 |
| Subcarrier spacing | kHz | 120 |
| Allocated resource blocks | PRBs | 66 |
| Number of consecutive PDSCH symbols |  |  |
| For Slots 0 and Slot i, if mod(i, 5) = 4 for i from {0,…,159} |  | N/A |
|  For Slot i, if mod(i, 5) = 3 for i from {0,…, 159} |  | 9 |
|  For Slot i, if mod(i, 5) = {0,1,2} for i from {1,…,159} |  | 13 |
| Allocated slots per 2 frames |  | 127 |
| MCS table |  | 64QAM |
| MCS index |  | 13 |
| Modulation |  | 16QAM |
| Target Coding Rate |  | 0.48 |
| Number of MIMO layers |  | 2 |
| Number of DMRS REs |  |  |
| For Slots 0 and Slot i, if mod(i, 5) = 4 for i from {0,…,159} |  | N/A |
|  For Slot i, if mod(i, 5) = 3 for i from {0,…, 159} |  | 12 |
|  For Slot i, if mod(i, 5) = {0,1,2} for i from {1,…,159} |  | 12 |
| Overhead for TBS determination |  | 6 |
| Information Bit Payload per Slot  |  |  |
|  For Slots 0 and Slot i, if mod(i, 5) = 4 for i from {0,…,159} | Bits | N/A |
|  For Slot i, if mod(i, 5) = 3 for i from {0,…, 159} | Bits | 22536 |
|  For Slot i, if mod(i, 5) = {0,1,2} for i from {1,…,159} | Bits | 34816 |
| Transport block CRC per Slot |  |  |
|  For Slots 0 and Slot i, if mod(i, 5) = 4 for i from {0,…,159} | Bits | N/A |
|  For Slot i, if mod(i, 5) = 3 for i from {0,…, 159} | Bits | 24 |
|  For Slot i, if mod(i, 5) = {0,1,2} for i from {1,…,159} | Bits | 24 |
| Number of Code Blocks per Slot |  |  |
|  For Slots 0 and Slot i, if mod(i, 5) = 4 for i from {0,…,159} | CBs | N/A |
|  For Slot i, if mod(i, 5) = 3 for i from {0,…, 159} | CBs | 3 |
|  For Slot i, if mod(i, 5) = {0,1,2} for i from {1,…,159} | CBs | 5 |
| Binary Channel Bits Per Slot |  |  |
|  For Slots 0 and Slot i, if mod(i, 5) = 4 for i from {0,…,159} | Bits | N/A |
|  For Slots i = 80, 81 | Bits | 69960 |
|  For Slots i = 82 | Bits | 73128 |
|  For Slots i = 83 | Bits | 48840 |
|  For Slot i, if mod(i, 5) = 3 for i from {0,…, 159} | Bits | 48840 |
|  For Slot i, if mod(i, 5) = {0,1,2} for i from {1,…,79,84,…,159} | Bits | 73128 |
| Max. Throughput averaged over 2 frames | Mbps | 201.434 |
| Note 1: SS/PBCH block is transmitted in slot #0 with periodicity 20 msNote 2: Slot i is slot index per 2 frames Note 3: PDSCH is scheduled in PRB numbers from 0 to 32. Note 4: PDSCH is scheduled in PRB numbers from 33 to 65. |

### A.3B.3 Fixed Reference Channels for PDSCH performance requirements (64QAM)

The parameters for the reference measurement channels are specified in table A.3B.3-1 for FR2-1 mIAB-MT PDSCH performance requirements.

Table A.3B.3-1: PDSCH Reference Channel for TDD UL-DL pattern FR2.120-1 (64QAM)

|  |  |  |
| --- | --- | --- |
| **Parameter** | **Unit** | **Value** |
| Reference channel |  | R.PDSCH.5-3.1 TDD |
| Channel bandwidth | MHz | 100 |
| Subcarrier spacing | kHz | 120 |
| Allocated resource blocks | PRBs | 66 |
| Number of consecutive PDSCH symbols |  |  |
| For Slots 0 and Slot i, if mod(i, 5) = 4 for i from {0,…,159} |  | N/A |
|  For Slots i = 80, 81 |  | 13 |
|  For Slot i, if mod(i, 5) = 3 for i from {0,…, 159} |  | 9 |
|  For Slot i, if mod(i, 5) = {0,1,2} for i from {1,…,159} |  | 13 |
| Allocated slots per 2 frames |  | 127 |
| MCS table |  | 64QAM |
| MCS index |  | 18 |
| Modulation |  | 64QAM |
| Target Coding Rate |  | 0.46 |
| Number of MIMO layers |  | 1 |
| Number of DMRS REs |  |  |
| For Slots 0 and Slot i, if mod(i, 5) = 4 for i from {0,…,159} |  | N/A |
|  For Slots i = 80, 81 |  | 12 |
|  For Slot i, if mod(i, 5) = 3 for i from {0,…, 159} |  | 12 |
|  For Slot i, if mod(i, 5) = {0,1,2} for i from {1,…,159} |  | 12 |
| Overhead for TBS determination |  | 6 |
| Information Bit Payload per Slot  |  |  |
|  For Slots 0 and Slot i, if mod(i, 5) = 4 for i from {0,…,159} | Bits | N/A |
|  For Slots i = 80, 81 | Bits | 25104 |
|  For Slot i, if mod(i, 5) = 3 for i from {0,…, 159} | Bits | 16136 |
|  For Slot i, if mod(i, 5) = {0,1,2} for i from {1,…,159} | Bits | 25104 |
| Transport block CRC per Slot |  |  |
|  For Slots 0 and Slot i, if mod(i, 5) = 4 for i from {0,…,159} | Bits | N/A |
|  For Slots i = 80, 81 | Bits | 24 |
|  For Slot i, if mod(i, 5) = 3 for i from {0,…, 159} | Bits | 24 |
|  For Slot i, if mod(i, 5) = {0,1,2} for i from {1,…,159} | Bits | 24 |
| Number of Code Blocks per Slot |  |  |
|  For Slots 0 and Slot i, if mod(i, 5) = 4 for i from {0,…,159} | CBs | N/A |
|  For Slots i = 80, 81 | CBs | 3 |
|  For Slot i, if mod(i, 5) = 3 for i from {0,…, 159} | CBs | 2 |
|  For Slot i, if mod(i, 5) = {0,1,2} for i from {1,…,159} | CBs | 3 |
| Binary Channel Bits Per Slot |  |  |
|  For Slots 0 and Slot i, if mod(i, 5) = 4 for i from {0,…,159} | Bits | N/A |
|  For Slots i = 80, 81 | Bits | 52470 |
|  For Slot i, if mod(i, 5) = 3 for i from {0,…, 159} | Bits | 36630 |
|  For Slot i, if mod(i, 5) = {0,1,2} for i from {1,…,79,82,…,159} | Bits | 54846 |
| Max. Throughput averaged over 2 frames | Mbps | 145.062 |
| Note 1: SS/PBCH block is transmitted in slot #0 with periodicity 20 msNote 2: Slot i is slot index per 2 frames |

### A.3B.4 Fixed Reference Channels for PDCCH performance requirements

The parameters for the reference measurement channels are specified in table A.3B.4-1 for FR1 mIAB-MT PDCCH performance requirements.

The parameters for the reference measurement channels are specified in table A.3B.4-2 for FR2-1 mIAB-MT PDCCH performance requirements.

Table A.3B.4-1: PDCCH Reference Channels (Time domain allocation 1 symbol)

|  |  |  |
| --- | --- | --- |
| **Parameter** | **Unit** | **Value** |
| Reference channel |  | R.PDCCH.2-1.2 TDD | R.PDCCH.2-1.3 TDD |
| Subcarrier spacing | kHz | 30 | 30 |
| CORESET frequency domain allocation |  | 102 | 90 |
| CORESET time domain allocation |  | 1 | 1 |
| Aggregation level |  | 4 | 8 |
| DCI Format |  | 1\_1 | 1\_1 |
| Payload (without CRC) | Bits | 53 | 53 |

Table A.3B.4-2: PDCCH Reference Channels (Time domain allocation 1 symbol)

|  |  |  |
| --- | --- | --- |
| **Parameter** | **Unit** | **Value** |
| Reference channel |  | R.PDCCH.5-1.2 TDD |
| Subcarrier spacing | kHz | 120 |
| CORESET frequency domain allocation |  | 60 |
| CORESET time domain allocation |  | 1 |
| Aggregation level |  | 4 |
| DCI Format |  | 1\_1 |
| Payload (without CRC) | Bits | 56 |

### A.3B.4 Reference measurement channels for PBCH demodulation requirements

#### A.3B.4.1 Reference measurement channels for FR1

Table A.3B.4.1-1: PBCH Reference Channel

|  |  |  |
| --- | --- | --- |
| **Parameter** | **Unit** | **Value** |
| Reference channel |  | R.PBCH.2 |
| SS/PBCH block subcarrier spacing | kHz | 30 |
| Modulation |  | QPSK |
| Target coding rate |  | 56/864 |
| Payload (without CRC and timing related PBCH payload bits) | bits | 24 |

#### A.3B.4.2 Reference measurement channels for FR2

Table A.3B.4.2-1: PBCH Reference Channel

|  |  |  |
| --- | --- | --- |
| **Parameter** | **Unit** | **Value** |
| Reference channels |  | R.PBCH.5 |
| SS/PBCH block subcarrier spacing | kHz | 120 |
| Modulation |  | QPSK |
| Target coding rate |  | 56/864 |
| Payload (without CRC and timing related PBCH payload bits) | bits | 24 |

**< Next change >**

### A.3B.5 TDD UL-DL configurations

TDD UL-DL configurations for FR1 performance requirements are provided in Tables A.3B.5-1. TDD UL-DL configurations for FR2-1 performance requirements are provided in Tables A.3B.5-2.

Table A.3B.5-1: TDD UL-DL configuration for SCS 30 kHz

|  |  |  |
| --- | --- | --- |
| **Parameter** | **Unit** | **UL-DL pattern** |
| **FR1.30-1** | **FR1.30-5** |
| TDD Slot Configuration pattern (Note 1) |  | 7DS2U | DSUU |
| Special Slot Configuration (Note 2) |  | 6D+4G+4U | 12D+2G |
| *referenceSubcarrierSpacing* | kHz | 30 | 30 |
| pattern1 |  |  |  |  |
| *dl-UL-TransmissionPeriodicity* | ms | 5 | 2 |
| *nrofDownlinkSlots* |  | 7 | 1 |
| *nrofDownlinkSymbols* |  | 6 | 12 |
| *nrofUplinkSlot* |  | 2 | 2 |
| *nrofUplinkSymbols* |  | 4 | 0 |
| pattern2 |  |  |  |  |
| *dl-UL-TransmissionPeriodicity* | ms | N/A | N/A |
| *nrofDownlinkSlots* |  | N/A | N/A |
| *nrofDownlinkSymbols* |  | N/A | N/A |
| *nrofUplinkSlot* |  | N/A | N/A |
| *nrofUplinkSymbols* |  | N/A | N/A |
| The number of slots between PDSCH and corresponding HARQ-ACK information (Note 3) |  | 8 if mod(i,10) = 07 if mod(i,10) = 16 if mod(i,10) = 25 if mod(i,10) = 35 if mod(i,10) = 44 if mod(i,10) = 53 if mod(i,10) = 62 if mod(i,10) = 7 | 3 if mod(i,4) = 02 if mod(i,4) = 1 |

Table A.3B.5-2: TDD UL-DL configuration for SCS 120 kHz

|  |  |  |
| --- | --- | --- |
| **Parameter** | **Unit** | **UL-DL pattern** |
| **FR2.120-1** |
| TDD Slot Configuration pattern (Note 1) |  | DDDSU |
| Special Slot Configuration (Note 2) |  | 10D+2G+2U |
| *referenceSubcarrierSpacing* | kHz | 120 |
| pattern1 | *dl-UL-TransmissionPeriodicity* | ms | 0.625 |
| *nrofDownlinkSlots* |  | 3 |
| *nrofDownlinkSymbols* |  | 10 |
| *nrofUplinkSlot* |  | 1 |
| *nrofUplinkSymbols* |  | 2 |
| The number of slots between PDSCH and corresponding HARQ-ACK information(Note 3) |  | 4 if mod(i,5) = 03 if mod(i,5) = 12 if mod(i,5) = 26 if mod(i,5) = 3 |
| Note 1: D denotes a slot with all DL symbols; S denotes a slot with a mix of DL, UL and guard symbols; U denotes a slot with all UL symbols. The field is for information.Note 2: D, G, U denote DL, guard and UL symbols, respectively. The field is for information.Note 3: i is the slot index per frame; i = {0,…,79} |

**< End of change >**