**3GPP TSG- Meeting #111**

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| *CR-Form-v12.2* |
| **CHANGE REQUEST** |
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|  |  | **CR** |  | **rev** | **1** | **Current version:** |  |  |
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| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* |
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| ***Proposed change affects:*** | UICC apps |  | ME | **X** | Radio Access Network | **X** | Core Network |  |

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| ***Title:***  |   |
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| ***Source to WG:*** |  |
| ***Source to TSG:*** |  |
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| ***Work item code:*** |  |  | ***Date:*** |  |
|  |  |  |  |  |
| ***Category:*** |  |  | ***Release:*** |  |
|  | *Use one of the following categories:****F*** *(correction)****A*** *(mirror corresponding to a change in an earlier release)****B*** *(addition of feature),* ***C*** *(functional modification of feature)****D*** *(editorial modification)*Detailed explanations of the above categories canbe found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | *Use one of the following releases:Rel-8 (Release 8)Rel-9 (Release 9)Rel-10 (Release 10)Rel-11 (Release 11)…Rel-16 (Release 16)Rel-17 (Release 17)Rel-18 (Release 18)Rel-19 (Release 19)* |
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| ***Reason for change:*** | In a normative clause introduced by CR3705r1, what is intended to be a UE requirement has been phrased as “the UE is expected to”. However, the Forword section of TS 38.133 specifies that for requirements, the word “shall” is to be used. The error shall be corrected in order to comply with the specification drafting rules (TR 21.801), particularly with the following objectives: to be consistent, clear and accurate, and to be comprehensible to qualified persons who have not participated in its preparation. |
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| ***Summary of change:*** | Clause 8.10E.3.2:The requirement is phrased as that the UE “is expected to” perform an action. As “expected to” is not a requirement, it is changed to “shall”.Additionally removing a stray underlining. |
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| ***Consequences if not approved:*** | The specification will remain inconsistent and unclear to those who have not participated in its preparation. The specification quality will degrade further over time as the errors will be copied when new requirements are added. |
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| ***Clauses affected:*** | 8.10E.3.2 |
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|  | **Y** | **N** |  |  |
| ***Other specs*** |  | **X** |  Other core specifications  | TS/TR ... CR ...  |
| ***affected:*** |  | **X** |  Test specifications | TS/TR ... CR ...  |
| ***(show related CRs)*** |  | **X** |  O&M Specifications | TS/TR ... CR ...  |
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| ***Other comments:*** | Corresponding changes are proposed across the normative part of TS 38.133 in related CRs: R4-2407783 (Cat F), R4-2407784 (Cat A), R4-2407785 (Cat F), R4-2407786 (Cat F), R4-2407787 (Cat F), R4-2407788 (Cat F) , R4-2407789 (Cat F). |
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| ***This CR's revision history:*** | Rev.1: Changed “shall be able to” to “shall”. |

1st CORRECTION

8.10E.3.2 MAC-CE based dual DL TCI state switching delay for mDCI

The requirement in this clause applies when UE is capable of *multiDCI-MultiTRP-r16* and configured with different *CORESETPoolIndex*.

When a MAC CE command indicating a TCI state switch for *CORESETPoolIndex p* is received at slot n, and if the TCI state is known, UE shall be able to receive on the target TCI state at the first slot that is after slot n+ THARQ + $3N\_{slot}^{subframe,µ}$+ TOk\*( Tfirst-SSBp, + TSSB-proc) / *NR slot length*. The UE shall be able to receive PDCCH with the old TCI states until slot n+ THARQ + $3N\_{slot}^{subframe,µ}$.

Where:

- THARQ is the timing between DL data transmission and acknowledgement as specified in TS 38.213 [3].

- Tfirst-SSBp is time to first SSB transmission (i.e., SSB associated to *CORESETPoolIndex p*) after MAC CE command is decoded by the UE; The SSB shall be the QCL-TypeA or QCL-TypeC to target TCI state.

- TSSB-proc = 2 ms.

- TOk = 1 if target TCI state is not in the active TCI state list for PDSCH, 0 otherwise.

When UE receive TCI state switch command for CORESETPoolIndex pwhile UE performing TCI state switch of CORESETPoolIndex q*,* UE shall receive on new TCI states of CORESETPoolIndex p and q after completing both the TCI state switch of CORESETPoolIndex p and q.

END OF CORRECTIONS