**3GPP TSG-RAN WG4 Meeting # 111 *R4-2409507r1***

**Fukuoka City, Fukuoka, Japan, May 20 – 24, 2024**

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| *CR-Form-v12.3* |
| **CHANGE REQUEST** |
|  |
|  | **38.101-3** | **CR** | **<CR#>** | **rev** | **-** | **Current version:** | **18.5.1** |  |
|  |
| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* |
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| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| ***Proposed change affects:*** | UICC apps |  | ME | **X** | Radio Access Network |  | Core Network |  |

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|  |
| ***Title:***  |  |
|  |  |
| ***Source to WG:*** | ZTE Corporation, Sanechips |
| ***Source to TSG:*** | R4 |
|  |  |
| ***Work item code:*** | NR\_CADC\_R18\_3BDL\_xBUL-Core |  | ***Date:*** | 2024-05-05 |
|  |  |  |  |  |
| ***Category:*** | **F** |  | ***Release:*** | Rel-18 |
|  | *Use one of the following categories:****F*** *(correction)****A*** *(mirror corresponding to a change in an earlier release)****B*** *(addition of feature),* ***C*** *(functional modification of feature)****D*** *(editorial modification)*Detailed explanations of the above categories canbe found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | *Use one of the following releases:Rel-8 (Release 8)Rel-9 (Release 9)Rel-10 (Release 10)Rel-11 (Release 11)…Rel-17 (Release 17)Rel-18 (Release 18)Rel-19 (Release 19) Rel-20 (Release 20)* |
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| ***Reason for change:*** | Based on the discussion paper in R4-2409510, to optimize the inter-band DC band combinations, for inter-band DC configurations with FR2 part of the uplink configurations, it is proposed to optimize the configuration tables with the grouping rules as below.* For inter-band NR-DC configurations between FR1 and FR2, the delimiter “/” could be used for the FR2 part of the uplink configurations, such as DC\_nxA\_nyA/B/C, where nx and ny are FR1 NR band and FR2 NR band, and A, B and C are the corresponding bandwidth classes respectively.
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| ***Summary of change:*** | 1. Re-group the uplink NR DC configurations between FR1 and FR2 for three bands NR DC band combinations.
2. Add a note to indicate the denotation of uplink configurations.
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| ***Consequences if not approved:*** | The uplink NR DC configurations will be redundant between FR1 and FR2. |
|  |  |
| ***Clauses affected:*** | 5.5B.7.2 |
|  |  |
|  | **Y** | **N** |  |  |
| ***Other specs*** |  | **X** |  Other core specifications  | TS/TR ... CR ...  |
| ***affected:*** | **X** |  |  Test specifications | TS/TR ... CR ... 38.521-3 |
| ***(show related CRs)*** |  | **X** |  O&M Specifications | TS/TR ... CR ...  |
|  |  |
| ***Other comments:*** |  |
|  |  |
| ***This CR's revision history:*** |  |

### *<< Start of changes >>*

#### 5.5B.7.2 Inter-band NR-DC configurations between FR1 and FR2 (three bands)

Table 5.5B.7-2: Inter-band NR-DC configurations between FR1 and FR2 (three bands)

| **Downlink NR DC****configuration** | **Uplink NR DC****configuration** |
| --- | --- |
| DC\_n1A-n3A-n257ADC\_n1A-n3A-n257GDC\_n1A-n3A-n257HDC\_n1A-n3A-n257I | DC\_n1A-n3ADC\_n1A-n257A/G/H/IDC\_n3A-n257A/G/H/I |
| DC\_n1A-n3A-n258ADC\_n1A-n3A-n258DDC\_n1A-n3A-n258GDC\_n1A-n3A-n258HDC\_n1A-n3A-n258IDC\_n1A-n3A-n258J | DC\_n1A-n3ADC\_n1A-n258A/D/G/H/I/JDC\_n3A-n258A/D/G/H/I/J |
| DC\_n1A-n18A-n257ADC\_n1A-n18A-n257GDC\_n1A-n18A-n257HDC\_n1A-n18A-n257I | DC\_n1A-n18ADC\_n1A-n257A/G/H/IDC\_n18A-n257A/G/H/I |
| DC\_n1A-n28A-n257ADC\_n1A-n28A-n257GDC\_n1A-n28A-n257HDC\_n1A-n28A-n257I | DC\_n1A-n28ADC\_n1A-n257A/G/H/IDC\_n28A-n257A/G/H/I |
| DC\_n1A-n28A-n258ADC\_n1A-n28A-n258DDC\_n1A-n28A-n258GDC\_n1A-n28A-n258HDC\_n1A-n28A-n258IDC\_n1A-n28A-n258J | DC\_n1A-n28ADC\_n1A-n258A/D/G/H/I/JDC\_n28A-n258A/D/G/H/I/J |
| DC\_n1A-n41A-n257ADC\_n1A-n41A-n257GDC\_n1A-n41A-n257HDC\_n1A-n41A-n257I | DC\_n1A-n41ADC\_n1A-n257A/G/H/IDC\_n41A-n257A/G/H/I |
| DC\_n1A-n77A-n257ADC\_n1A-n77A-n257GDC\_n1A-n77A-n257HDC\_n1A-n77A-n257I | DC\_n1A-n257A/G/H/IDC\_n77A-n257A/G/H/I |
| DC\_n1A-n77(2A)-n257ADC\_n1A-n77(2A)-n257GDC\_n1A-n77(2A)-n257HDC\_n1A-n77(2A)-n257I | DC\_n1A-n77ADC\_n1A-n257A/G/H/IDC\_n77A-n257A/G/H/I |
| DC\_n1A-n78A-n257A1DC\_n1A-n78A-n257G1DC\_n1A-n78A-n257H1DC\_n1A-n78A-n257I1DC\_n1A-n78A-n257J1DC\_n1A-n78A-n257K1DC\_n1A-n78A-n257L1DC\_n1A-n78A-n257M1 | DC\_n1A-n78ADC\_n1A-n257A/G/H/I/J/KDC\_n78A-n257A/G/H/I/J/K |
| DC\_n1A-n79A-n257ADC\_n1A-n79A-n257GDC\_n1A-n79A-n257HDC\_n1A-n79A-n257I | DC\_n1A-n257A/G/H/IDC\_n79A-n257A/G/H/I |
| DC\_n2A-n5A-n260ADC\_n2A-n5A-n260GDC\_n2A-n5A-n260HDC\_n2A-n5A-n260IDC\_n2A-n5A-n260JDC\_n2A-n5A-n260KDC\_n2A-n5A-n260LDC\_n2A-n5A-n260M | DC\_n2A-n5ADC\_n2A-n260A/G/H/I/J/K/L/MDC\_n5A-n260A/G/H/I/J/K/L/M |
| DC\_n2A-n5A-n261ADC\_n2A-n5A-n261GDC\_n2A-n5A-n261HDC\_n2A-n5A-n261IDC\_n2A-n5A-n261JDC\_n2A-n5A-n261KDC\_n2A-n5A-n261LDC\_n2A-n5A-n261M | DC\_n2A-n5ADC\_n2A-n261A/G/H/IDC\_n5A-n261A/G/H/I |
| DC\_n2A-n5A-n261(2G)DC\_n2A-n5A-n261(G-H)DC\_n2A-n5A-n261(A-G-H)DC\_n2A-n5A-n261(G-I)DC\_n2A-n5A-n261(2H)DC\_n2A-n5A-n261(A-G-I)DC\_n2A-n5A-n261(H-I)DC\_n2A-n5A-n261(A-G)DC\_n2A-n5A-n261(A-H)DC\_n2A-n5A-n261(2A-H)DC\_n2A-n5A-n261(A-2G)DC\_n2A-n5A-n261(A-I)DC\_n2A-n5A-n261(2A-I)DC\_n2A-n5A-n261(2A)DC\_n2A-n5A-n261(3A)DC\_n2A-n5A-n261(2A-G) | DC\_n2A-n5ADC\_n2A-n261A/G/H/IDC\_n5A-n261A/G/H/I |
| DC\_n2A-n12A-n260ADC\_n2A-n12A-n260GDC\_n2A-n12A-n260HDC\_n2A-n12A-n260IDC\_n2A-n12A-n260JDC\_n2A-n12A-n260KDC\_n2A-n12A-n260LDC\_n2A-n12A-n260M | DC\_n2A-n12ADC\_n2A-n260A/G/H/I/J/K/L/MDC\_n12A-n260A/G/H/I/J/K/L/M |
| DC\_n2A-n14A-n260ADC\_n2A-n14A-n260GDC\_n2A-n14A-n260HDC\_n2A-n14A-n260IDC\_n2A-n14A-n260JDC\_n2A-n14A-n260KDC\_n2A-n14A-n260LDC\_n2A-n14A-n260M | DC\_n2A-n14ADC\_n2A-n260A/G/H/I/J/K/L/MDC\_n14A-n260A/G/H/I/J/K/L/M |
| DC\_n2A-n30A-n260ADC\_n2A-n30A-n260GDC\_n2A-n30A-n260HDC\_n2A-n30A-n260IDC\_n2A-n30A-n260JDC\_n2A-n30A-n260KDC\_n2A-n30A-n260LDC\_n2A-n30A-n260M | DC\_n2A-n30ADC\_n2A-n260A/G/H/I/J/K/L/MDC\_n30A-n260A/G/H/I/J/K/L/M |
| DC\_n2A-n48A-n260ADC\_n2A-n48A-n260GDC\_n2A-n48A-n260HDC\_n2A-n48A-n260IDC\_n2A-n48A-n260JDC\_n2A-n48A-n260KDC\_n2A-n48A-n260LDC\_n2A-n48A-n260M | DC\_n2A-n260A/G/H/IDC\_n48A-n260A/G/H/I |
| DC\_n2A-n48(2A)-n260ADC\_n2A-n48(2A)-n260GDC\_n2A-n48(2A)-n260HDC\_n2A-n48(2A)-n260IDC\_n2A-n48(2A)-n260JDC\_n2A-n48(2A)-n260KDC\_n2A-n48(2A)-n260LDC\_n2A-n48(2A)-n260M | DC\_n2A-n260A/G/H/IDC\_n48A-n260A/G/H/I |
| DC\_n2A-n48B-n260ADC\_n2A-n48B-n260GDC\_n2A-n48B-n260HDC\_n2A-n48B-n260IDC\_n2A-n48B-n260JDC\_n2A-n48B-n260KDC\_n2A-n48B-n260LDC\_n2A-n48B-n260M | DC\_n2A-n260A/G/H/IDC\_n48A-n260A/G/H/I |
| DC\_n2A-n48A-n261ADC\_n2A-n48A-n261GDC\_n2A-n48A-n261HDC\_n2A-n48A-n261IDC\_n2A-n48A-n261JDC\_n2A-n48A-n261KDC\_n2A-n48A-n261LDC\_n2A-n48A-n261M | DC\_n2A-n261A/G/H/IDC\_n48A-n261A/G/H/I |
| DC\_n2A-n48A-n261(G-H)DC\_n2A-n48A-n261(A-G-H)DC\_n2A-n48A-n261(2H)DC\_n2A-n48A-n261(H-I)DC\_n2A-n48A-n261(A-G-I)DC\_n2A-n48A-n261(A-H)DC\_n2A-n48A-n261(2G)DC\_n2A-n48A-n261(2A-H)DC\_n2A-n48A-n261(A-2G)DC\_n2A-n48A-n261(G-I)DC\_n2A-n48A-n261(2A-I)DC\_n2A-n48A-n261(A-G)DC\_n2A-n48A-n261(2A-G)DC\_n2A-n48A-n261(A-I)DC\_n2A-n48A-n261(2A)DC\_n2A-n48A-n261(3A) | DC\_n2A-n261A/G/H/IDC\_n48A-n261A/G/H/I |
| DC\_n2A-n48(2A)-n261ADC\_n2A-n48(2A)-n261GDC\_n2A-n48(2A)-n261HDC\_n2A-n48(2A)-n261IDC\_n2A-n48(2A)-n261JDC\_n2A-n48(2A)-n261KDC\_n2A-n48(2A)-n261LDC\_n2A-n48(2A)-n261M | DC\_n2A-n261A/G/H/IDC\_n48A-n261A/G/H/I |
| DC\_n2A-n48(2A)-n261(G-H)DC\_n2A-n48(2A)-n261(A-G-H)DC\_n2A-n48(2A)-n261(2H)DC\_n2A-n48(2A)-n261(H-I)DC\_n2A-n48(2A)-n261(A-G-I)DC\_n2A-n48(2A)-n261(A-H)DC\_n2A-n48(2A)-n261(2G)DC\_n2A-n48(2A)-n261(2A-H)DC\_n2A-n48(2A)-n261(A-2G)DC\_n2A-n48(2A)-n261(G-I)DC\_n2A-n48(2A)-n261(2A-I)DC\_n2A-n48(2A)-n261(A-G)DC\_n2A-n48(2A)-n261(2A-G)DC\_n2A-n48(2A)-n261(A-I)DC\_n2A-n48(2A)-n261(2A)DC\_n2A-n48(2A)-n261(3A) | DC\_n2A-n261A/G/H/IDC\_n48A-n261A/G/H/I |
| DC\_n2A-n48B-n261ADC\_n2A-n48B-n261GDC\_n2A-n48B-n261HDC\_n2A-n48B-n261IDC\_n2A-n48B-n261JDC\_n2A-n48B-n261KDC\_n2A-n48B-n261LDC\_n2A-n48B-n261M | DC\_n2A-n261A/G/H/IDC\_n48A-n261A/G/H/I |
| DC\_n2A-n48B-n261(G-H)DC\_n2A-n48B-n261(A-G-H)DC\_n2A-n48B-n261(2H)DC\_n2A-n48B-n261(H-I)DC\_n2A-n48B-n261(A-G-I)DC\_n2A-n48B-n261(A-H)DC\_n2A-n48B-n261(2G)DC\_n2A-n48B-n261(2A-H)DC\_n2A-n48B-n261(A-2G)DC\_n2A-n48B-n261(G-I)DC\_n2A-n48B-n261(2A-I)DC\_n2A-n48B-n261(A-G)DC\_n2A-n48B-n261(2A-G)DC\_n2A-n48B-n261(A-I)DC\_n2A-n48B-n261(2A)DC\_n2A-n48B-n261(3A) | DC\_n2A-n261A/G/H/IDC\_n48A-n261A/G/H/I |
| DC\_n2A-n66A-n260ADC\_n2A-n66A-n260GDC\_n2A-n66A-n260HDC\_n2A-n66A-n260IDC\_n2A-n66A-n260JDC\_n2A-n66A-n260KDC\_n2A-n66A-n260LDC\_n2A-n66A-n260M | DC\_n2A-n66ADC\_n2A-n260A/G/H/I/J/K/L/MDC\_n66A-n260A/G/H/I/J/K/L/M |
| DC\_n2A-n66A-n261ADC\_n2A-n66A-n261GDC\_n2A-n66A-n261HDC\_n2A-n66A-n261IDC\_n2A-n66A-n261JDC\_n2A-n66A-n261KDC\_n2A-n66A-n261LDC\_n2A-n66A-n261M | DC\_n2A-n66ADC\_n2A-n261A/G/H/IDC\_n66A-n261A/G/H/I |
| DC\_n2A-n66A-n261(2G)DC\_n2A-n66A-n261(G-H)DC\_n2A-n66A-n261(A-G-H)DC\_n2A-n66A-n261(G-I)DC\_n2A-n66A-n261(2H)DC\_n2A-n66A-n261(A-G-I)DC\_n2A-n66A-n261(H-I)DC\_n2A-n66A-n261(A-G)DC\_n2A-n66A-n261(A-H)DC\_n2A-n66A-n261(2A-H)DC\_n2A-n66A-n261(A-2G)DC\_n2A-n66A-n261(A-I)DC\_n2A-n66A-n261(2A-I)DC\_n2A-n66A-n261(2A)DC\_n2A-n66A-n261(3A)DC\_n2A-n66A-n261(2A-G) | DC\_n2A-n66ADC\_n2A-n261A/G/H/IDC\_n66A-n261A/G/H/I |
| DC\_n2A-n77A-n260ADC\_n2A-n77A-n260GDC\_n2A-n77A-n260HDC\_n2A-n77A-n260IDC\_n2A-n77A-n260JDC\_n2A-n77A-n260KDC\_n2A-n77A-n260LDC\_n2A-n77A-n260MDC\_n2A-n77C-n260ADC\_n2A-n77C-n260GDC\_n2A-n77C-n260HDC\_n2A-n77C-n260IDC\_n2A-n77C-n260JDC\_n2A-n77C-n260KDC\_n2A-n77C-n260LDC\_n2A-n77C-n260M | DC\_n2A-n77ADC\_n2A-n260A/G/H/I/J/K/L/MDC\_n77A-n260A/G/H/I/J/K/L/M |
| DC\_n2A-n77A-n261ADC\_n2A-n77A-n261GDC\_n2A-n77A-n261HDC\_n2A-n77A-n261IDC\_n2A-n77A-n261JDC\_n2A-n77A-n261KDC\_n2A-n77A-n261LDC\_n2A-n77A-n261MDC\_n2A-n77C-n261ADC\_n2A-n77C-n261GDC\_n2A-n77C-n261HDC\_n2A-n77C-n261IDC\_n2A-n77C-n261JDC\_n2A-n77C-n261KDC\_n2A-n77C-n261LDC\_n2A-n77C-n261M | DC\_n2A-n261A/G/H/IDC\_n77A-n261A/G/H/I |
| DC\_n2A-n77A-n261(G-H)DC\_n2A-n77A-n261(A-G-H)DC\_n2A-n77A-n261(G-I)DC\_n2A-n77A-n261(2H)DC\_n2A-n77A-n261(A-G-I)DC\_n2A-n77A-n261(H-I)DC\_n2A-n77A-n261(A-H)DC\_n2A-n77A-n261(2G)DC\_n2A-n77A-n261(2A-H)DC\_n2A-n77A-n261(A-2G)DC\_n2A-n77A-n261(A-I)DC\_n2A-n77A-n261(2A-I)DC\_n2A-n77A-n261(A-G)DC\_n2A-n77A-n261(2A-G)DC\_n2A-n77A-n261(2A)DC\_n2A-n77A-n261(3A)DC\_n2A-n77C-n261(G-H)DC\_n2A-n77C-n261(A-G-H)DC\_n2A-n77C-n261(G-I)DC\_n2A-n77C-n261(2H)DC\_n2A-n77C-n261(A-G-I)DC\_n2A-n77C-n261(H-I)DC\_n2A-n77C-n261(A-H)DC\_n2A-n77C-n261(2G)DC\_n2A-n77C-n261(2A-H)DC\_n2A-n77C-n261(A-2G)DC\_n2A-n77C-n261(A-I)DC\_n2A-n77C-n261(2A-I)DC\_n2A-n77C-n261(A-G)DC\_n2A-n77C-n261(2A-G)DC\_n2A-n77C-n261(2A)DC\_n2A-n77C-n261(3A) | DC\_n2A-n261A/G/H/IDC\_n77A-n261A/G/H/I |
| DC\_n3A-n7A-n258ADC\_n3A-n7A-n258BDC\_n3A-n7A-n258CDC\_n3A-n7A-n258DDC\_n3A-n7A-n258EDC\_n3A-n7A-n258FDC\_n3A-n7A-n258GDC\_n3A-n7A-n258HDC\_n3A-n7A-n258IDC\_n3A-n7A-n258JDC\_n3A-n7A-n258KDC\_n3A-n7A-n258LDC\_n3A-n7A-n258M | DC\_n3A-n258A/G/H/IDC\_n7A-n258A/G/H/I |
| DC\_n3A-n7B-n258ADC\_n3A-n7B-n258BDC\_n3A-n7B-n258CDC\_n3A-n7B-n258DDC\_n3A-n7B-n258EDC\_n3A-n7B-n258FDC\_n3A-n7B-n258GDC\_n3A-n7B-n258HDC\_n3A-n7B-n258IDC\_n3A-n7B-n258JDC\_n3A-n7B-n258KDC\_n3A-n7B-n258LDC\_n3A-n7B-n258M | DC\_n3A-n258A/G/H/IDC\_n7A-n258A/G/H/I |
| DC\_n3A-n18A-n257ADC\_n3A-n18A-n257GDC\_n3A-n18A-n257HDC\_n3A-n18A-n257I | DC\_n3A-n18ADC\_n3A-n257A/G/H/IDC\_n18A-n257A/G/H/I |
| DC\_n3A-n28A-n257A1DC\_n3A-n28A-n257G1DC\_n3A-n28A-n257H1DC\_n3A-n28A-n257I1 | DC\_n3A-n28ADC\_n3A-n257A/G/H/IDC\_n28A-n257A/G/H/I |
| DC\_n3A-n28A-n258ADC\_n3A-n28A-n258DDC\_n3A-n28A-n258GDC\_n3A-n28A-n258HDC\_n3A-n28A-n258IDC\_n3A-n28A-n258J | DC\_n3A-n28ADC\_n3A-n258A/D/G/H/I/JDC\_n28A-n258A/D/G/H/I/J |
| DC\_n3A-n41A-n257ADC\_n3A-n41A-n257GDC\_n3A-n41A-n257HDC\_n3A-n41A-n257I | DC\_n3A-n41ADC\_n3A-n257A/G/H/IDC\_n41A-n257A/G/H/I |
| DC\_n3A-n77A-n257A1DC\_n3A-n77A-n257G1DC\_n3A-n77A-n257H1DC\_n3A-n77A-n257I1 | DC\_n3A-n77ADC\_n3A-n257A/G/H/IDC\_n77A-n257A/G/H/I |
| DC\_n3A-n77(2A)-n257A1DC\_n3A-n77(2A)-n257G1DC\_n3A-n77(2A)-n257H1DC\_n3A-n77(2A)-n257I1 | DC\_n3A-n77ADC\_n3A-n257A/G/H/IDC\_n77A-n257A/G/H/I |
| DC\_n3A-n78A-n257A1DC\_n3A-n78A-n257G1DC\_n3A-n78A-n257H1DC\_n3A-n78A-n257I1 | DC\_n3A-n78ADC\_n3A-n257A/G/H/IDC\_n78A-n257A/G/H/I |
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| DC\_n77(2A)-n79A-n258ADC\_n77(2A)-n79A-n258DDC\_n77(2A)-n79A-n258GDC\_n77(2A)-n79A-n258HDC\_n77(2A)-n79A-n258IDC\_n77(2A)-n79A-n258J | DC\_n77A-n79ADC\_n77A-n258A/D/G/H/I/JDC\_n79A-n258A/D/G/H/I/J |
|  DC\_n78A-n79A-n257ADC\_n78A-n79A-n257GDC\_n78A-n79A-n257HDC\_n78A-n79A-n257I | DC\_n78A-n79ADC\_n78A-n257A/G/H/IDC\_n79A-n257A/G/H/I |
| DC\_n78(2A)-n79A-n257ADC\_n78(2A)-n79A-n257GDC\_n78(2A)-n79A-n257HDC\_n78(2A)-n79A-n257I | DC\_n78A-n79ADC\_n78A-n257A/G/H/IDC\_n79A-n257A/G/H/I |
| DC\_n78A-n79A-n259ADC\_n78A-n79A-n259GDC\_n78A-n79A-n259HDC\_n78A-n79A-n259IDC\_n78A-n79A-n259JDC\_n78A-n79A-n259KDC\_n78A-n79A-n259LDC\_n78A-n79A-n259M | DC\_n78A-n79ADC\_n78A-n259A/G/H/I/J/K/L/MDC\_n79A-n259A/G/H/I/J/K/L/M |
| DC\_n79A-n257A-n259ADC\_n79A-n257A-n259GDC\_n79A-n257A-n259HDC\_n79A-n257A-n259IDC\_n79A-n257A-n259JDC\_n79A-n257A-n259KDC\_n79A-n257A-n259LDC\_n79A-n257A-n259MDC\_n79A-n257G-n259ADC\_n79A-n257G-n259GDC\_n79A-n257G-n259HDC\_n79A-n257G-n259IDC\_n79A-n257G-n259JDC\_n79A-n257G-n259KDC\_n79A-n257G-n259LDC\_n79A-n257G-n259MDC\_n79A-n257H-n259ADC\_n79A-n257H-n259GDC\_n79A-n257H-n259HDC\_n79A-n257H-n259IDC\_n79A-n257H-n259JDC\_n79A-n257H-n259KDC\_n79A-n257H-n259LDC\_n79A-n257H-n259MDC\_n79A-n257I-n259ADC\_n79A-n257I-n259GDC\_n79A-n257I-n259HDC\_n79A-n257I-n259IDC\_n79A-n257I-n259JDC\_n79A-n257I-n259KDC\_n79A-n257I-n259LDC\_n79A-n257I-n259M | DC\_n79A-n257A/G/H/IDC\_n79A-n259A/G/H/I/J/K/L/M |
| NOTE 1: Applicable for UE supporting inter-band NR DC with mandatory simultaneous Rx/Tx capability.NOTE x: The delimiter “/” is only used in the uplink configurations for the sake of simplicity. For example, DC\_nxA-nyA/B/C denotes DC\_nxA-nyA, DC\_nxA-nyB and DC\_nxA-nyC, where nx and ny are two NR bands, ny is a FR2 band and A, B and C are the corresponding bandwidth classes respectively. |

### *<< End of changes >>*