3GPP TSG RAN WG1 #118bis R1-2409106

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**Agenda item: 9.10.1**

**Source: Moderator (Nokia)**

**Title: Moderator summary #3 - Enabling TX/RX for XR during RRM measurements**

**Document for: Discussion and Decision**

# Introduction

The following objectives were agreed to be part of Rel19 WI on XR, XR (eXtended Reality) for NR Phase 3 [1]:

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| - Specify support for multimodality in RAN for UL and DL [RAN3]: NOTE: This is subject to alignment with SA2, e.g., if MMSID is not available from CN, then UE assistance information-based approach as an alternative. RAN#106 to check handling of uplink discard based on SA2/SA4 outputs on whether the corresponding information is available at the UE.NOTE: Review in RAN#106 the conclusions in SA2 side (including MMSID delivery to RAN) to allocation for further TUs in RAN3 from Q1/25 onwards.- Specify enhancements to enable transmission/reception in gaps/restrictions that are caused by RRM measurements (from inter-frequency RRM measurement gaps, or intra-frequency measurements, or other scheduling restrictions etc). [RAN1, RAN2, RAN4] - Specify the corresponding measurement gap and scheduling restriction to enable the identified enhancements with RRM performance impact taken into consideration, work being triggered by LS. [RAN4]- Specify Enhancements for support of UL scheduling to enable high XR capacity while meeting delay requirements/avoiding too late PDUs, as follows [RAN2]:- Specify additional Logical Channel priority handling using delay/deadline information of packets;- Specify enhanced DSR (Delay Status Report) reporting with multiple pairs of remaining time and buffer size for an LCG.- Specify the following user plane enhancements [RAN2]:- RLC re-transmission related enhancements for operation of RLC Acknowledged Mode (AM) with small packet delay budget. - Extend Release 18 standalone mechanism to support NR-NR dual connectivity as follows [RAN3]:- PDU set based handling;- ECN marking;- Burst Arrival Time reporting, if needed;- PSI Discard coordination, if needed;NOTE: No RAN2 impact from above items.- Specify uplink congestion signalling [RAN2]: - Specify in MAC layer XR rate control signalling over downlink per QoS flow/per DRB to enable faster source rate adaption to uplink congestion.- Specify Core requirements related to the above objectives as necessary [RAN4]. |

According to the Work Item description [1], RAN1 shall carry the normative work for the following objective:

* Specify enhancements to enable transmission/reception in gaps/restrictions that are caused by RRM measurements (from inter-frequency RRM measurement gaps, or intra-frequency measurements, or other scheduling restrictions etc). [RAN1, RAN2, RAN4]

This document provides a summary of contributions submitted to RAN1#118bis under agenda item 9.10.1 Enabling TX/RX for XR during RRM measurements.

# Enabling TX/RX for XR during RRM measurements

## Details of Alt. 1-1 (Explicit indication by DCI)

### Companies proposals and observations

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| **Company** | **Proposals/Observations** |
| Apple | Proposal 1: To facilitate adaptation of legacy MGs, Network Controlled Small Gaps, MGs for multi-SIM, MGs for positioning with a unified signaling design, MGs can be indexed. A MG index set consists of one or more MG index, MGs referred by a MG index set can be skipped through a single NW indication.  |
| CATT | Proposal 1: The 1-bit Tx/Rx indication should always present in the scheduling DCI, including DCI format x\_1/x\_2/x\_3. Proposal 2: The default of 1-bit Tx/Rx indication should be set to “no cancellation”. Proposal 3: The 1-bit Tx/Rx indication could be set at the time for measurement gap cancellation before the minimum time offset prior to MGs or scheduling restriction.Proposal 4: If the running retransmission timer is overlapping with the MG or restriction occasion, the MG or the restriction occasion would be indicated “cancelation” by the 1-bit Tx/Rx indication.Proposal 5: The 1-bit Tx/Rx indication would be with the default value “no cancellation” in the scheduling DCIs at the start of DRX ON in each C-DRX cycle.Proposal 6: When the UE is indicated to skip the MG or scheduling restriction occasion in a given XR traffic arrival cycle, UE could also terminate or suspend the RRM cancelation behavior based on the following aspects:* The XR transmission is completed a time offset prior to the start of upcoming measurement gap, which is enough for preparing the upcoming RRM measurement.
* The XR transmission is not expected to be scheduled within the upcoming measurement gaps indicated by the Rel-17 PDCCH skipping indication.
* UE receives the DRX command MAC CE a time offset prior to the start of upcoming measurement gap, which is enough for preparing the upcoming RRM measurement.
* UE reports the CG-PUSCH TOs with the value of '1' via the UTO-UCI, in which the Tx/Rx are not collided with the upcoming measurement gap.
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| CMCC | Proposal 1: Regarding the DCI formats carrying the new one-bit field used for skipping indication, support DCI formats 0\_1 and 1\_1.Proposal 2: Regarding the dynamic indication to enable Tx/Rx in particular gap/restriction that are caused by RRM measurements, support to introduce a new higher layer parameter which is used to enable the inclusion of the new one-bit field in DCI formats 0\_1 and 1\_1.Proposal 3: Regarding the new one-bit field in DCI used for skipping indication, bit equal to 1 indicates the corresponding gap/restriction occasion is to be skipped while bit equal to 0 indicates the corresponding gap/restriction occasion is not to be skipped. Proposal 4. The 1bit skipping indication applies to all overlapped gaps/restrictions simultaneously when multiple MG/SMTC configurations are configured to a UE. |
| Ericsson | Observation 1 The design alternatives should be assessed to decide for a baseline design approach that results in a feature providing improved performance with reasonable level of complexity.Observation 2 Due to uncertainty in application packet arrival and size, as well as uncertainty in scheduling a transmission and/or its retransmission, a dynamic solution provides the network with the flexibility needed to improve XR capacity when a MG is required to enable UE measurements in a particular occasion or being skipped.Observation 3 Any semi-static approach is simple from UE perspective but inefficient from the NW perspective. Due to the inbuilt uncertainty on the need for utilizing a MG for serving the traffic, determining a proper configuration/pattern/time window that meets the intended objectives is impractical and results in resource wastage and unnecessary complexity.Observation 4 Explicit dynamic solution based on Alt. 1-1 provides the network with the flexibility needed to improve XR capacity while minimizing the specification impact.Proposal 1 Confirm the Working assumption in RAN1#118 for supporting Alt. 1-1.Proposal 2 Support Alt.1-1 for the DCI formats 0\_X/1\_X with X=0,1 and 2.Proposal 3 Support Alt. 1-1 where the indication in the DCI format on a scheduling cell corresponds to the MG on the scheduling cell (i.e. Option 1).Proposal 4 Support Alt. 1-1 where once an MG in indicated to be “skipped” by a scheduling DCI, it shall not be indicated to be “not skipped” by another scheduling DCI. |
| Fraunhofer IIS, Fraunhofer HHI | Proposal 1: The DCI can indicate the dynamic cancellation of only one MG occasion. This provides the scheduler sufficient flexibility needed to improve the XR capacity by dynamically adapting to the incoming data packets from the XR application.Proposal 2: Once indicated as skipped, a MG occasion should remain consistently indicated as skipped.Observation 1: Using one-bit signaling in the DCI to dynamically indicate the status of the earliest MG occasion starting after the ending symbol of the PDCCH carrying the bit-field may in some cases be ambiguous.Proposal 3: The dynamic indication of an MG occasion which is to be skipped is provided explicitly in the DCI by the ID of the corresponding MG configuration (measGapId). The earliest MG occasion of this MG configuration starting after the ending symbol of the PDCCH carrying the DCI is to be skipped.Proposal 4: The scheduling DCI formats x\_0, x\_1, and x\_2 (with x=0,1) can be used to dynamically indicate the skipping of a MG occasion. |
| Google | Proposal 2: Any gap/restriction occasion overlapping fully or partially with the time offset is not considered as the gap/restriction occasion indicated by the DCI.Proposal 3: UE should not skip the measurement gap/restriction if the overlapping CG PUSCH occasion within the multi-PUSCH CG configuration is cancelled by the UTO-UCIProposal 5: Overriding skipping indication is permitted at least when the UE cancels a CG PUSCH overlapping with the measurement gap/restriction. |
| Huawei, HiSilicon | Proposal 1: For “Alt 1-1: Explicit indication by DCI to skip a particular gap/restriction”, support:* This 1-bit field is included in DCI format 0\_1/1\_1, 0\_2/1\_2 and 0\_3/1\_3;
* A bit value of ‘0’ indicates that the gap/restriction caused by RRM measurements is not skipped, and a bit value of ‘1’ indicates that the gap/restriction caused by RRM measurements is skipped.
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| InterDigital | Observation 1: Scheduling restrictions can have major impact on transmission/reception of XR data with tight delay budgets. Delaying the transmissions to after MG results in not meeting QoS and impacts capacity.Observation 2: When there is an ongoing data Tx/Rx that may use only a subset of slots in a gap occasion, it can be useful to skip some slots at the beginning or end of the gap occasion rather than the entire gap occasion. Skipping some slots in between a gap occasion is not practical given the multiple times where RF retuning is involvedProposal 1: Confirm working assumption to support explicit indication by DCI to skip a particular gap/restriction (Alt 1-1)Proposal 2: Support the scheduling DCI formats 0\_1, 0\_3, 1\_1 or 1\_3 (as baseline) for explicity indicating to skip a particular gap/restriction Proposal 3: Support DCI-based skipping that is configurable for a subset of MG configurations/restrictions (e.g. for concurrent gaps) |
| Lenovo | Proposal 1: A UE first applies measurement skipping DCI command before resolving collision amongst concurrent measurement gaps. Proposal 2: Discuss whether time gap between DCI and earlier occasion of concurrent occasions needs to be larger than a minimum time (e.g., DCI processing time) to apply measurement occasion skipping prior to resolving collision amongst concurrent measurement gaps.Proposal 3: DCI indication of skipping a measurement occasion cannot be overridden later.Proposal 4: The bit in the DCI is used to indicate whether to skip the first gap/restriction occasion after a minimum time offset required between the last symbol of the last PDCCH candidate carrying the DCI format and the start of corresponding skipped gap/restriction occasion indicated by the DCI. |
| LGE | Proposal 1: Consider the following options for gap/restriction skipping behavior in Alt. 1-1:* + Option 1: (Uni-directional skipping) Once an MG is skipped, it cannot be re-enabled by subsequent indications
	+ Option 2: (Last DCI prioritization) Prioritize the last DCI received until the minimum time offset from the start of the corresponding skipped gap/restriction

Proposal 2: Support that DCI format indicates to skip a particular gap/restriction without PDSCH/PUSCH schedulingProposal 3: Support configuration for Alt. 1-1 that specifies applicable or protected gaps/restrictions |
| MediaTek | Proposal 4: Introduce a new single-bit DCI field. When MG skipping is enabled by RRC, the DCI field is present. When MG skipping is disabled by RRC, the DCI field is not present. Proposal 5: Support both DL-scheduling DCI and UL-scheduling DCI. Proposal 6: Support DCI formats 0\_1/1\_1 and 0\_2/1\_2. Do not support 0\_3/1\_3. |
| Meta | Proposal 1: Regarding DCI formats for triggering, 1\_1/0\_1 and 1\_2/0\_2 are both supported. Proposal 2: Regarding DCI content, bit equal to 1 means gap/restriction occasion is skipped, bit equal to 0 means gap/restriction occasion is not skipped. |
| NEC | Proposal 1: confirm the working assumption in RAN1#118.Proposal 2: support UE to ignore a skipping indication for a MG/restriction occasion if the measurement in this MG/restriction occasion is critical. Proposal 3: if UE received a DCI which indicates to skip a MG/restriction occasion, UE does not expect to receive another DCI which indicates the MG/restriction occasion should not be skipped. |
| Nokia | Observation 1: RAN1 shall comply with conclusions/decisions from RAN4 on which gap/restriction types are applicable for skipping.Observation 2: Explicit indication by DCI to skip a particular gap/restriction (Alt 1-1) is sufficiently flexible to cover all cases and it can be implemented to mimic the behaviour of other solutions. Therefore, additional solutions are not necessary for Release 19.Proposal 1: Clarify the note from previous agreement as follows:* Note: Minimum time offset(s) between the end of ~~[the first]~~ received dynamic indication and start of corresponding gap(s)/restriction(s) occasion that is going to be skipped shall be introduced.

Proposal 2: The term “first gap/restriction” refers to the first applicable gap(s)/restriction(s).Proposal 3: For Alt 1-1: Explicit indication by DCI to skip a particular gap/restriction, it should be possible to configure the following scheduling DCI formats with a skipping indication bit included:* DCI formats 1\_1 and 0\_1 as well as DCI formats 1\_2 and 0\_2

Proposal 4: For DCI content of Alt.1-1 the following is suggested:* Bit equal to 1 means applicable gap/restriction occasion is skipped.

Proposal 5: Once the UE has received DCI indication to skip (bit set to 1) the next applicable measurement gap/restriction, UE does not expect the indication to be changed until time offset before the next applicable gap/restriction.Proposal 6: The presence of skipping indication in supported DCI formats can be configured individually for each DCI format via a new higher layer parameter. |
| NTT DOCOMO | Proposal 1: DL scheduling DCI format (e.g. DCI format 1\_1/1\_2) and UL scheduling DCI format (e.g. DCI format 0\_1/0\_2) can be used for the dynamic indication.* The 1 bit field presence in a DCI format is configured by RRC.

Proposal 2: UE doesn’t expect different indications for a same gap/restriction occasion. |
| OPPO | Proposal 1: Based on RAN1 #118 working assumption,* A new field of one bit is introduced into DCI formats 0\_1/2/3 and 1\_1/2/3, and the presence of the new field configured by RRC per DCI format;
* The first gap(s)/restriction(s) occasion is among the gap(s)/restriction(s) occasions that are eligible to be skipped, where the eligibility is subject to RAN4 discussion.

Proposal 2: How UE handles skipping of a single gap/restriction occasion based on the corresponding indications in multiple DCIs should be discussed, preferably with the following options:* Option 1: If a UE receives at least one DCI indicating to skip a gap/restriction occasion, the UE skips the gap/restriction occasion.
* Option 2: If a UE receives a DCI indicating to skip a gap/restriction occasion, the UE does not expect to receive a later DCI indicating no skipping of the same gap/restriction occasion and the UE skips the gap/restriction occasion.
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| Panasonic | Proposal 1: At least DCI formats 0\_2/1\_2 should support carrying MG skipping indication.Proposal 2: A gNB should send DCIs with consistent MG skipping content corresponding to a single MG occasion. In particular, the gNB should not change the indication content for a MG occasion after indicating to skip the MG indication.Proposal 3: The order of applying MG skipping indication and handling the MG collision should be defined. |
| Qualcomm | Proposal 1: Dynamic indication to enable Tx/Rx in a particular gap/restriction are based on DCI formats 0\_1 and 1\_1.Proposal 2: RAN1 clarifies whether the UE follows the dynamic indication bit in a DCI format to enable Tx/Rx in a particular gap/restriction that is caused by RRM measurements if the DCI format does not schedule a PDSCH.Proposal 3: When overlapping between gaps/restrictions that are caused by RRM measurements occurs, the dynamic indication to enable Tx/Rx in a particular gap/restriction that is caused by RRM measurements is applied after the overlapping is resolved.Proposal 4: RAN1 does not assume all gap/restriction configurations and all gaps/restrictions within a same configuration are subject to the dynamic indication to enable Tx/Rx in a particular gap/restriction that is caused by RRM measurements. |
| Samsung | Proposal 1: Consider DCI format x\_1 and x\_2 for 1-bit indication to enable Tx/Rx in gaps/restrictions that are caused by RRM measurements. Proposal 2: Consider ‘1’ means “a particular gap/restriction is skipped” and ‘0’ means “a particular gap/restriction is not skipped”. Proposal 3: The UE determines whether or not to skip a gap/restriction based on the value of the indication in the last DCI that applicable for the gap/restriction and the UE correctly receives. |
| Sony | Observation 1: UE has better knowledge, particularly for the UL traffic. Hence, UE assistance information could be beneficial in assisting gNB to allow XR traffic when there is a collision between XR traffic and RRM measurement.Observation 2: UE assistance information indicating the number of gap(s) / restriction(s) that can be skipped during a configured RRM measurement is beneficial in the operation of XR transmission during RRM measurement.Observation 3: Skipping RRM measurement may affect the quality and/or latency of the reported RRM measurement. It would be beneficial for gNB to know whether the RRM measurement has been compromised or not.Proposal 1: Confirm the working assumption to support dynamic indication to enable Tx/Rx in particular gap/restriction that are caused by RRM measurements. The dynamic indication is an explicit indication by DCI to skip a particular gap/restriction.Proposal 2: Support to introduce a new bit-field in the existing DCI format (e.g., DCI x\_1). |
| Spreadtrum Communications | Proposal 1: To address the ambiguity on the DCI indication in scenarios involving multiple configurations, two interpretations emerge, with a slight preference for Option 1:* Option 1: The DCI indication is applicable to the first gap/restriction occasion after a minimum time offset, irrespective of which configuration it belongs to among the multiple MG configurations.
* Option 2: The DCI indication is strictly confined to the first gap/restriction occasion after a minimum time offset, which belongs to a particular one among the multiple MG configurations.

Proposal 2: Study how to indicate the later gap/restriction in scenarios where overlapping occurs between two gaps/restrictions occasion when multiple configurations are activated.Proposal 3: Considering the following details of DCI contents:1. A value of 0 signifies that measurements are to be conducted during gaps/restrictions, whereas a value of 1 denotes Tx/Rx operations within these intervals, skipping the gap/restriction occasion.
2. Enable MG skipping enhancements through higher-layer RRC parameters, with DCI format 1\_1 or 0\_1 by default.
3. The availability of DCI formats 1\_2 or 0\_2 depends on the specific configuration of higher-layer RRC parameters.
4. When a gap/restriction occasion is indicated as “cancelled (1)” earlier, gNB is not allowed to be indicated as “NOT cancelled (0)” later; when a gap/restriction occasion is indicated as “NOT cancelled (0)” earlier, gNB is allowed to be indicated as “cancelled (1)” later.
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| TCL | Proposal 1. Confirming the working assumption, for solutions based on triggering/enabling by network signaling to enable Tx/Rx in gaps/restrictions that are caused by RRM measurements select the following option:* Alt. 1: Dynamic indication to enable Tx/Rx in particular gap/restriction that are caused by RRM measurements.
	+ Alt 1-1: Explicit indication by DCI to skip a particular gap/restriction;
		- Indication is included as part of scheduling DCI:
			* Bit-field size is one bit;
				+ The bit in the DCI is used to indicate whether to skip the first gap/restriction occasion after a minimum time offset required between the last symbol of the PDCCH carrying the DCI format and the start of corresponding skipped gap/restriction occasion indicated by the DCI.

Proposal 2: The current UE-specific DCI format (e.g. DCI format 0\_x/1\_x) can be used for dynamic indication to enable Tx/Rx within measurement gap(s)/restriction(s). |
| vivo | Proposal 1: To enable Tx/Rx in gaps/restrictions that are caused by RRM measurements with Alt. 1, DCI formats 1\_1 and 1\_2 are supported.* The presence of the one-bit indication in different DCI formats is configured per DCI format.
* FFS DCI formats 0\_1 and 0\_2.

Proposal 2: For a DCI indicating to skip a particular gap/restriction occasion, the DCI can indicate invalid FDRA without scheduling a PDSCH reception.Proposal 3: When a UE is configured with multiple concurrent measurement gap configurations, if there are collided measurement gap occasions, the UE first solves collision among different measurement gap occasions as legacy, and DCI indication to enable Tx/Rx in a particular gap/restriction that are caused by RRM measurements is applied to a survived measurement gap occasion after collision handling.Proposal 4: For solutions based on triggering/enabling by network signaling to enable Tx/Rx in gaps/restrictions that are caused by RRM measurements, the bit in a DCI is used to indicate whether to skip the first skippable gap/restriction occasion after a minimum time offset required between the last symbol of the PDCCH carrying the DCI and the start of corresponding skipped gap/restriction occasion indicated by the DCI.* FFS: how to determine whether an occasion is skippable or not
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| Xiaomi | Observation 3：The scheme based on dynamic indications does not align well with the target use case where non-integer periodic XR services do not align with integer periodic RRM..Proposal 3：Support Alt 3-1 as the solution to enable Tx/Rx in gaps/restrictions that are caused by RRM measurements. |
| ZTE | Observation 1: Multiple measurement gaps can be configured for one UE, including e.g., Rel-17 concurrent measurement gap.Observation 2: In the scenario of multiple overlapped gaps/restrictions configured and activated for one UE, single bit indication may cause ambiguities on UE behaviors.Proposal 1: For the scenario of single gap/restriction configured and activated for one UE, support the bit in the DCI is used to indicate whether to skip the first gap/restriction after a minimum time offset.Proposal 2: In the scenario of multiple overlapped gaps/restrictions configured and activated for one UE, the 1-bit field in DCI signaling for the measurement gap can be down-selected in one of the following two ways:• Option 1: The 1-bit field in DCI indicates to skip two conflicting measurement gap occasions simultaneously.• Option 2: The 1-bit field in DCI indicates to skip the measurement gap occasion with high priority.Proposal 3: Support indicating to skip measurement gap/restriction via non-fall back scheduling DCI, i.e., DCI format x\_1, x\_2, x\_3. |

### Moderator's summary of contributions

The following list summarizes details of Alt. 1-1 (Explicit indication by DCI) that companies provided in Tdocs submitted to RAN1#118bis:

**Scheduling DCI formats to include 1 bit indication field:**

* Support DCI formats 0\_0/1\_0: **Ericsson, Fraunhofer**
* Support DCI formats 0\_1/1\_1: **Ericsson, Fraunhofer, MediaTek, Meta, Nokia, NTT DOCOMO, Samsung, vivo** (FFS: 0\_1 and 0\_2), **CMCC, Qualcomm, CATT, Huawei, InterDigital, OPPO, TCL, ZTE**
* Support DCI formats 0\_2/1\_2: **Ericsson, Fraunhofer, MediaTek, Meta, Nokia, NTT DOCOMO, Samsung, vivo** (FFS: 0\_1 and 0\_2), **Panasonic, CATT, Huawei, OPPO, TCL, ZTE**
* Support DCI formats 0\_3/1\_3: **CATT, Huawei, InterDigital, OPPO, TCL, ZTE**
* Do not support DCI formats x\_3 (where x is 0 or 1): **MediaTek**

**Scheduling DCI formats that do not schedule data:**

RAN1 clarifies whether the UE follows the dynamic indication bit in a DCI format to enable Tx/Rx in a particular gap/restriction that is caused by RRM measurements if the DCI format does not schedule a PDSCH: **Qualcomm, vivo, LGE**

**Interpretation of 1-bit indication field in DCI:**

* Bit equal to 1 indicates the corresponding gap/restriction occasion is to be skipped: **CMCC, Huawei, Meta, Nokia, Samsung, Spreadtrum**
* Bit equal to 0 indicates the corresponding gap/restriction occasion is not to be skipped: **CMCC, Huawei, Meta, Samsung, Spreadtrum**
* The default of 1-bit Tx/Rx indication should be set to “no cancellation”: **CATT**
	+ The 1-bit Tx/Rx indication would be with the default value “no cancellation” in the scheduling DCIs at the start of DRX ON in each C-DRX cycle: **CATT**
	+ If the running retransmission timer is overlapping with the MG or restriction occasion, the MG or the restriction occasion would be indicated “cancelation” by the 1-bit Tx/Rx indication: **CATT**

**Changing the indication from “skipped” to “not skipped” and vice versa:**

* From “skipped” to “not skipped”:
* Once an MG is indicated to be “skipped” by a scheduling DCI, the MG shall not be indicated to be “not skipped” by another scheduling DCI: **Ericsson, Fraunhofer, Lenovo, NEC, Nokia, OPPO, Panasonic, Spreadtrum, LGE**
* Overriding skipping indication is permitted at least when the UE cancels a CG PUSCH overlapping with the measurement gap/restriction: **Google**
* The UE determines whether or not to skip a gap/restriction based on the value of the indication in the last DCI that applicable for the gap/restriction and the UE correctly receives (overriding is possible?): **Samsung, LGE**
* The XR transmission is completed a time offset prior to the start of upcoming measurement gap, which is enough for preparing the upcoming RRM measurement: **CATT**
* The XR transmission is not expected to be scheduled within the upcoming measurement gaps indicated by the Rel-17 PDCCH skipping indication: **CATT**
* UE receives the DRX command MAC CE a time offset prior to the start of upcoming measurement gap, which is enough for preparing the upcoming RRM measurement: **CATT**
* UE should not skip the measurement gap/restriction if the overlapping CG PUSCH occasion within the multi-PUSCH CG configuration is cancelled by the UTO-UCI: **CATT, Google**
* From “not skipped” to “skipped”:
	+ When a gap/restriction occasion is indicated as “NOT cancelled (0)” earlier, gNB is allowed to be indicated as “cancelled (1)” later: **Spreadtrum**

**Configuration of one bit indication field in DCI formats:**

* Introduce a new higher layer parameter which is used to enable the inclusion of the new one-bit field in DCI: **CMCC, MediaTek, NTT DOCOMO**
* The presence of the new field is configured by RRC per DCI format: **Nokia,** **OPPO, Spreadtrum**
* 1 bit indication is always present in DCI formats: **CATT**

**Clarification of “first gap/restriction” that indication is referring to:**

* The first gap(s)/restriction(s) occasion is among the gap(s)/restriction(s) occasions that are eligible to be skipped, where the eligibility is subject to RAN4 discussion: **OPPO**
	+ *Related to different types of gaps/restrictions that RAN4 is discussing*
* The term “first gap/restriction” refers to the first applicable gap(s)/restriction(s): **Nokia**
	+ *Related to different types of gaps/restrictions that RAN4 is discussing*
* The first skippable gap/restriction (FFS: how to determine whether an occasion is skippable or not): **vivo**
	+ *Related to the same configuration and some additional restrictions which gaps inside this configuration cannot be skipped based on UAI*

**Clarification of indication:**

* DCI-based skipping that is configurable for a subset of MG configurations/restrictions: **InterDigital**
* The DCI indication is applicable to the first gap/restriction occasion after a minimum time offset, irrespective of which configuration it belongs to among the multiple MG configurations: **Spreadtrum** (1st pref)
* The DCI indication is strictly confined to the first gap/restriction occasion after a minimum time offset, which belongs to a particular one among the multiple MG configurations: **Spreadtrum**
* The bit in the DCI is used to indicate whether to skip the first gap/restriction occasion after a minimum time offset required between the last symbol of the last PDCCH candidate carrying the DCI format and the start of corresponding skipped gap/restriction occasion indicated by the DCI: **Lenovo**
* Indication in the DCI format on a scheduling cell corresponds to the MG on the scheduling cell: **Ericsson**

**Indicating MG configurations that can or cannot be skipped:**

* Index MG configuration, A MG index set consists of one or more MG index, MGs referred by a MG index set can be skipped through a single NW indication: **Apple**
* Indicate the MG ID: **Fraunhofer**
* The list of indices of gaps/restrictions to be applied or protected: **LGE**

**Clarify the note from previous agreement:**

Note: Minimum time offset(s) between the end of ~~[the first]~~ received dynamic indication and start of corresponding gap(s)/restriction(s) occasion that is going to be skipped shall be introduced: **Nokia**

**About working assumption on Alt. 1-1:**

* Confirm working assumption: **Ericsson, InterDigital, NEC, Sony, TCL**
* Further discuss the working assumption: **Apple**

### Discussion: Round #1

Moderator’s comments and recommendations:

There are several details that need to be discussed and decided for Alt. 1-1. A summary of companies views on the issues below can be found in Sec. 2.1.2.

**Scheduling DCI formats to include 1 bit indication field:**

A number of companies supported including 1 bit indication field to DCI formats x\_1/x\_2 (where x is 1 and 0).

Supporting these formats shall provide sufficient opportunities to indicate skipping of a gap/restriction. There are certain benefits of supporting x\_2 in addition to x\_1 since it can have a smaller DCI size based on configurations. Some companies did not support including the indication as part of DCI format x\_3 due to quite large size of this DCI format. Few companies proposed to change fallback DCI format; however, it is not recommended to include the indication to that format since DCI size shall be aligned across all UEs and be decodable in case device configurations is not clear at a gNB side.

Moderator’s recommendation is to support DCI formats x\_1/x\_2 for indication to skip gap/restriction caused by RRM measurements.

**Scheduling DCI formats that do not schedule data**

As mentioned by companies, there are scheduling DCIs that do not schedule data but used for other purposes e.g., SPS release, SCell dormancy, etc. Based on moderator’s understanding, if bit field is included in the DCI format x\_1/x\_2 UE shall consider that field regardless of the purpose of DCI. If companies see it beneficial, we can clarify this further that no special behavior is assumed in Rel19.

**Interpretation of 1-bit DCI field and changing the indication from “skip” to “do not skip” and vice versa:**

We shall clarify the meaning of a bit in a new bit field and possibility to change the indication for the same gap/restriction. Following similar principle for UTO-UCI, it is suggested once we indicate gap/restriction as “skipped” we do not change it to “not skipped”. However, changing from “not skipped” to “skipped” shall be supported since there might be multiple DCIs prior to gap/restriction and before minimum time offset, so gNB can indicate “skip” closer to gap/restriction.

Thus, the following is proposed based on companies’ input:

* Bit equal to 1 indicates the corresponding gap/restriction occasion is to be “skipped”. Once a gap/restriction is indicated to be “skipped” by a DCI, UE does not expect the gap/restriction to be indicated as ‘not skipped’ by another DCI.
* Bit equal to 0 indicates the corresponding gap/restriction occasion is to be “not skipped”. Once a gap/restriction is indicated to be “not skipped” by a DCI, the gap/restriction can be indicated to be “skipped” by another DCI.

**Configuration of one bit indication field in DCI formats**

Next, we shall decide how one bit indication is included in the DCI formats, i.e., one higher layer parameter to configure indication in all DCI formats supported to carry such indication or one higher layer parameter to configure indication per DCI format that supports such indication. It would be good to collect your views on possible way forward.

**Clarification of “first gap/restriction” that indication is referring to**

Last meeting we agreed on some details for Alt. 1-1, including the formulation: “The bit in the DCI is used to indicate *whether to skip the first gap/restriction occasion* after a minimum time offset required between the last symbol of the PDCCH carrying the DCI format and the start of corresponding skipped gap/restriction occasion indicated by the DCI.”

As commented by various companies, RAN4 is currently working on types of gaps that can or cannot be considered for possible skipping. Therefore, it would be good to clarify this point in RAN1 that the first gap/restriction is among gap/restriction configuration that are allowed for skipping from RAN4 perspective.

**Clarification of indication:**

There were other points raised related to the indication that we can further clarify:

* The DCI indication is applicable to the first gap/restriction occasion after a minimum time offset, irrespective of which configuration it belongs to among the multiple MG configurations;
* The bit in the DCI is used to indicate whether to skip the first gap/restriction occasion after a minimum time offset required between the last symbol of the last PDCCH candidate carrying the DCI format and the start of corresponding skipped gap/restriction occasion indicated by the DCI – *this is related to the case with PDCCH repetition.*
* Indication in the DCI format on a scheduling cell corresponds to the MG on the scheduling cell.

**Indicating MG configurations that can or cannot be skipped:**

There were contributions proposing a solution to indicate which gap configurations can or cannot be skipped e.g., using indexes of gaps. From moderator’s point of view, it is related to the discussion above on clarification of “first gap/restriction”. In RAN1 we need to clarify that we only refer to those gap/restriction configurations that can be potentially skipped as per RAN4 decision. It will be up to RAN4 to decide how to capture in specs which gap/restriction can or cannot be skipped. We can have a separate discussion on that to clarify our way forward.

**Clarify the note from previous agreement:**

Another point that shall be considered is the square brackets in the notes from earlier agreement: “Note: Minimum time offset(s) between the end of [the first] received dynamic indication and start of corresponding gap(s)/restriction(s) occasion that is going to be skipped shall be introduced”.

Since we already agreed that there is a minimum time offset between indication and gap/restriction, the [the first] in the note above is no longer needed and we can remove it.

**About working assumption on Alt. 1-1:**

Some companies suggested confirming working assumption on Alt. 1-1. RAN4 just received RAN1 LS with the working assumption, it would be good to give them time to discuss it. We can come back to that discussion later this week if RAN4 makes progress on that topic.

**Please, provide your view (in the table below) regarding the following questions:**

|  |
| --- |
| **Q1:** Do you agree with the following proposals? Please, feel free to suggest modifications if necessary. **Proposal 2.1.1**For Alt 1-1: explicit indication by DCI to skip a particular gap/restriction, one bit indication is included as part of DCI formats 0\_1/1\_1 and 0\_2/1\_2.**Proposal 2.1.2**For Alt 1-1: explicit indication by DCI to skip a particular gap/restriction:* Bit equal to 1 indicates the corresponding gap/restriction occasion is to be “skipped”. Once a gap/restriction is indicated to be “skipped” by a DCI, UE does not expect the gap/restriction to be indicated as ‘not skipped’ by another DCI.
* Bit equal to 0 indicates the corresponding gap/restriction occasion is to be “not skipped”. Once a gap/restriction is indicated to be “not skipped” by a DCI, the gap/restriction can be indicated to be “skipped” by another DCI.

**Proposal 2.1.3**Clarify the note from previous agreement as follows:* Note: Minimum time offset(s) between the end of ~~[the first]~~ received dynamic indication and start of corresponding gap(s)/restriction(s) occasion that is going to be skipped shall be introduced.

**Q2:** Would you like to clarify the following: “UE follows the indication bit in a scheduling DCI format (e.g., 1\_1/1\_2) to enable Tx/Rx in a particular gap/restriction that is caused by RRM measurements if the scheduling DCI format does not schedule data and used for other purposes, e.g., SPS release, SCell dormancy, etc.”?**Q3:** For configuration of one bit indication to “skip/not skip” in DCI formats, please indicate your preferred option:* **Option 1:** One higher layer parameter to configure indication in all DCI formats supported to include such indication.
* **Option 2:** One higher layer parameter to configure indication per DCI format supporting such indication.

**Q4:** Do you agree to further clarify “first gap/restriction” as “first applicable/eligible/etc gap/restriction”? It would be up to RAN4 to define the applicability/eligibility/etc of the gap/restriction (they are currently discussing types of gaps that can or cannot be skipped).**Proposal 2.1.4**The first gap/restriction occasion is among those types of gap/restriction configurations that are eligible to be skipped, where the eligibility is subject to RAN4 discussion.**Q5:** Please, share your view about further clarification (in *italic*) related to the indication raised by companies and indicate whether you agree to further clarify it (proponents, please feel free to further clarify the motivation from additional clarification):1. The DCI indication is applicable to the first gap/restriction occasion after a minimum time offset, *irrespective of which configuration it belongs to among the multiple gap/restriction configurations*;
2. The bit in the DCI is used to indicate whether to skip the first gap/restriction occasion after a minimum time offset required *between the last symbol of the last PDCCH candidate* carrying the DCI format and the start of corresponding skipped gap/restriction occasion indicated by the DCI;
3. Indication in the DCI format *on a scheduling cell corresponds to the MG on the scheduling cell*.

**Q6:** Do you agree to leave the decision on how to indicate which gap/restrictions configurations can or cannot be skipped up to RAN4? **Q7:** Please, share your view related to a way forward with working assumption for Alt. 1-1.**Q8**: Any other details for Alt. 1-1 that need to be clarified? |

|  |  |
| --- | --- |
| **Company** | **Answers/Comments** |
| Qualcomm | Q1: Proposal 2.1.1:DCI formats 0\_2/1\_2 were originally specified for URLLC data scheduling. If DCI formats 0\_2/1\_2 are to be used to carry the gap/restriction skipping indication, can the proponents clarify whether the DCI format 0\_2/1\_2s are meant to be used to schedule XR data or just to carry the skipping indication but not necessarily schedule XR data?Proposal 2.1.2:RAN1 has only discussed how to skip a configured gap/restriction for UE Tx/Rx, but has not discussed whether an early indication to skip a gap can be reverted to not skipped. This can be indicated by the following sentence in the working assumption. "Alt. 1: Dynamic indication to enable Tx/Rx in particular gap/restriction that are caused by RRM measurements..."We are fine with Proposal 2.1.2 in general. It is more challenging for the UE to revert from "skipped" to "not skipped". For this case, a separate minimum time offset that is larger than that for indicating a configured gap to "skipped" needs to be defined. Having said this, we think the “corresponding gap/restriction occasion” for a DCI format including the 1 bit can be different depending on bit value.Proposal 2.1.3:Together with the working assumption, "the first" may have indicated that once a gap/restriction is indicated to be "skipped", it does not matter what the bit value is in the subsequent DCIs and these DCIs can be ignored. Then, this has implied proposal 2.1.2. Based on this, we think "the first" does not need to be removed.Q2:Yes, we think this should be clarified so that there is no ambiguity in UE implementation. I.e., whether value of the 1 bit in this DCI format should be ignored by the UE or the UE also follows the bit value even it does not schedule the PDSCH. If the DCI format is followed by the UE, it provides additional opportunities for network to indicate skipped gaps/restrictions. This also implies that a DCI format that indicates skipped gap/restriction does not necessarily schedule the XR data.Q3:We think option 2 is a better.Q4Proposal 2.1.4:Yes, we agree with the proposal. At least in RAN1 discussion we do not assume all configurations of gaps/restritions due to RRM measurement or all gaps/restrictions in a configuration are subject to skipping.Q5:1.We can confirm this understanding once Proposal 2.1.4 is confirmed first. I.e., the DCI indication is applicable to the first “skippable” gap/restriction occasion after a minimum time offset *irrespective of which configuration it belongs to among the multiple gap/restriction configurations*;2.If the key point of this clarification is "last PDCCH candidate", it depends on Proposal 2.1.2. We do not think this needs to be separately discussed. If the key point of this clarification is "the last symbol", this is included in the following agreement and is up to RAN4 discussion. AgreementIf Alt. 1 from RAN1#117 agreement is supported, minimum time offset(s) X between indication to skip and skipped measurement occasion is up to RAN4 to discuss and decide on particular value(s).In either case, we do not think this clarification needs to be discussed separately.3.We support this proposal. RAN1 has not studied and discussed the necessity for gap skipping for CA case. For CA, the multiple carriers should provide enough resources for prompt XR data transfer within the PDB and there is no need to skip gaps/restrictions. It is even fine to us to restrict the feature of DCI indicated gap/restriction skipping to SC.Q6:Yes, we agree with this. It should be also up to RAN4 whether any gap/restriction of a gap/restriction configuration can or cannot be skipped. Based on this, the conclusion can be:leave the decision on how to indicate which gap/restrictions configurations and which gap/restrictions of a gap/restriction configuration can or cannot be skipped up to RAN4Q7:Working assumption was made because companies wanted to check with RAN4 first for issues of the DCI based indication for gap skipping. RAN4 has not really discussed this yet. Therefore, we think RAN1 does not need to confirm the working assumption in this meeting. |
| InterDigital | **Q1:**Proposal 2.1.1: Using DCI format x\_1 for indicating skipping of a gap occasion makes sense given its usage for single cell scheduling. However, it is not clear to us the need to also support format x\_2 for indicating gap skipping.Proposal 2.1.2:Fine with the proposal. Proposal 2.1.3:Fine to drop ‘the first’ from the note, although it does seem to be related to proposal 2.1.2. **Q2:** Generally fine with the understanding that the UE follows the configured gap-skipping indication regardless of the purpose and what the other bits indicate in the DCI format.**Q3:**Option 2**Q4:**Generally agree that the discussion on eligibility of which of the gap/restriction configurations are skippable is up to RAN4. However, it may be useful to clarify the need for proposal 2.1.4, before we wait for RAN4 conclusion. Additionally, since it is possible that the different eligible gap/restriction configurations may be configured with different priority values, it may be useful to discuss/clarify whether the skipping is applied on the first gap/restriction occasion regardless of priority of the configuration or the priority is considered before skipping.  |
| Samsung | Q1: OkayQ2: Non-scheduling DCI is feasible since DCI format would include the corresponding 1-bit skipping indicator. Q3: Either way is okay. But, option 2 seems align with current RRC structure. Q4: AgreeQ5: No further clarification is not needed. It is related to RAN4 discussion. A6: Agree. |
| Lenovo | Q1: OkQ2: OK to clarifyQ3: option 2 to provide more flexibilityQ4: fine to clarify; however, it may be better to just wait for RAN4, and not spend time nowQ5: 1,3: wait for RAN4; 2: OKQ6: YesQ7: we can wait for RAN4 |
| New H3C | Q1: OKQ3: prefer option 2. Q4: OKQ6: OK |
| LG | **Q1:** We think it would be better to add x\_3 if it works as same as other DCI format. Q2: We don’t think it should be DCI without SCH. We would like to propose to introduce additional option to indicate gap skipping without scheduling in addition to gap skipping with scheduling. Due to significant minimum time gap, gNB cannot predict traffic in that advance. **Q3:** If we consider DCI format x\_2, it should be option 2. **Q4:** the previous agreement is about “the type” of gap/restriction. The status of gap/restriction are still out scope. **Q5:** For the “*irrespective of which configuration…*”, we think it should be configurable and it should be semi-statically predictable, due to significant length of minimum time gap. Given that, it should target the first applicable gap based on configured target cell/FR.Regarding “*on a scheduling cell …*”, we think it is not appropriate due to significant length of minimum time gap. The cell should be pre-configured based on the cell that serves XR traffic and gap configuration. **Q6:** Agree for the type of gap/restriction as we agreed before. |
| Ericsson | Q1. Agree with three proposals..For Proposal 2.1.1, We are also OK to limit only to DCI 0\_1/1\_1 if there is a strong concern to include 0\_2/1\_2.Q2: Yes. When we mentioned Scheduling DCI, according to spec, in means DCI formats 0\_x/1\_x with x=0,1,2,3. Of course, these DCI can do other actions without scheduling any data, for example trigger A-CSI without UL-SCH. So, the above is more clarification but doesn’t need any spec impact.Q3: The Options are not clear. What is important to clarify the behavior per serving cell. In “ServingCellConfig”, via the search space configuration of the serving cell, we determine which DCI formats UE is expected to monitor. Q4: Agree.Q5:1. Not needed. Covered by Q4.
2. Not needed. The timing is based on transmitted/received PDCCH carrying DCI format enabled by RRM skipping (i.e. includes the bit-field)
3. Needed clarification but not as the way it is formulated. We discussed two options in our contribution , whether it corresponding to the scheduled cell, or the scheduling cell. Both are determined by the DCI and we have to decide which one.

Q6: Yes. But this question seems to imply the same as Q4. Q7: Please see our comment to Q5-3. We need to clarify whether it is scheduling cell or the scheduled cell (our proposal available in our contribution). |
| Spreadtrum | Q1: Proposal 2.1.1, support. Proposal 2.1.2, we support 0/1 interpretation for skip/non-skip. Our question is for the overriding, they are only valid for DCIs in different monitoring occasion. And for the same monitoring occasion, what is DCI indication rules should be further consideration. Proposal 2.1.3, support. Q2: it can be discussed after more progress on DCI formats. Q3: another option is one parameter enable DCI format 0\_1/1\_1. And second parameter enable DCI format 0\_2/1\_2 if supported. Q4: Proposal 2.1.4, it is common understanding that it is up to RAN4, so we support.Q5: 1. support2. it has been captured by previous agreement. 3. Need more discussion, not only this proposal, but also DCI on which cell can be configured with this explicit indication.Q6: Yes Q7: Wait more progress in RAN4.  |
| ZTE, Sanechips | **Q1:**Proposal 2.1.1:In our opinion, DCI format 0\_3/1\_3 need to be supported. Since XR traffic is characterized by large packet size and stringent latency, it is feasible for XR traffic to transmitted in the CA scenario. Proposal 2.1.2:We are fine with the proposal.Proposal 2.1.3:We are fine with the proposal.**Q2:**We think this issue should be clarified. And we think the UE also follows the bit value in DCI which does not schedule the PDSCH/PUSCH. For example, this kind of DCI is capable of indicating the gap/restriction occasion skipping overlapped with SPS/CG occasions.**Q4:**Proposal 2.1.4:One thing need to be clarified is on the various types of gap/restriction configurations. Since concurrent measurement gaps is discussed in Chapter 2.2 and also upon RAN4 discussion, we think a unified conclusion can be made as follows:Proposal 2.1.4-revThe first gap/restriction occasion is among those types of gap/restriction configurations that are eligible to be skipped, where the eligibility is subject to RAN4 discussion.Note: the type of concurrent measurement gaps configuration is not precluded.**Q5:**For the issue 3, i.e., “Indication in the DCI format on a scheduling cell corresponds to the MG on the scheduling cell”, we think it is not necessary to have the limitation. The reason is similar with our analysis for Proposal 2.1.1.  |
| vivo | Q1:Proposal 2.1.1: Companies want to support both DL and UL DCI formats to provide sufficient opportunities to indicate skipping of a gap/restriction. But, the necessity for UL DCI format is unclear. For XR, we think for enabling tx/rx in gaps/restrictions is mainly used for DL traffic, and for UL traffic, considering the characteristics of small packet size or larger PDB, there is no need to skip a gap/restriction occasion. In addition, if a DL DCI without scheduling a PDSCH reception is supported, gNB can send a DCI only to indicate UE to skip a gap/restriction occasion. The additional benefit to support UL DCI format is when gNB make UL scheduling, gNB can indicate a UE to skip a gap/restriction occasion for DL traffic. But in practical, when the gNB performs UL scheduling, it may not require to check DL XR traffic status. Therefore, the motivation of introducing DCI format 0\_1 and 0\_2 is unclear. Proposal 2.1.2: generally fine.Proposal 2.1.3: fineQ2: the scheduling DCI format should be DL DCI formats only. If UL DCI formats are also supported as Proposal 2.1.1. UL DCI formats have to schedule a UL transmission.Q3: Option 2Q4: We think the applicability/eligibility/etc of the gap/restriction can also include that for a certain type of gap which supports skipping, some of the gap occasions are protected as ‘can’t be skipped’ by gNB’s configuration or other way, such as UAI. So we suggest to update Proposal 2.1.4 as:The first gap/restriction occasion is among those ~~types of~~ gap/restriction configurations that are eligible to be skipped, where the eligibility is subject to RAN4 discussion.Q5: 1. Agree with QC. 2. More clarification is needed. 3. Currently, for any other indication in a DCI, it is applied to scheduled cell. We think this logic should be followed. Once DCI format X\_3 is not supported for skipping indication, we don’t find any issue to specify the indication is applied to the scheduled cell.Q6: Agree. Agree with the conclusion given by QC.Q7: Since we just sent an LS to RAN4 in last RAN1 meeting, RAN4 needs time to work on it. It is premature to confirm the working assumption. RAN1can work on the details of alt 1-1. |
| TCL | Q1:Proposal 2.1.1:In our understanding, DCI format 0\_3/1\_3 can also be supported.Proposal 2.1.2OKProposal 2.1.3OKQ2: Yes, We think it’s necessary and suitable for certain semi-static transmissions that overlap with a Mea-gap. Q3:Either way is okay for us.Q4: We are fine with the proposal. |
| Huawei, HiSilicon | Q1: Proposal 2.1.1: We think DCI format 0\_3/1\_3 is applicable for skipping a particular gap/restriction. Some companies doubt the reliability of DCI format x\_3 due to its largest bit size. It is common understanding that the reliability of DCI is relatively high. The reliability of DCI format x\_3 will not be reduced severely by adding only 1-bit. It is unreasonable to drop DCI format 0\_3.Proposal 2.1.2: We support Proposal 2.1.2. The gap/restriction indicated as skipped by the previous DCI cannot be indicated as not skipped, which is friendly for UE implementation.Proposal 2.1.3: This proposal is fine for us.Q2: Generally, if the new bit-field is added in DCI formats and no matter what the purpose of these DCI formats is, it is reasonable for UE to follow the bit-field indicated in the DCI formats. Q3: We think Option2 is better.Q4: Agree Proposal 2.1.4.Q5: 1. & 2. Maybe RAN1 needs to wait for RAN4’s progress about the type of RRM measurements that applicable of the skipping indication. RAN1 can also leave these to maintenance phase, since issues will be clearer once spec is ready.3. We are not sure companies have common understanding on the meaning of “scheduling cell” and “scheduled cell”. Let’s consider cross-carrier scheduling or single DCI scheduling multiple cells scenarios. For example, assume DCI is sent on carrier 1 and schedules data on carrier 2. Then, it’s reasonable to skip gap/restriction on carrier 2, not on carrier 1. Since only the RRM measurements overlapped with scheduling data need to be skipped. To avoid confusion, we suggest the following clarification:Indication in the DCI format *~~on a scheduling cell~~ corresponds to the MG on the ~~scheduling~~ cell where data is scheduled*.Q6: RAN1 does not need to discuss this. There is only 1 bit, it’s impossible to indicate different indexes. Q7: No need to discuss this before receiving reply LS from RAN4. |
| Apple | 2.1.2: just using “1” for skipping, and “0” for doing nothing. No need to define complicated rules.In general, the skipping is applied to skippable MG/scheduling restriction only. So Q.5 and Q6 are coupled. |
| Moderator | **Summary and comments:****Q1:** **Proposal 2.1.1**For Alt 1-1: explicit indication by DCI to skip a particular gap/restriction, one bit indication is included as part of DCI formats 0\_1/1\_1 and 0\_2/1\_2.**Ok with proposal: Samsung, Lenovo, New H3C, Ericsson, Spreadtrum****X\_2 not needed: InterDigital****+ X\_3: LG, ZTE, TCL****0\_1, 0\_2 not needed: vivo****QC: can the proponents clarify whether the DCI format 0\_2/1\_2s are meant to be used to schedule XR data or just to carry the skipping indication but not necessarily schedule XR data? Moderator to QC: we discussed this last meeting, it is not possible to distinguish between types of data scheduled by DCI. We can further discuss it during offline.** **@vivo: According to moderator’s understanding, including bit in the DCI format is not equal to protecting the data that is scheduled by this DCI. These are just containers to convey the indication to skip upcoming measurement occasion. And DCI 0\_1/0\_2 provides additional possibility to convey the indication it is not necessarily related to protecting UL inside the skipped occasion. We can discuss it during offline session as well.****@All: The supported formats can be discussed further. Some companies would like to add x\_3, some would like to remove x\_2, or focus on 1\_1, 1\_2 only.****Proposal 2.1.2**For Alt 1-1: explicit indication by DCI to skip a particular gap/restriction:* Bit equal to 1 indicates the corresponding gap/restriction occasion is to be “skipped”. Once a gap/restriction is indicated to be “skipped” by a DCI, UE does not expect the gap/restriction to be indicated as ‘not skipped’ by another DCI.
* Bit equal to 0 indicates the corresponding gap/restriction occasion is to be “not skipped”. Once a gap/restriction is indicated to be “not skipped” by a DCI, the gap/restriction can be indicated to be “skipped” by another DCI.

**Ok: QC, InterDigital, Samsung, Lenovo, New H3C, Ericsson, ZTE, vivo, TCL****Proposal 2.1.3**Clarify the note from previous agreement as follows:* Note: Minimum time offset(s) between the end of ~~[the first]~~ received dynamic indication and start of corresponding gap(s)/restriction(s) occasion that is going to be skipped shall be introduced.

**Ok to clarify: InterDigital, Samsung, Lenovo, New H3C, Ericsson, Spreadtrum, ZTE, vivo, TCL****@QC: Indeed, the first DCI that indicates skipping matter, since the rest shall then follow the same indication for the same measurement occasion. But all other DCI carrying this indication for the measurement occasion will anyway be X (time offset) time before measurement occasion as per our working agreement from last meeting.****Q2:** Would you like to clarify the following: “UE follows the indication bit in a scheduling DCI format (e.g., 1\_1/1\_2) to enable Tx/Rx in a particular gap/restriction that is caused by RRM measurements if the scheduling DCI format does not schedule data and used for other purposes, e.g., SPS release, SCell dormancy, etc.”?**Ok to clarify: QC, InterDigital, Lenovo, Ericsson (no need for spec changes), ZTE, TCL****Clarify after more progress on DCI: Spreadtrum****@All: Most companies are ok with clarification, please, note that the intention is not to create any new behavior.** **Q3:** For configuration of one bit indication to “skip/not skip” in DCI formats, please indicate your preferred option:* **Option 1:** One higher layer parameter to configure indication in all DCI formats supported to include such indication: TCL
* **Option 2:** One higher layer parameter to configure indication per DCI format supporting such indication: **QC, InterDigital, Samsung, Lenovo, New H3C, LG, vivo, TCL**
* **Option 3: one parameter enable DCI format 0\_1/1\_1. And second parameter enable DCI format 0\_2/1\_2 if supported: Spreadtrum**

**@Ericsson: The comment is not clear to moderator, if UE is configured with the indication to be included in the DCI format, it will look for this bitfield when it receives such DCI format. We can check it in the offline session.****@ALL: Option 2 seems supported by majority. Thus, the following proposal is suggested:****Proposal 2.1.5:**For **Alt 1-1**: Explicit indication by DCI to skip a particular gap/restriction, one higher layer parameter to configure the indication per each DCI format supporting such indication is introduced.**Q4:** Do you agree to further clarify “first gap/restriction” as “first applicable/eligible/etc gap/restriction”? It would be up to RAN4 to define the applicability/eligibility/etc of the gap/restriction (they are currently discussing types of gaps that can or cannot be skipped).**Proposal 2.1.4**The first gap/restriction occasion is among those types of gap/restriction configurations that are eligible to be skipped, where the eligibility is subject to RAN4 discussion.**Agree: Qualcomm, Samsung, Lenovo, New H3C, Ericsson, Spreadtrum, TCL****Wait for RAN4 decision on types of gaps: InterDigital, Lenovo****@ZTE: we made an agreement that it is RAN4 that is discussing which types of gaps/restrictions are supported for potential skipping, RAN1 is only clarifying about “first gap/restriction” that is not any gap/restriction but only those that RAN4 agreed on.****@vivo: the proposal is about types of gaps/restrictions. RAN4 did not agree to protect occasions inside certain measurement configurations it is up to RAN4 to decide.****@ALL: based on input above most companies agree to clarify this further.****Q5:** Please, share your view about further clarification (in *italic*) related to the indication raised by companies and indicate whether you agree to further clarify it (proponents, please feel free to further clarify the motivation from additional clarification):1. The DCI indication is applicable to the first gap/restriction occasion after a minimum time offset, *irrespective of which configuration it belongs to among the multiple gap/restriction configurations*:
	* **Depends on Proposal 2.1.4: QC, vivo**
	* **No need to clarify: Samsung, Ericsson (covered by Q4)**
	* **Depends on RAN4: Lenovo**
	* **Ok to clarify: Spreadtrum**
2. The bit in the DCI is used to indicate whether to skip the first gap/restriction occasion after a minimum time offset required *between the last symbol of the last PDCCH candidate* carrying the DCI format and the start of corresponding skipped gap/restriction occasion indicated by the DCI:

**No need to clarify: QC, Samsung, Ericsson, Spreadtrum****Ok to clarify: Lenovo**1. Indication in the DCI format *on a scheduling cell corresponds to the MG on the scheduling cell*:
	* **Ok: QC**
	* **No need to clarify: Samsung**
	* **Depends on RAN4: Lenovo**
	* **MG on the scheduled cell: LG, vivo**
	* **Clarify whether MG is on the scheduling cell or on the scheduled cell: Ericsson**

**@Ericsson: Proposal 3 Support Alt. 1-1 where the indication in the DCI format on a scheduling cell corresponds to the MG on the scheduling cell (i.e. Option 1): The option above is exactly this proposal but we can clarify whether it is for scheduled cell or scheduling cell.****@All: For Issue 1 it seems most companies do not see a need to clarify it further. Proponents, please, feel free to motivate the need for clarification.****For issue 2, it also seems that companies do not see a need to clarify it. Proponents, please, feel free to motivate the need for clarification.****On Issue 3: Some companies wanted to clarify whether the indication is referring to MG on scheduling cell or scheduled cell. We can take this discussion during offline.****Q6:** Do you agree to leave the decision on how to indicate which gap/restrictions configurations can or cannot be skipped up to RAN4? * **Agree: QC, Samsung, Lenovo, New H3C, LG, Ericsson, Spreadtrum, vivo**

**@ALL: All companies are ok with the suggestion, we can close the discuss on this.****Q7:** Please, share your view related to a way forward with working assumption for Alt. 1-1.* **Give RAN4 time to discuss working assumptions: QC, Lenovo, Spreadtrum, vivo**

**@All: most companies would like to wait for RAN4 progress on this matter.** |
| Moderator | **Follow-up on Q1 and Q3 combined**Please, have a look at the updated proposals:**Proposal 2.1.1-v2**For Alt 1-1: explicit indication by DCI to skip a particular gap/restriction, one bit indication is included as part of DCI formats 0\_1/1\_1 and 0\_2/1\_2.* Explicit indication ~~by DCI to skip a particular gap/restriction~~ , can be configured for each DCI format individually by higher layer. ~~one higher layer parameter to configure the indication per each DCI format supporting such indication is introduced~~.

**Proposal 2.1.2**For Alt 1-1: explicit indication by DCI to skip a particular gap/restriction:* Bit equal to 1 indicates the corresponding gap/restriction occasion is to be “skipped”. Once a gap/restriction is indicated to be “skipped” by a DCI, UE does not expect the gap/restriction to be indicated as ‘not skipped’ by another DCI.
* Bit equal to 0 indicates the corresponding gap/restriction occasion is to be “not skipped”. Once a gap/restriction is indicated to be “not skipped” by a DCI, the gap/restriction can be indicated to be “skipped” by another DCI.

**Proposal 2.1.3**Clarify the note from previous agreement as follows:* Note: Minimum time offset(s) between the end of ~~[the first]~~ received dynamic indication and start of corresponding gap(s)/restriction(s) occasion that is going to be skipped shall be introduced.

**Follow-up on Q2**:Please, find the following clarification, note that no new behavior is suggested, therefore, not additional agreement seems necessary:“UE follows the indication bit in a scheduling DCI format (e.g., 1\_1/1\_2) to enable Tx/Rx in a particular gap/restriction that is caused by RRM measurements if the scheduling DCI format does not schedule data and used for other purposes, e.g., SPS release, SCell dormancy, etc”**Follow up on Q4**:For Proposal 2.1.4 companies had different views. Additional clarification from moderator: the proposal is related to our previous agreement about types of gaps/restrictions that RAN4 is currently discussing (RAN1 116-bis) and clarification to the first gaps/restrictions are related to those gaps/restrictions configurations that will be considered by RAN4. There was additional interpretation from few companies, please see it below, where the indication refers to those gaps/restrictions that are allowed for skipping within the same configuration. These are two different issues, the latter one is still to be discussed and decided in RAN4, whether additional pattern from UE to gNB will be introduced (related to UAI), it is premature to have such agreement in RAN1 from moderator’s point of view. Moreover, we agreed that this topic is not discussed in RAN1 unless there is additional input from RAN4 provided on UAI.**QC, vivo**: The first gap/restriction occasion is among those ~~types of~~ gaps/restrictions ~~configurations~~ that are eligible to be skipped, where the eligibility is subject to RAN4 discussion.**@All: Please check the proposal below.****Proposal 2.1.4-v1**The first gap/restriction occasion is among those types of gap/restriction configurations that are eligible to be skipped, where the eligibility is subject to RAN4 discussion.**Follow up on Q5**:For Issue 1 it seems most companies do not see a need to clarify it further. Proponents, please, feel free to motivate the need for clarification.For issue 2, it also seems that companies do not see a need to clarify it. Proponents, please, feel free to motivate the need for clarification.On Issue 3: Some companies wanted to clarify whether the indication is referring to MG on scheduling cell or scheduled cell. We can continue discussing it in the moderator’s summary and during offline session.**Follow up on Q6:** Most companies are ok with the suggestion, we can close the discuss on this.**Follow up on Q7:** Most companies would like to wait for RAN4 progress on this matter.**Follow up on Q8:** No additional issues were identified so far. |
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### High Priority Discussion: Round #2

Moderator’s comment:

During online session on Tuesday, the following was agreed:

**Proposal 2.1.1-v3**

At least for self-scheduling case, for explicit indication by DCI to skip a particular gap/restriction, one bit indication is included as part of DCI formats 0\_1/1\_1 and 0\_2/1\_2.

* FFS: Cross carrier scheduling.
* FFS: DCI formats 0\_3/1\_3.
* Explicit indication can be configured for each DCI format individually by higher layer.

**Proposal 2.1.3**

Clarify the note from previous agreement as follows:

* Note: Minimum time offset(s) between the end of ~~[the first]~~ received dynamic indication and start of corresponding gap(s)/restriction(s) occasion that is going to be skipped shall be introduced.

We also discussed the following proposal:

**Proposal 2.1.2-v3**

For explicit indication by DCI to skip a particular gap/restriction:

* Bit equal to 1 indicates the corresponding gap/restriction occasion is to be “skipped”.
	+ Option 1: Once a gap/restriction is indicated to be “skipped” by a DCI, UE does not expect the gap/restriction to be indicated as ‘not skipped’ by later DCI.
* Bit equal to 0 indicates the corresponding gap/restriction occasion is to be “not skipped”. Once a gap/restriction is indicated to be “not skipped” by a DCI, the gap/restriction can be indicated to be “skipped” by later DCI.

Most companies supported Option 1 which does not allow to change the indication from “skip” to “do not skip” by motivating that it is more friendly to UE implementation since as soon as UE received indication to “skip” it can start preparing for skipping of corresponding measurement occasion. Otherwise, UE will need to wait until the last possible PDCCH occasion before timeline and only then apply the indication whether to skip or not.

Some companies preferred Option 2 which allows to change the indication from “skip” to “not skip” while there is still time before time offset. The motivation was it is easier from spec perspective and does not give extra limitation to gNB.

* + Option 2: Once a gap/restriction is indicated to be “skipped” by a DCI, the gap/restriction can be indicated to be “not skipped” by later DCI

Moderator’s suggestion is to support Option 1, since most probably gNB will not randomly indicate to skip when there is no such need and then change it back to “not skip”. If we go with Option 2, then only last indication will be useful, basically gNB will have only one shot to tell UE what to do with the occasion. From specification point, the text will be similar like we captured for UTO-UCI, it was just one sentence, so no big problem to capture it for this case as well.

We also agreed to the following FFS:

* FFS: Cross carrier scheduling.

• FFS: DCI formats 0\_3/1\_3.

Moderator would like to clarify your view related to that FFSs as well.

For cross slot scheduling, there was a question about indication, to which cell it is referring to, to scheduling cell or to scheduled cell. Moderator would like to get more views on that point.

For DCI formats 0\_3/1\_3 there was also several questions about indication, how to make a mapping between indication and measurement occasions from different cells in multiple cells are scheduled with the same DCI. Please, share you view in the table below.

**Please, provide your view (in the table below) regarding the following question:**

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| **Q1: Please, share you view on Option 1 and Option 2, and select one you support:****Proposal 2.1.2-v3**For explicit indication by DCI to skip a particular gap/restriction:* Bit equal to 1 indicates the corresponding gap/restriction occasion is to be “skipped”.
	+ Option 1: Once a gap/restriction is indicated to be “skipped” by a DCI, UE does not expect the gap/restriction to be indicated as ‘not skipped’ by later DCI.
	+ Option 2: Once a gap/restriction is indicated to be “skipped” by a DCI, the gap/restriction can be indicated to be “not skipped” by later DCI
* Bit equal to 0 indicates the corresponding gap/restriction occasion is to be “not skipped”. Once a gap/restriction is indicated to be “not skipped” by a DCI, the gap/restriction can be indicated to be “skipped” by later DCI.

**Q2:** Do you support an indication to “skip/do not skip” to be configured in case a cross-carrier scheduling is configured for DCI formats x\_1 and x\_2? If yes, how do you see the mapping of indication to measurement occasions, e.g., it is referring to scheduling cell or to scheduled cell.**Q3:** Do you support including one bit indication to “skip/do not skip” to DCI formats 0\_3/1\_3? If yes, how do you see the mapping of one bit indication to measurement occasions from different cells, e.g., it is referring to scheduling cell or to scheduled cell(s) (which one).**Q4:** Do you see any other important issues that shall be clarified for Alt. 1-1? |

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| **Company** | **Answers/Comments** |
| ZTE, Sanechips | For Proposal 2.1.2-v3, I think second bullet is sufficient from gNB implementation aspect. And option 1 is also acceptable. |
| Samsung | Q1: Neither option 1 nor option 2 is needed. Option 2 is equivalent to no agreement – UE follows the indication by the DCI subject to timeline.Q2: OK. The DCI is applicable to the scheduled cell, as usual. Q3: No. In addition to complicating specifications and UE features, DCI format 0\_3/1\_3 is for reducing PDCCH overhead which is not an issue for XR. |
| Lenovo | Q1: option 1. Specification impact of option 1 is negligible and the option is UE implementation friendly (avoids re-doing actions that may have already been started, e.g., for skipping measurement gaps). Besides, wondering if option 2 needs any timeline definition between consecutive DCIs indicating opposite skipping indications.Q2: wondering if supporting cross-carrier skipping indication requires enlarging the minimum time required for skipping in a self-scheduling scenario (e.g., d+T\_MG; wherein T\_MG is the minimum time required for same-cell scheduling; and ‘d’ is the additional timing for the case of cross-carrier scheduling), and since the UE doesn’t know in advance if it is cross-carrier scheduled, the enlarged timeline is always applied irrespective of same-carrier or cross-carrier scheduling.Q3: if cross-carrier skipping is agreed (Q2), then the first applicable MG can also be indicated as skipped via DCI formats x-3 |
| Moderator | Please, check the following proposals before online session on Wednesday.**Topic 1 (interpretation of a bit):**Select between two proposals **Proposal 2.1.2-v5-option 1 or Proposal 2.1.2-v4-option 2****Proposal 2.1.2-v5-option 1**For explicit indication by a DCI to skip a particular gap/restriction:* Bit equal to 1 indicates the corresponding gap/restriction occasion is to be “skipped”.
* When bit equal to 0, UE ignores the indication of this field in the DCI.

Note: Once a gap/restriction is indicated to be “skipped” by a DCI, UE assumes the gap/restriction is skipped irrespective of indication by a later DCI.**Proposal 2.1.2-v4-option 2**For explicit indication by DCI to skip a particular gap/restriction:* Bit equal to 1 indicates the corresponding gap/restriction occasion is to be “skipped”.
* Bit equal to 0 indicates the corresponding gap/restriction occasion is to be “not skipped”.

Note: Once a gap/restriction is indicated to be “skipped” or “not skipped” by a DCI, the gap/restriction can be indicated to be “not skipped” or “skipped”, respectively by a later DCI.**Topic 2 (FFSs from agreement we made yesterday):****Proposal 2.1.5-v2**In case of cross carrier scheduling, for explicit indication by DCI to skip a particular gap/restriction, one bit indication included as a part of DCI formats 0\_1/1\_1 and 0\_2/1\_2 corresponds to a scheduled cell.Support one of the options below **Proposal 2.1.6-v1-alt1 or Proposal 2.1.6-v1-alt2**:**Proposal 2.1.6-v1-alt1**For explicit indication by DCI to skip a particular gap/restriction, in addition to DCI formats 0\_1/1\_1 and 0\_2/1\_2, one bit indication is also included as a part of DCI formats 0\_3/1\_3.**Proposal 2.1.6-v1-alt2**For explicit indication by DCI to skip a particular gap/restriction, one bit indication is not included as a part of DCI formats 0\_3/1\_3. |
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### High Priority Discussion: Round #3

From moderator’s point of view, interpretation of a bit and the possibility to change the indication from “1” to “0” can be discussed separately. As one intermediate step, we can at least agree on the interpretation of the bit in the indication field. Whether it is possible to change from “1” to “0” can be left as FFS.

Another issue left for Alt. 1-1 among those we discussed this week is support of DCI format 0\_3/1\_3. It would be good to understand whether we support these formats or not since we may need to think about mapping of indication and measurement occasion across cells if such DCI formats are supported.

Additionally, moderator would like to collect companies’ views on what additional issues, ambiguities, questions are left the design of this feature. Moderator’s recommendation is to put those in the table with your comments (please see question 3 below).

**Please, provide your view (in the table below) regarding the following questions:**

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| **Q1: Do you support the following proposal:****Proposal 2.1.2-v6**For explicit indication by DCI to skip a particular gap/restriction:* Bit equal to 1 indicates the corresponding gap/restriction occasion is to be “skipped”.
* Bit equal to 0 indicates that UE behavior for the corresponding gap/restriction occasion is as per legacy behavior.
* FFS: whether changing the indication from “1” to “0” for the gap/restriction occasion is allowed or not.

**Q2:** **Please, indicate which of the following proposals you support.****Proposal 2.1.6-v1-alt1**For explicit indication by DCI to skip a particular gap/restriction, in addition to DCI formats 0\_1/1\_1 and 0\_2/1\_2, one bit indication is also included as a part of DCI formats 0\_3/1\_3.**Proposal 2.1.6-v1-alt2**For explicit indication by DCI to skip a particular gap/restriction, one bit indication is not included as a part of DCI formats 0\_3/1\_3.**Q3:** Do you see any other important issues that shall be clarified for Alt. 1-1? Please, elaborate on an issue, ambiguity, extra clarification that you think might be needed. Moderator will collect input from companies with this question and provide a summary so that other companies can think about those before the next meeting. |

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| **Company** | **Answers/Comments** |
| Qualcomm | Q1We do not support Proposal 2.1.2-v6 which is basically a different version of the option 2. The UE behavior for the corresponding gap/restriction occasion is as per legacy behavior only if the UE has only received DCI(s) with the bit equal to 0 for the measurement gap but has never received DCI(s) with the indication with bit equal to 1.**Proposal 2.1.2-v6**For explicit indication by DCI to skip a particular gap/restriction:* Bit equal to 1 indicates the corresponding gap/restriction occasion is to be “skipped”.
* Bit equal to 0 indicates that UE behavior for the corresponding gap/restriction occasion is as per legacy behavior.
* FFS: whether changing the indication from “1” to “0” for the gap/restriction occasion is allowed or not.
 |
| Moderator | **Moderator’s comment:****Please, check further updates for the proposal in Q1. During this week, two options were discussed. Moderator’s recommendation is to list them so companies can compare those before the next meeting.****Proposal 2.1.2-v7**For explicit indication by a DCI to skip a particular gap/restriction:* Bit equal to 1 indicates the corresponding gap/restriction occasion is to be “skipped”.
* FFS: Option 1 - When bit equal to 0 UE ignores the indication of this field in the DCI.
* FFS: Option 2 - Bit equal to 0 indicates that UE behavior for the corresponding gap/restriction occasion is as per legacy behavior.
	+ Changing the indication from “1” to “0” for the gap/restriction occasion is allowed
 |
| ZTE, Sanechips | **Q1:** We suggest to remove the sub-bullet of Option 2. Moreover, indicating bit ‘1’ and then indicating bit ‘0’ seems not reasonable from gNB perspective. For example, assuming there are three DCIs corresponds to one same gap/restriction occasion, then following indication is possible and make more sense than other cases.Case 1: ‘1 1 1’, Case 2: ‘0 1 1’Case 3: ‘0 0 1’In that respect, UE has same behaviour no matter interpret indication base on latest DCI or multiple DCIs received by UE. However, it may be problematic when gNB changes the indication from “1” to “0” for gap/restriction occasion. So we think the Option 2 is feasible with deleting sub-bullet of Option 2 as follows:**Proposal 2.1.2-v7-rev1**For explicit indication by a DCI to skip a particular gap/restriction:* Bit equal to 1 indicates the corresponding gap/restriction occasion is to be “skipped”.
* FFS: Option 1 - When bit equal to 0 UE ignores the indication of this field in the DCI.
* FFS: Option 2 - Bit equal to 0 indicates that UE behavior for the corresponding gap/restriction occasion is as per legacy behavior.
	+ ~~Changing the indication from “1” to “0” for the gap/restriction occasion is allowed~~

**Q2:**We support Proposal 2.1.6-v1-alt1 at least in the case when per UE MG configuration is configured. In that case, there is no need to think about mapping of indication and measurement occasion across cells, since all cell share same measurement occasion.And we think DCI format 0\_3/1\_3 need to be supported. Since XR traffic is characterized by large packet size and stringent latency, it is feasible for XR traffic to transmitted in the CA scenario. |
| Lenovo | Q1: OKQ2: given the X-carrier scheduling agreement, Alt2 seems simpler; otherwise, for alt1 we need to include additional DCI bits or determine rules for determining the skippable MG across carriers. |
| vivo | In NR specification, we specify a lot of timelines for different cases. These timelines are to make sure UE is capable to complete the instruction from gNB side. UE may not need to check whether timeline is met or not, because if the timeline is not met, UE’s behaviour is not specified. UE can do anything. Regarding whether UE needs to wait to start its processing until timeline, it is up to UE. In practical, UE does not wait to the timeline, UE will start its processing after the decoding of a DCI. If another later DCI indicates UE to change this processing, it is not easy. For example, the spec support HP PUCCH/PUSCH cancels LP PUCCH/PUSCH. But the cancellation timeline is longer than the PUCCH/PUSCH scheduling timeline and it is an additional UE capability. Come back to gap skipping, when UE receives a DCI indicating ‘skipped’, UE can start its processing for RRM and data transmission. For example, there is a PUCCH/PUSCH configured/scheduled within the gap. UE will start its preparation for PUCCH/PUSCH transmission in the data module, and UE will re-arrange its RRM schedule based on the fact the gap occasion is not available. If later, UE receives a second DCI indicating ‘not skipped’. For the data module, the ongoing preparation needs to be cancelled, for the RRM module, UE needs to re-arrange its RRM measurement schedule again. It is very not friendly for gNB to let UE reverse its processing. For the case of 1—0, and 0—1, some company think UE behaviour is the same for option 2. This assumes that UE will wait until the timeline. Actually, UE does not need to wait. The difference between these two cases is similar as (the gNB directly schedules a HP PUCCH/PUSCH) vs (the gNB schedules a LP PUCCH/PUSCH first and then sends a DCI to cancel the LP PUCCH/PUSCH) in somehow. UE behaviours are different. Q2: Since we already have the following agreement. We don’t see any issue for cross-carrier scheduling until now.**Agreement**In case of cross carrier scheduling, for explicit indication by DCI to skip a particular gap/restriction, one bit indication included as a part of DCI formats 0\_1/1\_1 and 0\_2/1\_2 corresponds to a scheduled cell.Q3: we don’t support. firstly, we don’t see the necessity to support. and then there are some issues to support X\_3 format, such as which cell are indicated? How to handle the case that the scheduled cells correspond to different gap/restriction configuration? If the timeline for gap skipping has relation with frequency range, SCS, gap type, etc, how to determine the gap indicated by this DCI? etc.Q4: For the above agreement, we want to clarify that since the indication is to indicate whether a gap/restriction occasion is skipped or not. When a gap/restriction occasion corresponds to a scheduled cell is skipped. Since the gap/restriction occasion also corresponds to some other cells, gNB can also schedule data transmission on the other cells and UE also needs to do PDCCH monitoring or UL transmission on the other cells corresponds to the same gap/restriction occasion.For example, when UE is configured with per UE/FR measurement gap, scheduling restrictions are applicable to all the cells/ all the cell in the same FR. As following there is a per UE gap, legacy behavior is, UE will not perform data transmission on both Pcell and Scell due to a single per UE gap configuration. If the DCI indicates the gap occasion corresponds to Scell is skipped, UE will perform TX/RX transmission as no gap on both Pcell and Scell within the gap occasion. |
| LG | Q1: We support the proposal. Q2: We support Alt. 1. We think x\_3 could be supported considering the similarity to cross carrier scheduling. Q3: We think one bit configuration for the case of cross-carrier scheduling need to be discussed.- For the cross-carrier scheduling case, one bit indication in a DCI is included based on scheduled cell configuration per DCI format  |
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## Concurrent measurement gaps

### Companies proposals and observations

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| **Company** | **Proposals/Observations** |
| CMCC | Proposal 4. The 1bit skipping indication applies to all overlapped gaps/restrictions simultaneously when multiple MG/SMTC configurations are configured to a UE. |
| Lenovo | Proposal 1: A UE first applies measurement skipping DCI command before resolving collision amongst concurrent measurement gaps.  |
| OPPO | Proposal 4: The execution order between R17/18 collision handling for gaps/restrictions caused by RRM measurements and R19 enabling Tx/Rx in gaps/restrictions should be further studied. |
| Panasonic | Proposal 3: The order of applying MG skipping indication and handling the MG collision should be defined. |
| Qualcomm | Proposal 3: When overlapping between gaps/restrictions that are caused by RRM measurements occurs, the dynamic indication to enable Tx/Rx in a particular gap/restriction that is caused by RRM measurements is applied after the overlapping is resolved. |
| Spreadtrum Communications | Proposal 2: Study how to indicate the later gap/restriction in scenarios where overlapping occurs between two gaps/restrictions occasion when multiple configurations are activated. |
| vivo | Proposal 3: When a UE is configured with multiple concurrent measurement gap configurations, if there are collided measurement gap occasions, the UE first solves collision among different measurement gap occasions as legacy, and DCI indication to enable Tx/Rx in a particular gap/restriction that are caused by RRM measurements is applied to a survived measurement gap occasion after collision handling. |
| ZTE | Observation 1: Multiple measurement gaps can be configured for one UE, including e.g., Rel-17 concurrent measurement gap.Observation 2: In the scenario of multiple overlapped gaps/restrictions configured and activated for one UE, single bit indication may cause ambiguities on UE behaviors.Proposal 2: In the scenario of multiple overlapped gaps/restrictions configured and activated for one UE, the 1-bit field in DCI signaling for the measurement gap can be down-selected in one of the following two ways:• Option 1: The 1-bit field in DCI indicates to skip two conflicting measurement gap occasions simultaneously.• Option 2: The 1-bit field in DCI indicates to skip the measurement gap occasion with high priority. |

### Moderator's summary of contributions

**Concurrent measurement gaps:**

* 1 bit applies to all overlapped gaps/restrictions: **CMCC, ZTE**
* UE first applies measurement skipping DCI command before resolving collision amongst concurrent measurement gaps: **Lenovo**
	+ Whether time gap between DCI and earlier occasion of concurrent occasions needs to be larger than a minimum time: Lenovo
* The dynamic indication to enable Tx/Rx in a particular gap/restriction that is caused by RRM measurements is applied after the overlapping is resolved: **Qualcomm, vivo, ZTE**
* Further discuss and decide on the order of applying skipping: **OPPO, Panasonic, Spreadtrum**

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| Spreadtrum | Agree |

### Discussion: Round #1

Moderator’s comment and recommendation:

RAN4 is discussing what types of gaps shall be considered in this work item. Type 2 gaps that allows for multiple concurrent gaps is still under debate whether or not it will be considered for potential skipping. From moderator point of view, it is premature to discuss the potential collision resolution in RAN1 given no decision about supporting such type in RAN4 has been taken. Moderator proposes to wait for RAN4 decision on types of gaps and thus postponing this discussion.

**Please, provide your view (in the table below) regarding the following question:**

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| **Q1:** Do you agree with moderator’s recommendation? If not, please share an alternative way forward. |

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| **Company** | **Answers/Comments** |
| Qualcomm | Q1:Yes, we can wait until RAN4 made the decision.  |
| InterDigital | Fine with moderator’s recommendation |
| Samsung | Agree. |
| Lenovo | Agree with the moderator. |
| New H3C | Agree |
| LG | Yes, we can wait until RAN4 made the decision. |
| Ericsson | Our view is that collision resolution rules are already specified in RAN4 and no need to be discussed in RAN1 or RAN4. The skipping is applied to the prioritized gap, after collision resolution. Maybe RAN1 can clarify that point.**Skipping is applied after collision resolution on con-current MGs.** |
| Spreadtrum | Agree |
| ZTE, Sanechips | **Q1:**In Chapter 2.1, we have the proposal 2.1.4 for the various types of gap/restriction configurations, so based on moderator’s recommendation, we think a unified conclusion can be made as follows:Proposal 2.1.4-revThe first gap/restriction occasion is among those types of gap/restriction configurations that are eligible to be skipped, where the eligibility is subject to RAN4 discussion.Note: the type of concurrent measurement gaps configuration is not precluded. |
| vivo | ok |
| TCL | Agree |
| Panasonic | We agree with the recommendation. |
| Apple | The order of running 38.213 and 38.331 should be clarified. The complexity of the design is partly due to concurrency handling. Since we need to wait for RAN4’s response, then proposals in section 2.1.3 will need more time to be checked out, e.g., considering RAN4 response. |
| Moderator | Summary and comments:Agree to postpone this discussion: **Qualcomm, InterDigital, Samsung, Lenovo, New H3C, LG, Spreadtrum, vivo, TCL, Panasonic**Skipping is applied after collision resolution on concurrent MGs: **Ericsson**@Apple: The decision on concurrent gaps (if RAN4 supports to have those for possible skipping) will impact only the collision resolution, whether the collision is resolved first before skipping is applied or after. Proposals in Sec. 2.1.3 are not impacted by RAN4 decision on concurrent gaps.@ALL: Most companies are fine to postpone the discussion on collision resolution until RAN4 decides whether such gaps are supported or not. Thus, it is proposed to close the discussion for this meeting. |
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## Partial skipping

### Companies proposals and observations

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| **Company** | **Proposals/Observations** |
| InterDigital | Proposal 4: Support partial skipping of a gap/restriction with the skipping indication in DCI |
| LGE | Proposal 6: Support slot-level cancelling/skipping gaps/restrictions that are caused by RRM measurements to enable Tx/Rx |
| MediaTek | Proposal 3: Do not support partial skipping.  |
| Nokia | Proposal 7: Partial skipping of gap/restrictions caused by RRM measurements is not supported in Release 19. |
| NTT DOCOMO | Proposal 3: Not support the case where an occasion of gap/restrictions caused by RRM measurements are cancelled/skipped partially. |
| OPPO | Proposal 3: It is not supported in R19 XR to partially cancel/skip an occasion of gaps/restrictions caused by RRM measurements.  |
| Sony | Proposal 3: Support partial skipping of gap(s)/restriction(s) to allow more scheduling opportunities. |
| Xiaomi | Observation 1：The scheduling of UEs with higher capabilities can be expedited as they are able to complete their RRM measurements earlier.Observation 2：The enhancement of system scheduling efficiency can be achieved by effectively reducing the idle time of UEs caused by RRM measurement, particularly in scenarios with low system resource occupancy rate.Proposal 1：Relevant reporting and processing mechanisms for mitigating UEs idle time caused by RRM measurements could be deliberated in RAN1.Proposal 2：Partial cancellation or skipping of gaps or restrictions caused by RRM measurements could be futher discussed in RAN1. |
| ZTE | Proposal 4: The case where an occasion(s) of gap/restrictions that are caused by RRM measurements are cancelled/skipped partially should not be excluded.Proposal 5: RAN1 continues to discuss and decide whether or not to introduce the case where an occasion(s) of gap/restrictions that are caused by RRM measurements are cancelled/skipped partially in the SMTC or NCSG. |

### Moderator's summary of contributions

A number of companies expressed their view related to partial skipping. The views are summarized below.

**Support partial skipping**: InterDigital, LGE, Sony, Xiaomi, ZTE

* If the UE perform transmission / reception for such short period then in practice, the UE can still continue to perform RRM measurement for the remaining measurement gap: **Sony, Interdigital, LGE**
* In some scenarios scheduling restriction is only for SSB symbols: **ZTE**

**Do not support partial skipping**: MediaTek, Nokia, NTT DOCOMO, OPPO

* Large RAN4 impact, RAN4 needs complicated study that how to treat MG length: **NTT DOCOMO**
* Due to high RAN4 workload for the full skipping (necessary RRM requirements, minimum DCI processing time for MG skipping, etc), partial skipping can be deprioritized for this feature: **MediaTek**
* can lead to big impacts to both specification and UE implementation: **OPPO**

### Discussion: Round #1

Moderator’s comment and recommendation:

We have an FFS point to further study partial skipping: “FFS: Whether or not/How to support of the case where an occasion(s) of gap/restrictions that are caused by RRM measurements are cancelled/skipped partially”.

Given that RAN1 selected a solution for full skipping, moderator’s recommendation is to discuss this topic and decide whether to support partial skipping or not. Some companies support partial skipping as it gives more possibilities for measurements, other companies share their concerns about complexity of such behavior and not clear scenarios where measurements can be conducted taken into account the RF re-tuning time, etc. Benefits and drawbacks provided in companies contributions can be found in Section 2.3.2. To facilitate the discussion, please share your view related to that topic.

**Please, provide your view (in the table below) regarding the following question:**

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| **Q1:** Please, share your view related to partial skipping support, i.e., do you support it or not, details of solution if you support partial skipping, etc. |

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| **Company** | **Answers/Comments** |
| Qualcomm | We do not support partial skipping for Rel-19 because it introduces too much impact to the specification and UE implementation.  |
| InterDigital | We are supportive of partial skipping in a limited sense, assuming that skipping here does not imply doing data Tx/Rx in the middle of a gap occasion. In scenarios where there is an ongoing data Tx/Rx that may use only some of the beginning slots in a long gap occasion (e.g. 6ms), it can be useful for XR data to skip such slots at the beginning of the gap occasion rather than the entire gap occasion. Spec impact is expected to be marginal anyways as partial skipping can be accommodated within the ongoing discussion on DCI-based skipping. For example, upon receiving the DCI format with the skipping indication the UE may assume the corresponding gap is skipped partially when the scheduled DL/UL resources use only some slots overlapping with the skipped gap occasion at the beginning of gap occasion. Skipping some symbols/slots in the middle of a gap occasion is not practical given multiple times where RF retuning is involved.  |
| Samsung | Not support. There will be huge specification impacts and complex UE implementation issues.  |
| Lenovo | We still can postpone the discussion till we get RAN4’s response on which occasions can be skipped. Also, may be a good idea to send an LS to RAN4, and ask about the feasibility and potential use-cases of partial skipping.  |
| LG | We support the partial skipping in the limited way.Currently, the actual required transmission duration (1 or 2 ms) are clearly smaller than gap duration (5 or 8 ms). If UE have prepared to do some measurement, the impact would be marginal to skip only few slots rather than skip whole gap. In addition to this, skip whole gap would bring more side effect than partial skipping. For example, by skipping whole gap, UE revives TX/RX that have restricted, not only targeted transmission. And processing time of those would need to be compared with time gap between gap skipping indication, which is more complicate than what we expect. we think it is necessary to discuss since it is a trade-off between specification impact vs. implementation impact.  |
| Ericsson | We were proponent of partial skipping but our understanding after RAN! WA last meeting and the way timeline is defined is that it is not supported any more. |
| Spreadtrum | It is RAN4 issue. No need to discuss it in RAN1 |
| ZTE, Sanechips | We support partial skipping following current agreed Alt 1-1 mechanism. As shown in the following figure, for SMTC window and network control small gap (NCSG), the switch between data transmission/reception and measurement can be within several symbols. To this end, when only a part of SMTC window or NCSG is after the required minimum time offset as illustrated in Figure 5, the scheduling restriction in the later part of SMTC window or NCSG can be relaxed for.  |
| vivo | Same view as QC. For partial skipping, from UE implementation perspective, the remaining time may not be sufficient for UE to perform measurement, it is meaningless to support partial skipping in that case. It may need to discuss the condition to support partial skipping and the impact on RRM measurement, it is too complicated.  |
| TCL | We support partial skipping. Generally, not all measurement restrictions in the time domain need to be filled with data transmission/reception. For instance, only some slots located at the beginning of the measurement duration may be used for data transmission/reception. In our view, skipping a few slots does not differ significantly from skipping the entire Mea-gap. |
| Panasonic | We think the partial skipping would complicate the feature. |
| Moderator | Summary and comments:Support partial skipping: **InterDigital, LG, ZTE, TCL*** the corresponding gap is skipped partially when the scheduled DL/UL resources use only some slots overlapping with the skipped gap occasion at the beginning of gap occasion: InterDigital

Do not support partial skipping: **Qualcomm, Samsung, vivo, Panasonic (?)*** too much impact to the specification and UE implementation: Qualcomm, Samsung

Postpone the discussion: **Lenovo**Up to RAN4, no need to discuss it in RAN1: **Spreadtrum**@All: No common view to support partial skipping, we can continue discussion in offline session. |
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### Medium Priority Discussion: Round #2

Moderator’s comment:

According to the first round of discussion in moderator’s summary, there is no majority view to support partial skipping. Companies commented that partial skipping will greatly impact RAN4 work as well as increase UE complexity. There are quite a lot of open issues for RAN4 to discuss even with full skipping case. The workload for RAN4 may be too high if partial skipping is supported from RAN1 perspective.

Moderator’s recommendation is to focus on finalizing the details for full skipping in RAN1 and not support partial skipping in Release 19.

**Please, provide your view (in the table below) regarding the following question:**

|  |
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| **Q1:** Do you agree with the following proposal?Proposal 2.3.1:From RAN1 perspective, the case where an occasion(s) of gap/restrictions that are caused by RRM measurements are cancelled/skipped partially is not supported in Release 19. |

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| **Company** | **Answers/Comments** |
| ZTE, Sanechips | Generally fine with this conclusion. One minor comment for clarification is that, can we remove ‘(s)’ from occasion~~(s)~~, then we know the meaning of partially basically focus on partial occasion. And we know that indication of multiple occasions is not supported. |
| Lenovo | OK |
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## Other types of solutions

### Companies proposals and observations

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| **Company** | **Proposals/Observations** |
| Google | Proposal 1: Support Option 1 for dynamic scheduling and Option 2 for configured grant.Proposal 4: The skipping of measurement gaps/restrictions could be activated/deactivated per Configured Grant configuration |
| MediaTek | Observation 1: Network-controlled solutions are suitable from the perspective of XR traffic arrival characteristics while UE-triggering based solutions are suitable from the perspective of satisfying measurement requirements.Proposal 1: Consider UE-triggering based solutions for measurement occasion skipping. At least, UE triggering solutions should be considered based on measurement report triggering on the condition that serving cell measurements are below or above a threshold (e.g., event-A2). |
| Nokia | Proposal 8: Other solutions than Explicit indication by DCI to skip a particular gap/restriction (Alt 1-1) are not necessary for Release 19. |

### Moderator's summary of contributions

The views related to additional solutions are summarised below.

* The skipping of measurement gaps/restrictions could be activated/deactivated per Configured Grant configuration: **Google**
* Support Option 1 for dynamic scheduling and Option 2 for configured grant: **Google**
* Consider UE-triggering based solutions for measurement occasion skipping: **MediaTek**
* UE triggering solutions should be considered based on measurement report triggering on the condition that serving cell measurements are below or above a threshold (e.g., event-A2): **MediaTek**
* Other solutions than Explicit indication by DCI to skip a particular gap/restriction (Alt 1-1) are not necessary for Release 19: **Nokia**

### Discussion: Round #1

Moderator’s comment and recommendation:

We have an FFS point related to solutions other than network triggered solutions to enable Tx/Rx of gaps/restrictions caused by RRM measurements. There was a proposal to consider UE triggering solution when UE indicates to gNB it does not need a measurement occasion. Another proposal is related to different solutions to dynamic grant and configured grant scenarios. The latter seems to be related to the network-based solutions that we have already selected.

The moderator’s recommendation is to discuss and decide whether solutions other than network triggered solutions are needed.

**Please, provide your view (in the table below) regarding the following question:**

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| **Q1:** Do you support other types of solutions to enable Tx/Rx in gaps/restrictions caused by RRM measurements to be specified in Release 19? Please, elaborate your answer, including why other solutions are necessary/not necessary, details of the proposed solutions, etc. |

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| **Company** | **Answers/Comments** |
| Qualcomm | Benefit of DCI based indication of gap/restriction skipping only shows up for dynamically scheduled XR data with jittering and uncertain packet size. For periodic traffic with fixed packet size, predetermined arrival time and tight PDB, the DCI based solution introduces overhead without clear benefit. For this, we think the semi-static solution for SPS and CG based XR data transmission can be considered in Rel-19.  |
| Samsung | Not support. DCI solution is enough.  |
| Lenovo | Priority should be given to completing the current solution. Afterwards, if needed, we can discuss whether the skipping of measurement gaps/restrictions could be activated/deactivated per Configured Grant configuration, considering there could be multiple CG configurations, but only some of them are for XR, so only PUSCH for these CG configurations could cancel the MG if they overlap with each other.  |
| LG | We think semi-static solution based on SPS/CG is simple but beneficial.  |
| Ericsson | Same view as Samsung. |
| vivo | Agree with QC. For XR traffic delivered by SPS or CG, it is more efficient to use semi-static solution based on the overlapping between resources of SPS or CG and gap occasions. gNB does not need to send a DCI in every period. |
| Panasonic | The dynamic indication solution should be sufficient. |
| Apple | We support semi-static configuration as adaptation mechanism. |
| Moderator | Summary and comments:Do not support other types of solutions: **Samsung, Ericsson, Panasonic**@Qualcomm, vivo, LG, all: this question was about FFS bullet, where we discuss solutions other than network triggered solutions, the intention is not to bring the same discussion about dynamic vs semi-static solutions.@All: Please, feel free to continue discussion on this topic. Please, focus on the solutions that were not previously discussed as part of network triggered solutions. We are not re-opening the discussion on dynamic vs semi-static solutions here. We can discuss more during offline session. |
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### Medium Priority Discussion: Round #2

Moderator’s comment:

According to first round of discussions in moderator’s summary, it was not a majority view to support solutions other than network triggered solutions. Please, note that we are not discussing dynamic vs semi-static solution here. This discussion is about the following FFS:

**Agreement**

Consider at least solutions based on triggering/enabling by network signaling to enable Tx/Rx in gaps/restrictions that are caused by RRM measurements.

* FFS: Other types of solutions.
* Whether or not/how to account for any UE assistance information/indication in addition to other information available at the network

Moderator’s recommendation is not to consider solutions other than solution based on triggering/enabling by network signaling in Release 19 and focus on finalizing details of Alt. 1-1 in RAN1.

**Please, provide your view (in the table below) regarding the following question:**

|  |
| --- |
| **Q1:** Do you agree with the following proposal?Proposal 2.4.1:From RAN1 perspective, solutions other than solution based on triggering/enabling by network signaling to enable Tx/Rx in gaps/restrictions that are caused by RRM measurements are not considered in Release 19. |

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| --- | --- |
| **Company** | **Answers/Comments** |
| ZTE, Sanechips | Agree with moderator’s proposal. |
| Lenovo | OK |
| Panasonic | We are fine with the proposal. |
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## Other issues

### Companies proposals and observations

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| **Company** | **Proposals/Observations** |
| Apple | Proposal 2: If dynamic signaling based design is adopted, lead time for dynamic skipping is 5 ms after the end of the PDCCH containing a DCI containing a dynamic measurement skipping indication.Proposal 3: If dynamic signaling based design is adopted:when a slot is designated as not a “valid downlink slot” due to overlap with a configured measurement gap occasion, and that measurement gap occasion is skipped due to RRM measurement adaptation for XR, for the basic UE feature supporting RRM measurement adaptation, the slot is not converted into a “valid downlink slot” and the slot may be eligible to be designated as “valid downlink slot” subject to additional UE capability. Proposal 4: RAN1 takes time to evaluate the working assumption reached at RAN1 #118 and should not rush to confirmation of the working assumption. |
| Fraunhofer IIS, Fraunhofer HHI | Observation 2: RAN2 has an ongoing Study Item on AI/ML for mobility in NR, one of the objectives of which is to evaluate potential benefits and gains of RRM measurement prediction.Observation 3: RRM measurements prediction can be used by the UE in MGs/restrictions which have been deactivated to enable transmissions/receptions for XR traffic instead of performing the actual measurements. |
| LGE | Proposal 4: Introduce dynamic or semi-static time offset indication mechanisms to enhance the feasibility of gap/restriction skipping in Alt. 1-1.Proposal 5: Support to configure the searchspace which is required to be monitored only when there is an upcoming gap/restriction that are caused by RRM measurements. |
| MediaTek | Proposal 2: Wait for RAN4 progress and response before further discussing UE assistance information. |
| Sony | Proposal 4: Support UE Assistance Information (UAI) indicating the gNB on the MG(s) to be skipped by the UE. |
| TCL | Proposal 3: Re-use the current processing timeline for PDSCH or PUSCH as the minimum time gap between the end of the skipping command and the start of the skipped measurement occasion.Proposal 4：UE assistance information/indication to notify gNB whether enabling Tx/Rx for XR during RRM measurements restriction can be considered.Proposal 5: Interaction between DRX and solutions to enable Tx/Rx during measurement restrictions can be studied. |
| vivo | Proposal 5: Enhancement on enabling Tx/Rx in gaps/restrictions that are caused by RRM measurements outside DRX active time is not considered in R19. |

### Moderator's summary of contributions

There are few additional issues raised by companies in their contributions. Please, find the list that summarises these issues below.

**Issue 1: Valid downlink slot:**

* When a slot is designated as not a “valid downlink slot” due to overlap with a configured measurement gap occasion, and that measurement gap occasion is skipped, do not change to “valid downlink slot”: **Apple**

**Issue 2: Interaction with DRX:**

* Interaction between DRX and solutions to enable Tx/Rx during measurement restrictions can be studied: **TCL**
* Enhancement on enabling Tx/Rx in gaps/restrictions that are caused by RRM measurements outside DRX active time is not considered in R19: **vivo**

**Issue 3: Search space for DCI with indication to skip/not to skip:**

* Configure the searchspace which is required to be monitored only when there is an upcoming gap/restriction that are caused by RRM measurements: **LGE**

**Issue 4: Timeline discussion:**

* If dynamic signaling based design is adopted, lead time for dynamic skipping is 5 ms after the end of the PDCCH containing a DCI containing a dynamic measurement skipping indication: **Apple**
* Re-use the current processing timeline for PDSCH or PUSCH: **TCL**
* Introduce dynamic or semi-static time offset indication mechanisms: **LGE**

**Issue 5: UAI:**

* Support UE Assistance Information (UAI) indicating the gNB on the MG(s) to be skipped by the UE: **Sony**
* Wait for RAN4 progress and response before further discussing UE assistance information: **MediaTek**
* UE assistance information/indication to notify gNB whether enabling Tx/Rx for XR during RRM measurements restriction can be considered: **TCL**

### Discussion: Round #1

Moderator’s comments and recommendations:

**Issue 1:** During RAN1#116 an agreement was made, saying: when an occasion(s) of gaps/restrictions that are caused by RRM measurements are cancelled/skipped fully, UE is assumed to receive/transmit in the gaps/restrictions that are caused by RRM measurements as it would without any (measurement etc. related) gaps/restrictions that are caused by RRM measurements. From moderator’s point of view, issue 1 (please see above for the exact description) is related to that agreement and according to that agreement “not valid downlink slot” will become valid as the operation would be as there were no gap/restriction.

**Issue 2**: Interaction with DRX. Currently it is not clear whether there is a need to do something with DRX operation. It is also under RAN2 domain, we can deprioritize this topic in RAN1.

**Issue 3**: Is related to extra monitoring occasion for PDCCH carrying indication to skip prior to measurement occasion.

**Issue 4 and 5** (time offset value and UAI) were already discussed in RAN1 and it is up to RAN4 to continue the discussion, no additional information has been received in RAN1 so far.

**Please, share your view (in the table below) related to the following questions:**

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| **Q1:** Please, share your view related to Issues 1-3. Particularly:* For issue 1, do you agree that according to RAN1 earlier agreement, “not valid downlink slot” will become “valid” when gap/restriction is skipped?
* For issue 2, do you see anything that needs to be clarified in RAN1 regarding interaction with DRX?
* For issue 3, do you see a need to consider extra monitoring occasion for PDCCH carrying indication to skip, e.g., before each measurement occasion?

**Q2:** Do you agree with moderator’s recommendations for Issues 4-5? **Q3**: Do you see any other issues that need to be discussed? |

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| **Company** | **Answers/Comments** |
| Qualcomm | Q1:For Issue 1, does the spec mean the slot remains "not valid" if we do not make any further conclusion? We think it is not good to draw a universal conclusion without looking at the specific cases related to "valid downlink slot" and "not valid downlink slot". The DCI based gap skipping is a dynamic mechanism. If we make the conclusion as recommended by the moderator, the dynamic indication is propagated to use cases where "valid downlink slot" and "not valid downlink slot" are involved. This may cause additional timeline issues and should be checked case by case.For Issue 2, we think the DCI based gap skipping indication at least applies to gaps/restrictions within the CDRX active. Whether it is applicable to outside CDRX active time, it may depend on whether the target use case only includes dynamically scheduled XR data or also includes semi-persistent or periodic XR data. If it does not apply to semi-persistent or periodic XR data, there seems no need to skip gaps/restrictions outside CDRX active time. This depends on whether semi-persistent and periodic data can be configured to semi-statically override gap/restrictions in issue 1. Based on this, we think Issue 2 can be discussed after Issue 1.For Issue 3, we prefer not to define the gap skipping indication dedicated PMOs to cause additional UE PDCCH decoding efforts.Q2:We agree with moderator’s recommendations for Issue 4 and 5. |
| Lenovo | Q1: Issue 1: seems so.Issue 2 and 3: No.Q2: yes |
| Ericsson | Q1:Issue 1: In our understanding, this feature based on WA (DCI based) does not change the validity of DL/UL slot, because the validity is defined based on semi-static parameters such as TDD configuration. Those, are not changed.Issue 2: No need to change. Within the DRX on duration, the UE is supported to monitor ODCCH. Now, if before skipping not monitored during DRX, after skipping then should be monitored.Issue 3: No. Q2: Agree with Moderator |
| vivo | Q1: We agree the understanding that “not valid downlink slot” will become “valid” when gap/restriction is skipped.We think the interaction with DRX issue needs further discussed. It is fine to discuss this issue either in RAN1 or RAN2.It can be up to gNB’s configuration. No any new mechanism is needed.Q2: agree |
| Apple | For Issue 1, we see timeline issue, and it can affect/complicate the UE implementation unnecessarily. So we think it should be discussed. |
| Moderator | Summary and comments**Issue 1:**@Qualcomm: Moderator is not recommending any conclusion here and only clarifies that we already agreed to the behavior for the issue 1 in RAN1#116 (please check below):**Agreement**From RAN1 perspective, when an occasion(s) of gaps/restrictions that are caused by RRM measurements are cancelled/skipped fully, UE is assumed to receive/transmit in the gaps/restrictions that are caused by RRM measurements as it would without any (measurement etc. related) gaps/restrictions that are caused by RRM measurements.* FFS: Whether or not/How to support of the case where an occasion(s) of gap/restrictions that are caused by RRM measurements are cancelled/skipped partially

@Ericsson: Based on moderator’s understanding the issue is related to the following (TS 38.214: 5.2.2.5 CSI reference resource definition):A slot in a serving cell shall be considered to be a valid downlink slot if: - it comprises at least one higher layer configured downlink or flexible symbol, and - it does not fall within a configured measurement gap for that UE **@All: Lets continue the discussion on this issue in the moderator’s summary.****Issue 2:**No need to discuss it in RAN1: Lenovo, EricssonNeed discussion, could be RAN1 or RAN2: vivoNeed discussion about gaps outside active period of DRX: Qualcomm@All: Some companies see a need to discuss the interaction with DRX. Since DRX is under RAN2 responsibility, it is recommended that the discussion is continued there.**Issue 3:** No need for additional PDCCH monitoring occasion: Qualcomm, Lenovo, Ericsson, vivo@All: No support for introducing new PDCCH monitoring occasion at this point.**Issue 4-5:** Most companies are ok with moderator’s recommendation, the discussion is closed. |
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### Low priority Discussion: Round #2

Moderator’s comments:

Based on the first round of discussion, the only issue left to clarify from moderator’s perspective is Issue 1:

**Issue 1: Valid downlink slot:**

* When a slot is designated as not a “valid downlink slot” due to overlap with a configured measurement gap occasion, and that measurement gap occasion is skipped, do not change to “valid downlink slot”: **Apple**

**Agreement**

From RAN1 perspective, when an occasion(s) of gaps/restrictions that are caused by RRM measurements are cancelled/skipped fully, UE is assumed to receive/transmit in the gaps/restrictions that are caused by RRM measurements as it would without any (measurement etc. related) gaps/restrictions that are caused by RRM measurements.

* FFS: Whether or not/How to support of the case where an occasion(s) of gap/restrictions that are caused by RRM measurements are cancelled/skipped partially

The issue is related to the following TS 38.214: 5.2.2.5 CSI reference resource definition:

A slot in a serving cell shall be considered to be a valid downlink slot if:

- it comprises at least one higher layer configured downlink or flexible symbol, and

- it does not fall within a configured measurement gap for that UE

According to moderator’s view, we already made an agreement (see above), that in case of skipped measurement gap/restriction UE will receive/transmit as there were no measurement gap/restriction. Therefore, for Issue 1, “not valid downlink slot” in case of skipped measurement gap will become “valid downlink slot”. Please, note, that the agreement was not related to particular design of signalling solution, regardless of final design (dynamic vs semi-static), this would be the procedure based on the agreement.

**Please, provide your view (in the table below) regarding the following question:**

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| --- |
| **Q1:** Do you agree with moderator’s understanding that according to the agreement (see above), in Issue 1, “not valid downlink slot” due to skipped measurement gap will become “valid downlink slot” as in TS 38.214: 5.2.2.5 CSI reference resource definition? |

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| **Company** | **Answers/Comments** |
| Lenovo | We tend to agree with the moderator. We can further check for any potential timeline issue after the minimum time between DCI and MG for skipping the MG is determined by RAN4. |
| Panasonic | We share the same understanding as the moderator. |
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# Offline sessions

## Offline session on Monday

**Topic 1:**

We can further discuss DCI formats to be supported for skipping indication:

**Proposal 2.1.1**

For Alt 1-1: explicit indication by DCI to skip a particular gap/restriction, one bit indication is included as part of DCI formats 0\_1/1\_1 and 0\_2/1\_2.

**Topic 2 (related to Q3 in round 1 discussions):**

**Proposal 2.1.5:**

For **Alt 1-1**: Explicit indication by DCI to skip a particular gap/restriction, one higher layer parameter to configure the indication per each DCI format supporting such indication is introduced.

**Topic 3:**

**Proposal 2.1.4**

The first gap/restriction occasion is among those types of gap/restriction configurations that are eligible to be skipped, where the eligibility is subject to RAN4 discussion.

**Topic 4:**

**Proposal 2.1.2**

For Alt 1-1: explicit indication by DCI to skip a particular gap/restriction:

* Bit equal to 1 indicates the corresponding gap/restriction occasion is to be “skipped”. Once a gap/restriction is indicated to be “skipped” by a DCI, UE does not expect the gap/restriction to be indicated as ‘not skipped’ by another DCI.
* Bit equal to 0 indicates the corresponding gap/restriction occasion is to be “not skipped”. Once a gap/restriction is indicated to be “not skipped” by a DCI, the gap/restriction can be indicated to be “skipped” by another DCI.

**Proposal 2.1.3**

Clarify the note from previous agreement as follows:

* Note: Minimum time offset(s) between the end of ~~[the first]~~ received dynamic indication and start of corresponding gap(s)/restriction(s) occasion that is going to be skipped shall be introduced.

**Topic 5:**

Clarify the following understanding: “UE follows the indication bit in a scheduling DCI format (e.g., 1\_1/1\_2) to enable Tx/Rx in a particular gap/restriction that is caused by RRM measurements if the scheduling DCI format does not schedule data and used for other purposes, e.g., SPS release, SCell dormancy, etc.”

**During offline:**

**Topic 1:**

We can further discuss DCI formats to be supported for skipping indication:

**Proposal 2.1.1-v1**

For Alt 1-1: explicit indication by DCI to skip a particular gap/restriction, one bit indication is included as part of DCI formats 0\_1/1\_1 and 0\_2/1\_2.

For **Alt 1-1**: Explicit indication by DCI to skip a particular gap/restriction, one higher layer parameter to configure the indication per each DCI format supporting such indication is introduced.

Support x\_3: ZTE, OPPO

Do not support x\_3: vivo, QC, CMCC

QC: DCI format may not schedule data that overlaps with the gap.

**Topic 2 (related to Q3 in round 1 discussions):**

**Proposal 2.1.5:**

For **Alt 1-1**: Explicit indication by DCI to skip a particular gap/restriction, one higher layer parameter to configure the indication per each DCI format supporting such indication is introduced.

**Topic 3:**

**Proposal 2.1.4-v1**

The first gap/restriction occasion is among those types of gap/restriction configurations that are eligible to be skipped, where the eligibility is subject to RAN4 discussion.

QC, vivo: The first gap/restriction occasion is among those ~~types of~~ gaps/restrictions ~~configurations~~ that are eligible to be skipped, where the eligibility is subject to RAN4 discussion.

## Offline session on Tuesday

**Topic 1:**

Quick check on the following proposals before online session today:

**Proposal 2.1.1-v2**

For Alt 1-1: explicit indication by DCI to skip a particular gap/restriction, one bit indication is included as part of DCI formats 0\_1/1\_1 and 0\_2/1\_2.

* Explicit indication can be configured for each DCI format individually by higher layer.

**Proposal 2.1.4-v1**

The first gap/restriction occasion is among those types of gap/restriction configurations that are eligible to be skipped, where the eligibility is subject to RAN4 discussion.

**Proposal 2.1.2**

For Alt 1-1: explicit indication by DCI to skip a particular gap/restriction:

* Bit equal to 1 indicates the corresponding gap/restriction occasion is to be “skipped”. Once a gap/restriction is indicated to be “skipped” by a DCI, UE does not expect the gap/restriction to be indicated as ‘not skipped’ by another DCI.
* Bit equal to 0 indicates the corresponding gap/restriction occasion is to be “not skipped”. Once a gap/restriction is indicated to be “not skipped” by a DCI, the gap/restriction can be indicated to be “skipped” by another DCI.

**Proposal 2.1.3**

Clarify the note from previous agreement as follows:

* Note: Minimum time offset(s) between the end of ~~[the first]~~ received dynamic indication and start of corresponding gap(s)/restriction(s) occasion that is going to be skipped shall be introduced.

**Topic 2:**

Discuss the following FFS point from agreement:

**Agreement (RAN1#116)**

Consider at least solutions based on triggering/enabling by network signaling to enable Tx/Rx in gaps/restrictions that are caused by RRM measurements.

* FFS: Other types of solutions.
* Whether or not/how to account for any UE assistance information/indication in addition to other information available at the network

**Topic 3 (if time allows)**:

Discuss the following FFS point from agreement:

**Agreement (RAN1#116)**

From RAN1 perspective, when an occasion(s) of gaps/restrictions that are caused by RRM measurements are cancelled/skipped fully, UE is assumed to receive/transmit in the gaps/restrictions that are caused by RRM measurements as it would without any (measurement etc. related) gaps/restrictions that are caused by RRM measurements.

* FFS: Whether or not/How to support of the case where an occasion(s) of gap/restrictions that are caused by RRM measurements are cancelled/skipped partially

**After offline today:**

**Topic 1:**

Quick check on the following proposals before online session today:

**Proposal 2.1.1-v3**

At least for ~~single carrier and~~ self-scheduling case, for ~~Alt 1-1:~~ explicit indication by DCI to skip a particular gap/restriction, one bit indication is included as part of DCI formats 0\_1/1\_1 and 0\_2/1\_2.

FFS: ~~CA~~, cross carrier scheduling

FFS: 0\_3/1\_3.

* Explicit indication can be configured for each DCI format individually by higher layer.

**Proposal 2.1.2-v2**

For Alt 1-1: explicit indication by DCI to skip a particular gap/restriction:

* Bit equal to 1 indicates the corresponding gap/restriction occasion is to be “skipped”. Once a gap/restriction is indicated to be “skipped” by a DCI, UE does not expect the gap/restriction to be indicated as ‘not skipped’ by ~~another~~ later DCI.
* Bit equal to 0 indicates the corresponding gap/restriction occasion is to be “not skipped”. Once a gap/restriction is indicated to be “not skipped” by a DCI, the gap/restriction can be indicated to be “skipped” by ~~another~~ later DCI.

## Offline session on Wednesday

**Topic 1 (high priority):**

We shall select between two different options. **Proposal 2.1.2-v4-option 1** does not allow to change the indication from skipped to not skipped for the same gap/restriction, while **Proposal 2.1.2-v4-option 2** allows to change the indication from skipped to not skipped for the same gap/restriction.

**Proposal 2.1.2-v4-option 1**

For explicit indication by DCI to skip a particular gap/restriction:

* Bit equal to 1 indicates the corresponding gap/restriction occasion is to be “skipped”. Once a gap/restriction is indicated to be “skipped” by a DCI, UE does not expect the gap/restriction to be indicated as ‘not skipped’ by later DCI.
* Bit equal to 0 indicates the corresponding gap/restriction occasion is to be “not skipped”. Once a gap/restriction is indicated to be “not skipped” by a DCI, the gap/restriction can be indicated to be “skipped” by later DCI.

**Proposal 2.1.2-v4-option 2**

For explicit indication by DCI to skip a particular gap/restriction:

* Bit equal to 1 indicates the corresponding gap/restriction occasion is to be “skipped”.
* Bit equal to 0 indicates the corresponding gap/restriction occasion is to be “not skipped”.

Note: Once a gap/restriction is indicated to be “skipped” or “not skipped” by a DCI, the gap/restriction can be indicated to be “not skipped” or “skipped”, respectively by later DCI.

**Topic 2:**

Discuss two FFSs from agreement on Tuesday:

First FFS: Cross carrier scheduling.

**Proposal 2.1.5-v1**

In case of cross carrier scheduling, for explicit indication by DCI to skip a particular gap/restriction, one bit indication included as part of DCI formats 0\_1/1\_1 and 0\_2/1\_2 corresponds to a scheduled cell.

Second FFS: DCI format 0\_3/1\_3:

Discuss whether we support these formats for indication to “skip/do not skip” or not. If yes, what is the mapping between indication and the scheduled cell/scheduling cell measurement occasions.

**Topic 3:**

For two remaining FFSs on partial skipping and solutions other than solution based on network triggering:

Proposal 2.3.1-v1:

From RAN1 perspective, the case where an occasion of gap/restrictions that are caused by RRM measurements are cancelled/skipped partially is not supported in Release 19.

Proposal 2.4.1:

From RAN1 perspective, solutions other than solution based on triggering/enabling by network signaling to enable Tx/Rx in gaps/restrictions that are caused by RRM measurements are not considered in Release 19.

**After offline session:**



**Proposal 2.1.2-v5-option 1**

For explicit indication by a DCI to skip a particular gap/restriction:

* Bit equal to 1 indicates the corresponding gap/restriction occasion is to be “skipped”.
* When bit equal to 0 UE ignores the indication of this field in the DCI

Note: Once a gap/restriction is indicated to be “skipped”” by a DCI, UE assumes the gap/restriction is skipped irrespective of indication by later DCI

* + ~~Note: It implies when bit equal to 0 no new UE behavior for the corresponding gap/restriction occasion regarding i.e., RRM measurement remains the same as legacy~~
* ~~2 -~~

~~Overriding from 1 to 0 is not possible~~

**Proposal 2.1.2-v5-option 1-clean**

For explicit indication by a DCI to skip a particular gap/restriction:

* Bit equal to 1 indicates the corresponding gap/restriction occasion is to be “skipped”.
* When bit equal to 0, UE ignores the indication of this field in the DCI.

Note: Once a gap/restriction is indicated to be “skipped” by a DCI, UE assumes the gap/restriction is skipped irrespective of indication by later DCI.

# Proposals for online sessions

## Online session on Tuesday

For today online session the following proposals are suggested for agreements:

**Proposal 2.1.1-v3**

At least for self-scheduling case, for explicit indication by DCI to skip a particular gap/restriction, one bit indication is included as part of DCI formats 0\_1/1\_1 and 0\_2/1\_2.

* FFS: Cross carrier scheduling.
* FFS: DCI formats 0\_3/1\_3.
* Explicit indication can be configured for each DCI format individually by higher layer.

**Proposal 2.1.2-v3**

For ~~Alt 1-1:~~ explicit indication by DCI to skip a particular gap/restriction:

* Bit equal to 1 indicates the corresponding gap/restriction occasion is to be “skipped”. Further down select between two options during online session:
	+ Option 1: Once a gap/restriction is indicated to be “skipped” by a DCI, UE does not expect the gap/restriction to be indicated as ‘not skipped’ by later DCI.
	+ Option 2: Once a gap/restriction is indicated to be “skipped” by a DCI, the gap/restriction can be indicated to be “not skipped” by later DCI
* Bit equal to 0 indicates the corresponding gap/restriction occasion is to be “not skipped”. Once a gap/restriction is indicated to be “not skipped” by a DCI, the gap/restriction can be indicated to be “skipped” by later DCI.

**Proposal 2.1.3**

Clarify the note from previous agreement as follows:

* Note: Minimum time offset(s) between the end of ~~[the first]~~ received dynamic indication and start of corresponding gap(s)/restriction(s) occasion that is going to be skipped shall be introduced.

## Online session on Wednesday

**Topic 1 (choose one of the proposals below related to bit interpretation):**

**Proposal 2.1.2-v5-option 1**

For explicit indication by a DCI to skip a particular gap/restriction:

* Bit equal to 1 indicates the corresponding gap/restriction occasion is to be “skipped”.
* When bit equal to 0, UE ignores the indication of this field in the DCI.

Note: Once a gap/restriction is indicated to be “skipped” by a DCI, UE assumes the gap/restriction is skipped irrespective of indication by a later DCI.

**Proposal 2.1.2-v4-option 2**

For explicit indication by DCI to skip a particular gap/restriction:

* Bit equal to 1 indicates the corresponding gap/restriction occasion is to be “skipped”.
* Bit equal to 0 indicates the corresponding gap/restriction occasion is to be “not skipped”.

Note: Once a gap/restriction is indicated to be “skipped” or “not skipped” by a DCI, the gap/restriction can be indicated to be “not skipped” or “skipped”, respectively by a later DCI.

**Topic 2.1 (related to FFS on cross carrier scheduling from agreement on Tuesday):**

**Proposal 2.1.5-v2**

In case of cross carrier scheduling, for explicit indication by DCI to skip a particular gap/restriction, one bit indication included as a part of DCI formats 0\_1/1\_1 and 0\_2/1\_2 corresponds to a scheduled cell.

**Topic 2.2 (related to FFS on DCI formats 0\_3/1\_3 from agreement on Tuesday). Choose one of the proposals below (support or do not support DCI formats 0\_3/1\_3 to provide indication whether to “skip” or not)**

**Proposal 2.1.6-v1-alt1**

For explicit indication by DCI to skip a particular gap/restriction, in addition to DCI formats 0\_1/1\_1 and 0\_2/1\_2, one bit indication is also included as a part of DCI formats 0\_3/1\_3.

**Proposal 2.1.6-v1-alt2**

For explicit indication by DCI to skip a particular gap/restriction, one bit indication is not included as a part of DCI formats 0\_3/1\_3.

## Online session on Friday

For today online session, three proposals are suggested for possible agreement.

**Proposal 2.3.1-v1:**

From RAN1 perspective, the case where an occasion~~(s)~~ of gap/restriction~~s~~ that is caused by RRM measurements is cancelled/skipped partially is not supported in Release 19.

**Proposal 2.1.6-v2**

**Alt1**

For explicit indication by DCI to skip a particular gap/restriction, in addition to DCI formats 0\_1/1\_1 and 0\_2/1\_2, one bit indication can be included as a part of DCI formats 0\_3/1\_3.

* Explicit indication can be configured for each DCI format individually by higher layer.

**Alt2**

For explicit indication by DCI to skip a particular gap/restriction, one bit indication is not included as a part of DCI formats 0\_3/1\_3.

**Proposal 2.1.2-v7**

For explicit indication by a DCI to skip a particular gap/restriction:

* Bit equal to 1 indicates the corresponding gap/restriction occasion is to be “skipped”.
* FFS: Option 1 - When bit equal to 0 UE ignores the indication of this field in the DCI.
* FFS: Option 2 - Bit equal to 0 indicates that UE behavior for the corresponding gap/restriction occasion is as per legacy behavior.
	+ Changing the indication from “1” to “0” for the gap/restriction occasion is allowed

# Agreements

## RAN1#116

**Agreement**

Consider at least solutions based on triggering/enabling by network signaling to enable Tx/Rx in gaps/restrictions that are caused by RRM measurements.

* FFS: Other types of solutions.
* Whether or not/how to account for any UE assistance information/indication in addition to other information available at the network

**Agreement**

From RAN1 perspective, when an occasion(s) of gaps/restrictions that are caused by RRM measurements are cancelled/skipped fully, UE is assumed to receive/transmit in the gaps/restrictions that are caused by RRM measurements as it would without any (measurement etc. related) gaps/restrictions that are caused by RRM measurements.

* FFS: Whether or not/How to support of the case where an occasion(s) of gap/restrictions that are caused by RRM measurements are cancelled/skipped partially

**Agreement**

For solutions based on triggering/enabling by network signaling to enable Tx/Rx in gaps/restrictions that are caused by RRM measurements consider the following alternatives or combinations for further down-selection:

* Alt. 1: Dynamic indication to enable Tx/Rx in particular gap(s)/restriction(s) that are caused by RRM measurements.
	+ FFS: details
* Alt. 2: Semi-persistent solution to enable Tx/Rx in gaps/restrictions that are caused by RRM measurements.
	+ FFS: details
* Alt. 3: Semi-static solution to enable TX/RX in gaps/restrictions that are caused by RRM measurements.
	+ FFS: details
* Alt. 4: Dynamic solution to adapt/change gap/SMTC configuration to enable TX/RX in gaps/restrictions that are caused by RRM measurements.
	+ FFS: details
* Alt. 5: Rule-based solution to enable TX/RX in gaps/restrictions that are caused by RRM measurements:
	+ FFS: details

Companies are encouraged to use the EVM in TR38.835 if they are submitting simulation results.

Working Assumption

RAN1 aims to develop/identify solution(s) to enable Tx/Rx in gaps/restrictions that are caused by RRM measurements agnostic in RAN1 normative work to types of gaps/restrictions that are caused by RRM measurements.

Note: UE features related to the developed solution(s) is a separate discussion.

## RAN1#116-bis

**Agreement**

For solutions based on triggering/enabling by network signaling to enable Tx/Rx in gaps/restrictions that are caused by RRM measurements consider the following alternatives or combinations for further down-selection:

* Alt. 1: Dynamic indication to enable Tx/Rx in particular gap(s)/restriction(s) that are caused by RRM measurements.
	+ FFS: **Alt 1-1**: Explicit indication by DCI to skip a particular gap(s)/restriction(s);
	+ FFS: **Alt 1-2**: Explicit indication by DCI to indicate a time window where to skip a particular gap(s)/restriction(s);
	+ FFS: **Alt 1-3**: Implicit indication by DCI scheduling a transmission/reception overlapping with a gap(s)/restriction(s) to skip the gap(s)/restriction(s);
	+ FFS: DCI format, DCI content, DCI bit-field size;
	+ FFS: Whether indication is for one or more occasions;
	+ FFS: How to consider time offset between the end of received dynamic indication and start of gap(s)/restriction(s) occasion that is going to be skipped.
* Alt. 2: Semi-persistent solution to enable Tx/Rx in gaps/restrictions that are caused by RRM measurements.
	+ FFS: **Alt 2-1**: gNB sends a skipping activation command, UE will skip gaps/restrictions until de-activation command is received.
	+ FFS: **Alt 2-1a**: gNB sends an activation command to enable pre-configured gap(s)/restriction(s), UE will skip gap(s)/restriction(s) after de-activation command is received.
	+ FFS: **Alt 2-2**: RRM measurement adaptation is applied to all MG configurations/scheduling restrictions due to all SMTC configurations, or is applied to selected MG configuration(s) and/or scheduling restrictions due to selected SMTC configuration(s) and is conducted in a time-window, and time-windows are derived from a semi-persistent ~~configuration~~ activation for their periodicity, offset and duration.
	+ FFS: **Alt 2-3**: Activate/de-activate one or more of pre-configured pattern(s) via MAC-CE to indicate occasions where Tx/Rx is prioritized over gap(s)/restriction(s);
	+ FFS: Details of activation/deactivation MAC-CE command
	+ FFS: How to consider time offset between activation/deactivation command and start of gap(s)/restriction(s) occasion that is going to be skipped.
* Alt. 3: Semi-static solution to enable TX/RX in gaps/restrictions that are caused by RRM measurements.
	+ FFS: **Alt 3-1**: Configure a pattern(s) via RRC to indicate occasions where to skip gaps/restrictions;
		- FFS: Details of pattern
	+ FFS: **Alt 3-2**: Gaps/restrictions skipping is applied to all MG configurations/scheduling restrictions due to all SMTC configurations / RRM measurements, or is applied to selected MG configuration(s) and/or scheduling restrictions due to selected SMTC configuration(s) / RRM measurement(s) and is conducted in a time-window, and time-windows are derived from a semi-static configuration for their periodicity, offset and duration.
	+ FFS: **Alt 3-3**: Gaps/restrictions that are caused by RRM measurements are skipped if collided with particular semi-statically pre-configured Tx/Rx occasions.
	+ FFS: **Alt. 3-4**: Gaps/restrictions that are caused by RRM measurements are skipped based on semi-statically configured priority information for particular semi-statically pre-configured Tx/Rx and/or particular gaps/restrictions.

**Agreement**

Confirm the working assumption from RAN1 #116 with updates:

* RAN1 aims to develop/identify solution(s) to enable Tx/Rx in gaps/restrictions that are caused by RRM measurements agnostic in RAN1 normative work to types of gaps/restrictions that are caused by RRM measurements.
	+ It is up to RAN4 to discuss which type of gaps/restrictions caused by RRM measurements can be cancelled/skipped
	+ Note: UE features related to the developed solution(s) is a separate discussion

**Agreement**

RAN1 continues to discuss and decide whether or not to introduce new UE assistance information for solution(s) to enable Tx/Rx in gaps/restrictions that are caused by RRM measurements. At least the following UE assistance information is considered for further study:

* FFS: UE assistance information related to measurement occasions:
	+ FFS: The number of needed measurement gaps/SMTC with restrictions within a time period;
	+ FFS: The maximum number or ratio of MGs/SMTC with restrictions that can be skipped within a time period;
	+ FFS: The number of required SSBs within a time period;
	+ FFS: The number of consecutive RRM measurements that can be skipped;
	+ FFS: The maximum interval between two consecutively reserved gap/restriction occasions for RRM measurements;
	+ FFS: The patterns of gap(s)/restriction(s) where skipping is feasible or acceptable;
* FFS: UE assistance information related to channel conditions:
	+ FFS: RSRP is below/above search threshold (s-MeasureConfig);
* FFS: UE assistance information related to traffic:
	+ FFS: PSI (PDU set importance);
* FFS: UE assistance information related to UE mobility:
	+ FFS: L3 parameters related to mobility, e.g., static or not

Companies are encouraged to provide additional details (e.g. how often the UE assistance info is provided, timing, applicable scenarios, performance gains, etc) on their preferred scheme.

Note: From specification point of view, there is no mandated gNB behavior in response to any of the UE assistance information.

RAN1 to make decision, from RAN1 perspective, in RAN1#117 on the support of UE assistance information.

## RAN1#117

**Agreement**

For solutions based on triggering/enabling by network signaling to enable Tx/Rx in gaps/restrictions that are caused by RRM measurements, select one or combination among only Alt1 and Alt3 from RAN1#116bis.

**Conclusion**

RAN1 does not further discuss new UE assistance information related to channel conditions, traffic, UE mobility.

Proposal 2.1.2-v7

For solutions based on triggering/enabling by network signaling to enable Tx/Rx in gaps/restrictions that are caused by RRM measurements consider the following alternatives or combinations for further down-selection:

* Alt. 1: Dynamic indication to enable Tx/Rx in particular gap(s)/restriction(s) that are caused by RRM measurements.
	+ FFS: **Alt 1-1**: Explicit indication by DCI to skip a particular gap(s)/restriction(s);
		- Indication is included as part of scheduling DCI:
			* FFS: Bit-field size is one bit;
			* FFS: Bit-field size is >1 bit;
		- Note: Minimum time offset(s) between the end of [the first] received dynamic indication and start of corresponding gap(s)/restriction(s) occasion that is going to be skipped shall be introduced.
	+ FFS: **Alt 1-2**: Explicit indication by DCI to indicate a time window where to skip a particular gap(s)/restriction(s);
		- Note: Minimum time offset between the end of received dynamic indication and start of gap(s)/restriction(s) occasion in time window that is going to be skipped shall be introduced.
	+ FFS: **Alt 1-3**: Implicit indication by DCI scheduling a transmission/reception overlapping with a gap(s)/restriction(s) to skip the gap(s)/restriction(s);
		- Note: Minimum time offset between the end of received dynamic indication and start of gap(s)/restriction(s) occasion that is going to be skipped shall be introduced.
	+ FFS: DCI format, DCI content, DCI bit-field size;
	+ FFS: Whether indication is for one or more occasions;
	+ FFS: How to consider time offset between the end of received dynamic indication and start of gap(s)/restriction(s) occasion that is going to be skipped.
* Alt. 3: Semi-static solution to enable TX/RX in gaps/restrictions that are caused by RRM measurements.
	+ FFS: **Alt 3-1**: Configure a pattern(s) via RRC to indicate occasions where to skip gaps/restrictions;
		- FFS: Details of pattern:
			* FFS: Pattern is based on periodicity, offset and duration;
			* FFS: Pattern is based on a bitmap;
		- FFS: whether a pattern is applied to all or subset of configured MG configurations/scheduling restrictions.
	+ FFS: **Alt 3-3**: Gaps/restrictions that are caused by RRM measurements are skipped if collided with particular semi-statically pre-configured Tx/Rx occasions.
	+ FFS: **Alt. 3-4**: Gaps/restrictions that are caused by RRM measurements are skipped based on semi-statically configured priority information for particular semi-statically pre-configured Tx/Rx and/or particular gaps/restrictions.

**Conclusion**

There is no consensus in RAN1 to support UE assistance information related to measurements occasions. The reason for this situation is lack of consensus on the need/feasibility for UAI and lack of technical understanding on issues outside of RAN1 expertise (e.g. impact of RRM measurement performance). It is up to other working groups to trigger further work in RAN1 on UE assistance information.

Proposal 2.3.3-v4:

RAN1 agrees to send an LS to RAN4 (CC: RAN2) to convey the following information about UE assistance information (including the conclusion on UAI):

|  |  |
| --- | --- |
| **1. Overall Description:**RAN1 discussed UE assistance information related to measurements occasions and related to channel conditions, traffic, UE mobility. The following agreement and conclusions were agreed:

|  |
| --- |
| **Agreement**RAN1 continues to discuss and decide whether or not to introduce new UE assistance information for solution(s) to enable Tx/Rx in gaps/restrictions that are caused by RRM measurements. At least the following UE assistance information is considered for further study:* FFS: UE assistance information related to measurement occasions:
	+ FFS: The number of needed measurement gaps/SMTC with restrictions within a time period;
	+ FFS: The maximum number or ratio of MGs/SMTC with restrictions that can be skipped within a time period;
	+ FFS: The number of required SSBs within a time period;
	+ FFS: The number of consecutive RRM measurements that can be skipped;
	+ FFS: The maximum interval between two consecutively reserved gap/restriction occasions for RRM measurements;
	+ FFS: The patterns of gap(s)/restriction(s) where skipping is feasible or acceptable;
* FFS: UE assistance information related to channel conditions:
	+ FFS: RSRP is below/above search threshold (s-MeasureConfig);
* FFS: UE assistance information related to traffic:
	+ FFS: PSI (PDU set importance);
* FFS: UE assistance information related to UE mobility:
	+ FFS: L3 parameters related to mobility, e.g., static or not

Companies are encouraged to provide additional details (e.g. how often the UE assistance info is provided, timing, applicable scenarios, performance gains, etc) on their preferred scheme.Note: From specification point of view, there is no mandated gNB behavior in response to any of the UE assistance information. RAN1 to make decision, from RAN1 perspective, in RAN1#117 on the support of UE assistance information.**Conclusion**RAN1 does not further discuss new UE assistance information related to channel conditions, traffic, UE mobility.**Conclusion**There is no consensus in RAN1 to support UE assistance information related to measurements occasions. The reason for this situation is lack of consensus on the need/feasibility for UAI and lack of technical understanding on issues outside of RAN1 expertise (e.g. impact of RRM measurement performance). It is up to other working groups to trigger further work in RAN1 on UE assistance information. |

Particularly, it was discussed whether impact on RRM performance from skipping measurement occasions may be reduced if UE sends additional information: * Information about the maximum number of MGs/SMTC with restrictions that can be skipped within a time period.
* Information about the patterns of gap(s)/restriction(s) where skipping is feasible or acceptable.

**2. Actions:****To RAN4:****ACTION:** RAN1 kindly asks RAN4 to consider the above information into account and decide whether or not to introduce any UE assistance information related to measurement occasions. |

Final LS in R1-2405736.

## RAN1#118

**Agreement**

For solutions based on triggering/enabling by network signaling to enable Tx/Rx in gaps/restrictions that are caused by RRM measurements select one among the following options:

**Option 1: Support Alt. 1-1:**

* Alt. 1: Dynamic indication to enable Tx/Rx in particular gap(s)/restriction(s) that are caused by RRM measurements.
	+ **Alt 1-1**: Explicit indication by DCI to skip a particular gap(s)/restriction(s);
		- Indication is included as part of scheduling DCI:
			* FFS: Bit-field size is one bit;
			* FFS: Bit-field size is >1 bit;
		- Note: Minimum time offset(s) between the end of [the first] received dynamic indication and start of corresponding gap(s)/restriction(s) occasion that is going to be skipped shall be introduced.
	+ FFS: DCI format, DCI content, DCI bit-field size;
	+ FFS: Whether indication is for one or more occasions;
	+ FFS: How to consider time offset between the end of received dynamic indication and start of gap(s)/restriction(s) occasion that is going to be skipped.

**Option 2: Support Alt. 3-1:**

* Alt. 3: Semi-static solution to enable TX/RX in gaps/restrictions that are caused by RRM measurements.
	+ **Alt 3-1**: Configure a pattern(s) via RRC to indicate occasions where to skip gaps/restrictions;
		- FFS: Details of pattern:
			* FFS: Pattern is based on periodicity, offset and duration;
			* FFS: Pattern is based on a bitmap;
		- FFS: whether a pattern is applied to all or subset of configured MG configurations/scheduling restrictions.

**Agreement**

If Alt. 1 from RAN1#117 agreement is supported, minimum time offset(s) X between indication to skip and skipped measurement occasion is up to RAN4 to discuss and decide on particular value(s).

**Working Assumption**

For solutions based on triggering/enabling by network signaling to enable Tx/Rx in gaps/restrictions that are caused by RRM measurements select the following option:

* Alt. 1: Dynamic indication to enable Tx/Rx in particular gap/restriction that are caused by RRM measurements.
	+ **Alt 1-1**: Explicit indication by DCI to skip a particular gap/restriction;
		- Indication is included as part of scheduling DCI:
			* Bit-field size is one bit;
				+ The bit in the DCI is used to indicate whether to skip the first gap/restriction occasion after a minimum time offset required between the last symbol of the PDCCH carrying the DCI format and the start of corresponding skipped gap/restriction occasion indicated by the DCI.

Send an LS to RAN4 to inform them of the above working assumption and ask them if there is any issue with it. Final LS in R1-2407561.

# References

|  |  |  |
| --- | --- | --- |
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 | Revised WID on XR (eXtended Reality) for NR Phase 3 | Nokia (Rapporteur) |
| 1. [R1-2407674](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_118b/Docs/R1-2407674.zip)
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| 1. [R1-2407720](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_118b/Docs/R1-2407720.zip)
 | Discussion on enabling TX/RX for XR during RRM measurements | Spreadtrum Communications |
| 1. [R1-2407801](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_118b/Docs/R1-2407801.zip)
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| 1. [R1-2407877](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_118b/Docs/R1-2407877.zip)
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| 1. [R1-2408077](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_118b/Docs/R1-2408077.zip)
 | Discussion on measurement gap for XR | ZTE Corporation, Sanechips |
| 1. [R1-2408091](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_118b/Docs/R1-2408091.zip)
 | Enabling TX/RX for XR during RRM measurements | Lenovo |
| 1. [R1-2408153](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_118b/Docs/R1-2408153.zip)
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| 1. [R1-2408220](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_118b/Docs/R1-2408220.zip)
 | Discussion on enabling TX/RX for XR during RRM measurements | NEC |
| 1. [R1-2408256](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_118b/Docs/R1-2408256.zip)
 | Enabling TX/RX for XR during RRM measurements | TCL |
| 1. [R1-2408259](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_118b/Docs/R1-2408259.zip)
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| 1. [R1-2408314](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_118b/Docs/R1-2408314.zip)
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| 1. [R1-2408424](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_118b/Docs/R1-2408424.zip)
 | Views on Enabling TX/RX for XR during RRM measurements | Sony |
| 1. [R1-2408494](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_118b/Docs/R1-2408494.zip)
 | Discussion on TX/RX for XR during RRM measurements | Apple (UK) Limited |
| 1. [R1-2408535](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_118b/Docs/R1-2408535.zip)
 | Discussion on Enabling TX/RX for XR During RRM Measurements | Meta |
| 1. [R1-2408583](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_118b/Docs/R1-2408583.zip)
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| 1. [R1-2408603](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_118b/Docs/R1-2408603.zip)
 | Discussion on enabling TX/RX for XR during RRM measurements | Panasonic |
| 1. [R1-2408662](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_118b/Docs/R1-2408662.zip)
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| 1. [R1-2408682](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_118b/Docs/R1-2408682.zip)
 | Discussion on XR during RRM measurements | LG Electronics |
| 1. [R1-2408715](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_118b/Docs/R1-2408715.zip)
 | Enabling TX RX for XR during RRM measurements | MediaTek Inc. |
| 1. [R1-2408801](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_118b/Docs/R1-2408801.zip)
 | Discussion on Enaling TX/RX for XR during RRM | NTT DOCOMO, INC. |
| 1. [R1-2408866](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_118b/Docs/R1-2408866.zip)
 | Enabling Tx/Rx for XR during RRM measurements | Qualcomm Incorporated |
| 1. [R1-2408892](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_118b/Docs/R1-2408892.zip)
 | Enabling TX/RX for XR traffic during RRM measurement gaps | Ericsson |