April 27, 2017

Ms. Susanna Kooistra
Mr. Maurice Pope
3GPP Mobile Competence Centre (ETSI)
650, Route Des Lucioles
06921 Sophia-Antipolis
France

Subject: Nomination of Mr. Puneet Jain as candidate for 3GPP TSG SA WG2 Vice Chairman

Dear Susanna, Dear Maurice,

On behalf of Intel (ATIS member), I am pleased to nominate Mr. Puneet Jain for the position of 3GPP TSG SA WG2 Vice Chairman in the election during 3GPP TSG SA WG2#121 meeting, May 15 – 19, 2017 in Hangzhou, P. R. China.

Puneet has 20 years of experience in standardization, research and product development in telecom and wireless industry. Puneet currently serves as the Vice Chairman of TSG SA WG2 and has done so for one term (2015-2017). He has actively contributed to and organized work in TSG SA WG2, as head of the Intel TSG SA WG2 delegation and rapporteur of various work items, on Machine Type Communications (MTC)/Cellular Internet of Things (CIoT).

If elected, Puneet will provide support for TSG SA WG2 Chairman and ensure efficient and successful progress of the TSG SA WG2 meeting and related activities.

Mr. Puneet Jain is informed about and aware of the antitrust/competition laws and regulations of relevant jurisdictions, and he shall comply with such laws while acting in his capacity as TSG SA WG2 Vice Chairman.

Intel is fully committed to provide the necessary support and resources to ensure that Puneet accomplishes the duties and responsibilities as the Vice Chairman of 3GPP TSG SA WG2.

Yours sincerely,

Dr. Geng Wu
Head of Intel Wireless Standards
Intel Fellow
Next Generation and Standards (NGS)

Position of candidacy: 3GPP TSG SA WG2 Vice Chairman

Candidate Name: Puneet Jain

Member: Intel

Partner Organization: ATIS

Brief Curriculum Vitae:

Puneet Jain is a Lead Systems Architect in the Intel Communication and Devices Group at Intel Corporation since March, 2001. At Intel he is driving research and standardization on MAC, RAN, and end-to-end network architecture for mobile broadband technologies. His responsibilities include architecting and standardization of Machine to Machine (M2M)/Internet of Things (IoT), design and driving development choices for 3GPP LTE and 5G systems.

Puneet has 20 years of experience in standardization, research and product development in telecom and wireless industry. He has participated in various global standardization bodies such as 3GPP, WiMAX Forum, IEEE 802.16j, ETSI TC M2M, oneM2M, GSMA, NPF, ATIS etc. Puneet currently serves as the Vice Chairman of TSG SA WG2 and has done so for one term (2015-2017). As TSG SA WG2 Vice Chair, Puneet has chaired numerous sessions on 3GPP, non-3GPP and 5G tracks.

Puneet is head of Intel delegation in 3GPP SA WG2 since 2010 and active contributor to SA WG2 and TSG SA since 2006. He has served as Rapporteur of several study/work items - SIMTC, MTCe-SDDTE, AESE, MONTE, FS_AE_CIoT, CIoT, FS_CIoT_Ext, CIoT_Ext on Machine Type Communication (MTC)/Cellular Internet of Things (CIoT) since 3GPP Rel-11 in SA WG2 and has chaired many drafting and breakout sessions.

Puneet has also served as oneM2M Steering Committee Vice Chairman (2014–2016). He was active contributor to oneM2M functional Architecture and oneM2M-3GPP interworking aspects.

In his previous roles at Intel, he was chairman of WiMAX-3GPP interworking subteam at WiMAX Forum, involved in design and development of ATM RAN reference implementation for Intel Network Processors, design and development for Modular Building Blocks for IP/UDP Header compression/decompression in 3G Networks and standardization of ATM Functional API at Network Processing Forum.

Prior to Intel, he worked at Center for Development of Telematics (C-DoT) in India where he led a team for design and development for Mobility Management and BSS Application Protocol for GSM MSC/VLR and was key contributor to technical evaluation and network configuration of C-DoT MSC/VLR during launch of Indian Mobile Personal communication Services (IMPCS) by Department of Telecommunications, Government of India. He graduated with honors in Computer Science and Engineering from G.J. University, India in 1997.