



April 27, 2015

Ms. Susanna Kooistra
Mr. Maurice Pope
3GPP Mobile Competence Centre
650, route des Lucioles
F-06921 Sophia-Antipolis
France

Subject: Nomination of Mr. Puneet Jain as 3GPP TSG SA WG2 Vice Chairman Candidate

Dear Susanna, Dear Maurice,

On behalf of Intel Corporation, ATIS member, I am pleased to nominate Mr. Puneet Jain for the position of 3GPP TSG SA WG2 Vice Chairman in the election during 3GPP SA2#109 meeting, May 25 – 29, 2015 in Fukuoka, Japan.

Puneet has 18 years of experience in telecom and wireless industry. He is an active contributor to 3GPP TSG SA WG2 and TSG SA since 2006. He is rapporteur of several work items on Machine Type Communication (MTC) since 3GPP Rel-11 in SA WG2 and has chaired many drafting and breakout sessions.

If elected, Puneet will provide support for TSG SA WG2 Chairman and ensure efficient and successful progress of the TSG SA WG2 meeting and related activities.

Puneet will be trained to comply with all applicable antitrust/competition laws and regulations while acting in his capacity as TSG SA WG2 Vice Chairman.

Intel will provide all the necessary support and resources to ensure that Puneet accomplishes the duties and responsibilities as the Vice Chairman of 3GPP TSG SA WG2.

Puneet's brief resume is attached.

Yours sincerely,

A handwritten signature in blue ink, appearing to read "Asha R Keddy".

Asha R Keddy
Vice President, Platform Engineering Group
General Manager, Next Generation and Standards (NGS)

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Position of candidacy: 3GPP TSG SA WG2 Vice Chairman

Candidate Name: Puneet Jain

Member: Intel

Partner Organization: ATIS

Brief Curriculum Vitae:

Puneet Jain is a Lead Systems Architect in the Intel Communication and Devices Group at Intel Corporation since March, 2001. At Intel he is driving research and standardization on MAC, RAN, and end-to-end network architecture for mobile broadband technologies. His responsibilities include architecting and standardization of Machine to Machine (M2M)/Internet of Things (IoT), design and driving development choices for 3GPP LTE and 5G systems.

Puneet has 18 years of experience in telecom and wireless industry. He is an active contributor to 3GPP SA WG2 and TSG SA since 2006. He is rapporteur of several work items - SIMTC, MTCe-SDDTE, AESE, MONTE, FS_AE_CIoT, on Machine Type Communication (MTC) since 3GPP Rel-11 in SA WG2 and has chaired many drafting and breakout sessions. Puneet has been actively involved in oneM2M and currently serve as oneM2M Steering Committee Vice Chairman. He was active contributor to oneM2M functional Architecture and oneM2M-3GPP interworking aspects.

In his previous roles at Intel, he was chairman of WiMAX-3GPP interworking subteam at WiMAX Forum, involved in design and development of ATM RAN reference implementation for Intel Network Processors, and standardization of ATM Functional API at Network Processing Forum.

Prior to Intel, he worked at Center for Development of Telematics (C-DoT) in India where he led a team for design and development for Mobility Management and BSS Application Protocol for GSM MSC/VLR and was key contributor to technical evaluation and network configuration of C-DoT MSC/VLR during launch of Indian Mobile Personal communication Services (IMPCS) by Department of Telecommunications, Government of India. He graduated with honors in Computer Science and Engineering from G.J. University, India in 1997.

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