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January 2, 2025

Ms. Elisabetta Comin and Mr. Joern Krause ETSI Mobile Competence Centre 650, route des Lucioles 06921 Sophia Antipolis France

NOMINATION OF CANDIDATE FOR 3GPP TSG RAN PLENARY CHAIR

Dear Ms. Comin and Mr. Krause,

I am pleased to inform you that Samsung Electronics Co., Ltd. (TTA) is nominating Dr. Younsun Kim as a candidate for the position of 3GPP TSG RAN Plenary Chair for the upcoming election in RAN#107 meeting.

If elected, Younsun will strive to ensure that the development of the 3GPP specifications continues in an efficient, fair and productive manner, ensuring that the content produced is of top quality. Samsung will provide all the necessary support for Younsun to allow him to undertake the role and responsibilities.

Younsun is informed about and will be trained to comply with the applicable antitrust/completion laws and regulations of relevant jurisdictions and he will, if elected, comply with such laws while acting in his capacity as the chair of 3GPP TSG RAN Plenary.

Best Regards,

Jin-Kyu Han Vice President, Samsung Research, Samsung Electronics Co., Ltd.

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Younsun Kim is a Master (VP of Technology) in Samsung Electronics where he has been contributing to the design of cellular air interface standards for over 25 years.

From 1999 to 2005, Younsun participated in 3GPP2 meetings as a technical delegate for WG3 (physical layer) and has actively contributed to the standardization of cdma2000 Rev. A/B, 1xEVDO, and 1xEVDV. Since 2009, he

has been participating in 3GPP TSG RAN WG1 and has actively contributed to a wide range of features in LTE-Adv, LTE-Adv Pro, 5G, and 5G-Adv.

Younsun has been serving as 3GPP TSG RAN WG1 Chair since 2021. He also served as a Vice-Chair of the same working group from 2017 to 2021. As RAN1 Chair and Vice-Chair, he successfully managed and led the technical discussions of wide range of 5G and LTE topics. During his terms as RAN1 Chair, RAN1 has successfully delivered Rel-17 (during COVID-19 pandemic) and Rel-18 – the first release of 5G-Adv.

Younsun received B.S. and M.S. degrees in electronic engineering from Yonsei University, Korea, and Ph.D. degree in electrical engineering from University of Washington, USA, in 1996, 1999, and 2009, respectively.