

Step name

L2M

ts_AssignCN_Domain
 ts_CheckCell_IsConfigured
 ts_CheckTimer
 ts_CheckUE_Idle
 ts_DecrementCellPowerLevel
 ts_IncrementCellPowerLevel
 ts_InitVariables
 ts_NAS_Delay
 ts_SaveCellInfo
 ts_SetAttenuationLevel
 ts_SetCellCfg
 ts_SetTmpCellInfo
 ts_SwitchCellPowerLevels
 ts_CMAC_CipherCfg
 ts_CMAC_NewU_RNTI_Reconf
 ts_CMAC_Pag1Cfg
 ts_CMAC_Pag2Cfg
 ts_CMAC_PagingCfgIMSI
 ts_CMAC_Release
 ts_CPHY_ActTime
 ts_CPHY_Commit
 ts_CPHY_TrCh_Release
 ts_CRLC_CipherConfigRB2
 ts_CRLC_DL_CipherCfg
 ts_CRLC_DL_CipherCfg_RAB
 ts_CRLC_DL_Integrity
 ts_CRLC_Release
 ts_CRLC_ResumeRB2
 ts_CRLC_ResumeRB_FACH
 ts_CRLC_ResumeSRB_134
 ts_CRLC_ResumeSRBs
 ts_CRLC_SuspendRB2
 ts_CRLC_SuspendRB_FACH
 ts_CRLC_SuspendSRB_134
 ts_CRLC_SuspendSRBs
 ts_CRLC_UL_CipherCfg
 ts_CRLC_UL_CipherCfg_RAB
 ts_CRLC_UL_Integrity
 ts_CreateCellFACH_2_PRACH
 ts_RB_2ndCCCH_Cfg
 ts_SS_1DCH_DCCH_Cfg
 ts_SS_1DCH_DCCH_DTCH_Modify
 ts_SS_1DCH_DCCH_Modify
 ts_SS_2DCH_Modify
 ts_SS_2DCH_Modify_UL32k
 ts_SS_4DCH_Modify
 ts_SS_BCH_SCH_CPICH_Cfg
 ts_SS_CPHY_OutOfSyncReceive
 ts_SS_CPHY_SyncReceive
 ts_SS_CellCfg

ts_SS_CreateCellDCH
 ts_SS_CreateCellFACH
 ts_SS_CreateCellFACH_BMC
 ts_SS_LowerLayerBackNormalNAS
 ts_SS_LowerLayerFailNAS
 ts_SS_PCH_2FACH_CCCH_DCCH_BCCH_Cfg
 ts_SS_PCH_2FACH_CCCH_DCCH_BCCH_DTCH_Cfg
 ts_SS_PCH_2FACH_CCCH_DCCH_BCCH_DTCH_Modify
 ts_SS_PCH_FACH_CCCH_Cnfg
 ts_SS_PCH_FACH_CCCH_DCCH_BCCH_CTCH_DTCH_Cfg
 ts_SS_RACH_CCCH_Cfg
 ts_SS_RACH_CCCH_DCCH_DTCH_Cfg
 ts_SS_RACH_CCCH_DCCH_DTCH_Modify
 ts_SS_RB0_Cfg
 ts_SS_RB10_TM_Cfg
 ts_SS_RB10_To_RB12_TM_Cfg
 ts_SS_RB1_To_RB4_Cfg
 ts_SS_RB20_AM_21_UM_Cfg
 ts_SS_RB20_AM_PS_Cfg
 ts_SS_RB20_AM_PS_UL_DL_Cfg
 ts_SS_RB21_UM_Cnfg
 ts_SS_RB30_Cfg
 ts_SS_RB_BCCH_BCH_Cfg
 ts_SS_RB_BCCH_FACH_Cfg
 ts_SS_RB_PCCH_Cfg
 ts_SS_ReconfigRAB_ToSRB
 ts_SS_Release
 ts_SS_ResetRLC_SRB
 ts_SS_StopCell
 ts_SS_StopCell_Except_pCCPCH
 ts_SS_StopRL
 ts_SecondRACH_CCCH_Config
 ts_CS_IdleUpdated
 ts_CS_IdleUpdatedSpecial
 ts_GMM_AttachReject
 ts_GMM_Authentication
 ts_GMM_AuthenticationInit
 ts_GMM_DetachMT
 ts_GMM_DetachOnSwitchOff
 ts_GMM_PS_Registration
 ts_GMM_PagingResp
 ts_GMM_ServiceRequest
 ts_IdleUpdated
 ts_IdleUpdatedSpecial
 ts_MM_Authentication
 ts_MM_AuthenticationInit
 ts_MM_IMSI_Detach
 ts_MM_PwrOrUSIM_Off
 ts_MM_PwrOrUSIM_On
 ts_MM_SecurityOff
 ts_MM_SecurityOn
 ts_PS_IdleUpdated

ts_PS_IdleUpdatedSpecial
ts_PS_PagingType1_PTMSI
po_ConnectionAndSS_Release
po_ConnectionAndSS_Releases
ts_RRC_SendRB_SetUp_DCH_57_6k_CS
ts_RRC_SendRB_SetUp_DCH_64k_CS
ts_RRC_SendRB_SetUp_DCH_64k_PS
ts_RRC_SendRB_SetUp_DCH_Speech
ts_RRC_SendRB_SetUp_FACH_PS
ts_RRC_SetUpRAB
ts_RRC_SetUpRAB_AM_64k_PS
ts_RRC_SetUpRAB_AM_UM_64k_PS
ts_RRC_SetUpRAB_DCH_AM_15Lis
ts_RRC_SetUpRAB_DCH_AM_7Lis
ts_RRC_SetUpRAB_DCH_AM_7_15Lis
ts_RRC_SetUpRAB_DCH_UM_15Lis
ts_RRC_SetUpRAB_DCH_UM_7Lis
ts_RRC_SetUpRAB_DCH_UM_7_15Lis
ts_RRC_SetUpRAB_UM_64k_PS
ts_RRC_ConnEst
ts_RRC_ConnEst_DCH_MT_IMSI
ts_RRC_ConnEst_DCH_MT_IMSI_NoReact
ts_RRC_ConnEst_DCH_MT_NoReactPagType2
ts_RRC_ConnEst_DCH_MT_PTMSI
ts_RRC_ConnEst_DCH_MT_TMSI
ts_RRC_ConnEst_DCH_MT_TMSI_NoReact
ts_RRC_ConnRel
ts_RRC_PagNotifyIdleMode
ts_RRC_PagType1
ts_RRC_PagType1_TMSI_Cause
ts_RRC_PagType2
ts_RRC_PhyChannelReconf
ts_RRC_PhyChannelReconfFail
ts_RRC_RB_ReconfUM_PDCP
ts_RRC_RandAccFail
ts_RRC_SignConnectRel
ts_SS_ReConfPhyChDCH_ToDCH
ts_RRC_InitSecurityIE_RAB
ts_RRC_Security
ts_RRC_SecurityRAB
ts_SS_RelSRB_SecurityOffSS
ts_CellDependentPara
ts_ChangeMIB_ValueTag
ts_ChangeSB1_ValueTag
ts_RegionalParaInit
ts_Scheduling
ts_SendDefSysInfo
ts_SendMIB
ts_SendNoSeg
ts_SendPage1_ModifySI
ts_SendSB1
ts_SendSIB1

ts_SendSIB11
 ts_SendSIB12
 ts_SendSIB16
 ts_SendSIB18
 ts_SendSIB2
 ts_SendSIB3
 ts_SendSIB4
 ts_SendSIB5
 ts_SendSIB5_Special
 ts_SendSIB6
 ts_SendSIB7
 ts_SendSysInfoWithSpecialSIB1
 ts_SendSysInfoWithSpecialSIB5_And6
 ts_SendSysInfoWithSpecialSIB5_Reschedule
 ts_SysInfoModifyMM
 ts_SysInfoModifySIB1_RRC
 ts_SysInfoModifySIB3_And4_RRC
 ts_SysInfoModifySIB5_And6_RRC
 ts_TimedSysInfoModifyMM
 ts_TC_ActivateRB_TestMode
 ts_TC_CloseUE_TestLoop
 ts_TC_DeactivateRB_TestMode
 ts_TC_OpenUE_TestLoop
 ts_AT_InitCallCS
 ts_AT_InitConnection
 ts_AT_OrgPS_Call
 ts_AT_SetQoS
 ts_AT_TriggerGMM_Attach
 ts_AT_TriggerGMM_Detach
 ts_MMI_PLMN_SelModeAuto
 ts_MMI_PLMN_SelModeMan
 ts_MMI_PLMN_SelPerf
 ts_MMI_UE_PwrOff
 ts_MMI_UE_PwrOn
 ts_MMI_UE_SwitchOff
 ts_MMI_UE_SwitchOn
 ts_MMI_UE_SwitchOnTriggerGMM_Attach
 ts_MMI_USIM_Insert
 ts_MMI_USIM_Remove
 ts_BMC_SendCBS_Message

Total: 194

L3M

ts_CC_BS20MO
 ts_CC_BS20MT
 ts_CC_BS30MO
 ts_CC_BS30MT
 ts_CC_BasicServMO
 ts_CC_BasicServMO_Def
 ts_CC_BasicServMT
 ts_CC_BasicServMT_Def
 ts_CC_CheckServSupported
 ts_CC_CheckState

ts_CC_EnterU01
 ts_CC_EnterU1
 ts_CC_EnterU10_MO_Def
 ts_CC_EnterU10_MT_Def
 ts_CC_EnterU4_Def
 ts_CC_InitTCV_MO
 ts_CC_InitTCV_MT
 ts_CC_RcvCallConf
 ts_CC_RcvSetupOrEsetup
 ts_CC_TS61_MO
 ts_CC_TS61_MT
 ts_CC_TelephonyMO
 ts_GMM_ServReqAcqDef
 ts_MM_CM_ServReqDef
 ts_MM_IdReq
 ts_MM_StartCellA
 ts_ActivatePDP_AcceptMO
 ts_DeactivatePDP_ContextMT
 ts_SetTI_Rsp
 ts_AT_AnswerCall
 ts_AT_CMSS
 ts_AT_CmdCBST
 ts_AT_DeactPDP_Context
 ts_AT_InitSMS_MO
 ts_AT_TerminateCall
 ts_MMI_CheckDTCH_ThroughConn
 ts_UT_ConfigUE_MO
 ts_UT_ConfigUE_MT
 ts_UT_USIM2_Insert
 ts_NAS_ConnRejectMO
 ts_NAS_PagingRsp

Total: 41

RRC_M

ts_CheckUE_DataTransmission
 ts_CheckUE_NoDataTransmission
 ts_ConfigAdditionalDL
 ts_ConnectedModePagingType1
 ts_IdleModePagingType1
 ts_RRC_ConnEstCS_MO_P3_P4
 ts_RRC_ConnEstPS_MO_P5_P6
 ts_RRC_InitVariables
 ts_RRC_InitVariablesCS
 ts_RRC_InitVariablesPS
 ts_RRC_NAS_CallSetupCS_MO_P7_P8
 ts_RRC_NAS_SessionActPS_MO_P9_P10
 ts_RRC_RAB_EstCS_MO_P11
 ts_RRC_RAB_EstPS_MO_P13
 ts_SetSS_DisableAck
 ts_SetSS_EnableAck
 ts_SwitchRx
 ts_SwitchTx
 ts_TransitFACH_ToCellPCH_P15_P16

ts_TransitFACH_ToURA_PCH_P17_P18
 ts_VerifyNoAccess
 ts_PCH_2FACH_ModifyUL64_DL32
 ts_CRLC_SuspendRAB20
 ts_CRLC_ResumeRAB20
 ts_SS_ReconfTrChDCH_ToDCH_64k
 ts_SS_ReconfFACH_ToDCH_PS
 ts_SS_ReconfDCH_ToFACH
 ts_SS_2_FACH_1_RACH_Modify
 ts_SS_ReConfFACH_ToDCH
 ts_SS_2_FACH_1_RACH_ModifyDCH_Cfg
 ts_SS_ReconfPhyChFACH_ToDCH
 ts_SS_ReConfPhyChFACH_ToFACH
 ts_SS_ReConfPhyChPCH
 ts_SS_ReConfTrCh_1
 ts_SS_ReConfTrChDCH_ToDCH
 ts_SS_ReConfTrChDCH_ToFACH
 ts_SS_ReConfTrChFACH_ToFACH
 ts_SS_ReConfTrChPCH
 ts_SS_RemovePhyChDCH
 ts_SS_RemovePhyChFACH
 ts_SS_RemoveTrChDCH

Total: 41

RRC

ts_SendModifiedSysInfoNoSIB12
 ts_SendSysInfoModifiedSIB3And4
 ts_SendSysInfoModifiedSIB12
 ts_SysInfoModifySIB12_RRC
 ts_ModifiedRegionalParalnitWithSIB12
 ts_ModifiedRegionalParalnitNoSIB12
 ts_ChangeMIB_ValueTagSpecificValue
 ts_SysInfoModifySIB11_MIB_RRC
 ts_SysInfoModifySIB1_SIB11_RRC
 ts_SysInfoModifySIB11_RRC
 ts_SIB12_MsgNoSegments
 ts_SendSystemInfoChangeInd
 ts_SysInfoModifySIB12_MIB_RRC
 ts_RRC_ConnEstToFACH
 ts_RRC_ConnEstFACH
 ts_RRC_ConnEstToDCH
 ts_RRC_ConnEstTo_FACH
 ts_SendModifiedSysInfo
 ts_PagingType1_IMSI
 ts_PagingOnAllConfiguredCells
 ts_RecieveRandomAccessReq
 ts_RecieveRandomAccessReqAnyCell_1
 ts_RegistrationReject
 ts_SendModifiedSysInfoSIB11_12
 ts_ModifiedRegionalParalnit6_1_2_SIB_11_12
 ts_SendDefSysInfoIdleCellNoHCS_Def
 ts_ModifiedRegionalParalnit6_1_2
 pr_GotoState6_9_Or6_10_MO

ts_RB_ConfCommunication
 pr_RB_SetupMO
 pr_RB_SetupMO_FACH
 ts_SetupRAB_Send
 ts_SetupRAB_FACH_ToDCH_PS
 ts_SetupRAB_DCH_Speech
 ts_SetupRAB_PS_DCH_ToFACH
 ts_SetupRAB_TM_DCH_ToDCH
 ts_InitCapability
 ts_InitPDCP_Capability
 ts_DL_InitTurboSupport
 ts_UL_InitTurboSupport
 ts_InitSimultaneousSCCPCH_DPCH_Reception

Total: 41

RAB

ts_RB_InitTest_CS
 ts_RB_InitTest_PS
 ts_RB_SubTest1_CS
 ts_RB_SubTest2_CS
 ts_RB_SubTest3_CS
 ts_RB_SubTest1_PS
 ts_RB_SubTest2_PS
 ts_RRC_PagType1_P_TMSI_Cause
 ts_RB_SubTest3_PS
 ts_RB_SendRB_SetUp_StreamUnknown_28_8k
 ts_RB_SendRB_SetUp_StreamUnknown_14_4k
 ts_RB_SendRB_SetUp_ConvUnknown_32k_40
 ts_RB_SendRB_SetUp_ConvUnknown_32k_20
 ts_RB_SendRB_SetUp_ConvUnknown_64k_40
 ts_RB_SendRB_SetUp_ConvUnknown_28_8
 ts_RB_SendRB_SetUp_Speech_4_75k
 ts_RB_SendRB_SetUp_Speech_5_15k
 ts_RB_SendRB_SetUp_Speech_5_9k
 ts_RB_SendRB_SetUp_Speech_6_7k
 ts_RB_SendRB_SetUp_Speech_7_4k
 ts_RB_SendRB_SetUp_Speech_7_95k
 ts_RB_SendRB_SetUp_Speech_10_2k
 ts_SendRB_SetUp_DCH_64k_128kPS
 ts_2DCH_ModifyConvUnknown
 ts_2DCH_ModifyConvUnknown_28_8
 ts_2DCH_ModifyStreamUnknown_14_4
 ts_2DCH_ModifyStreamUnknown_28_8
 ts_3DCH_ModifySpeech
 ts_4DCH_ModifySpeech102
 ts_RB10_To_RB11_TM_ConfigSpeech
 ts_RB10_To_RB12_TM_ConfigSpeech
 ts_RB_SetupRAB_CS1
 ts_RB_SetupRAB_CS
 ts_RB_SetupRAB_PS
 ts_2DCH_ModifyStreamUnknown_64k_0k
 ts_2DCH_ModifyStreamUnknown_128k_0k
 ts_2DCH_ModifyInteractiveBackGround_32k_8kPS_10_TC

ts_2DCH_ModifyInteractiveBackGround_32k_8kPS_10_CC
 ts_5DCH_ModifySpeech_12_2k_StreamingUnknown_57_6k
 ts_5DCH_ModifySpeech_12_2k_ConvUnknown_64k_20
 ts_5DCH_ModifySpeech_12_2k_ConvUnknown_64k_40
 ts_3DCH_ModifyConvUnknown_64k_ConvUnknown_64k_20
 ts_3DCH_ModifyConvUnknown_64k_ConvUnknown_64k_40
 ts_RB_SendRB_SetUp_StreamUnknown_64k_0k
 ts_RB_SendRB_SetUp_StreamUnknown_128k_0k
 ts_RB_SendRB_SetUp_DCH_32k_8kPS_10_TC
 ts_RB_SendRB_SetUp_DCH_32k_8kPS_10_CC
 ts_RB_SendRB_SetUp_Speech_12_2k_StreamingUnknown_57_6k
 ts_RB_SendRB_SetUp_Speech_12_2k_ConvUnknown_64k_20
 ts_RB_SendRB_SetUp_Speech_12_2k_ConvUnknown_64k_40
 ts_RB_SendRB_SetUp_ConvUnknown_64k_ConvUnknown_64k_20
 ts_RB_SendRB_SetUp_ConvUnknown_64k_ConvUnknown_64k_40
 ts_RB10_To_11_TM_CS_Config
 ts_RB10_To_13_TM_CS_Config
 ts_RB20_AM_PS_Config1
 ts_RB_SubTest_RB11
 ts_RB_SubTest_RB13
 ts_RB_SubTest_RB10
 ts_RB_SubTest_RB10_TM
 ts_RB_SubTest_RB10_RB11
 ts_RB_SubTest_RB10_RB11_RB12
 ts_RB_SubTest_RB10_RB11_RB12_RB13
 ts_RB_SubTest_RB10_RB11_RB12_RB13_TM
 ts_RB_SubTest_RB10_RB11_RB12_RB20
 ts_RB_SubTest_RB10_RB13
 ts_RB_SubTest_RB10_RB13_TM
 ts_RB_SubTest_RB10_RB20
 ts_RB_SubTest_RB20
 ts_RB_SubTest_RB13_TM
 ts_RB_SubTest_AM
 ts_RB_SubTest1_to_receive_testdata
 ts_RB_SubTest1_to_receive_testdata_TM
 ts_InitSecurityIE_RAB1
 ts_RRC_Security_RAB1
 ts_SetCellConfig1

Total: 72

BMC

ts_BMC_SetupUM_CellPch
 ts_BMC_SetupUM_IdleMode
 ts_BMC_SetupUM_UraPch
 ts_Transit_FACH_ToURA_PCH
 ts_CalculationOfBMC_CBS_schedulingLevel2
 ts_MMI_ChkCBS_Ignored
 ts_MMI_ChkCBS_Received

Total: 7

MAC

ts_GenericSetupProceduresToBGP6_2
 ts_GetAuthReqSegment
 ts_WaitForStatusPDU

ts_SendAuthReq
 ts_TestNack
 ts_TestAck
 ts_RRC_ConnEstForMAC_Testing

Total: 7

PDCP

ts_PDCP_SetupAM
 ts_PDCP_SetupUM
 ts_PDCP_SetupUM_AM
 ts_RB_Release
 ts_RRC_RAB_ConnectionRelease_DCH
 ts_po_ConnectionCellRelease
 ts_AM_Transmission_Of_PDCP_Data
 ts_AM_Transmission_Of_PDCP_NoHeader
 ts_Check_Received_IP_Packet_PDCP_DataPDU_with_RFC2507configuration
 ts_Check_Received_IP_Packet_PDCP_DataPDU_without_compression
 ts_Check_Received_IP_Packet_PDCP_NoHeader_PDU_without_compression
 ts_Check_Received_IP_Packet_PDCP_SeqNumPDU_with_RFC2507configuration
 ts_Check_Received_TCPIP_Packet_PDCP_DataPDU_without_RFC2507configuration
 ts_IP_HeaderCompressionRfc2507
 ts_IP_HeaderDecompressionRfc2507
 ts_PDCP_AM_DataPDU_Transmission_with_RFC2507configuration
 ts_PDCP_AM_DataPDU_Transmission_without_compression
 ts_PDCP_AM_NoHeaderPDU_Transmission_without_compression
 ts_PDCP_UM_DataPDU_Transmission_with_RFC2507configuration
 ts_PDCP_UM_DataPDU_Transmission_without_compression
 ts_PDCP_UM_NoHeaderPDU_Transmission_without_compression
 ts_UM_Transmission_Of_PDCP_Data
 ts_UM_Transmission_Of_PDCP_NoHeader
 ts_receive_and_check_not_returned_compressed_IP_Packet_with_PDCP_PDU

Total: 12

RLC

pr_GenericSetupProcedures
 pr_InitialiseUM_RLC_StateVariables
 pr_InitialiseAM_RLC_StateVariables
 pr_RB_SetupUM7
 pr_RB_SetupUM7_48k
 pr_RB_SetupUM15_64k
 pr_RB_SetupAM7
 pr_RB_SetupAM7_48k
 pr_RB_SetupAM15_64k
 pr_RB_SetupUM15
 pr_RB_SetupAM15
 pr_CloseUE_TestLoop
 po_RB_Release
 po_OpenUE_TestLoop
 po_GenericCleanupProcedures
 ts_IncrementUM_VTUS
 ts_IncrementUM_VRUS
 ts_TxUMD_PRBS
 ts_RxUMD_PRBS
 ts_CreateUMD_PDU

ts_IncrementAM_VTS
 ts_IncrementAM_VRR
 ts_TxAMD_PRBS
 ts_RxAMD_IgnoreStatusAck
 ts_RxAMD_NackPU0IgnoreStatusAck
 ts_CreateAMD_PDU
 ts_UpdateVRH
 ts_GetRxAM_PRBS
 ts_GetTxAM_PRBS
 ts_GetRxUM_PRBS
 ts_GetTxUM_PRBS
 ts_CalculatePaddingSize
 ts_RLC_Delay
 ts_RRC_SetUpRAB_AM_7_RLC
 ts_RRC_SetUpRAB_UM_7_RLC
 ts_RRC_SetUpRAB_AM_15_RLC
 ts_RRC_SetUpRAB_UM_15_RLC
 ts_SS_RB_TM_Cfg_RLC

Total: 38

NAS

ts_CC_AutocallingAttempt
 ts_CC_BasicServMO_Tel
 ts_CC_BasicServMT_Tel
 ts_CC_CheckStateU0_MO
 ts_CC_CheckStateU0_MT
 ts_CC_CreateCellA
 ts_CC_Disconnect
 ts_CC_EnterU10_3_MO
 ts_CC_EnterU10_4_MO
 ts_CC_EnterU10_MO
 ts_CC_EnterU10_MT
 ts_CC_EnterU11_3
 ts_CC_EnterU11_4
 ts_CC_EnterU12
 ts_CC_EnterU12_3
 ts_CC_EnterU1_4
 ts_CC_EnterU3
 ts_CC_EnterU3_3
 ts_CC_EnterU3_4
 ts_CC_EnterU4
 ts_CC_EnterU4_3
 ts_CC_EnterU4_4
 ts_CC_EnterU6_2
 ts_CC_EnterU6_4
 ts_CC_EnterU7
 ts_CC_EnterU7_2
 ts_CC_EnterU8
 ts_CC_EnterU8_2
 ts_CC_EnterU8_4
 ts_CC_EnterU9
 ts_CC_EnterU9_2
 ts_CC_EnterU9_4

ts_CC_PrEnterU1
 ts_CC_PrEnterU10_4_MO
 ts_CC_PrEnterU10_MO
 ts_CC_PrEnterU10_MT
 ts_CC_PrEnterU11_3
 ts_CC_PrEnterU11_3Timer
 ts_CC_PrEnterU11_4
 ts_CC_PrEnterU12
 ts_CC_PrEnterU12_3
 ts_CC_PrEnterU19
 ts_CC_PrEnterU19_4
 ts_CC_PrEnterU19_4Timer
 ts_CC_PrEnterU1_4
 ts_CC_PrEnterU1_Timer
 ts_CC_PrEnterU3
 ts_CC_PrEnterU3_3
 ts_CC_PrEnterU3_4
 ts_CC_PrEnterU4
 ts_CC_PrEnterU4_3
 ts_CC_PrEnterU4_4
 ts_CC_PrEnterU7
 ts_CC_PrEnterU7_2
 ts_CC_PrEnterU8
 ts_CC_PrEnterU8_2
 ts_CC_PrEnterU8_4
 ts_CC_PrEnterU9_4
 ts_CC_EnterU10_MT_IMSI
 ts_AT_CheckAlerting
 ts_AT_CheckAlertingStop
 ts_AT_DTMF_StartTone
 ts_MMI_AucallingInitCall
 ts_MMI_AucallingInitCall_BlackList
 ts_MMI_AucallingResetBlacklist
 ts_MMI_CheckDTCH_NotThroughConnected
 ts_MMI_DTMF_CheckTone
 ts_MMI_EnableCallRefusal
 ts_UT_InitCM_ServReq
 ts_UT_InitNonCallRelSS
 ts_AT_SetMinQoS
 ts_AT_SecondaryPDP_Context
 ts_AT_ModifyPDP_Context
 ts_AT_InitEmergencyCall
 ts_MM_ChangeLAC
 ts_MM_ChkEcallIMEI
 ts_MM_ChkEcallIMSI
 ts_MM_ChkEcallIMSI_NoKey
 ts_MM_IMSI_Attach
 ts_MM_IMSI_AttachIni
 ts_MM_IMSI_DetachNoReaction
 ts_MM_LupAndStop
 ts_MM_LupAuth
 ts_MM_LupAuth2

ts_MM_LupAuth3
 ts_MM_LupAuth4
 ts_MM_LupAuth5
 ts_MM_LupAuthRpt
 ts_MM_LupIMSI
 ts_MM_LupInit
 ts_MM_LupInit2
 ts_MM_LupPer
 ts_MM_LupPer2
 ts_MM_LupPerFail
 ts_MM_LupPerRej
 ts_MM_LupPerRej2
 ts_MM_LupProc
 ts_MM_LupProc2
 ts_MM_LupProc3
 ts_MM_LupRej
 ts_MM_LupRej2
 ts_MM_NoCM_Services
 ts_MM_RmvFbdnPLMN
 ts_MM_RmvFbdnPLMN_1
 ts_MM_StartCellC
 ts_MM_TMSI_Reallocation
 ts_MM_Start2_Cells
 ts_MM_StartCellB
 ts_GMM_AuthenticationInit_InvalidMAC
 ts_GMM_AuthenticationInit_InvalidSQN
 ts_GMM_Config_CellA_CellB
 ts_GMM_Config_CellA_CellB_CellC
 ts_IMSI_AttachSequence
 ts_PeriodicLocationUpdatingSequence
 ts_MMI_SetOpModeA
 ts_MMI_SetOpModeC
 ts_MMI_USIM_InsertClean
 ts_MMI_InserUSIMAccessClassX
 ts_MMI_Trigger_GMM_AttachForNonPS_Services
 ts_MMI_Trigger_GMM_DetachForNonPS_Services
 ts_PS_PagingResp
 ts_CS_PagingResp
 ts_PS_PagingType1BCCH_Modified
 ts_VerifyNoAccess
 ts_VerifyNoNAS
 ts_CS_Paging_TMSI
 ts_CS_Paging_IMSI
 ts_PS_Paging_IMSI
 ts_PS_Paging_PTMSI
 ts_RRC_CellUpdate_DCCH_3_4k
 ts_RegistrationOnCS
 ts_RegistrationOnCS2
 ts_RegistrationOnCS_IfOpModeA
 ts_RegistrationOnCS2_IfOpModeA
 ts_ModifyPDP_Context_MT_Accept
 ts_SecPDP_ContextAccept_MO

ts_NAS_LowerLayerFail

Total: 137

SMS

ts_SMSCS_SetupMO

ts_SMSCS_SetupMO_Part1

ts_SMSCS_SetupMT

ts_SMSCS_SetupMT_U10

ts_SMSPS_SetupMO

ts_SMSPS_SetupMO_Part1

ts_SMSPS_SetupMT

ts_SMS_InitTI

ts_SMS_SetupMO_Mode

ts_AT_CMGD

ts_AT_CMGD_All

ts_AT_CMGF

ts_AT_CMGR

ts_AT_CMGW

ts_AT_CNMI

ts_AT_CPMS

ts_AT_CSCA

ts_AT_CSCS

ts_AT_CSMP_Rel

ts_AT_CSMS

ts_AT_EmptyMsgStorage

ts_AT_InitSMS_ThreeMsgs

ts_AT_Rmv1SM

ts_MMI_ChkCBS_Received

ts_MMI_ChkMCEF_Reset

ts_MMI_ChkMCEF_Set

ts_MMI_ChkMsgDisplayed

ts_MMI_ChkMsgIndicated

ts_MMI_ChkMsgNotIndicated

ts_MMI_ChkUSIM_AttIndMemProblem

ts_MMI_ChkUSIM_AttIndOK

ts_MMI_ConnectUSIM_Simulator

ts_MMI_InitSMS_CmdDel

ts_MMI_InitSMS_CmdEnq

ts_MMI_SM_Reply

ts_UT_ChkAllSM_But4th

ts_UT_RecallAndIndicate

ts_UT_RecallAndNotDisplay

ts_UT_SM_IndicateAndReply

ts_SM_ActCtxt

ts_SM_ActCtxt_MO

ts_SM_ActCtxt_MT

ts_SM_DeactCtxt_MO

ts_SM_DeactCtxt_MT

ts_GMM_ServiceRequestWithoutAccept

Total: 41