

**Source:** TSG-SA WG4  
**Title:** CRs to TS 06.54  
**Document for:** Approval  
**Agenda Item:** 7.4.3

The following CRs were agreed at SA WG4 meetings #13 and/or #14 and are presented to TSG SA #10 for approval.

Spec	CR	Rev	Phase	Subject	Cat	Ver	WG	Meeting	S3 doc
06.54	A007		Ph 2	Correction to the test vectors of the alternative EFR version	F	4.1.0	S4	S4-14	S4-000671
06.54	A008		R96	Correction to the test vectors of the alternative EFR version	A	5.2.0	S4	S4-14	S4-000672
06.51	A009		R97	Correction to the test vectors of the alternative EFR version	A	6.1.0	S4	S4-14	S4-000673
06.51	A010		R98	Correction to the test vectors of the alternative EFR version	A	7.1.0	S4	S4-14	S4-000674
06.51	A011		R99	Correction to the test vectors of the alternative EFR version	A	8.1.0	S4	S4-14	S4-000675

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## CHANGE REQUEST

⌘ **06.54 CR A007** ⌘ rev **-** ⌘ Current version: **4.1.0** ⌘

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**Proposed change affects:** ⌘ (U)SIM  ME/UE  Radio Access Network  Core Network

<b>Title:</b>	⌘ Corrections to the test vectors of the alternative EFR version		
<b>Source:</b>	⌘ TSG-SA WG4		
<b>Work item code:</b>	⌘ Alternative EFR implementation	<b>Date:</b>	⌘ 11-Dec-2000
<b>Category:</b>	⌘ <b>F</b>	<b>Release:</b>	⌘ <b>2</b>
	<p>Use <u>one</u> of the following categories:</p> <p><b>F</b> (essential correction)  <b>A</b> (corresponds to a correction in an earlier release)  <b>B</b> (Addition of feature),  <b>C</b> (Functional modification of feature)  <b>D</b> (Editorial modification)</p> <p>Detailed explanations of the above categories can be found in 3GPP TR 21.900.</p>		<p>Use <u>one</u> of the following releases:</p> <p><b>2</b> (GSM Phase 2)  <b>R96</b> (Release 1996)  <b>R97</b> (Release 1997)  <b>R98</b> (Release 1998)  <b>R99</b> (Release 1999)  <b>REL-4</b> (Release 4)  <b>REL-5</b> (Release 5)</p>

<b>Reason for change:</b>	⌘ <ul style="list-style-type: none"> <li>• test vectors of VAD option 1 were used to test VAD option 2</li> <li>• test vectors were stored in UNIX byte format, PC byte format should be used</li> <li>• more information on the used encoder input vectors should be added</li> </ul>
<b>Summary of change:</b>	⌘ <ul style="list-style-type: none"> <li>• correct input sequences used for VAD option 2</li> <li>• test vectors stored in PC byte format (this is the standard format for test sequences)</li> <li>• input sequences (*.inp) added to the zip-file</li> <li>• more information on input vectors added (to section 10)</li> </ul>
<b>Consequences if not approved:</b>	⌘ Test coverage of VAD option 2 will not be guaranteed. Manufacturers might be confused by unexpected byte format.

<b>Clauses affected:</b>	⌘ Section 10 + test vectors of alternative EFR version (zip-file)		
<b>Other specs affected:</b>	⌘ <input type="checkbox"/> Other core specifications <input type="checkbox"/> Test specifications <input type="checkbox"/> O&M Specifications	⌘	
<b>Other comments:</b>	⌘		

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- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

**Table 9: Location and size of compressed 8 bit PCM DTX test sequences**

Disk No.	File Name	No. of Frames	size		
			*.INP	*.COD	*.OUT
5-8/8	DTX01-X	710	113 600	349 320	113 600
5-8/8	DTX02-X	933	149 280	459 036	149 280
5-8/8	DTX03-X	156	24 960	76 752	24 960
5-8/8	DTX04-X	245	39 200	120 540	39 200
5-8/8	DTX05-X	56	8 960	27 552	8 960
5-8/8	DTX06-X	771	123 360	379 332	123 360
5-8/8	DTX07-X	1188	190 080	584 496	190 080

In addition to the test sequences above, special input (seqsyncX.inp) and output (syncxxxX.cod) sequences for frame synchronization are provided. The X again stands for A and  $\mu$  law compressed PCM. The synchronization procedure is described in clause 8.

**Table 10: Location, size and justification of compressed 8 bit PCM test sequences**

Disk No.	Purpose of Sequence	Name of Sequence	No. of Frames	Size in Bytes	Justification
5-8/8	Frame Synchronisation (input)	SEQSYNCX.INP	4	640	-
5-8/8	Frame Synchronisation (output)	SYNC000X.COD	1	492	Right
5-8/8		SYNC001X.COD	1	492	Right
5-8/8		SYNC002X.COD	1	492	Right
"		"	"	"	"
"		"	"	"	"
"		"	"	"	"
5-8/8		SYNC159X.COD	1	492	Right
5-8/8	Frame Synchronisation (output)	SYNC000X.COD	1	492	Right
5-8/8		SYNC001X.COD	1	492	Right
5-8/8		SYNC002X.COD	1	492	Right
"		"	"	"	"
"		"	"	"	"
"		"	"	"	"
5-8/8		SYNC159X.COD	1	492	Right

## 10 Alternative Enhanced Full Rate implementation using the Adaptive Multi Rate 12.2 kbit/s mode

The 12.2 kbit/s mode of the Adaptive Multi Rate speech coder described in TS 26.071 is functionally equivalent to the GSM Enhanced Full Rate speech coder. An alternative implementation of the Enhanced Full Rate speech service based on the 12.2 kbit/s mode of the Adaptive Multi Rate coder is allowed. Alternative implementations shall implement the functionality specified in TS 26.071 for the 12.2 kbit/s mode, with the exception and difference that the DTX transmission format from (GSM 06.81) and the comfort noise generation from (GSM 06.62) and the decoder homing frame from GSM 06.60 shall be used.

The test sequences are derived from the corresponding AMR test sequences. The modifications that were made and the use of the respective sequences are described below. The input sequences are identical to the AMR test input sequences \*.inp.

### Speech codec test sequences

- t\_efr: with DTX disabled

t00.inp ... t22.inp (encoder input, from TS 26.074)

t00\_efr.cod ... t22\_efr.cod (encoder output)

t00\_efr.dec ... t22\_efr.dec (decoder input)

t00\_efr.out ... t22\_efr.out (decoder output)

- ~~\_\_\_dtx\_efr:~~ with DTX enabled, VAD option 1

Dtx1.inp ... Dtx4.inp (encoder input, from TS 26.074)

Ddtx1\_efr.cod ... Ddtx4\_efr.cod (encoder output)

Ddtx1\_efr.dec ... Ddtx4\_efr.dec (decoder input)

Ddtx1\_efr.out ... Ddtx4\_efr.out (decoder output)

- ~~\_\_\_\_\_~~ with DTX enabled, VAD option 2

Dt21.inp .... Dt24.inp (encoder input, from TS 26.074)

Ddtx21\_efr2.cod ... Ddtx24\_efr2.cod (encoder output)

Ddtx21\_efr2.dec ... Ddtx24\_efr2.dec (decoder input)

Ddtx21\_efr2.out ... Ddtx24\_efr2.out (decoder output)

The format of the \*.cod files is identical to the GSM\_EFR \*.cod file format (244 Data Bits, VadFlag, SpFlag equaling 246 Words per 20ms frame). The format of the \*.dec files is identical to the GSM\_EFR \*.dec file format, that is (Bfi, 244 Data Bits, Sid, Taf equaling 247 Words per frame (20ms).

In summary, the differences to the AMR Mode MR122 test sequences are:

- DTX handling (VadFlag and SpFlag instead of TxType; different SID frames)
- Decoder homing frame (Decoder homing frame for GSM\_EFR is different than for AMR MR122).

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## CHANGE REQUEST

⌘ **06.54 CR A008** ⌘ rev **-** ⌘ Current version: **5.2.0** ⌘

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**Proposed change affects:** ⌘ (U)SIM  ME/UE  Radio Access Network  Core Network

<b>Title:</b>	⌘ Corrections to the test vectors of the alternative EFR version		
<b>Source:</b>	⌘ TSG-SA WG4		
<b>Work item code:</b>	⌘ Alternative EFR implementation	<b>Date:</b>	⌘ 11-Dec-2000
<b>Category:</b>	⌘ <b>A</b>	<b>Release:</b>	⌘ R96
	Use <u>one</u> of the following categories: <b>F</b> (essential correction) <b>A</b> (corresponds to a correction in an earlier release) <b>B</b> (Addition of feature), <b>C</b> (Functional modification of feature) <b>D</b> (Editorial modification) Detailed explanations of the above categories can be found in 3GPP TR 21.900.		Use <u>one</u> of the following releases: 2 (GSM Phase 2) R96 (Release 1996) R97 (Release 1997) R98 (Release 1998) R99 (Release 1999) REL-4 (Release 4) REL-5 (Release 5)

<b>Reason for change:</b>	⌘ <ul style="list-style-type: none"> <li>• test vectors of VAD option 1 were used to test VAD option 2</li> <li>• test vectors were stored in UNIX byte format, PC byte format should be used</li> <li>• more information on the used encoder input vectors should be added</li> </ul>
<b>Summary of change:</b>	⌘ <ul style="list-style-type: none"> <li>• correct input sequences used for VAD option 2</li> <li>• test vectors stored in PC byte format (this is the standard format for test sequences)</li> <li>• input sequences (*.inp) added to the zip-file</li> <li>• more information on input vectors added (to section 10)</li> </ul>
<b>Consequences if not approved:</b>	⌘ Test coverage of VAD option 2 will not be guaranteed. Manufacturers might be confused by unexpected byte format.

<b>Clauses affected:</b>	⌘ Section 10 + test vectors of alternative EFR version (zip-file)		
<b>Other specs affected:</b>	⌘ <input type="checkbox"/> Other core specifications <input type="checkbox"/> Test specifications <input type="checkbox"/> O&M Specifications	⌘	
<b>Other comments:</b>	⌘		

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**Table 9: Location and size of compressed 8 bit PCM DTX test sequences**

Disk No.	File Name	No. of Frames	size		
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5-8/8	DTX01-X	710	113 600	349 320	113 600
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5-8/8	DTX05-X	56	8 960	27 552	8 960
5-8/8	DTX06-X	771	123 360	379 332	123 360
5-8/8	DTX07-X	1188	190 080	584 496	190 080

In addition to the test sequences above, special input (seqsyncX.inp) and output (syncxxxX.cod) sequences for frame synchronization are provided. The X again stands for A and  $\mu$  law compressed PCM. The synchronization procedure is described in clause 8.

**Table 10: Location, size and justification of compressed 8 bit PCM test sequences**

Disk No.	Purpose of Sequence	Name of Sequence	No. of Frames	Size in Bytes	Justification
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"		"	"	"	"
"		"	"	"	"
"		"	"	"	"
5-8/8		SYNC159X.COD	1	492	Right
5-8/8	Frame Synchronisation (output)	SYNC000X.COD	1	492	Right
5-8/8		SYNC001X.COD	1	492	Right
5-8/8		SYNC002X.COD	1	492	Right
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"		"	"	"	"
"		"	"	"	"
5-8/8		SYNC159X.COD	1	492	Right

## 10 Alternative Enhanced Full Rate implementation using the Adaptive Multi Rate 12.2 kbit/s mode

The 12.2 kbit/s mode of the Adaptive Multi Rate speech coder described in TS 26.071 is functionally equivalent to the GSM Enhanced Full Rate speech coder. An alternative implementation of the Enhanced Full Rate speech service based on the 12.2 kbit/s mode of the Adaptive Multi Rate coder is allowed. Alternative implementations shall implement the functionality specified in TS 26.071 for the 12.2 kbit/s mode, with the exception and difference that the DTX transmission format from (GSM 06.81) and the comfort noise generation from (GSM 06.62) and the decoder homing frame from GSM 06.60 shall be used.

The test sequences are derived from the corresponding AMR test sequences. The modifications that were made and the use of the respective sequences are described below. The input sequences are identical to the AMR test input sequences \*.inp.

### Speech codec test sequences

- t\_efr: with DTX disabled

t00.inp ... t22.inp (encoder input, from TS 26.074)

t00\_efr.cod ... t22\_efr.cod (encoder output)

t00\_efr.dec ... t22\_efr.dec (decoder input)



t00\_efr.out ... t22\_efr.out (decoder output)

- ~~\_\_\_dtx\_efr:~~ with DTX enabled, VAD option 1

Dtx1.inp ... Dtx4.inp (encoder input, from TS 26.074)

Ddtx1\_efr.cod ... Ddtx4\_efr.cod (encoder output)

Ddtx1\_efr.dec ... Ddtx4\_efr.dec (decoder input)

Ddtx1\_efr.out ... Ddtx4\_efr.out (decoder output)

- \_\_\_\_\_ with DTX enabled, VAD option 2

Dt21.inp .... Dt24.inp (encoder input, from TS 26.074)

Ddtx21\_efr2.cod ... Ddtx24\_efr2.cod (encoder output)

Ddtx21\_efr2.dec ... Ddtx24\_efr2.dec (decoder input)

Ddtx21\_efr2.out ... Ddtx24\_efr2.out (decoder output)

The format of the \*.cod files is identical to the GSM\_EFR \*.cod file format (244 Data Bits, VadFlag, SpFlag equaling 246 Words per 20ms frame). The format of the \*.dec files is identical to the GSM\_EFR \*.dec file format, that is (Bfi, 244 Data Bits, Sid, Taf equaling 247 Words per frame (20ms).

In summary, the differences to the AMR Mode MR122 test sequences are:

- DTX handling (VadFlag and SpFlag instead of TxType; different SID frames)
- Decoder homing frame (Decoder homing frame for GSM\_EFR is different than for AMR MR122).

## CHANGE REQUEST

⌘ **06.54 CR A009** ⌘ rev **-** ⌘ Current version: **6.1.0** ⌘

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<b>Work item code:</b>	⌘ Alternative EFR implementation	<b>Date:</b>	⌘ 11-Dec-2000
<b>Category:</b>	⌘ <b>A</b>	<b>Release:</b>	⌘ R97
	Use <u>one</u> of the following categories: <b>F</b> (essential correction) <b>A</b> (corresponds to a correction in an earlier release) <b>B</b> (Addition of feature), <b>C</b> (Functional modification of feature) <b>D</b> (Editorial modification) Detailed explanations of the above categories can be found in 3GPP TR 21.900.		Use <u>one</u> of the following releases: 2 (GSM Phase 2) R96 (Release 1996) R97 (Release 1997) R98 (Release 1998) R99 (Release 1999) REL-4 (Release 4) REL-5 (Release 5)

<b>Reason for change:</b>	⌘ <ul style="list-style-type: none"> <li>• test vectors of VAD option 1 were used to test VAD option 2</li> <li>• test vectors were stored in UNIX byte format, PC byte format should be used</li> <li>• more information on the used encoder input vectors should be added</li> </ul>
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<b>Clauses affected:</b>	⌘ Section 10 + test vectors of alternative EFR version (zip-file)		
<b>Other specs affected:</b>	⌘ <input type="checkbox"/> Other core specifications <input type="checkbox"/> Test specifications <input type="checkbox"/> O&M Specifications	⌘	
<b>Other comments:</b>	⌘		

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"		"	"	"	"	"
"		"	"	"	"	"
"		"	"	"	"	"
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"		"	"	"	"	"
"		"	"	"	"	"
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## 10 Alternative Enhanced Full Rate implementation using the Adaptive Multi Rate 12.2 kbit/s mode

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### Speech codec test sequences

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t00\_efr.cod ... t22\_efr.cod (encoder output)

t00\_efr.dec ... t22\_efr.dec (decoder input)

t00\_efr.out ... t22\_efr.out (decoder output)

- ~~\_\_\_dtx\_efr:~~ with DTX enabled, VAD option 1

Dtx1.inp ... Dtx4.inp (encoder input, from TS 26.074)

Ddtx1\_efr.cod ... Ddtx4\_efr.cod (encoder output)

Ddtx1\_efr.dec ... Ddtx4\_efr.dec (decoder input)

Ddtx1\_efr.out ... Ddtx4\_efr.out (decoder output)

- \_\_\_\_\_ with DTX enabled, VAD option 2

Dt21.inp .... Dt24.inp (encoder input, from TS 26.074)

Ddtx21\_efr2.cod ... Ddtx24\_efr2.cod (encoder output)

Ddtx21\_efr2.dec ... Ddtx24\_efr2.dec (decoder input)

Ddtx21\_efr2.out ... Ddtx24\_efr2.out (decoder output)

The format of the \*.cod files is identical to the GSM\_EFR \*.cod file format (244 Data Bits, VadFlag, SpFlag equaling 246 Words per 20ms frame). The format of the \*.dec files is identical to the GSM\_EFR \*.dec file format, that is (Bfi, 244 Data Bits, Sid, Taf equaling 247 Words per frame (20ms).

In summary, the differences to the AMR Mode MR122 test sequences are:

- DTX handling (VadFlag and SpFlag instead of TxType; different SID frames)
- Decoder homing frame (Decoder homing frame for GSM\_EFR is different than for AMR MR122).

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## CHANGE REQUEST

⌘ **06.54 CR A010** ⌘ rev **-** ⌘ Current version: **7.1.0** ⌘

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<b>Title:</b>	⌘ Corrections to the test vectors of the alternative EFR version		
<b>Source:</b>	⌘ TSG-SA WG4		
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<b>Category:</b>	⌘ <b>A</b>	<b>Release:</b>	⌘ R98
	Use <u>one</u> of the following categories: <b>F</b> (essential correction) <b>A</b> (corresponds to a correction in an earlier release) <b>B</b> (Addition of feature), <b>C</b> (Functional modification of feature) <b>D</b> (Editorial modification) Detailed explanations of the above categories can be found in 3GPP TR 21.900.		Use <u>one</u> of the following releases: 2 (GSM Phase 2) R96 (Release 1996) R97 (Release 1997) R98 (Release 1998) R99 (Release 1999) REL-4 (Release 4) REL-5 (Release 5)

<b>Reason for change:</b>	⌘ <ul style="list-style-type: none"> <li>test vectors of VAD option 1 were used to test VAD option 2</li> <li>test vectors were stored in UNIX byte format, PC byte format should be used</li> <li>more information on the used encoder input vectors should be added</li> </ul>
<b>Summary of change:</b>	⌘ <ul style="list-style-type: none"> <li>correct input sequences used for VAD option 2</li> <li>test vectors stored in PC byte format (this is the standard format for test sequences)</li> <li>input sequences (*.inp) added to the zip-file</li> <li>more information on input vectors added (to section 10)</li> </ul>
<b>Consequences if not approved:</b>	⌘ Test coverage of VAD option 2 will not be guaranteed. Manufacturers might be confused by unexpected byte format.

<b>Clauses affected:</b>	⌘ Section 10 + test vectors of alternative EFR version (zip-file)		
<b>Other specs affected:</b>	⌘ <input type="checkbox"/> Other core specifications <input type="checkbox"/> Test specifications <input type="checkbox"/> O&M Specifications	⌘	
<b>Other comments:</b>	⌘		

### How to create CRs using this form:

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[http://www.3gpp.org/3G\\_Specs/CRs.htm](http://www.3gpp.org/3G_Specs/CRs.htm). Below is a brief summary:

- 1) Fill out the above form. The symbols above marked ⌘ contain pop-up help information about the field that they are closest to.
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- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

**Table 9: Location and size of compressed 8 bit PCM DTX test sequences**

Disk No.	File Name	No. of Frames	size		
			*.INP	*.COD	*.OUT
5-8/8	DTX01-X	710	113 600	349 320	113 600
5-8/8	DTX02-X	933	149 280	459 036	149 280
5-8/8	DTX03-X	156	24 960	76 752	24 960
5-8/8	DTX04-X	245	39 200	120 540	39 200
5-8/8	DTX05-X	56	8 960	27 552	8 960
5-8/8	DTX06-X	771	123 360	379 332	123 360
5-8/8	DTX07-X	1188	190 080	584 496	190 080

In addition to the test sequences above, special input (seqsyncX.inp) and output (syncxxxX.cod) sequences for frame synchronization are provided. The X again stands for A and  $\mu$  law compressed PCM. The synchronization procedure is described in clause 8.

**Table 10: Location, size and justification of compressed 8 bit PCM test sequences**

Disk No.	Purpose of Sequence	Name of Sequence	No. of Frames	Size in Bytes	Justification	
5-8/8	Frame Synchronisation (input)	SEQSYNCX.INP	4	640	-	
5-8/8	Frame Synchronisation (output)	SYNC000X.COD	1	492	Right	
5-8/8		SYNC001X.COD	1	492	Right	
5-8/8		SYNC002X.COD	1	492	Right	
"		"	"	"	"	"
"		"	"	"	"	"
"		"	"	"	"	"
5-8/8		SYNC159X.COD	1	492	Right	
5-8/8	Frame Synchronisation (output)	SYNC000X.COD	1	492	Right	
5-8/8		SYNC001X.COD	1	492	Right	
5-8/8		SYNC002X.COD	1	492	Right	
"		"	"	"	"	"
"		"	"	"	"	"
"		"	"	"	"	"
5-8/8		SYNC159X.COD	1	492	Right	

## 10 Alternative Enhanced Full Rate implementation using the Adaptive Multi Rate 12.2 kbit/s mode

The 12.2 kbit/s mode of the Adaptive Multi Rate speech coder described in TS 26.071 is functionally equivalent to the GSM Enhanced Full Rate speech coder. An alternative implementation of the Enhanced Full Rate speech service based on the 12.2 kbit/s mode of the Adaptive Multi Rate coder is allowed. Alternative implementations shall implement the functionality specified in TS 26.071 for the 12.2 kbit/s mode, with the exception and difference that the DTX transmission format from (GSM 06.81) and, the comfort noise generation from (GSM 06.62) and the decoder homing frame from GSM 06.60 shall be used.

The test sequences are derived from the corresponding AMR test sequences. The modifications that were made and the use of the respective sequences are described below. The input sequences are identical to the AMR test input sequences \*.inp.

### Speech codec test sequences

- t\_efr: with DTX disabled

t00.inp ... t22.inp (encoder input, from TS 26.074)

t00\_efr.cod ... t22\_efr.cod (encoder output)

t00\_efr.dec ... t22\_efr.dec (decoder input)



t00\_efr.out ... t22\_efr.out (decoder output)

- ~~\_\_\_dtx\_efr:~~ with DTX enabled, VAD option 1

Dtx1.inp ... Dtx4.inp (encoder input, from TS 26.074)

Ddtx1\_efr.cod ... Ddtx4\_efr.cod (encoder output)

Ddtx1\_efr.dec ... Ddtx4\_efr.dec (decoder input)

Ddtx1\_efr.out ... Ddtx4\_efr.out (decoder output)

- ~~\_\_\_\_\_~~ with DTX enabled, VAD option 2

Dt21.inp .... Dt24.inp (encoder input, from TS 26.074)

Ddtx21\_efr2.cod ... Ddtx24\_efr2.cod (encoder output)

Ddtx21\_efr2.dec ... Ddtx24\_efr2.dec (decoder input)

Ddtx21\_efr2.out ... Ddtx24\_efr2.out (decoder output)

The format of the \*.cod files is identical to the GSM\_EFR \*.cod file format (244 Data Bits, VadFlag, SpFlag equaling 246 Words per 20ms frame). The format of the \*.dec files is identical to the GSM\_EFR \*.dec file format, that is (Bfi, 244 Data Bits, Sid, Taf equaling 247 Words per frame (20ms).

In summary, the differences to the AMR Mode MR122 test sequences are:

- DTX handling (VadFlag and SpFlag instead of TxType; different SID frames)
- Decoder homing frame (Decoder homing frame for GSM\_EFR is different than for AMR MR122).

## CHANGE REQUEST

⌘ **06.54 CR A011** ⌘ rev **-** ⌘ Current version: **8.1.0** ⌘

For **HELP** on using this form, see bottom of this page or look at the pop-up text over the ⌘ symbols.

**Proposed change affects:** ⌘ (U)SIM  ME/UE  Radio Access Network  Core Network

<b>Title:</b>	⌘ Corrections to the test vectors of the alternative EFR version		
<b>Source:</b>	⌘ TSG-SA WG4		
<b>Work item code:</b>	⌘ Alternative EFR implementation	<b>Date:</b>	⌘ 11-Dec-2000
<b>Category:</b>	⌘ <b>A</b>	<b>Release:</b>	⌘ R99
	Use <u>one</u> of the following categories: <b>F</b> (essential correction) <b>A</b> (corresponds to a correction in an earlier release) <b>B</b> (Addition of feature), <b>C</b> (Functional modification of feature) <b>D</b> (Editorial modification) Detailed explanations of the above categories can be found in 3GPP TR 21.900.		Use <u>one</u> of the following releases: 2 (GSM Phase 2) R96 (Release 1996) R97 (Release 1997) R98 (Release 1998) R99 (Release 1999) REL-4 (Release 4) REL-5 (Release 5)

<b>Reason for change:</b>	⌘ <ul style="list-style-type: none"> <li>• test vectors of VAD option 1 were used to test VAD option 2</li> <li>• test vectors were stored in UNIX byte format, PC byte format should be used</li> <li>• more information on the used encoder input vectors should be added</li> </ul>
<b>Summary of change:</b>	⌘ <ul style="list-style-type: none"> <li>• correct input sequences used for VAD option 2</li> <li>• test vectors stored in PC byte format (this is the standard format for test sequences)</li> <li>• input sequences (*.inp) added to the zip-file</li> <li>• more information on input vectors added (to section 10)</li> </ul>
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"		"	"	"	"	"
"		"	"	"	"	"
"		"	"	"	"	"
5-8/8		SYNC159X.COD	1	492	Right	
5-8/8	Frame Synchronisation (output)	SYNC000X.COD	1	492	Right	
5-8/8		SYNC001X.COD	1	492	Right	
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- ~~\_\_\_dtx\_efr:~~ with DTX enabled, VAD option 1

Dtx1.inp ... Dtx4.inp (encoder input, from TS 26.074)

Ddtx1\_efr.cod ... Ddtx4\_efr.cod (encoder output)

Ddtx1\_efr.dec ... Ddtx4\_efr.dec (decoder input)

Ddtx1\_efr.out ... Ddtx4\_efr.out (decoder output)

- ~~\_\_\_\_\_~~ with DTX enabled, VAD option 2

Dt21.inp .... Dt24.inp (encoder input, from TS 26.074)

Ddtx21\_efr2.cod ... Ddtx24\_efr2.cod (encoder output)

Ddtx21\_efr2.dec ... Ddtx24\_efr2.dec (decoder input)

Ddtx21\_efr2.out ... Ddtx24\_efr2.out (decoder output)

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