**3GPP TSG-RAN4 Meeting #108-bis *R4-2315283***

Xiamen, China, Oct 9 – Oct 13, 2023

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| *CR-Form-v12.2* |
| **CHANGE REQUEST** |
|  |
|  | **38.133** | **CR** |  | **rev** |  | **Current version:** | **18.3.0** |  |
|  |
| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* |
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| ***Proposed change affects:*** | UICC apps |  | ME | **x** | Radio Access Network |  | Core Network |  |

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|  |
| ***Title:***  | Draft CR on Rel-18 Intra-frequency measurement impact due to MUSIM gap |
|  |  |
| ***Source to WG:*** | MediaTek inc. |
| ***Source to TSG:*** | R4 |
|  |  |
| ***Work item code:*** | NR\_DualTxRx\_MUSIM-Core |  | ***Date:*** | 2023-09-26 |
|  |  |  |  |  |
| ***Category:*** | **B** |  | ***Release:*** | Rel-18 |
|  | *Use one of the following categories:****F*** *(correction)****A*** *(mirror corresponding to a change in an earlier release)****B*** *(addition of feature),* ***C*** *(functional modification of feature)****D*** *(editorial modification)*Detailed explanations of the above categories canbe found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | *Use one of the following releases:Rel-8 (Release 8)Rel-9 (Release 9)Rel-10 (Release 10)Rel-11 (Release 11)…Rel-16 (Release 16)Rel-17 (Release 17)Rel-18 (Release 18)Rel-19 (Release 19)* |
|  |  |
| ***Reason for change:*** | Introduce RRM requirements due to MSUIM gap impact on L3 measurements (Intra-frequency measurement) in Rel-18. |
|  |  |
| ***Summary of change:*** | Updating (Intra-frequency measurement) requirements with/without gap to account the impact of MUSIM gaps, including the definition of the scaling factor Kp, Kgap and scheduling availability, and all the relevant context in sections 9.2.5 and 9.2.6. |
|  |  |
| ***Consequences if not approved:*** | RRM requirements impacted by MUSIM gap configuration will be missing for L3 measurements. |
|  |  |
| ***Clauses affected:*** | 9.2.5, 9.2.6 |
|  |  |
|  | **Y** | **N** |  |  |
| ***Other specs*** |  | **X** |  Other core specifications  | TS/TR ... CR ...  |
| ***affected:*** | **X** |  |  Test specifications | TS 38.533 |
| ***(show related CRs)*** |  | **X** |  O&M Specifications | TS/TR ... CR ...  |
|  |  |
| ***Other comments:*** |  |
|  |  |
| ***This CR's revision history:*** |  |

**----------------------START OF CHANGE 1----------------------------**

### 9.2.5 Intrafrequency measurements without measurement gaps

#### 9.2.5.1 Intrafrequency cell identification

The UE shall be able to identify a new detectable intra-frequency cell within Tidentify\_intra\_without\_index if the UE is not indicated to report SSB based RRM measurement result with the associated SSB index(*reportQuantityRsIndexes* or *maxNrofRSIndexesToReport* is not configured), or the UE is indicated that the neighbour cell is synchronous with the serving cell (*deriveSSB-IndexFromCell* is enabled). Otherwise UE shall be able to identify a new detectable intra frequency cell within Tidentify\_intra\_with\_index. The UE shall be able to identify a new detectable intra frequency SS block of an already detected cell within Tidentify\_intra\_without\_index. It is assumed that *deriveSSB-IndexFromCell* is always enabled for FR1 TDD and FR2 with SCS smaller or equal to 480 kHz.

Tidentify\_intra\_without\_index = (TPSS/SSS\_sync\_intra + T SSB\_measurement\_period\_intra) ms

Tidentify\_intra\_with\_index = (TPSS/SSS\_sync\_intra + T SSB\_measurement\_period\_intra + TSSB\_time\_index\_intra) ms

Where:

 TPSS/SSS\_sync\_intra: it is the time period used in PSS/SSS detection given in table 9.2.5.1-1, 9.2.5.1-2, 9.2.5.1-4 (deactivated SCell) or 9.2.5.1-5 (deactivated SCell) or 9.2.5.1-9 (deactivated SCell) or 9.2.5.1-11 or 9.2.5.1-12 (deactivated PSCell) or 9.2.5.1-13 (deactivated PSCell).

- For UE supporting power class 6 with *highSpeedMeasFlagFR2-r17* configured, if SMTC <= 40ms, TPSS/SSS\_sync\_intra is given in Table 9.2.5.1-11; otherwise, TPSS/SSS\_sync\_intra is given in Table 9.2.5.1-2.

 TSSB\_time\_index\_intra: it is the time period used to acquire the index of the SSB being measured given in table 9.2.5.1-3, 9.2.5.1-15 (FR2-2), 9.2.5.1-6 (deactivated SCell), 9.2.5.1-10(deactivated SCell) or 9.2.5.1-14 (deactivated PSCell).

 T SSB\_measurement\_period\_intra: equal to a measurement period of SSB based measurement given in table 9.2.5.2-1, table 9.2.5.2-2 table 9.2.5.2-3 (deactivated SCell), 9.2.5.2-4(deactivated SCell), 9.2.5.2-5 or 9.2.5.2-6(deactivated SCell), 9.2.5.2-8(deactivated PSCell) or 9.2.5.2-9(deactivated PSCell).

- For UE supporting power class 6 with *highSpeedMeasFlagFR2-r17* configured, if SMTC <= 40ms, TSSB\_measurement\_period\_intra is given in Table 9.2.5.2-7; otherwise, T SSB\_measurement\_period\_intra is given in Table 9.2.5.2-2.

 CSSFintra: it is a carrier specific scaling factor and is determined

 according to CSSFoutside\_gap,i in clause 9.1.5.1 for measurement conducted outside measurement gaps, i.e. when intra-frequency SMTC is fully non overlapping or partially overlapping with measurement gaps or NCSG, or according to CSSFwithin\_gap,i in clause 9.1.5.2 for measurement conducted within measurement gaps, i.e. when intra-frequency SMTC is fully overlapping with measurement gaps, or according to CSSFwithin\_ncsg,i in clause 9.1.5.3 for measurement conducted within NCSG, i.e. when intra-frequency SMTC is fully overlapping with NCSG.

 For a UE that supports Pre-MG, an SMTC occasion is only considered to be overlapped by Pre-MG if the Pre-MG is activated.

 if the high layer in TS 38.331 [2] signalling of *smtc2* is configured, the assumed periodicity of intra-frequency SMTC occasions corresponds to the value of higher layer parameter *smtc2*; Otherwise the assumed periodicity of intra-frequency SMTC occasions corresponds to the value of higher layer parameter *smtc1*.

 Mpss/sss\_sync\_w/o\_gaps: For a UE supporting FR2-1 power class 1 or 5, Mpss/sss\_sync\_w/o\_gaps =40. For a UE supporting power class 2, Mpss/sss\_sync\_w/o\_gaps =24. For a UE supporting FR2-1 power class 3, Mpss/sss\_sync\_w/o\_gaps =24. For a UE supporting FR2-1 power class 4, Mpss/sss\_sync\_w/o\_gaps =24. For a UE supporting FR2-2 power class 1, Mpss/sss\_sync\_w/o\_gaps = 60. For a UE supporting FR2-2 power class 2, Mpss/sss\_sync\_w/o\_gaps = 36. For a UE supporting FR2-2 power class 3, Mpss/sss\_sync\_w/o\_gaps = 36.

 Mmeas\_period\_w/o\_gaps: For a UE supporting FR2-1 power class 1 or 5, Mmeas\_period\_w/o\_gaps =40. For a UE supporting FR2-1 power class 2, Mmeas\_period\_w/o\_gaps =24. For a UE supporting FR2-1 power class 3, Mmeas\_period\_w/o\_gaps =24. For a UE supporting power class 4, Mmeas\_period\_w/o\_gaps =24. For a UE supporting FR2-2 power class 1, Mmeas\_period\_w/o\_gaps = 60. For a UE supporting FR2-2 power class 2, Mmeas\_period\_w/o\_gaps = 36. For a UE supporting FR2-2 power class 3, Mmeas\_period\_w/o\_gaps = 36.

MSSB\_index\_intra: For a UE supporting FR2-2 power class 1, MSSB\_index\_intra = 72 samples. For a UE supporting FR2-2 power class 2, MSSB\_index\_intra = 48 samples. For a UE supporting FR2 power class 3, MSSB\_index\_intra = 48 samples.

When UE supports *concurrentMeasGap-r17* and/or *MUSIM-GapConfig*, and is configured with concurrent measurement gaps and/or MUSIM gaps,

 Kp is the scaling factor for an SSB frequency layer to be measured without measurement gaps. Kp = Ntotal / Navailable, where Navailable and Ntotal are calculated as follows:

- For a window W of duration max(SMTC period, MGRP\_max), where MGRP max is the maximum MGRP across all configured per-UE measurement gap, periodic MUSIM gap(s), and/or per-FR measurement gap within the same FR as the SSB frequency layer, and starting from the beginning of any SMTC occasion:

- Ntotal is the total number of SMTC occasions within the window, including those overlapped with measurement gap and MUSIM gap occasions within the window, and

- Navailable is the number of SMTC occasions that are not overlapped with any non-dropped MG or MUSIM gap occasions within the window W, after accounting for measurement gap and MUSIM gap collisions by applying the measurement gap and MUSIM gap collision rule in section 9.1.8.3 and 9.1.10.x3, respectively.

 Kp = 1 when Navailable = 0 only for the case when SMTC occasions fully overlap with non-dropped MGs.

- Otherwise, when UE is not configured with or UE does not support concurrent measurement gaps and MUSIM gaps:

 When intra-frequency SMTC is fully non overlapping with measurement gaps or NCSG, or intra-frequency SMTC is fully overlapping with MGs or NCSG, Kp=1

 When intra-frequency SMTC is partially overlapping with measurement gaps, Kp = 1/(1- (SMTC period /MGRP)), where SMTC period < MGRP. When intra-frequency SMTC is partially overlapping with NCSG, Kp = 1/(1- (SMTC period /VIRP)), where SMTC period < VIRP. For calculation of Kp, if the high layer signalling (TS 38.331 [2]) of *smtc2* is configured, for cells indicated in the *pci-List* parameter in *smtc2*, the SMTC periodicity corresponds to the value of higher layer parameter *smtc2*; for the other cells, the SMTC periodicity corresponds to the value of higher layer parameter *smtc1.* If the higher layer signaling in TS38.331 [2] signalling of *smtc2* is present and smtc1 is fully overlapping with measurement gaps and smtc2 is partially overlapping with measurement gaps, requirements are not specified for Tidentify\_intra\_without\_index or Tidentify\_intra\_with\_index

 For FR2,

 Klayer1\_measurement=1,

- if all of the reference signals configured for RLM, BFD, CBD or L1-RSRP for beam reporting on any FR2 serving frequency in the same band outside measurement gap are not fully overlapped by intra-frequency SMTC occasions, or

- if all of the reference signal configured for RLM, BFD, CBD or L1-RSRP for beam reporting on any FR2 serving frequency in the same band outside measurement gap and fully-overlapped by intra-frequency SMTC occasions are not overlapped with any of the SSB symbols and the RSSI symbols, and 1 symbol before each consecutive SSB symbols and the RSSI symbols, and 1 symbol after each consecutive SSB symbols and the RSSI symbols, given that *SSB-ToMeasure* and *SS-RSSI-Measurement* are configured, where SSB symbols are indicated by the union set of SSB-ToMeasure from all the configured measurement objects on the same serving carrier which can be merged.and RSSI symbols are indicated by *SS-RSSI-Measurement*;

 Klayer1\_measurement=1.5, otherwise.

 If the above-mentioned reference signal configured for L1-RSRP measurement is aperiodic CSI-RS resource, longer cell identification delay would be expected.

 If MCG DRX is in use, cell identification requirements for intra-frequency measurement in MCG specified in Table 9.2.5.1-1, Table 9.2.5.1-2, Table 9.2.5.1-3, Table 9.2.5.1-4, Table 9.2.5.1-5 and Table 9.2.5.1-6 shall depend on the MCG DRX cycle. If SCG DRX is in use, cell identification requirements for intra-frequency measurement in SCG specified in Table 9.2.5.1-1, Table 9.2.5.1-2, Table 9.2.5.1-3, Table 9.2.5.1-4, Table 9.2.5.1-5, Table 9.2.5.1-6, Table 9.2.5.1-12, Table 9.2.5.1-13 and Table 9.2.5.1-14 shall depend on the SCG DRX cycle. Otherwise, the requirements for when DRX is not in use shall apply.

Table 9.2.5.1-1: Time period for PSS/SSS detection, (Frequency range FR1)

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | max( 600ms, ceil( 5 x Kp) x SMTC period )Note 1 x CSSFintra |
| DRX cycle≤ 320ms | max( 600ms, ceil(M2 Note 2x 5 x Kp) x max(SMTC period,DRX cycle)) x CSSFintra |
| DRX cycle>320ms | ceil(5 x Kp) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: When *highSpeedMeasFlag-r16* is not configured, M2 = 1.5; When *highSpeedMeasFlag-r16* is configured, M2 = 1.5 if SMTC periodicity > 40 ms;,otherwise M2=1.NOTE 3: When *highSpeedMeasFlag-r16* is configured, the requirements apply only to UE supporting either *measurementEnhancement-r16* or *intraNR-MeasurementEnhancement-r16* on measurements of the primary component carrier and do not apply to measurements of a secondary component carrier with active SCell.NOTE 4: When *highSpeedMeasCA-Scell-r17* is configured and UE supports *measurementEnhancementCA-r17*, M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2=1. |

Table 9.2.5.1-2: Time period for PSS/SSS detection, (Frequency range FR2)

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | max(600ms, ceil(Mpss/sss\_sync\_w/o\_gaps x KFR x Kp x Klayer1\_measurement)x SMTC period)Note 1 x CSSFintra |
| DRX cycle≤ 320ms | max(600ms, ceil(1.5 x Mpss/sss\_sync\_w/o\_gaps x KFR x Kp x Klayer1\_measurement)x max(SMTC period,DRX cycle)) x CSSFintra |
| DRX cycle>320ms | ceil(Mpss/sss\_sync\_w/o\_gaps x KFR x Kp x Klayer1\_measurement) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: KFR is a scaling factor depending on the frequency range and the SSB SCS. For FR2-1, KFR = 1. For FR2-2: KFR = 1 if the SCS of the SSB of the cell being detected is 120 kHz, KFR = 2 if the SCS of the SSB of the cell being detected is 480 kHz, and KFR = 3 if the SCS of the SSB of the cell being detected is 960 kHz. |

Table 9.2.5.1-3: Time period for time index detection (FR1)

|  |  |
| --- | --- |
| DRX cycle | TSSB\_time\_index\_intra |
| No DRX | max(120ms, ceil( 3 x Kp )x SMTC period)Note 1 x CSSFintra |
| DRX cycle≤ 320ms | max(120ms, ceil (M2 Note 2 x 3 x Kp) x max(SMTC period,DRX cycle)) x CSSFintra |
| DRX cycle>320ms | Ceil(3 x Kp) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: When *highSpeedMeasFlag-r16* is not configured, M2 = 1.5; When *highSpeedMeasFlag-r16* is configured, M2 = 1.5 if SMTC periodicity > 40 ms;,otherwise M2=1NOTE 3: When *highSpeedMeasFlag-r16* is configured, the requirements apply only to UE supporting either *measurementEnhancement-r16* or *intraNR-MeasurementEnhancement-r16* on measurements of the primary component carrier and do not apply to measurements of a secondary component carrier with active SCell.NOTE 4: When *highSpeedMeasCA-Scell-r17* is configured and UE supports *measurementEnhancementCA-r17*, M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2=1 |

Table 9.2.5.1-4: Time period for PSS/SSS detection, deactivated SCell (FR1)

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | Ceil(5 x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 320ms | Ceil(5 x Kp) x max(measCycleSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(5 x Kp) x max(measCycleSCell, DRX cycle) x CSSFintra |
| NOTE 1: The requirements also apply to deactivated SCG SCell. |

Table 9.2.5.1-5: Time period for PSS/SSS detection, deactivated SCell (FR2)

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | Ceil(Mpss/sss\_sync\_w/o\_gaps x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 320ms | Ceil(Mpss/sss\_sync\_w/o\_gaps x Kp) x max(measCycleSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(Mpss/sss\_sync\_w/o\_gaps x Kp) x max(measCycleSCell, DRX cycle) x CSSFintra |
| NOTE 1: The requirements also apply to deactivated SCG SCell. |

Table 9.2.5.1-6: Time period for time index detection, deactivated SCell (FR1)

|  |  |
| --- | --- |
| DRX cycle | TSSB\_time\_index\_intra |
| No DRX | Ceil(3 x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 320ms | Ceil(3 x Kp) x max(measCycleSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(3 x Kp) x max(measCycleSCell, DRX cycle) x CSSFintra |
| NOTE 1: The requirements also apply to deactivated SCG SCell. |

Table 9.2.5.1-7: Void

Table 9.2.5.1-8: Void

Table 9.2.5.1-9: Time period for PSS/SSS detection, deactivated SCell (FR1), when *highSpeedMeasCA-Scell-r17* is configured

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | Ceil(5 x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 320ms |  Ceil(5 x Kp) x max(measCycleSCell, M2 Note 1xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(5 x Kp) x max(measCycleSCell, DRX cycle) x CSSFintra |
| NOTE 1: M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2=1 |

Table 9.2.5.1-10: Time period for time index detection, deactivated SCell (FR1)，when *highSpeedMeasCA-Scell-r17* is configured

|  |  |
| --- | --- |
| DRX cycle | TSSB\_time\_index\_intra |
| No DRX | Ceil(3 x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 320ms |  Ceil(3 x Kp) x max(measCycleSCell, M2 Note 1xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(3 x Kp)x max(measCycleSCell, DRX cycle) x CSSFintra |
| NOTE 1: M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2=1 |

Table 9.2.5.1-11: Time period for PSS/SSS detection when *highSpeedMeasFlagFR2-r17* is configured, (Frequency range FR2) when SMTC period <= 40ms

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | max(600ms, ceil(M1Note 2 x Kp x Klayer1\_measurement)x SMTC period)Note 1 x CSSFintra |
| DRX cycle≤ 80ms | max(600ms, ceil(M1Note 2 x Kp x Klayer1\_measurement)x max(SMTC period,DRX cycle)) x CSSFintra |
| 80ms< DRX cycle≤ 320ms | ceil(1.5x Mpss/sss\_sync\_w/o\_gaps Note 3 x Kp x Klayer1\_measurement)x max(SMTC period,DRX cycle) x CSSFintra |
| DRX cycle>320ms | ceil(Mpss/sss\_sync\_w/o\_gaps Note 3 x Kp x Klayer1\_measurement) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: For UE supporting power class 6, M1= 6 if *highSpeedMeasFlagFR2-r17* = set1 or M1= 18 if *highSpeedMeasFlagFR2-r17* = set2NOTE 3: Void |

Table 9.2.5.1-12: Time period for PSS/SSS detection, deactivated PSCell (FR1)

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | Ceil(5 x Kp) x measCyclePSCell x CSSFintra |
| DRX cycle≤ 320ms | Ceil(5 x Kp) x max(measCyclePSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(5 x Kp) x max(measCyclePSCell, DRX cycle) x CSSFintra |

Table 9.2.5.1-13: Time period for PSS/SSS detection, deactivated PSCell (FR2)

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | Ceil(Mpss/sss\_sync\_w/o\_gaps x Kp) x measCyclePSCell x CSSFintra |
| DRX cycle≤ 320ms | Ceil(Mpss/sss\_sync\_w/o\_gaps x Kp) x max(measCyclePSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(Mpss/sss\_sync\_w/o\_gaps x Kp) x max(measCyclePSCell, DRX cycle) x CSSFintra |

Table 9.2.5.1-14: Time period for time index detection, deactivated PSCell (FR1)

|  |  |
| --- | --- |
| DRX cycle | TSSB\_time\_index\_intra |
| No DRX | Ceil(3 x Kp) x measCyclePSCell x CSSFintra |
| DRX cycle≤ 320ms | Ceil(3 x Kp) x max(measCyclePSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(3 x Kp) x max(measCyclePSCell, DRX cycle) x CSSFintra |

Table 9.2.5.1-15: Time period for time index detection (Frequency range FR2-2)

|  |  |
| --- | --- |
| DRX cycle | TSSB\_time\_index\_intra |
| No DRX | max(200ms, ceil(MSSB\_index\_intra x Kp x SMTC period) x CSSFintra |
| DRX cycle≤ 320ms | max(200ms, ceil(1.5 x MSSB\_index\_intra x Kp) x max(SMTC period, DRX cycle) x CSSFintra) |
| DRX cycle>320ms | Ceil(MSSB\_index\_intra x Kp )x DRX cycle x CSSFintra |

#### 9.2.5.2 Measurement period

The measurement period for intra-frequency measurements without gaps is as shown in table 9.2.5.2-1, 9.2.5.2-2, 9.2.5.2-3 (deactivated SCell), 9.2.5.2-4 (deactivated SCell), 9.2.5.2-8 (deactivated SCG applicable for PSCell) or 9.2.5.2-9 (deactivated SCG applicable for PSCell). When *highSpeedMeasFlag-r16* is configured, T SSB\_measurement\_period\_intra is specified in Table 9.2.5.2-5.

If the higher layer signaling in TS38.331 [2] signalling of *smtc2* is present and smtc1 is fully overlapping with measurement gaps and smtc2 is partially overlapping with measurement gaps, requirements are not specified for TSSB\_measurement\_period\_intra

For a UE that supports Pre-MG, an SMTC occasion is only considered to be overlapped by Pre-MG if the Pre-MG is activated.

If MCG DRX is in use, measurement period requirements for intra-frequency measurement in MCG specified in Table 9.2.5.2-1, Table 9.2.5.2-2, Table 9.2.5.2-3 and Table 9.2.5.2-4 shall depend on the MCG DRX cycle. If SCG DRX is in use, measurement period requirements for intra-frequency measurement in SCG specified in Table 9.2.5.2-1, Table 9.2.5.2-2, Table 9.2.5.2-3, Table 9.2.5.2-4, Table 9.2.5.2-8 and Table 9.2.5.2-9, shall depend on the SCG DRX cycle. Otherwise, the requirements for when DRX is not in use shall apply.

For FR2, a longer measurement period is allowed, if aperiodic CSI-RS resource is measured for L1-RSRP measurement on any FR2 serving frequency in the same band, and the CSI-RS resource is outside measurement gap and overlapped with any of the SSB symbols and the RSSI symbols, and 1 symbol before each consecutive SSB symbols and the RSSI symbols, and 1 symbol after each consecutive SSB symbols and the RSSI symbols. If *SSB-ToMeasure* or *SS-RSSI-Measurement* is configured, the SSB symbols are indicated by the union set of *SSB-ToMeasure* from all the configured measurement objects on the same band which can be merged and the RSSI symbols are indicated by *SS-RSSI-Measurement*.

Table 9.2.5.2-1: Measurement period for intra-frequency measurements without gaps (FR1)

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | max(200ms, ceil( 5 x Kp) x SMTC period)Note 1 x CSSFintra |
| DRX cycle≤ 320ms | max(200ms, ceil(1.5x 5 x Kp) x max(SMTC period,DRX cycle)) x CSSFintra |
| DRX cycle>320ms | ceil( 5 x Kp ) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identified |

Table 9.2.5.2-2: Measurement period for intra-frequency measurements without gaps (FR2)

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | max(400ms, ceil(Mmeas\_period\_w/o\_gaps x Kp x Klayer1\_measurement) x SMTC period)Note 1 x CSSFintra |
| DRX cycle≤ 320ms | max(400ms, ceil(1.5x Mmeas\_period\_w/o\_gaps x Kp x Klayer1\_measurement) x max(SMTC period,DRX cycle)) x CSSFintra  |
| DRX cycle>320ms | ceil(Mmeas\_period\_w/o\_gaps xKp x Klayer1\_measurement ) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identified |

Table 9.2.5.2-3: Measurement period for intra-frequency measurements without gaps (deactivated SCell) (FR1)

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | Ceil(5 x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 320ms | Ceil(5 x Kp) x max(measCycleSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(5 x Kp) x max(measCycleSCell, DRX cycle) x CSSFintra |
| NOTE 1: The requirements also apply to deactivated SCG SCel |

Table 9.2.5.2-4: Measurement period for intra-frequency measurements without gaps (deactivated SCell) (FR2)

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | Ceil(Mmeas\_period\_w/o\_gaps x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 320ms | Ceil(Mmeas\_period\_w/o\_gaps x Kp) x max(measCycleSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(Mmeas\_period\_w/o\_gaps x Kp) x max(measCycleSCell, DRX cycle) x CSSFintra |
| NOTE 1: The requirements also apply to deactivated SCG SCell. |

Table 9.2.5.2-5: T SSB\_measurement\_period\_intra When *highSpeedMeasFlag-r16* and/or highSpeedMeasCA-Scell-r17 is configured (Frequency range FR1

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX Note 2 | max(200ms, ceil( 5 x Kp) x SMTC period)Note 1 x CSSFintra |
| DRX cycle≤ 160ms | max(200ms, ceil(5 x M2 Note 2 x Kp) x max(SMTC period,DRX cycle)) x CSSFintra |
| 160ms < DRX cycle≤ 320ms | ceil(4 x M2 Note 2 x Kp) x DRX cycle x CSSFintra |
| DRX cycle>320ms | ceil( Y Note 3 x Kp ) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: M2 = 1.5 if SMTC period > 40 ms, otherwise M2=1NOTE 3: Y=3 when SMTC period <= 40ms, Y=5 when SMTC period > 40msNOTE 4: When *highSpeedMeasFlag-r16* is configured, the requirements apply only to UE supporting either *measurementEnhancement-r16* or *intraNR-MeasurementEnhancement-r16* on measurements of the primary component carrier and do not apply to measurements of a secondary component carrier with active SCell.NOTE 5: When highSpeedMeasCA-Scell-r17 is configured, the requirements apply to measurements of secondary component carrier with active SCell. |

Table 9.2.5.2-6: Measurement period for intra-frequency measurements without gaps (deactivated SCell) (FR1), when highSpeedMeasCA-Scell-r17 is configured

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | ceil( 5 x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 160ms | ceil(5 x Kp) x max(measCycleSCell, M2 Note 1 x DRX cycle) x CSSFintra |
| 160ms < DRX cycle≤ 320ms | ceil(4 x Kp) x max(measCycleSCell, M2 Note 1 x DRX cycle) |
| DRX cycle>320ms | ceil( Y Note 2 x Kp ) x max(measCycleSCell, DRX cycle) x CSSFintra |
| NOTE 1: M2 = 1.5 if SMTC periodicity > 40 ms, otherwise M2=1NOTE 2: Y=3 when SMTC <= 40ms, Y=5 when SMTC > 40ms |

Table 9.2.5.2-7: Measurement period for intra-frequency measurements without gaps when *highSpeedMeasFlagFR2-r17* is configured (FR2) when SMTC period <= 40ms

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | max(400ms, ceil(M1Note 2 x Kp x Klayer1\_measurement) x SMTC period)Note 1 x CSSFintra |
| DRX cycle≤ 80ms | max(400ms, ceil(M1Note 2 x Kp x Klayer1\_measurement) x max(SMTC period,DRX cycle)) x CSSFintra |
| 80ms< DRX cycle≤ 320ms | ceil(1.5x Mmeas\_period\_w/o\_gaps Note 3 x Kp x Klayer1\_measurement) x max(SMTC period,DRX cycle) x CSSFintra  |
| DRX cycle>320ms | ceil(Mmeas\_period\_w/o\_gaps Note 3 xKp x Klayer1\_measurement ) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: For UE supporting power class 6, M1= 6 if *highSpeedMeasFlagFR2-r17* = set1 or M1= 18 if *highSpeedMeasFlagFR2-r17* = set2 |

Table 9.2.5.2-8 Measurement period for intra-frequency measurements without gaps (deactivated SCG applicable for PSCell) (FR1)

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | Ceil(5 x Kp) x measCyclePSCell x CSSFintra |
| DRX cycle≤ 320ms | Ceil(5 x Kp) x max(measCyclePSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(5 x Kp) x max(measCyclePSCell, DRX cycle) x CSSFintra |

**Table 9.2.5.2-9: Measurement period for intra-frequency measurements without gaps (deactivated SCG applicable for PSCell) (FR2)**

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | Ceil(Mmeas\_period\_w/o\_gaps x Kp) x measCyclePSCell x CSSFintra |
| DRX cycle≤ 320ms | Ceil(Mmeas\_period\_w/o\_gaps x Kp) x max(measCyclePSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(Mmeas\_period\_w/o\_gaps x Kp) x max(measCyclePSCell, DRX cycle) x CSSFintra |

#### 9.2.5.3 Scheduling availability of UE during intra-frequency measurements

UE shall be capable of measuring without measurement gaps when the SSB is completely contained in the active bandwidth part of the UE. When any of the conditions in the following clauses is met, there are restrictions on the scheduling availability; otherwise, there is no scheduling restriction. Note that the SSB symbols indicated by the union set of SSB-ToMeasure from all the configured measurement objects on the same serving carrier which can be merged[2], if it is configured; otherwise, all *L* SSB symbols within the SMTC window duration defined in clause 4.1 of TS 38.213 [3] are included.

For a UE that supports Pre-MG, the requirements in 9.2.5.3 also apply when a Pre-MG is deactivated.

For UE supporting concurrent measurement gaps, when concurrent gaps are configured, the requirements in 9.2.5.3 are also applied to the slots that are not interrupted according to requirements in clause 9.1.X2.3.

For UE supporting MUSIM gaps, when MUSIM gaps are configured, the requirements in 9.2.5.3 are also applied to the slots that are not interrupted according to requirements in clause 9.1.10.x3.y2 and 9.1.10.x3.y3.

##### 9.2.5.3.1 Scheduling availability of UE performing measurements in TDD bands on FR1

When the UE performs intra-frequency measurements in a TDD band, the following restrictions apply due to SS-RSRP or SS-SINR measurement

- The UE is not expected to transmit PUCCH/PUSCH/SRS on SSB symbols to be measured, and on 1 data symbol before each consecutive SSB symbols to be measured and 1 data symbol after each consecutive SSB symbols to be measured within SMTC window duration. If the high layer in TS 38.331 [2] signalling of *smtc2*is configured, the SMTC periodicityfollows *smtc2*; Otherwise SMTC periodicity follows *smtc1.*

When the UE performs intra-frequency measurements in a TDD band, the following restrictions apply due to SS-RSRQ measurement

- The UE is not expected to transmit PUCCH/PUSCH/SRS on SSB symbols to be measured, RSSI measurement symbols, and on 1 data symbol before each consecutive SSB to be measured/RSSI symbols and 1 data symbol after each consecutive SSB to be measured/RSSI symbols within SMTC window duration. If the high layer signalling of *smtc2*is configured in TS 38.331 [2], the SMTC periodicityfollows *smtc2*; Otherwise the SMTC periodicity follows *smtc1.*

When TDD intra-band carrier aggregation is performed, the scheduling restrictions due to a given serving cell should also apply to all other serving cells in the same band on the symbols that fully or partially overlap with the aforementioned restricted symbols.

When TDD inter-band carrier aggregation is performed, the scheduling restrictions due to a given serving cell should also apply to another serving cell in a different band on the symbols that fully or partially overlap with the aforementioned restricted symbols, if UE does not have the capability of supporting *simultaneousRxTxInterBandCA* for this band pair.

##### 9.2.5.3.2 Scheduling availability of UE performing measurements with a different subcarrier spacing than PDSCH/PDCCH on FR1

For UE which do not support *simultaneousRxDataSSB-DiffNumerology* [14] the following restrictions apply due to SS-RSRP/RSRQ/SINR measurement

- If *deriveSSB\_IndexFromCell* is enabled the UE is not expected to transmit PUCCH/PUSCH/SRS or receive PDCCH/PDSCH/TRS/CSI-RS for CQI on SSB symbols to be measured, and on 1 data symbol before each consecutive SSB symbols to be measured and 1 data symbol after each consecutive SSB symbols to be measured within SMTC window duration. If the high layer signalling of *smtc2*is configured(in TS 38.331 [2]), the SMTC periodicityfollows *smtc2*; Otherwise the SMTC periodicity follows *smtc1.*

- If *deriveSSB\_IndexFromCell* is not enabled the UE is not expected to transmit PUCCH/PUSCH/SRS or receive PDCCH/PDSCH/TRS/CSI-RS for CQI on all symbols within SMTC window duration. If the high layer signalling of *smtc2*is configured in TS 38.331 [2], the SMTC periodicityfollows *smtc2*; Otherwise the SMTC periodicity follows *smtc1.*

If the following conditions are met:

- The UE has been notified about system information update through paging,

- The gap between the UE’s reception of PDCCH that UE monitors in the Type 2-PDCCH CSS set that notifies system information update, and the PDCCH that UE monitors in the Type0-PDCCH CSS set, is greater than 2 slots

The UE is expected to receive the PDCCH that the UE monitors in the Type0-PDCCH CSS set, and/or the corresponding PDSCH, on SSB symbols to be measured.

When intra-band carrier aggregation is performed, the scheduling restrictions due to a given serving cell should also apply to all other serving cells in the same band on the symbols that fully or partially overlap with the aforementioned restricted symbols.

##### 9.2.5.3.3 Scheduling availability of UE performing measurements on FR2

The following scheduling restriction applies due to SS-RSRP or SS-SINR measurement on an FR2 intra-frequency cell

- If *deriveSSB-IndexFromCell* is enabled the UE is not expected to transmit PUCCH/PUSCH/SRS or receive PDCCH/PDSCH/TRS/CSI-RS for CQI on SSB symbols to be measured, and on K data symbol(s) before each consecutive SSB symbols to be measured and K data symbol(s) after each consecutive SSB symbols to be measured within SMTC window duration.

- If *deriveSSB-IndexFromCell* is not enabled and the SCS of data and SSB symbols are smaller than 960kHz, the UE is not expected to transmit PUCCH/PUSCH/SRS or receive PDCCH/PDSCH/TRS/CSI-RS for CQI on all symbols within SMTC window duration.

- If *deriveSSB-IndexFromCell* is not enabled and the SCS of data or SSB symbols is 960kHz, the UE is not expected to transmit PUCCH/PUSCH/SRS or receive PDCCH/PDSCH/TRS/CSI-RS for CQI SSB symbols to be measured, and on K’ data symbol(s) before each consecutive SSB symbols to be measured and K’ data symbol(s) after each consecutive SSB symbols to be measured within SMTC window duration.

The following scheduling restriction applies to SS-RSRQ measurement on an FR2 intra-frequency cell

- If *deriveSSB-IndexFromCell* is enabled the UE is not expected to transmit PUCCH/PUSCH/SRS or receive PDCCH/PDSCH/TRS/CSI-RS for CQI on SSB symbols to be measured, RSSI measurement symbols, and on K data symbol(s) before each consecutive SSB to be measured/RSSI symbols and K data symbol(s) after each consecutive SSB to be measured/RSSI symbols within SMTC window duration

*-* If *deriveSSB-IndexFromCell* is not enabled and the SCS of data and SSB symbols are smaller than 960kHz, the UE is not expected to transmit PUCCH/PUSCH/SRS or receive PDCCH/PDSCH/TRS/CSI-RS for CQI on all symbols within SMTC window duration.

*-* If *deriveSSB-IndexFromCell* is not enabled and the SCS of SSB symbols is 960kHz, the UE is not expected to transmit PUCCH/PUSCH/SRS or receive PDCCH/PDSCH/TRS/CSI-RS for CQI on SSB symbols to be measured, RSSI measurement symbols, and on K’ data symbol(s) before each consecutive SSB to be measured/RSSI symbols and K’ data symbol(s) after each consecutive SSB to be measured/RSSI symbols within SMTC window duration.

where

- If the high layer signalling of *smtc2*is configured in TS 38.331 [2], the SMTC periodicityfollows *smtc2*; Otherwise the SMTC periodicity follows *smtc1.*

- The signaling *deriveSSB-IndexFromCell* is always enabled for FR2-1 and FR2-2 when SSB is using 120 kHz SCS and 480 kHz SCS.

- K=1 for a serving cell with data symbols of 120 kHz SCS

- K=4 for a serving cell with data symbols of 480 kHz SCS and SSB symbols of 120kHz or 480kHz SCS

- K=3 for a serving cell with data symbols of 480 kHz SCS and SSB symbols of 960kHz SCS

- K=7 for a serving cell with data symbols of 960 kHz SCS and SSB symbols of 120kHz or 480kHz SCS

- K=4 for a serving cell with data symbols of 960 kHz SCS and SSB symbols of 960kHz SCS

- K’=[2] for a serving cell with data symbols of 120 kHz SCS and SSB symbols of 960kHz SCS

- K’=[4] for a serving cell with data symbols of 480 kHz SCS and SSB symbols of 960kHz SCS

- K’=[7] for a serving cell with data symbols of 960 kHz SCS and SSB symbols of 960kHz SCS

When intra-band carrier aggregation in FR2 is performed, the scheduling restrictions due to a given serving cell should also apply to all other serving cells in the same band on the symbols that fully or partially overlap with aforementioned restricted symbols.

When inter-band carrier aggregation in FR2 is performed, there are no scheduling restrictions on FR2 serving cells in the bands due to SS-RSRP, SS-RSRQ or SS-SINR measurement on an FR2 intra-frequency cell in different bands, provided that UE is capable of independent beam management on this FR2 band pair. Additionally, there is no scheduling restriction if the UE is configured with different numerology between SSB on one FR2 band and data on the other FR2 band provided the UE is configured for IBM operation for the band pair.

Note: When inter-band carrier aggregation in FR2 is performed, the scheduling restrictions as defined in clause 9.2.5.3.1 due to a given serving cell should also apply to another serving cell in a different FR2 band on the symbols that fully or partially overlap with the aforementioned restricted symbols, if UE does not have the capability of supporting *simultaneousRxTxInterBandCA* for this FR2 band pair.

If following conditions are met:

- The UE has been notified about system information update through paging,

- The gap between the UE’s reception of PDCCH that UE monitors in the Type 2-PDCCH CSS set that notifies system information update, and the PDCCH that UE monitors in the Type0-PDCCH CSS set, is greater than 2 slots

For the SSB and CORESET for RMSI scheduling multiplexing patterns 3, the UE is expected to receive the PDCCH that the UE monitors in the Type0-PDCCH CSS set, and the corresponding PDSCH, on SSB symbols to be measured; and

For the SSB and CORESET for RMSI scheduling multiplexing patterns 2, the UE is expected to receive PDSCH that corresponds to the PDCCH that the UE monitors in the Type0-PDCCH CSS set, on SSB symbols to be measured.

##### 9.2.5.3.4 Scheduling availability of UE performing measurements on FR1 or FR2 in case of FR1-FR2 inter-band CA

There are no scheduling restrictions on FR1 serving cell(s) due to measurements performed on FR2 serving cell frequency layer. However, the scheduling restrictions as defined in clause 9.2.5.3.1 due to a given serving cell in FR2 should also apply to another serving cell in an FR1 band on the symbols that fully or partially overlap with the aforementioned restricted symbols, if UE does not have the capability of supporting *simultaneousRxTxInterBandCA* for this FR1-FR2 band pair.

There are no scheduling restrictions on FR2 serving cell(s) due to measurements performed on FR1 serving cell frequency layer. However, the scheduling restrictions as defined in clause 9.2.5.3.1 due to a given serving cell in FR1 should also apply to another serving cell in an FR2 band on the symbols that fully or partially overlap with the aforementioned restricted symbols, if UE does not have the capability of supporting *simultaneousRxTxInterBandCA* for this FR1-FR2 band pair.

#### 9.2.5.4 SFTD Measurements between PCell and PSCell

##### 9.2.5.4.1 Introduction

This clause contains SFTD measurement requirements for UE which supports NR-DC and is configured with a PSCell in RRC\_CONNECTED state. The UE shall perform SFTD measurement between PCell and PSCell, and report the SFTD result with/without SS-RSRP after the network requests with *reportType* for the associated *reportConfig* set to *reportSFTD*. The overall delay includes RRC procedure delay defined in clause 12 in TS 38.331 [2], and SFTD measurement reporting delay in clause 9.2.5.4.3..

##### 9.2.5.4.2 SFTD Measurement delay

When no DRX is used in either of PCell and PSCell, the physical layer measurement period of the SFTD measurement shall be Tmeasure\_SFTD1 = max(200, 5 x SMTC period) ms, where the SMTC period refers to the maximum between the configured SMTC period in PCell and PSCell.

When DRX is used in either of the PCell or the PSCell, or in both PCell and PSCell, the physical layer measurement period (Tmeasure\_SFTD1) of the SFTD measurement shall be as specified in Table 9.2.5.4.2-1.

Table 9.2.5.4.2-1: SFTD measurement requirement when DRX is used

|  |  |
| --- | --- |
| DRX cycle length (s) Note 3 | Tmeasure\_SFTD1 (s) |
| ≤0.04 | max(0.2, 5 x SMTC period) (Note2) |
| 0.04<DRX cycle≤0.32 | 8 x max(DRX cycle, SMTC period) |
| 0.32<DRX cycle≤10.24 | 5 x DRX cycle |
| Note 1: SMTC period in this table refers to the maximum between the configured SMTC period in PCell and PSCell. Note 2: Number of DRX cycles depends upon the DRX cycle in useNote 3: DRX cycle length in this table refers to the DRX cycle length configured for PCell or PSCell. When DRX is used in both PCell and PSCell, DRX cycle length in this table refers to the longer of the DRX cycle lengths for PCell and PSCell. |

If PSCell is changed without changing carrier frequency of PSCell, while the UE is performing SFTD measurements, the UE shall still meet SFTD measurement and accuracy requirements for the new PSCell. In this case the UE shall restart the SFTD measurement, and the total physical layer measurement period shall not exceed Tmeasure\_SFTD2 as defined by the following expression:

Tmeasure\_SFTD2 = (M+1)\*(Tmeasure\_SFTD1) + M\*TPSCell\_change\_NRDC

where:

 M is the number of times the NR PSCell is changed over the measurement period (Tmeasure\_SFTD2), and

 TPSCell\_change\_NRDC is the time necessary to change the PSCell; it can be up to 25ms.

If PCell is changed, or if PSCell is changed with different carrier frequency from PSCell, the UE shall terminate SFTD measurements.

The measurement accuracy for the SFTD measurement when DRX is used as well as when no DRX is used shall be as specified in the clause 10.1.21.

##### 9.2.5.4.3 SFTD Measurement Reporting Delay

The SFTD measurement reporting delay is defined as the time between a command that will trigger an SFTD measurement report and the point when the UE starts to transmit the measurement report over the air interface. This requirement assumes that the measurement report is not delayed by other RRC signalling on the DCCH. This measurement reporting delay excludes a delay uncertainty resulted when inserting the measurement report to the TTI of the uplink DCCH. The delay uncertainty is: 2 x TTIDCCH. This measurement reporting delay excludes any delay caused by no UL resources available for UE to send the measurement report.

The SFTD measurement reporting delay shall be less than measurement period defined in clause 9.2.5.4.2 plus the RRC procedure delay defined in TS 38.331 [2].

### 9.2.6 Intra-frequency measurements with measurement gaps

#### 9.2.6.1 Void

#### 9.2.6.2 Intra-frequency cell identification

When a measurement gap is provided or an activated Pre-MG is provided without any pre-MG status changed during the measurement period, the UE shall be able to identify a new detectable intra frequency cell within Tidentify\_intra\_without\_index if UE is not indicated to report SSB based RRM measurement result with the associated SSB index (*reportQuantityRsIndexes* or *maxNrofRSIndexesToReport* is not configured), or the UE has been indicated that the neighbour cell is synchronous with the serving cell (*deriveSSB-IndexFromCell* is enabled). Otherwise UE shall be able to identify a new detectable intra frequency cell within Tidentify\_intra\_with\_index. The UE shall be able to identify a new detectable intra frequency SS block of an already detected cell within Tidentify\_intra\_without\_index. It is assumed that *deriveSSB-IndexFromCell* is always enabled for FR1 TDD and FR2 with SCS smaller or equal to 480 kHz.

 Tidentify\_intra\_without\_index = TPSS/SSS\_sync\_intra + T SSB\_measurement\_period\_intra ms

 Tidentify\_intra\_with\_index = TPSS/SSS\_sync\_ntra + T SSB\_measurement\_period\_intra + TSSB\_time\_index\_intra ms

Where:

 TPSS/SSS\_sync\_intra: it is the time period used in PSS/SSS detection given in table 9.2.6.2-1, 9.2.6.2-2 or 9.2.6.2-9.

- For UE supporting power class 6 with *highSpeedMeasFlagFR2-r17* configured, if SMTC <= 40ms, TPSS/SSS\_sync\_intra is given in Table 9.2.6.2-9; otherwise, TPSS/SSS\_sync\_intra is given in Table 9.2.6.2-2.

 TSSB\_time\_index\_intra: it is the time period used to acquire the index of the SSB being measured given in table 9.2.6.2-3 or 9.2.6.2-10 (for FR2-2).

 T SSB\_measurement\_period\_intra: equal to a measurement period of SSB based measurement given in table 9.2.6.3-1 or 9.2.6.3-2.

- For UE supporting power class 6 with *highSpeedMeasFlagFR2-r17* configured, if SMTC <= 40ms, TSSB\_measurement\_period\_intra is given in Table 9.2.6.3-4; otherwise, T SSB\_measurement\_period\_intra is given in Table 9.2.6.3-2.

 CSSFintra: it is a carrier specific scaling factor and is determined according to CSSFwithin\_gap,i in clause 9.1.5.2 for measurement conducted within measurement gaps.

 Kgap is the scaling factor for a SSB frequency layer to be measured within an associated measurement gap pattern. Kgap = 1 when the UE is not configured with or the UE does not support concurrent measurement gaps or MUSIM gaps. Otherwise, Kgap = Ntotal / Navailable, where Navailable and Ntotal are calculated as follows:

 For a window W of duration max(SMTC period, MGRP\_max), where MGRP max is the maximum MGRP across all configured per-UE measurement gap and periodic MUSIM gaps, and per-FR measurement gap within the same FR as the SSB frequency layer, and starting from the beginning of any SMTC occasion:

-- Ntotal is the total number of SMTC occasions that are covered by instances of the associated measurement gap within the window W, including those overlapped with other measurement gap and MUSIM gap occasions within the window, and

 Navailable is the number of SMTC occasions that are covered by instances of the non-dropped associated measurement gap within the window W after accounting for measurement gap and MUSIM gap collisions by applying the measurement gap and MUSIM gap collision rule in section 9.1.8.3 and 9.1.10.x3, respectively.

 When concurrent measurement gaps and MUSIM gaps are configured, requirements in this clause do not apply if Navailable =0.

 Mpss/sss\_sync\_with\_gaps : For a UE supporting FR2-1 power class 1 or 5, Mpss/sss\_sync with\_gaps=40. For a UE supporting FR2-1 power class 2, Mpss/sss\_sync with\_gaps =24. For a UE supporting FR2-1 power class 3, Mpss/sss\_sync with\_gaps =24. For a UE supporting FR2-1 power class 4, Mpss/sss\_sync with\_gaps =24. For a UE supporting FR2-2 power class 1, Mpss/sss\_sync with\_gaps = 60. For a UE supporting FR2-2 power class 2, Mpss/sss\_sync with\_gaps = 36. For a UE supporting FR2-2 power class 3, Mpss/sss\_sync with\_gaps = 36.

 Mmeas\_period\_ with\_gaps: For a UE supporting FR2-1 power class 1 or 5, Mmeas\_period\_ with\_gaps =40. For a UE supporting FR2-1 power class 2, Mmeas\_period\_ with\_gaps =24. For a UE supporting FR2-1 power class 3, Mmeas\_period\_ with\_gaps =24. For a UE supporting FR2-1 power class 4, Mmeas\_period with\_gaps =24. For a UE supporting FR2-2 power class 1, Mmeas\_period\_ with\_gaps = 60. For a UE supporting FR2-2 power class 2, Mmeas\_period\_ with\_gaps = 36. For a UE supporting FR2-2 power class 3, Mmeas\_period\_ with\_gaps = 36.

- MSSB\_index\_intra: For a UE supporting FR2-2 power class 1, MSSB\_index\_intra = 72. For a UE supporting FR2-2 power class 2, MSSB\_index\_intra = 48. For a UE supporting FR2 power class 3, MSSB\_index\_intra = 48.

If the higher layer signaling in TS 38.331 [2] of *smtc2* is present and smtc1 is fully overlapping with measurement gaps and smtc2 is partially overlapping with measurement gaps, requirements are not specified for Tidentify\_intra\_without\_index or Tidentify\_intra\_with\_index.

If MCG DRX is in use, cell identification requirements for intra-frequency measurement in MCG specified in Table 9.2.6.2-1, Table 9.2.6.2-2, and Table 9.2.6.2-3 shall depend on the MCG DRX cycle. If SCG DRX is in use, cell identification requirements for intra-frequency measurement in SCG specified in Table 9.2.6.2-1, Table 9.2.6.2-2, and Table 9.2.6.2-3 shall depend on the SCG DRX cycle. Otherwise, the requirements for when DRX is not in use shall apply.

Table 9.2.6.2-1: Time period for PSS/SSS detection (FR1)

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | max(600ms, 5 x Kgap x max(MGRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 320ms | max(600ms, ceil(M2Note 1x 5 x Kgap) x max(MGRP, SMTC period,DRX cycle)) x CSSFintra |
| DRX cycle>320ms | Ceil( 5 x Kgap ) x max(MGRP, DRX cycle) x CSSFintra |
| NOTE 1: When *highSpeedMeasFlag-r16* is not configured, M2 = 1.5; When *highSpeedMeasFlag-r16* is configured, M2 = 1.5 if SMTC periodicity > 40 ms, otherwise M2=1.NOTE 2: When *highSpeedMeasFlag-r16* is configured, the requirements apply only to UE supporting either *measurementEnhancement-r16* or *intraNR-MeasurementEnhancement-r16* on measurements of the primary component carrier and do not apply to measurements of a secondary component carrier with active SCell.NOTE 3: For a UE supporting concurrent measurement gaps, if multiple concurrent gaps are configured, the MGRP is the periodicity of the MG pattern associated to the intra-frequency layer.NOTE 4: When highSpeedMeasCA-Scell-r17 is configured, the requirements apply to UE on measurements of secondary component carrier with active SCell. |

**Table 9.2.6.2-2: Time period for PSS/SSS detection (FR2)**

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | max(600ms, Mpss/sss\_sync\_with\_gaps x KFR x Kgap x max(MGRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 320ms | max(600ms, ceil(1.5x Mpss/sss\_sync\_with\_gaps x KFR x Kgap) x max(MGRP, SMTC period, DRX cycle))x CSSFintra |
| DRX cycle>320ms | Ceil( Mpss/sss\_sync\_with\_gaps x KFR x Kgap ) x max(MGRP, DRX cycle) x CSSFintra |
| NOTE 1: For a UE supporting concurrent gaps, if multiple concurrent gaps are configured, the MGRP is the periodicity of the MG pattern associated to the intra-frequency layer.NOTE 2: KFR is a scaling factor depending on the frequency range and the SSB SCS. For FR2-1, KFR = 1. For FR2-2: KFR = 1 if the SCS of the SSB of the cell being detected is 120 kHz, KFR = 2 if the SCS of the SSB of the cell being detected is 480 kHz, and KFR = 3 if the SCS of the SSB of the cell being detected is 960 kHz. |

Table 9.2.6.2-3: Time period for time index detection (Frequency range FR1)

|  |  |
| --- | --- |
| DRX cycle | TSSB\_time\_index\_intra |
| No DRX | max(120ms, ceil(3 x Kgap ) x max(MGRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 320ms | max(120ms, ceil(M2Note 1x 3 x Kgap) x max(MGRP, SMTC period,DRX cycle) x CSSFintra) |
| DRX cycle>320ms | Ceil(3 x Kgap )x max(MGRP, DRX cycle) x CSSFintra |
| NOTE 1: When *highSpeedMeasFlag-r16* is not configured, M2 = 1.5; When *highSpeedMeasFlag-r16* is configured, M2 = 1.5 if SMTC periodicity > 40 ms, otherwise M2=1.NOTE 2: When *highSpeedMeasFlag-r16* is configured, the requirements apply only to UE supporting either *measurementEnhancement-r16* or *intraNR-MeasurementEnhancement-r16* on measurements of the primary component carrier and do not apply to measurements of a secondary component carrier with active SCell.NOTE 3: For a UE supporting concurrent gaps, if multiple concurrent gaps are configured, the MGRP is the periodicity of the MG pattern associated to the intra-frequency layer.NOTE 4: When highSpeedMeasCA-Scell-r17 is configured, the requirements apply to UE on measurements of secondary component carrier with active SCell. |

Table 9.2.6.2-7: Void

Table 9.2.6.2-8: Void

Table 9.2.6.2-8: Void

Table 9.2.6.2-9: Time period for PSS/SSS detection when *highSpeedMeasFlagFR2-r17* is configured, (FR2) when SMTC period <=40ms

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | max(600ms, M1Note 2 x Kgap x max(MGRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 80ms | max(600ms, ceil(M1Note2 x Kgap) x max(MGRP, SMTC period, DRX cycle))x CSSFintra |
| 80ms< DRX cycle≤ 320ms | max(600ms, ceil(Mpss/sss\_sync\_with\_gaps x Kgap) x max(MGRP, SMTC period, DRX cycle))x CSSFintra |
| DRX cycle>320ms | Ceil( Mpss/sss\_sync\_with\_gaps x Kgap ) x max(MGRP, DRX cycle) x CSSFintra |
| NOTE 1: For a UE supporting concurrent gaps, if multiple concurrent gaps are configured, the MGRP is the periodicity of the MG pattern associated to the intra-frequency layer.NOTE 2: For UE supporting power class 6, M1= 6 if *highSpeedMeasFlagFR2-r17* = set1 or M1= 18 if *highSpeedMeasFlagFR2-r17* = set2NOTE 3: Void |

Table 9.2.6.2-10: Time period for time index detection (Frequency range FR2-2)

|  |  |
| --- | --- |
| DRX cycle | TSSB\_time\_index\_intra |
| No DRX | max(200ms, ceil(MSSB\_index\_intra x Kgap x max(MGRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 320ms | max(200ms, ceil(1.5 x MSSB\_index\_intra x Kgap) x max(MGRP, SMTC period, DRX cycle) x CSSFintra) |
| DRX cycle>320ms | Ceil(MSSB\_index\_intra x Kgap)x DRX cycle x CSSFintra |

#### 9.2.6.3 Intrafrequency Measurement Period

The requirements in this clause apply when a measurement gap is provided or when an activated Pre-MG is provided without any pre-MG status changed during the measurement period.

The measurement period for FR1 intrafrequency measurements with gaps is as shown in table 9.2.6.3-1.

The measurement period for FR2 intrafrequency measurements with gaps is as shown in table 9.2.6.3-2.

When *highSpeedMeasFlag-r16* is configured, T SSB\_measurement\_period\_intra is specified in Table 9.2.6.3-3.

If MCG DRX is in use, measurement period requirements for intra-frequency measurement in MCG specified in Table 9.2.6.3-1 and Table 9.2.6.3-2, shall depend on the MCG DRX cycle. If SCG DRX is in use, measurement period requirements for intra-frequency measurement in SCG specified in Table 9.2.6.3-1and Table 9.2.6.3-2, shall depend on the SCG DRX cycle. Otherwise, the requirements for when DRX is not in use shall apply.

For either an FR1 or FR2 serving cell, longer measurement period would be expected during the period Tidentify\_CGI when the UE is requested to decode an NR CGI.

Table 9.2.6.3-1: Measurement period for intra-frequency measurements with gaps(FR1)

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | max(200ms, ceil(5 x Kgap )x max(MGRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 320ms | max(200ms, ceil(1.5x 5 x Kgap) x max(MGRP, SMTC period,DRX cycle))x CSSFintra |
| DRX cycle>320ms | Ceil(5 x Kgap ) x max(MGRP, DRX cycle) x CSSFintra |
| NOTE 1: For a UE supporting concurrent gaps, if multiple concurrent gaps are configured, the MGRP is the periodicity of the MG pattern associated to the intra-frequency layer. |

Table 9.2.6.3-2: Measurement period for intra-frequency measurements with gaps(FR2)

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | max(400ms, ceil(Mmeas\_period with\_gaps x Kgap ) x max(MGRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 320ms | max(400ms, ceil(1.5 x Mmeas\_period with\_gaps x Kgap) x max(MGRP, SMTC period, DRX cycle)) Note 1 x CSSFintra |
| DRX cycle>320ms | Ceil( Mmeas\_period with\_gaps x Kgap ) x max(MGRP, DRX cycle) x CSSFintra |
| NOTE 1: For a UE supporting concurrent gaps, if multiple concurrent gaps are configured, the MGRP is the periodicity of the MG pattern associated to the intra-frequency layer. |

Table 9.2.6.3-3: Measurement period When *highSpeedMeasFlag-r16* is configured (Frequency Range FR1)

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | max(200ms, ceil( 5 x Kgap ) x max(MGRP, SMTC period)) Note 1 x CSSFintra |
| DRX cycle≤ 160ms | max(200ms, ceil(M2Note 2 x 5 x Kgap) x max(MGRP, SMTC period,DRX cycle)) x CSSFintra |
| 160ms < DRX cycle≤ 320ms | max(200ms, ceil(M2Note 2 x 4 x Kgap) x max(MGRP, DRX cycle)) x CSSFintra |
| DRX cycle>320ms | Ceil(Y Note 3 x Kgap ) x max(MGRP, DRX cycle) x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: M2 = 1.5 if SMTC periodicity > 40 ms, otherwise M2=1NOTE 3: Y=3 when SMTC <= 40ms, Y=5 when SMTC > 40msNOTE 4: When *highSpeedMeasFlag-r16* is configured, the requirements apply only to UE supporting either *measurementEnhancement-r16* or *intraNR-MeasurementEnhancement-r16* on measurements of the primary component carrier and do not apply to measurements of a secondary component carrier with active SCell.NOTE 5: For a UE supporting concurrent gaps, if multiple concurrent gaps are configured, the MGRP is the periodicity of the MG pattern associated to the intra-frequency layer.NOTE 6: When highSpeedMeasCA-Scell-r17 is configured, the requirements also apply to UE on measurements of secondary component carrier with active SCell. |

Table 9.2.6.3-4: Measurement period for intra-frequency measurements with gaps when *highSpeedMeasFlagFR2-r17* is configured (FR2) when SMTC period<=40ms

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | max(400ms, ceil(M1Note 2 x Kgap ) x max(MGRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 80ms | max(400ms, ceil(M1Note 2 x Kgap) x max(MGRP, SMTC period, DRX cycle)) Note 1 x CSSFintra |
| 80ms< DRX cycle≤ 320ms | max(400ms, ceil(Mmeas\_period with\_gaps x Kgap) x max(MGRP, SMTC period, DRX cycle)) Note 1 x CSSFintra |
| DRX cycle>320ms | Ceil( Mmeas\_period with\_gaps x Kgap ) x max(MGRP, DRX cycle) x CSSFintra |
| NOTE 1: For a UE supporting concurrent gaps, if multiple concurrent gaps are configured, the MGRP is the periodicity of the MG pattern associated to the intra-frequency layer.NOTE 2: For UE supporting power class 6, M1= 6 if *highSpeedMeasFlagFR2-r17* = set1 or M1= 18 if *highSpeedMeasFlagFR2-r17* = set2NOTE 3: Void |

**----------------------END OF CHANGES 1----------------------------**