3GPP TSG-RAN WG2 Meeting #112e R2-20xxxx

Online Meeting, 02 – 13 November 2020

**Agenda item: 6.6.2**

**Source: Ericsson (Rapporteur)**

**Title: Offline 604 Positioning RRC proposals**

**WID/SID: NR\_POS-Core - Release 16**

**Document for: Discussion and Decision**

# 1 Introduction

This document is the report of the following email discussion.

* [AT112-e][604][POS] Positioning RRC proposals (Ericsson)

 Scope: Discuss and resolve proposals 1 and 2 from R2-2010709.

 Intended outcome: Agreeable CR in R2-2010864

 Deadline: Tuesday 2020-11-10 1200 UTC

The reference document and the proposals to be discussed are listed below.

R2-2010709 Summary for RRC Corrections for Positioning Ericsson discussion

[Proposal 1 RAN2 to agree the posSIB validity inclusion in RRC and review the CR for posSIB validity check provided in R2-2008806 by email discussion.](#_Toc54681748)

[Proposal 2 RAN2 to provide correction for field description for fields (sfn-Offset and sfn-SSB-Offset) available in SSB-Configuration. The exact changes are captured via email discussion review.](#_Toc54681749)

# 2 PosSIB Validity Check

The CR [R2-2008806](https://www.3gpp.org/ftp/tsg_ran/WG2_RL2/TSGR2_112-e/Docs/R2-2008806.zip) provides the changes needed to describe how UE will determine the posSIB validity in RRC. The valueTag and expiration duration are defined in LPP layer however the areaScope and SystemInformationAreaID are part of RRC.

**Question 1**: do you agree with the changes in the CR in [R2-2008806](https://www.3gpp.org/ftp/tsg_ran/WG2_RL2/TSGR2_112-e/Docs/R2-2008806.zip) where the posSIB validity has been provided in RRC?

Please use the comments column to provide any suggested changes to the CR or to add explanations/alternatives if you disagree with the CR or any parts of it.

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| Answers to Question 1 |
| Company | Yes/No | Comments |
| Intel | Yes |  |
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# 3 sfn-SSB-Offset, sfn-Offset, sfn0-offset

R2-2008807, R2-2008808, R2-2010071, R2-2010270 provides CR for correction of field description for SSB-Configuration related to fields: sfn-Offset and sfn-SSB-Offset and sequence sfn0-offset.

The CR [R2-2010991](https://www.3gpp.org/ftp/tsg_ran/WG2_RL2/TSGR2_112-e/Docs/R2-2010991.zip) in consolidates the changes and provides a merged version.

**Question 2**: do you agree with the changes in the CR in [R2-2010991](https://www.3gpp.org/ftp/tsg_ran/WG2_RL2/TSGR2_112-e/Docs/R2-2010991.zip)?

Please use the comments column to provide any suggested changes to the CR or to add explanations if you disagree with the CR or any parts of it.

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| Answers to Question 3 |
| Company | Yes/No | Comments |
| Intel | Yes |  |
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# 4 Conclusion

Based on the discussion in the previous sections we propose the following:

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