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Agenda Item: AH21
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1. Summary

This paper gives some general description about how to monitor GSM from 1.28Mcps TDD.

2. Introduction and comparison with 3.84Mcps TDD

Due to the different operating bandwidth and the different frame structure, some measurement description about how to monitor GSM are different between the 3.84Mcps TDD and the 1.28Mcps TDD.

3. Proposal

We propose to add following paragraphs in the Working CR for TS25.225 as the description of how to monitor GSM from 1.28Mcps TDD.

---------- Changes to working CR of 25.225 begin ----------

A.2 Low data rate traffic using 1 uplink and 1 downlink slot (for 1.28Mcps TDD)

NOTE: The section evaluates the time to acquire the FCCH if all idle slots are devoted to the tracking of a FCCH burst, meaning that no power measurements is done concurrently. The derived figures are better than those for GSM. The section does not derive though any conclusion. A conclusion may be that the use of the idle slots is a valid option. An alternative conclusion may be that this is the only mode to be used, removing hence the use of the slotted frames for low data traffic or the need for a dual receiver, if we were to considering the monitoring of GSM cells only, rather than GSM, TDD and FDD.

If a single synthesiser UE uses only one uplink and one downlink slot, e.g. for speech communication, the UE is not in transmit or receive state during 5 slots in each frame. According to the timeslot numbers allocated to the traffic, this period can be split into two continuous idle intervals A and B as shown in the figure below.

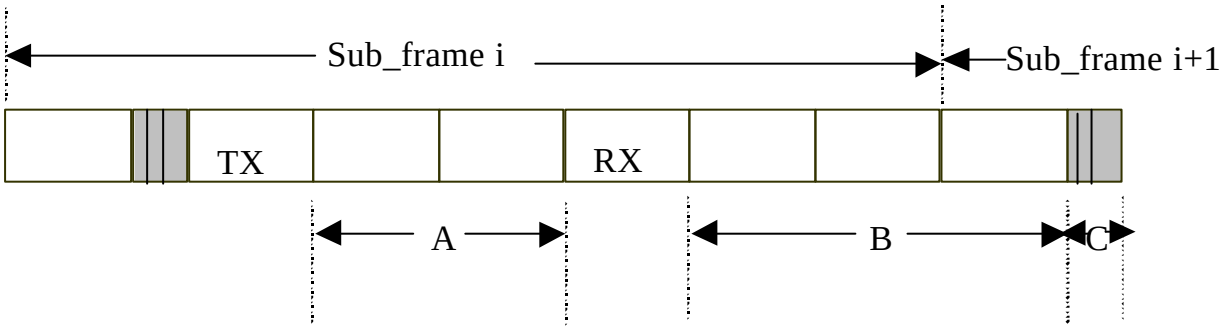

Figure A.1: Possible idle periods in a subframe with two occupied timeslots

A is defined as the number of idle slots between the Tx and Rx slots and B the number of idle slots between the Rx and Tx slots. It is clear that A+B=5 time slots and C is equal to the DwPTS+GP+UpPTS.

In the scope of low cost terminals, a [0.5] ms period is supposed to be required to perform a frequency jump from 1.28Mcps TDD to GSM and vice versa. This lets possibly two free periods of A*Timeslots-1 ms and B*Timeslots+C-1 ms during which the mobile station can monitor GSM, Timeslots being the slot period.

Following table evaluates the average synchronisation time and maximum synchronisation time, where the announced synchronisation time corresponds to the time needed to find the FCCH. The FCCH is supposed to be perfectly detected which means that it is entirely present in the monitoring window. The FCCH being found the SCH location is unambiguously known from that point. All the 5 idle slots and the DwPTS+GP+UpPTS are assumed to be devoted to FCCH tracking and the UL traffic is supposed to occupy the time slot 1.

Table A.1: example- of average and maximum synchronisation time with two busy timeslots per frame and with 0.5 ms switching time

Downlink time slot number	Number of free Timeslots in A	Number of free Timeslots in B	Average synchronisation time (ms)	Maximum synchronisation time (ms)
0	5	0	83	231
2	0	5	75	186
3	1	4	98	232
4	2	3	185	558
5	3	2	288	656
6	4	1	110	371

(*) All simulations have been performed with a random initial delay between GSM frames and 1.28Mcps TDD sub-frames.

Each configuration of Timeslots allocation described above allows a monitoring period sufficient to acquire synchronisation.

Considering about the frame structure of 1.28Mcps TDD, there are total 7 timeslot in each sub-frame that can be used as data traffic. If more than 1 uplink and/or 1 downlink TDD timeslot are used for data traffic, that means it will occupy at least 3 time slot, equal to 0.675*3=2.205ms. And more time slots for traffic data means more switching point are needed to switch between the GSM and the 1.28Mcps TDD. As it was mentioned above, each switching will take 0.5ms. As a result, the idle time left for monitoring the GSM will be very little. So monitoring GSM from 1.28Mcps TDD under this situation will be considered in the future. It will need more carefully calculation and simulation.

---------- Changes to working CR of 25.225 end ----------