3GPP TSG RAN WG1#15

Berlin/Germany

August 22nd – 25th, 2000

Agenda Item:	Correction to R'99
Source:	Mitsubishi Electric, Siemens AG
Title:	CR031r1 for TS25.221
	Number of codes signalling for the DL common midamble case
Document for:	Approval

Summary

By using variable shifts instead of a fixed shift to derive the common midamble for the UE's present in a timeslot, the number of simultaneously employed channelisation codes can be encoded and signalled to all UE's. The knowledge of the number of channelisation codes in turn simplifies the implementation of multiuser detection techniques in the UE, e.g. in the DL.

Section 5.6 in TS25.221 on midamble allocation in UL and DL is proposed to be revised for more clarity.

	e.g. for 3GPP us	00-1089 se the format TP-99xxx ise the format P-99-xxx
	CHANGE REQUEST Please see embedded help file at the page for instructions on how to fill in	
	25.221 CR 031r1 Current Version:	3.3.0
GSM (AA.BB) or 3G	G (AA.BBB) specification number ↑	t team
For submission list expected approval		(for SMG use only)
Proposed changed (at least one should be	nge affects: (U)SIM ME X UTRAN / Radio X Cor	e Network
Source:	Mitsubishi Electric, Siemens AG Date: 22/	08/2000
Subject:	Number of codes signalling for the DL common midamble case	
Work item:		
(only one category E shall be marked C	A Corresponds to a correction in an earlier release Rele B Addition of feature Rele C Functional modification of feature Rele D Editorial modification Rele	ase 2 ease 96 ease 97 ease 98 ease 99 X ease 00
<u>Reason for</u> change:	 By using variable shifts instead of a fixed shift to derive the common the UE's present in a timeslot, the number of simultaneous channelisation codes can be encoded and signalled to all UE's. The the number of channelisation codes in turn simplifies the imple multiuser detection techniques in the UE, e.g. in the DL. 	sly employed knowledge of
l	 Section 5.6 in TS25.221 on midamble allocation in UL and DL is prevised for more clarity. 	roposed to be
Clauses affecte	ed: Section 5.6; Insertion of a new annex B, previous annex B moved previous annex C moved to annex D	d to annex C,
Other specs	Other 3G core specifications X → List of CRs: CR480 to TS25.331 CR169 to TS25.423 CR199 to TS25.433	3
affected: Other	Other GSM core specifications \rightarrow List of CRs:MS test specifications \rightarrow List of CRs:BSS test specifications \rightarrow List of CRs:O&M specifications \rightarrow List of CRs:	
comments:		

5.6 Midamble Allocation for Physical Channels

In general, mMidambles are part of the physical channel configuration which is performed by higher layers. Three different midamble allocation schemes exist:

- UE specific midamble allocation: A UE specific midamble for DL or UL is explicitly assigned by higher layers.
- Default midamble allocation: The midamble for DL or UL is allocated by layer 1 depending on the associated channelisation code.
- Common midamble allocation: The midamble for the DL is allocated by layer 1 depending on the number of channelisation codes currently being present in the DL time slot.

Optionally, iIf no a midamble is not explicitly allocated assigned by higher layers and the use of the common midamble allocation scheme is not signalled by higher layers, a default the midamble allocation shall be used allocated by layer 1, based on the default midamble allocation scheme. This default midamble allocation scheme is given by a fixed association between midambles and channelisation codes, see clause A.3, and shall be applied individually to all channelisation codes within one time slot. Different associations apply for different burst types and cell configurations with respect to the maximum number of midambles.

5.6.1 Midamble Allocation for DL Physical Channels

Physical channels providing the beacon function shall always use the reserved midambles, see 5.4<u>5</u>. For DL physical channels that are located in the same time slot as the P-CCPCH, midambles shall be allocated based on the default midamble allocation scheme, using the association for burst type 1 and K=8 midambles. For all other DL physical channels, the midamble allocation is explicitly signalled assigned by higher layers or given by default allocated by layer 1.

5.6.1.1 Midamble Allocation by signalling from higher layers

Either a common or a UE specific midambles shall-may be signalled by higher layers to the UE's as a part of the physical channel configuration. Common or UE specific midambles may be applied only if the conditions in subclauses 5.6.1.1.1 and subclause 5.6.1.1.2 hold respectively. If the midamble is not signalled as a part of the physical channel configuration, midamble allocation by default shall be used.

5.6.1.1.1 Common Midamble

A common midamble may be assigned to all physical channels in one time slot, if:

- a single UE uses all physical channels in one time slot (as in the case of high rate service);

- or
- multiple UEs use the physical channels in one time slot; and

midambles are not used for PDSCH physical layer signalling.

5.6.1.1.2 UE specific Midamble

An individual midamble may be assigned to each of the UEs in one time slot, if:

- multiple UEs use the physical channels in one <u>DL</u> time slot; and
- beamforming is applied to all of these DL physical channels; and
- no closed loop TxDiversity is applied to any of these DL physical channels;

- PDSCH physical layer signalling based on the midamble is used.

5.6.1.2 Midamble Allocation by defaultlayer 1

5.6.1.2.1 Default midamble

If noa midamble is not explicitly allocated assigned and the use of the common midamble allocation scheme is not signalled by higher layers by signalling, the UE shall derive the midamble from the associated channelisation code and shall use an individual midamble for each channelisation code. For each association between midambles and channelisation codes in annex A.3, there is one primary channelisation code associated to each midamble. A set of secondary channelisation codes is associated to each primary channelisation code. All the secondary channelisation codes within a set use the same midamble as the primary channelisation code to which they are associated.

Higher layers shall allocate the channelisation codes in a particular order. Primary channelisation codes shall be allocated prior to associated secondary channelisation codes. If midambles are reserved for the beacon functionchannels, all primary and secondary channelisation codes that are associated with the reserved midambles shall not be used.

Primary and its associated secondary channelisation codes shall not be allocated to different UE's.

In the case that secondary channelisation codes are used, secondary channelisation codes of one set shall be allocated in ascending order, with respect to their numbering.

5.6.1.2.2 Common Midamble

The use of the common midamble allocation scheme is signalled to the UE by higher layers as a part of the physical channel configuration. A common midamble may be assigned by layer 1 to all physical channels in one DL time slot, if:

a single UE uses all physical channels in one DL time slot (as in the case of high rate service);

or

- multiple UEs use the physical channels in one DL time slot; and
- no beamforming is applied to any of these DL physical channels; and
- no closed loop TxDiversity is applied to any of these DL physical channels; and
- midambles are not used for PDSCH physical layer signalling.

The number of channelisation codes currently employed in the DL time slot is associated with the use of a particular common midamble. Different associations apply for different burst types and cell configurations with respect to the maximum number of midambles, see annex B.

5.6.2 Midamble Allocation for UL Physical Channels

If the midamble is part of the physical channel configuration explicitly assigned by higher layers, an individual midamble shall be assigned to all UE's in one <u>UL</u> time slot.

If no midamble is <u>explicitly allocated assigned</u> by higher layers, the UE shall derive the midamble from the assigned channelisation code as for DL physical channels. If the UE changes the SF according to the data rate, it shall always vary the channelisation code along the lower branch of the OVSF tree.

Annex B (normative) Signalling of the number of channelisation codes for the DL common midamble case

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The following mapping schemes shall apply for the association between the number of channelisation codes employed in a timeslot and the use of a particular midamble shift in the DL common midamble case. In the following tables the presence of a particular midamble shift is indicated by '1'. Midamble shifts marked with '0' are left unused. Mapping schemes B.3 and B.4 are not applicable to beacon timeslots where a P-CCPCH is present, because the default midamble allocation scheme is applied to these timeslots. Note that in mapping schemes B.3 and B.4, the fixed and pre-allocated channelisation code for the beacon channel is included into the number of indicated channelisation codes.

B.1 Mapping	scheme for Burst	Type 1	and K=16 Midambles.	

<u>m1</u>	<u>m2</u>	<u>m3</u>	<u>m4</u>	<u>m5</u>	<u>m6</u>	<u>m7</u>	<u>M8</u>	<u>m9</u>	<u>m10</u>	<u>m11</u>	<u>m12</u>	<u>m13</u>	<u>m14</u>	<u>m15</u>	<u>m16</u>	
<u>1</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>1 code</u>								
<u>0</u>	1	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	2 codes							
<u>0</u>	<u>0</u>	<u>1</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	3 codes						
<u>0</u>	<u>0</u>	<u>0</u>	1	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	4 codes
<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>1</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	5 codes
<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	1	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>6 codes</u>
<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>1</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	7 codes
<u>0</u>	<u>1</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>8 codes</u>						
<u>0</u>	1	<u>0</u>	9 codes													
<u>0</u>	1	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	10 codes								
<u>0</u>	<u>1</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	11 codes									
<u>0</u>	<u>0</u>	1	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	12 codes									
<u>0</u>	<u>0</u>	<u>0</u>	1	<u>0</u>	<u>0</u>	<u>0</u>	13 codes									
<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>1</u>	<u>0</u>	<u>0</u>	14 codes									
<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>1</u>	<u>0</u>	15 codes									
<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	1	16 codes									

B.2 Mapping scheme for Burst Type 1 and K=8 Midambles.

<u>M1</u>	<u>m2</u>	<u>m3</u>	<u>m4</u>	<u>m5</u>	<u>m6</u>	<u>m7</u>	<u>m8</u>	
1	<u>0</u>	1 code or 9 codes						
<u>0</u>	1	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	2 codes or 10 codes

<u>0</u>	<u>0</u>	1	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	3 codes or 11 codes
<u>0</u>	<u>0</u>	<u>0</u>	<u>1</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	4 codes or 12 codes
<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>1</u>	<u>0</u>	<u>0</u>	<u>0</u>	5 codes or 13 codes
<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	1	<u>0</u>	<u>0</u>	6 codes or 14 codes
<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	1	<u>0</u>	7 codes or 15 codes
<u>0</u>	1	8 codes or 16 codes						

B.3 Mapping scheme for beacon timeslots and K=16 Midambles.

<u>m1</u>	<u>m2</u>	<u>m3</u>	<u>M4</u>	<u>m5</u>	<u>m6</u>	<u>m7</u>	<u>M8</u>	<u>m9</u>	<u>m10</u>	<u>m11</u>	<u>M12</u>	<u>m13</u>	<u>m14</u>	<u>m15</u>	<u>m16</u>	
1	<u>x(^)</u>	1	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	1 codes or 13 codes						
<u>1</u>	<u>x(*)</u>	<u>0</u>	<u>1</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	2 codes or 14 codes
1	<u>x(^)</u>	<u>0</u>	<u>0</u>	<u>1</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	3 codes or 15 codes
1	<u>x(*)</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>1</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	4 codes or 16 codes
1	<u>x(^)</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	1	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	5 codes
1	<u>x(*)</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	1	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>6 codes</u>
1	<u>x(^)</u>	<u>0</u>	<u>1</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	7 codes							
1	<u>x(*)</u>	<u>0</u>	<u>0</u>	<u>1</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	8 codes							
1	<u>x(^)</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>1</u>	<u>0</u>	<u>0</u>	<u>0</u>	9 codes							
1	<u>x(*)</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	1	<u>0</u>	<u>0</u>	10 codes							
1	<u>x(^)</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>1</u>	<u>0</u>	11 codes							
1	<u>x(*)</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>1</u>	12 codes							

(*) In case of Block-STTD encoding for the P-CCPCH, midamble shift 2 is used by the diversity antenna

B.4 Mapping scheme for beacon timeslots and K=8 Midambles.

<u>m1</u>	<u>m2</u>	<u>m3</u>	<u>m4</u>	<u>m5</u>	<u>m6</u>	<u>m7</u>	<u>M8</u>	
1	<u>x(*)</u>	<u>1</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>1 or 7 or 13 codes</u>
1	<u>x(*)</u>	<u>0</u>	<u>1</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	2 or 8 or 14 codes
1	<u>x(*)</u>	<u>0</u>	<u>0</u>	<u>1</u>	<u>0</u>	<u>0</u>	<u>0</u>	3 or 9 or 15 codes
1	<u>x(*)</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>1</u>	<u>0</u>	<u>0</u>	4 or 10 or 16 codes
1	<u>x(*)</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	1	<u>0</u>	5 codes or 11 codes
1	<u>x(*)</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	1	6 codes or 12 codes

(*) In case of Block-STTD encoding for the P-CCPCH, midamble shift 2 is used by the diversity antenna

B.5 Mapping scheme for Burst Type 2 and K=6 Midambles.

<u>m1</u>	<u>m2</u>	<u>m3</u>	<u>m4</u>	<u>m5</u>	<u>m6</u>	
1	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>1 or 7 or 13 codes</u>
<u>0</u>	<u>1</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	2 or 8 or 14 codes
<u>0</u>	<u>0</u>	<u>1</u>	<u>0</u>	<u>0</u>	<u>0</u>	3 or 9 or 15 codes
<u>0</u>	<u>0</u>	<u>0</u>	1	<u>0</u>	<u>0</u>	4 or 10 or 16 codes
<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	1	<u>0</u>	5 or 11 codes
<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	<u>0</u>	1	6 or 12 codes

B.6 Mapping scheme for Burst Type 2 and K=3 Midambles.

<u>m1</u>	<u>m2</u>	<u>m3</u>	
1	<u>0</u>	<u>0</u>	<u>1 or 4 or 7 or 10 or 13 or 16 codes</u>
<u>0</u>	1	<u>0</u>	2 or 5 or 8 or 11 or 14 codes
<u>0</u>	<u>0</u>	1	3 or 6 or 9 or 12 or 15 codes

Annex BC (Informative): CCPCH Multiframe Structure

In the following figures B.1 to B.3 some examples for Multiframe Structures on Primary and Secondary CCPCH are given. The figures show the placement of Common Transport Channels on the Common Control Physical Channels. Additional S-CCPCH capacity can be allocated on other codes and timeslots of course, e.g. FACH capacity is related to overall cell capacity and can be configured according to the actual needs. Channel capacities in the annex are derived using bursts with long midambles (Burst format 1). Every TrCH-box in the figures is assumed to be valid for two frames (see row 'Frame #'), i.e. the transport channels in CCPCHs have an interleaving time of 20msec.

The actual CCPCH Multiframe Scheme used in the cell is described and broadcast on BCH. Thus the system information structure has its roots in this particular transport channel and allocations of other Common Channels can be handled this way, i.e. by pointing from BCH.

Release 1999					49)					3G 1	ΓS 2	5.22	1 V3	8.3.0	(200	00-00	6)									
Frame #	0 1	23	4 5	67	89				18 19		24 25		28 29								50 41				66 67	68 69	
CCPCHs in TS k, Code 0																											
CCPCHs in TS k+8, Co 0																											
								•		•																	

BCH transporting BCCH 2,71 kbps	FACH transporting BCCH 2,71 kbps	PCH 13,5kbps	PICH 2,71 kbps	FACH 27,1 kbps
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Figure B.1: Example for a multiframe structure for CCPCHs that is repeated every 72th frame

Frame #	01	23	3 4	56	67	89	10 11	12 13	14 15	16 17	18 19	20 21	22 23	24 25	26 27	28 29	30 31	32 33	34 35	36 37	38 39	40 41	42 43	44 45	46 47	48 49	50 41	52 53	54 55	56 57	58 59	60 61	62 63	64 65	66 67	68 69	
CCPCHs in TS k, Code 0																																					
CCPCHs in TS k, Code n																																					
CCPCHs in TS k+8, Co 0																																					

BCH transporting BCCH 2,71 kbps	FACH transporting BCCH 2,71 kbps	PCH 13,5kbps	PICH 2,71 kbps FACH 5	1,5 kbps
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Figure B.2: Example for a multiframe structure for CCPCHs that is repeated every 72th frame, n=1...7

Frame #	01	23	4	5 67	89	10 11	12 13		18 19		24 25					42 43	46 47		54 55		60 61	64 65	66 67	68 69	
CCPCHs in TS k, Code 0																									
CCPCHs in TS k+8, Co 0																									
							_	au			aat					1.10			 au						

BCH transporting BCCH 2,71 kbpsFACH transporting BCCH 1,355 kbpsPCH 13,5kbpsPICH 2,71 kbpsFACH 28,5 kbps

Figure B.3: Example for a multiframe structure for CCPCHs that is repeated every 72th frame

Annex CD (informative): Change history

					Change history		
Date	TSG #	TSG Doc.	CR	Rev	Subject/Comment	Old	New
14/01/00	RAN_05	RP-99591	-		Approved at TSG RAN #5 and placed under Change Control	-	3.0.0
14/01/00	RAN_06	RP-99691	001	02	Primary and Secondary CCPCH in TDD	3.0.0	3.1.0
14/01/00	RAN_06	RP-99691	002	02	Removal of Superframe for TDD	3.0.0	3.1.0
14/01/00	RAN_06	RP-99691	006	-	Corrections to TS25.221	3.0.0	3.1.0
14/01/00	RAN_06	RP-99691	007	1	Clarifications for Spreading in UTRA TDD	3.0.0	3.1.0
14/01/00	RAN_06	RP-99691	008	-	Transmission of TFCI bits for TDD	3.0.0	3.1.0
14/01/00	RAN_06	RP-99691	009	-	Midamble Allocation in UTRA TDD	3.0.0	3.1.0
14/01/00	RAN_06	RP-99690	010	-	Introduction of the timeslot formats to the TDD specifications	3.0.0	3.1.0
14/01/00	-	-	-		Change history was added by the editor	3.1.0	3.1.1
31/03/00	RAN_07	RP-000067	003	2	Cycling of cell parameters	3.1.1	3.2.0
31/03/00	RAN_07	RP-000067	011	-	Correction of Midamble Definition for TDD	3.1.1	3.2.0
31/03/00	RAN_07	RP-000067	012	-	Introduction of the timeslot formats for RACH to the TDD specifications	3.1.1	3.2.0
31/03/00	RAN_07	RP-000067	013	-	Paging Indicator Channel reference power	3.1.1	3.2.0
31/03/00	RAN_07	RP-000067	014	1	Removal of Synchronisation Case 3 in TDD	3.1.1	3.2.0
31/03/00	RAN_07	RP-000067	015	1	Signal Point Constellation	3.1.1	3.2.0
31/03/00	RAN_07	RP-000067	016	-	Association between Midambles and Channelisation Codes	3.1.1	3.2.0
31/03/00	RAN_07	RP-000067	017	-	Removal of ODMA from the TDD specifications	3.1.1	3.2.0
26/06/00	RAN_08	RP-000271	018	1	Removal of the reference to ODMA	3.2.0	3.3.0
26/06/00	RAN_08	RP-000271	019	-	Editorial changes in transport channels section	3.2.0	3.3.0
26/06/00	RAN_08	RP-000271	020	1	TPC transmission for TDD	3.2.0	3.3.0
26/06/00	RAN_08	RP-000271	021	-	Editorial modification of 25.221	3.2.0	3.3.0
26/06/00	RAN_08	RP-000271	023	-	Clarifications on TxDiversity for UTRA TDD	3.2.0	3.3.0
26/06/00	RAN_08	RP-000271	024	-	Clarifications on PCH and PICH in UTRA TDD	3.2.0	3.3.0
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