**3GPP TSG-RAN WG1 Meeting #116bis** ***R1-24xxxxx***

**Changsha, China, April 15-19, 2024**

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| *CR-Form-v12.3* |
| **Draft CHANGE REQUEST** |
|  |
|  | **38.212** | **CR** |  | **rev** | **-** | **Current version:** | **17.8.0** |  |
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| *For* [***HELP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* |
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| ***Proposed change affects:*** | UICC apps |  | ME | **X** | Radio Access Network | **X** | Core Network |  |

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| ***Title:***  | Rel-17 editorial corrections for TS 38.212 |
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| ***Source to WG:*** | Huawei |
| ***Source to TSG:*** |  |
|  |  |
| ***Work item code:*** | NR\_MBS-Core |  | ***Date:*** | 2024-04-23 |
|  |  |  |  |  |
| ***Category:*** | **F** |  | ***Release:*** | Rel-17 |
|  | *Use one of the following categories:****F*** *(correction)****A*** *(mirror corresponding to a change in an earlier release)****B*** *(addition of feature),* ***C*** *(functional modification of feature)****D*** *(editorial modification)*Detailed explanations of the above categories canbe found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | *Use one of the following releases:Rel-8 (Release 8)Rel-9 (Release 9)Rel-10 (Release 10)Rel-11 (Release 11)…Rel-17 (Release 17)Rel-18 (Release 18)Rel-19 (Release 19)Rel-20 (Release 20)* |
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| ***Reason for change:*** | Reflect the agreement for Rel-17 MBS from RAN1#116bis meeting. If the number of information bits in DCI format 4\_2 is less than the size of DCI format 4\_2 provided by the higher layer parameter *sizeDCI-4-2*, the defintion of the remaining bits is unclear.  |
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| ***Summary of change:*** | Clarify that the remaining bits are reserved if the number of information bits in DCI format 4\_2 is less than the size of DCI format 4\_2.  |
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| ***Consequences if not approved:*** | Specification is unclear. |
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| ***Clauses affected:*** | 7.3.1.5.3 |
|  |  |
|  | **Y** | **N** |  |  |
| ***Other specs*** |  | **X** |  Other core specifications  |  |
| ***affected:*** |  | **X** |  Test specifications |  |
| ***(show related CRs)*** |  | **X** |  O&M Specifications |  |
|  |  |
| ***Other comments:*** |  |
|  |  |
| ***This CR's revision history:*** |  |

7.3.1.5.3 Format 4\_2

DCI format 4\_2 is used for the scheduling of PDSCH for multicast in DL cell.

The following information is transmitted by means of the DCI format 4\_2 with CRC scrambled by G-RNTI for multicast or G-CS-RNTI configured by *MBS-RNTI-SpecificConfig*:

- Frequency domain resource assignment – number of bits determined by the following, where is the size of the common frequency resource as defined in Clause 18 of [5, TS38.213].

- bits if only resource allocation type 0 is configured, where is defined in Clause 5.1.2.2.1 of [6, TS38.214],

- bits if only resource allocation type 1 is configured, or

- bits if *resourceAllocation* in *pdsch-ConfigMulticast* is configured as '*dynamicSwitch'*.

- If *resourceAllocation* in *pdsch-ConfigMulticast* is configured as '*dynamicSwitch'*, the MSB bit is used to indicate resource allocation type 0 or resource allocation type 1, where the bit value of 0 indicates resource allocation type 0 and the bit value of 1 indicates resource allocation type 1.

- For resource allocation type 0, the LSBs provide the resource allocation as defined in Clause 5.1.2.2.1 of [6, TS 38.214].

- For resource allocation type 1, the LSBs provide the resource allocation as defined in Clause 5.1.2.2.2 of [6, TS 38.214]

- Time domain resource assignment – 0, 1, 2, 3, or 4 bits as defined in Clause 5.1.2.1 of [6, TS 38.214]. The bitwidth for this field is determined as bits, where *I* is the number of entries in the higher layer parameter *pdsch-TimeDomainAllocationList* if the higher layer parameter is configured; otherwise *I* is the number of entries in the default table.

- VRB-to-PRB mapping – 0 or 1 bit:

- 0 bit if only resource allocation type 0 is configured or if *vrb-ToPRB-Interleaver* in *pdsch-ConfigMulticast* is not configured;

- 1 bit according to Table 7.3.1.2.2-5 otherwise, only applicable to resource allocation type 1, as defined in Clause 7.3.1.6 of [4, TS 38.211].

- PRB bundling size indicator – 0 bit if the higher layer parameter *prb-BundlingType* is not configured in *pdsch-ConfigMulticast* or is set to 'staticBundling', or 1 bit if the higher layer parameter *prb-BundlingType* in *pdsch-ConfigMulticast* is set to 'dynamicBundling' according to Clause 5.1.2.3 of [6, TS 38.214].

- Rate matching indicator – 0, 1, or 2 bits according to higher layer parameters *rateMatchPatternGroup1* and *rateMatchPatternGroup2* in *pdsch-ConfigMulticast*, where the MSB is used to indicate *rateMatchPatternGroup1* and the LSB is used to indicate *rateMatchPatternGroup2* when there are two groups.

- ZP CSI-RS trigger – 0, 1, or 2 bits as defined in Clause 5.1.4.2 of [6, TS 38.214]. The bitwidth for this field is determined as bits, where is the number of aperiodic ZP CSI-RS resource sets configured in *pdsch-ConfigMulticast*.

For transport block 1:

- Modulation and coding scheme – 5 bits as defined in Clause 5.1.3.1 of [6, TS 38.214]

- New data indicator – 1 bit

- Redundancy version – 2 bits as defined in Table 7.3.1.1.1-2

For transport block 2 (only present if *maxNrofCodeWordsScheduledByDCI* configured in *pdsch-ConfigMulticast* equals 2):

- Modulation and coding scheme – 5 bits as defined in Clause 5.1.3.1 of [6, TS 38.214]

- New data indicator – 1 bit

- Redundancy version – 2 bits as defined in Table 7.3.1.1.1-2

- HARQ process number – 4 bits

- Downlink assignment index – number of bits as defined in the following

- 2 bits if the higher layer parameter *pdsch-HARQ-ACK-Codebook =dynamic* is configured for multicast, where the 2 bits are the counter DAI;

- 0 bits otherwise.

 If higher layer parameter *priorityIndicatorDCI-4-2* is configured in *pdsch-ConfigMulticast*, if the bit width of the Downlink assignment index in DCI format 4\_2 for one HARQ-ACK codebook is not equal to that of the Downlink assignment index in DCI format 4\_2 for the other HARQ-ACK codebook, a number of most significant bits with value set to '0' are inserted to smaller Downlink assignment index until the bit width of the Downlink assignment index in DCI format 4\_2 for the two HARQ-ACK codebooks are the same.

- PUCCH resource indicator – 3 bits as defined in Clause 9.2.3 of [5, TS 38.213]

- PDSCH-to-HARQ\_feedback timing indicator – 0, 1, 2, or 3 bits as defined in Clause 9.2.3 of [5, TS 38.213]. The bitwidth for this field is determined as bits, where *I* is the number of entries in the higher layer parameter *dl-DataToUL-ACK* in *pucch-ConfigMulticast1* if configured or *pucch-ConfigMulticast2* if configured; otherwise, *I* is the number of entries in the higher layer parameter *dl-DataToUL-ACK* in *PUCCH-Config.*

 If higher layer parameter *priorityIndicatorDCI-4-2* is configured in *pdsch-ConfigMulticast*, if the bit width of the PDSCH-to-HARQ\_feedback timing indicator in DCI format 4\_2 for one HARQ-ACK codebook is not equal to that of the PDSCH-to-HARQ\_feedback timing indicator in DCI format 4\_2 for the other HARQ-ACK codebook, a number of most significant bits with value set to '0' are inserted to smaller PDSCH-to-HARQ\_feedback timing indicator until the bit width of the PDSCH-to-HARQ\_feedback timing indicator in DCI format 4\_2 for the two HARQ-ACK codebooks are the same.

- Antenna port(s) – 4, 5, or 6 bits as defined by Tables 7.3.1.2.2-1/2/3/4, where the number of CDM groups without data of values 1, 2, and 3 refers to CDM groups {0}, {0,1}, and {0, 1,2} respectively. The antenna ports shall be determined according to the ordering of DMRS port(s) given by Tables 7.3.1.2.2-1/2/3/4.

If a UE is configured with both *dmrs-DownlinkForPDSCH-MappingTypeA* and *dmrs-DownlinkForPDSCH-MappingTypeB*, the bitwidth of this field equals , where is the "Antenna ports" bitwidth derived according to *dmrs-DownlinkForPDSCH-MappingTypeA* and is the "Antenna ports" bitwidthderived according to *dmrs-DownlinkForPDSCH-MappingTypeB*. A number of zeros are padded in the MSB of this field, if the mapping type of the PDSCH corresponds to the smaller value of and .

- Transmission configuration indication – 0 bit if higher layer parameter *tci-PresentInDCI* in *pdcch-ConfigMulticast* is not enabled; otherwise 3 bits as defined in Clause 5.1.5 of [6, TS38.214].

- DMRS sequence initialization – 1 bit.

- Priority indicator – 0 bit if higher layer parameter *priorityIndicatorDCI-4-2* is not configured in *pdsch-ConfigMulticast*; otherwise 1 bit as defined in Clause 9 in [5, TS 38.213].

- Enabling/disabling HARQ-ACK feedback indication –1 bit if higher layer parameter *harq-FeedbackEnablerMulticast* indicates *dci-enabler*, where value 1 indicates enabling HARQ-ACK feedback and value 0 indicates disabling HARQ-ACK feedback; 0 bit, otherwise.

The size of DCI format 4\_2 is configurable by higher layer parameter *sizeDCI-4-2* from 20 bits and up to 140 bits. If the number of information bits in DCI format 4\_2 is less than the size of DCI format 4\_2, the remaining bits are reserved.