**3GPP TSG RAN WG1 Meeting #106bis-e** **R1-21xxxxx**

**Oct 11th – Oct 19th, 2021**

**Agenda item: 8.2**

**Source: Rapporteur (Qualcomm Incorporated)**

**Title: Comments collection for RRC parameters for extending NR to 52.6-71GHz**

**Document for: Discussion and Decision**

# Introduction

This paper is a place holder to collect comments for RRC parameters for 60GHz work item. The RRC parameters are captured in the excel sheet in the same folder.

# Comments

## Initial access aspects

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| Company | View |
| Samsung  | For SSB SCS and PRACH SCS, it would be good to clarify with RAN2 for non-initial access case the configurable value can be from {120, 480, 960} and for initial access case the configurable value can be from {120, 480}, and RAN2 can further decide how to address this in their spec.  |
| ZTE, Sanechips | In RAN plenary #92 e-meeting, RAN1 has agreed only one CORESTE#0 SCS supported for each SSB SCS, i.e., (120, 120) kHz, (480, 480) kHz and (960, 960) kHz. In other words, FR2-2 only support CORESTE#0 SCS same as SSB SCS. It is not necessary to use *subCarrierSpacingCommon*in *MIB* to indicate the SCS of CORESET#0. Although the new function of *subCarrierSpacingCommon*has not been determined, it is clear that its field description should be revised for FR2-2. Thus we think the existing parameter *subCarrierSpacingCommon*in *MIB* should be captured into Rel-17 RRC parameter table, as it will no longer be used to indicate the SCS of CORESET#0 in FR2-2.Agreement* In addition to 120kHz, support 480 kHz SSB for initial access with support of CORESET#0/Type0-PDCCH configuration in the MIB with following constraints:
* only 480kHz CORESET#0/Type0-PDCCH SCS supported for 480 kHz SSB SCS
* Support ANR and PCI confusion detection for 120, 480 and 960kHz SCS based SSB, support CORESET#0/ Type0-PDCCH configuration in MIB of 120, 480 and 960kHz SSB
* Only 1 CORESTE#0/Type0-PDCCH SCS supported for each SSB SCS, i.e., (120, 120), (480, 480) and (960, 960).
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## PDCCH monitoring enhancements

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| Company | View |
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## Enhancements for PUCCH formats 0/1/4

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| Company | View |
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## Beam management for new SCSs

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| Company | View |
| ZTE, Sanechips | In the ongoing RAN1#106bis e-meeting, RAN1 has agreed the following agreement and conclusion.Agreement:For maxNumberRxTxBeamSwitchDL, support 1, 4 and 7 as candidate values for 960 kHz in addition to the agreed candidate value 2.* Note: this is Alt-1 from the RAN1#106 agreement.

Conclusion:For candidate values of timeDurationForQCL, beamSwitchTiming and beamReportTiming, * No additional candidate values are supported for 120 kHz, 480 kHz and 960 kHz
* Note: this is Alt-1 from the RAN1#106 agreement.

For parameter *maxNumberRxTxBeamSwitchDL*, the value range should be updated to “960KHz: 1, 2, 4, 7~~FFS additional values~~”.In addition, above agreement and conclusion can be added/updated into the column “Comment” of the RRC parameter table. |
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## PDSCH/PUSCH enhancements

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| Company | View |
| Samsung | Since multi-PUSCH scheduling for 120kHz SCS is supported, “(WA)” in row 25 and column J should be removed.  |

## Channel access mechanism

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| Company | View |
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## Others

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| Company | View |
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