

Source : Nortel Networks  
Title : Proposal for extension of PIL for Turbo Codes, for block sizes smaller than 320bits  
Document for : Discussion

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Some request to extend the PIL for block sizes lower than 320bits has been made in WG1. Nortel Networks would like to express their support to this idea, since this will provide more flexibility to the coding scheme choice for UEs that support Turbo Codes. It is our understanding is that only UEs implementing Turbo Codes would possibly use this extension. Moreover, the performance of 8PCCC Turbo Codes are also satisfactory for block sizes below 320bits.

Thus Nortel Networks are currently evaluating an extension which would be fully consistent with the current PIL definition and would lead to a minimal increase of complexity. Nortel Networks invite participation of interested parties in this effort. Technical details of this proposal will be sent to WG1 within the next ten days, in order to clarify the status of this extension as soon as possible. This should allow finalisation of the extension at our next meeting.