

**3GPP TSG RAN WG1 (Radio) Meeting #9**  
**Dresden, Germany. 30 NOV 1999 - 3 DEC 1999**

**Document R1-99k66**

e.g. for 3GPP use the format TP-99xxx  
or for SMG, use the format P-99-xxx

## CHANGE REQUEST

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**25.221 CR 003r1**

Current Version: **V3.0.0**

GSM (AA.BB) or 3G (AA.BBB) specification number ↑

↑ CR number as allocated by MCC support team

For submission to: **RAN #6**  
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for approval   
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**Proposed change affects:** (U)SIM  ME  UTRAN / Radio  Core Network   
(at least one should be marked with an X)

**Source:** Texas Instruments **Date:** 1 Dec 1999

**Subject:** Cycling of cell parameters

**Work item:** TS25.221

**Category:** F Correction   
A Corresponds to a correction in an earlier release   
B Addition of feature   
C Functional modification of feature   
D Editorial modification   
(only one category shall be marked with an X)

**Release:** Phase 2   
Release 96   
Release 97   
Release 98   
Release 99   
Release 00

**Reason for change:** Improvement in performance by reduction of false paths.

**Clauses affected:** 5.4

**Other specs affected:** Other 3G core specifications  → List of CRs: 25.223-CR002r1, 25.224-CR003r1  
Other GSM core specifications  → List of CRs:  
MS test specifications  → List of CRs:  
BSS test specifications  → List of CRs:  
O&M specifications  → List of CRs:

**Other comments:**

## 5.4 The physical synchronisation channel (PSCH)

In TDD mode code group of a cell can be derived from the synchronisation channel. Additional information, received from higher layers on SCH transport channel, is also transmitted to the UE in PSCH in case 3 from below. In order not to limit the uplink/downlink asymmetry the PSCH is mapped on one or two downlink slots per frame only.

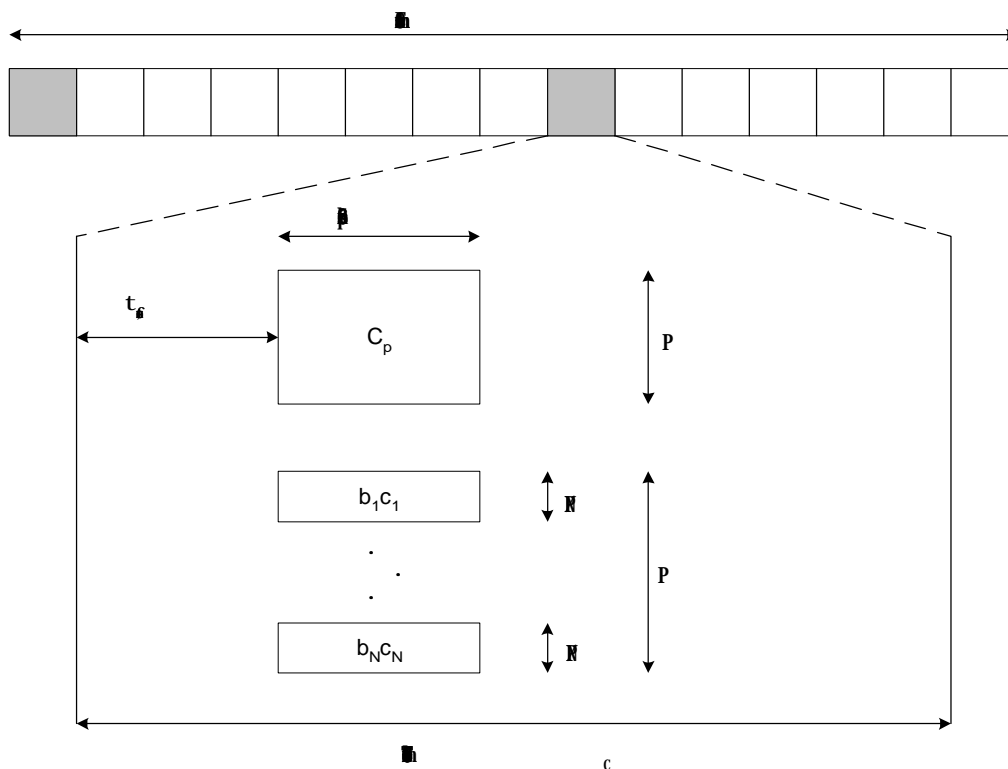
There are three cases of PSCH and PCCPCH allocation as follows:

- Case 1) PSCH and PCCPCH allocated in TS#k, k=0...14
- Case 2) PSCH in two TS and PCCPCH in the same two TS: TS#k and TS#k+8, k=0...6
- Case 3) PSCH in two TS, TS#k and TS#k+8, k=0...6, and the PCCPCH in TS#i, i=0...14, pointed by PSCH. Pointing is determined via the SCH from the higher layers.

These three cases are addressed by higher layers using the SCCH in TDD Mode. The position of PSCH (value of k) in frame can change on a long term basis in any case.

Due to this PSCH scheme, the position of PCCPCH is known from the PSCH. The PCCPCH are using burst type 1, spreading code  $a_{Q=16}^{(k=1)}$  and midamble  $m_1^{(1)}$ . To simplify measurements of PCCPCH power, this midamble shall not be used by other physical channels in the same timeslot.

Figure 15 is an example for transmission of PSCH, k=0, of Case 2 or Case 3.



**Figure 15: Scheme for Physical Synchronisation channel PSCH consisting of one primary sequence  $C_p$  and  $N=3$  parallel secondary sequences in slot  $k$  and  $k+8$**

(example for  $k=0$  in Case 2 or Case 3)

As depicted in figure 15, the PSCH consists of a primary and three secondary code sequences with 256 chips length. The primary and secondary code sequences are defined in TS 25.223 chapter 7 'Synchronisation codes'. The secondary codes are transmitted either in the I channel or the Q channel, depending on the code group.

Due to mobile to mobile interference, it is mandatory for public TDD systems to keep synchronisation between base stations. As a consequence of this, a capture effect concerning PSCH can arise. The time offset  $t_{\text{offset}}$  enables the system to overcome the capture effect.

The time offset  $t_{\text{offset}}$  is one of 32 values, depending on the cell parameter, thus on the code group of the cell, cf. 'table 7 Mapping scheme for Cell Parameters, Code Groups, Scrambling Codes, Midambles and  $t_{\text{offset}}$ ' in [8]. Note that the cell parameter will change from frame to frame, cf. 'Table 8 Alignment of cell parameter cycling and system frame number' in [8], but the cell will belong to only one code group and thus have one time offset  $t_{\text{offset}}$ . The exact value for  $t_{\text{offset}}$ , regarding column 'Associated  $t_{\text{offset}}$ ' in table 7 from [8] is given by:

$$\begin{aligned} t_{\text{offset},n} &= n \cdot T_c \left\lfloor \frac{2560 - 96 - 256}{31} \right\rfloor \\ &= n \cdot 71T_c ; \quad n = 0, \dots, 31 \end{aligned}$$

Please note that  $\lfloor x \rfloor$  denotes the largest integer number less or equal to  $x$  and that  $T_c$  denotes the chip duration.

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<b>25.223</b>	<b>CR</b>	<b>002r1</b>	Current Version: <b>V3.0.0</b>
GSM (AA.BB) or 3G (AA.BBB) specification number ↑		↑ CR number as allocated by MCC support team	
For submission to: <b>RAN #6</b>	for approval for information	<input checked="" type="checkbox"/>	strategic <input type="checkbox"/> non-strategic <input type="checkbox"/> (for SMG use only)
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**Proposed change affects:**      (U)SIM       ME       UTRAN / Radio       Core Network   
 (at least one should be marked with an X)

**Source:**      Texas Instruments      **Date:**      1 Dec 1999

**Subject:**      Cycling of cell parameters

**Work item:**      TS25.223

<b>Category:</b>	F Correction <input type="checkbox"/> A Corresponds to a correction in an earlier release <input type="checkbox"/> B Addition of feature <input type="checkbox"/> C Functional modification of feature <input checked="" type="checkbox"/> D Editorial modification <input type="checkbox"/>	<b>Release:</b>	Phase 2 <input type="checkbox"/> Release 96 <input type="checkbox"/> Release 97 <input type="checkbox"/> Release 98 <input type="checkbox"/> Release 99 <input checked="" type="checkbox"/> Release 00 <input type="checkbox"/>
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(only one category shall be marked with an X)

**Reason for change:**      Improvement in performance by reduction of false paths.

**Clauses affected:**      7.3

<b>Other specs affected:</b>	Other 3G core specifications <input checked="" type="checkbox"/> Other GSM core specifications <input type="checkbox"/> MS test specifications <input type="checkbox"/> BSS test specifications <input type="checkbox"/> O&M specifications <input type="checkbox"/>	→ List of CRs: → List of CRs: → List of CRs: → List of CRs: → List of CRs:	25.221-CR003r1, 25.224-CR003r1
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**Other comments:**

### 7.3 Evaluation of synchronisation codes

The evaluation of information transmitted in SCH on code group and frame timing is shown in table 7, where the 32 code groups are listed. Each code group is containing 4 specific scrambling codes (cf. section 6.3), each scrambling code associated with a specific short and long basic midamble code.

Each code group is additionally linked to a specific  $t_{Offset}$ , thus to a specific frame timing. By using this scheme, the UE can derive the position of the frame border due to the position of the SCH sequence and the knowledge of  $t_{Offset}$ . The complete mapping of Code Group to Scrambling Code, Midamble Codes and  $t_{Offset}$  is depicted in table 7.

**Table 7: Mapping scheme for Cell Parameters, Code Groups, Scrambling Codes, Midambles and  $t_{Offset}$**

CELL PARAMETER	Code Group	Associated Codes			Associated $t_{Offset}$
		Scrambling Code	Long Basic Midamble Code	Short Basic Midamble Code	
0	Group 1	Code 0	$m_{PL0}$	$M_{SL0}$	$t_0$
1		Code 1	$m_{PL1}$	$M_{SL1}$	
2		Code 2	$m_{PL2}$	$M_{SL2}$	
3		Code 3	$m_{PL3}$	$M_{SL3}$	
4	Group 2	Code 4	$m_{PL4}$	$M_{SL4}$	$t_1$
5		Code 5	$m_{PL5}$	$M_{SL5}$	
6		Code 6	$m_{PL6}$	$M_{SL6}$	
7		Code 7	$m_{PL7}$	$M_{SL7}$	
⋮					
124	Group 32	Code 124	$m_{PL124}$	$M_{SL124}$	$t_{31}$
125		Code 125	$m_{PL125}$	$M_{SL125}$	
126		Code 126	$m_{PL126}$	$M_{SL126}$	
127		Code 127	$m_{PL127}$	$M_{SL127}$	

For basic midamble codes  $m_p$  cf. TS 25.221, annex A ‘Basic Midamble Codes’.

Each cell will cycle through two sets of cell parameters in a code group with the cell parameters changing each frame. Table 8 shows how the cell parameters are cycled according to the SFN.

[Note: The use of cycling through four sets of cell parameters is ffs for Release ‘99].

**Table 8 Alignment of cell parameter cycling and SFN**

<b>Initial Cell Parameter Assignment</b>	<b>Code Group</b>	<b>Cell Parameter used when SFN mod 4 = 0</b>	<b>Cell Parameter used when SFN mod 4 = 1</b>	<b>Cell Parameter used when SFN mod 4 = 2</b>	<b>Cell Parameter used when SFN mod 4 = 3</b>
<u>0</u>	Group 1	<u>0</u>	<u>1</u>	<u>0</u>	<u>1</u>
<u>1</u>		<u>1</u>	<u>0</u>	<u>1</u>	<u>0</u>
<u>2</u>		<u>2</u>	<u>3</u>	<u>2</u>	<u>3</u>
<u>3</u>		<u>3</u>	<u>2</u>	<u>3</u>	<u>2</u>
<u>4</u>	Group 2	<u>4</u>	<u>5</u>	<u>4</u>	<u>5</u>
<u>5</u>		<u>5</u>	<u>4</u>	<u>5</u>	<u>4</u>
<u>6</u>		<u>6</u>	<u>7</u>	<u>6</u>	<u>7</u>
<u>7</u>		<u>7</u>	<u>6</u>	<u>7</u>	<u>6</u>
: : : :					
<u>124</u>	Group 32	<u>124</u>	<u>125</u>	<u>124</u>	<u>125</u>
<u>125</u>		<u>125</u>	<u>124</u>	<u>125</u>	<u>124</u>
<u>126</u>		<u>126</u>	<u>127</u>	<u>126</u>	<u>127</u>
<u>127</u>		<u>127</u>	<u>126</u>	<u>127</u>	<u>126</u>

<h2 style="margin: 0;">CHANGE REQUEST</h2>		<i>Please see embedded help file at the bottom of this page for instructions on how to fill in this form correctly.</i>
25.224	CR	003r1
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For submission to: <b>RAN #6</b>	for approval for information	Current Version: <b>V3.0.0</b>
<i>list expected approval meeting # here ↑</i>	<input checked="" type="checkbox"/>	(for SMG use only)
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**Subject:**      Cycling of cell parameters

**Work item:**      TS25.224

<b>Category:</b>	F Correction <input type="checkbox"/> A Corresponds to a correction in an earlier release <input type="checkbox"/> B Addition of feature <input type="checkbox"/> C Functional modification of feature <input checked="" type="checkbox"/> D Editorial modification <input type="checkbox"/>	<b>Release:</b>	Phase 2 <input type="checkbox"/> Release 96 <input type="checkbox"/> Release 97 <input type="checkbox"/> Release 98 <input type="checkbox"/> Release 99 <input checked="" type="checkbox"/> Release 00 <input type="checkbox"/>
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*(only one category shall be marked with an X)*

**Reason for change:**      Improvement in performance by reduction of false paths.

**Clauses affected:**      4.4.1

<b>Other specs affected:</b>	Other 3G core specifications <input checked="" type="checkbox"/> Other GSM core specifications <input type="checkbox"/> MS test specifications <input type="checkbox"/> BSS test specifications <input type="checkbox"/> O&M specifications <input type="checkbox"/>	→ List of CRs: 25.221-CR003r1, 25.223-CR002r1 → List of CRs: → List of CRs: → List of CRs: → List of CRs:
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**Other comments:**

## 4.4.1 Cell Search

During the initial cell search, the UE searches for a cell. It then determines the midamble, the downlink scrambling code and frame synchronisation of that cell. The initial cell search uses the Physical Synchronisation Channel (PSCH) described in [8]. The generation of synchronisation codes is described in [10].

This initial cell search is carried out in three steps:

### Step 1: Slot synchronisation

During the first step of the initial cell search procedure the UE uses the primary synchronisation code  $c_p$  to acquire slot synchronisation to the strongest cell. Furthermore, frame synchronisation with the uncertainty of 1 out of 2 is obtained in this step. A single matched filter (or any similar device) is used for this purpose, that is matched to the primary synchronisation code which is common to all cells.

### Step 2: Frame synchronisation and code-group identification

The Step 2 is described for the case where PSCH and PCCPCH are in timeslot  $k$  and  $k+8$  with  $k=0\dots6$ .

During the second step of the initial cell search procedure, the UE uses the modulated Secondary Synchronisation Codes to find frame synchronisation and identify one out of 32 code groups. Each code group is linked to a specific  $t_{\text{Offset}}$ , thus to a specific frame timing, and is containing 4 specific scrambling codes. Each scrambling code is associated with a specific short and long basic midamble code.

In Cases 2 and 3 it is required to detect the position of the next synchronization slots. To detect the position of the next synchronization slots, the primary synchronization code is correlated with the received signal at offsets of 7 and 8 time slots from the position of the primary code that was detected in Step 1.

Then, the received signal at the positions of the synchronization codes is correlated with the primary synchronization Code  $C_p$  and the secondary synchronization codes  $\{C_0, \dots, C_{15}\}$ . Note that the correlations can be performed coherently over  $M$  time slots, where at each slot a phase correction is provided by the correlation with the primary code. The minimal number of time slots is  $M=1$ , and the performance improves with increasing  $M$ .

### Step 3: Scrambling code identification

During the third and last step of the initial cell-search procedure, the UE determines the exact basic midamble code and the accompanying scrambling code used by the found cell. They are identified through correlation over the PCCPCH with all four midambles of the code group identified in the second step. Thus the third step is a one out of four decision. This step is taking into account that the PCCPCH containing the BCH is transmitted using the first spreading code ( $a_{Q=16}^{(h=1)}$  in [10]) and using the first midamble  $\mathbf{m}^{(1)}$  (derived from basic midamble code  $\mathbf{m}_p$  in [8]).

Thus PCCPCH code and midamble can be immediately derived when knowing scrambling code and basic midamble code. Note that the cell parameters change from frame to frame, cf. 'Table 8 Alignment of cell parameter cycling and system frame number' in [10].