

To: TSG RAN WG4

CC: TSG RAN WG1

Source: TSG RAN WG2

Title: **LS on status of the work on power control issues.**

At the last WG2 meeting, it was decided that the outer-loop power control algorithm can be in the UE and vary between a min and a max SIR value. Several questions were raised:

- Should Outer-loop control algorithm be specified in the standard or should some flexibility be provided?
- What is the most accurate value to base the outer-loop algorithm on (FER, BER, SIR ...)?

WG2 would like to know WG4's answers, if any, to these issues.

In a general manner, WG2 would like to be informed on the current status of the work of WG4 on power control issues.