3GPP RAN 5G-ACIA Evaluations Week 2

December 14th – 18th 2020

Source: Moderator (Ericsson)

Title: Review of provided simulation results and needed updates

Document for: Discussion, Decision

# 1 Introduction

AT RAN#89, the following was agreed in [RP-202069](https://protect2.fireeye.com/v1/url?k=41a5db26-1f051960-41a59bbd-86fc6812c361-73f443258ff773bf&q=1&e=bc078f84-983d-45f3-ab31-19e60d911036&u=https%3A%2F%2Fwww.3gpp.org%2Fftp%2Ftsg_ran%2FTSG_RAN%2FTSGR_89e%2FDocs%2FRP-202069.zip) on providing evaluations for 5G-ACIA:

* Start an offline email-based activity to provide evaluation results for 5G-ACIA
* One company volunteers as moderator
  + Proposes a work plan to follow
  + Ericsson is willing do this
* Discussions are on the RAN1\_NR reflector
  + Email activity only during short periods (< week) distributed across the time allocated to the activity
  + No email activity in weeks before/during/after RAN1 meetings or RAN defined inactive periods
  + All companies should strive to limit email activity as much as possible
  + Outcome of the offline discussion will directly go to RAN without need for discussion in RAN1 nor need for LS from RAN1 to RAN
* Target completion by RAN#91
* At RAN#91, RAN will decide on a response LS to 5G-ACIA

The moderator made the following proposal on a timeline:

1. 12-16 October 2020
   * Discussion on which URLLC features to include in the evaluations and simulation assumptions
2. 14-18 December 2020
   * First round of simulation results
3. 22-26 February 2021
   * Second round of simulation results
4. 8-12 March 2021
   * Finalization of the report to RAN#91

During week 1, the simulation assumptions were agreed as captures in the document below:

[https://www.3gpp.org/ftp/tsg\_ran/TSG\_RAN/TSGR\_90e/Inbox/Drafts/5G-ACIA October/Agreements/Agreements week 1 5G-ACIA.docx](https://www.3gpp.org/ftp/tsg_ran/TSG_RAN/TSGR_90e/Inbox/Drafts/5G-ACIA%20October/Agreements/Agreements%20week%201%205G-ACIA.docx)

For the second week, companies provided the first round of simulation results:

[https://www.3gpp.org/ftp/tsg\_ran/TSG\_RAN/TSGR\_91e/Inbox/Drafts/5G-ACIA December/Company Inputs/](https://www.3gpp.org/ftp/tsg_ran/TSG_RAN/TSGR_91e/Inbox/Drafts/5G-ACIA%20December/Company%20Inputs)

The input contributions are also listed in the reference section.

In this contribution, review comments from other companies are collected for each input. Additionally, input on changes to simulations assumptions and need for additional simulations for round 2 are provided by companies.

# 2 Company Inputs

## 2.1 Huawei/HiSilicon

[Contribution link](https://www.3gpp.org/ftp/tsg_ran/TSG_RAN/TSGR_91e/Inbox/Drafts/5G-ACIA%20December/Company%20Inputs/HwHiSi%20-%20Simulation%20results%20for%205G-ACIA%20in%20the%20first%20round.docx).

Other companies can provide questions and comments in the table below:

|  |  |
| --- | --- |
| Company | Questions and comments |
| Nokia | For the MU-MIMO results, would it be possible to share more details on the assumed transmission scheme, e.g. details on the precoding, CSI acquisition and in general multiplexing of UEs?  [HW/HiSi response]:   * SRS periodicity is set as to 5 ms and also the CSI periodicity is set to 5ms. * The precoding vectors of paired UEs are adjusted according to the estimated channel vectors from SRS based on ZF. * Rank adaption is adopted but the traditional rank adaption algorithm is used. Hence for max-rank = 4, the gNB almost always selects rank=4.   Is each BS limited to 2 Tx/Rx antenna ports?  [HW/HiSi response]:   * No. The antenna setting is as in TR 38.824, i.e., 4Tx/4Rx at the BS in case of 4GHz carrier frequency.   How was the scheduling done? I.e. was SPS/CG used, or was each TB scheduled independently? If SPS/CG was used, what are the modelling assumptions for the SPS/CG scheduling? E.g. is the FDRA fixed for each UE for the entire simulation? Is the MCS selected per UE or is it the same for all the UEs? Are there any adjustments of MCS/FDRA during the simulation?  [HW/HiSi response]:   * We use dyncamic scheduling, this gives the possibility to update the MCS according to CQI. Since the data arrival is deterministic, pre-scheduling is employed as expalined in our paper. |
| Qualcomm | * On “distributed MIMO”: is it mTRP Tx only, or is it ICIC and other features as well?   [HW/HiSi response]: Coherent JT transmission from all TRPs for each UE. We did not consider ICIC.   * For the latency figure, are they identical for DL & UL?   [HW/HiSi response]: Yes, because we assume a symmetric frame structure and also symmetric processing times at both the BS and the UE   * Please clarify how the MCS selection and radio link adaptation are used, especially in the context of “distributed MIMO”.   [HW/HiSi response]: This is done according to SRS and CSI. Similar to the single TRP multi-antenna SU/MU scheme, and the only difference is that each TRP has a power constraint. Please note that the RB allocation is performed according to CSI and SRS, but since full cell cooperation is performed, the reported values are very high and hence the highest MCS index is usually selected, i.e. 2 PRBs will usually be selected. In case there would be resources left after the allocation, the resources are then proportionally assigned to the UEs and hence a smaller MCS index could be used for transmission. |
| Ericsson | * + For the distributed MIMO, how was ‘the coordinated or coherent transmissions from different BSs’ done? Do the BSs coordinate to eliminate interference? Or they transmit coherently to improve SNR? Or both? But somehow the DL geometry shown in Figure 1 is worse than E/// plot, and E/// plot does not use any coordinated or coherent transmission.   [HW/HiSi response]: Coherent JT transmission is done from all TRPs to the UEs in order to improve the SINR. For the geometry, no cell cooperation is considered   * + For blocked or failed packets, “E2E latency is set to 1ms“. Shouldn’t the E2E latency be set to infinity or at least some value >1ms?   [HW/HiSi response]: We think it does not really matter if this value is set to exactly 1ms or to a larger value. We assume that in practice there won’t be any UE that has exactly 1ms delay. So we regard UE that <1ms latency as meeting the requirement in our simulation. But we are ok to chantge this according if it is requested.   * + For the number of users in Table 2 and Table 3, it’s curious how the numbers come from. They don’t seem to based on real time scheduler that allocates different amount of PRB according to actual SNR of each UE. For example, 272, 544 and 1088 are simply multiples of 272 (PRB). Does this mean that each UE gets a fixed number of 1 or 2 or 4 PRBs?   [HW/HiSi response]: The number is obtained from the simulation.   * For the SU case, the allocation is done according to the feedback CQI, but the CQI is always very high (note that we assume have full cell cooperation). As a result two RBs are allocated to each UE. * For the MU case, the allocation is done according to the CQI feedback and the UE pairing result at the BS. As a result. two RBs are allocated to each UE in case of 1088 UEs. For the 544 UEs, 4 PRBs are allocated to each UE.   Note that although we dynamically allocate the RBs to the UEs according to the feedback CQI and UE pairing results, each UE would select the highest MCS since no inter-cell interference exists (due to cell cooperation) and the feedback SINR is very high. (This would be different if interference from neighboring BSs is considered). As a result, in case of SU and MU, each UE only occupies 2 RBs initially. But if some RBs are left unused after scheduling, we will then allocate these RBs to the UEs proportionally to achieve a low SE. Hence, in case of 4-layer MU with 544 UEs, about 4RBs are finally allocated to each UE. |
| vivo | * For D-MIMO, how to perform signal processing and transmission coordination? All or part of BSs can transmit/recieve by SFN way? Or they can coordiantion scheduling information, e.g.resource allocation?   [HW/HiSi response]: In D-MIMO, coherent JT transmission from all BSs is used for cell cooperation. This is similar to SFN, but in the traditional setting of SFN, each BS is equipped with one antenna port and hence, no precoding exists. Meanwhile, there is no coherent transmission in SFN and the only gain is the power and diversity gain. Also, in the traditional setting of SFN, no MU is assumed.   * For dynamic scheduling, whether retransmission is enabled or not?   [HW/HiSi response]: It is not enabled. |
| Intel | Did you evaluate uncoordinated operation and how it would compare to the coordinated scheduling?  Do you think PDCCH overhead with dynamic pre-scheduling of up to 272 UEs in every slot is accurately accounted?  How the latency distribution is obtained: per packet or per UE? If it is per UE, then how a single value from all the packets of a UE is calculated? |

## 2.2 Intel

[Contribution link](https://www.3gpp.org/ftp/tsg_ran/TSG_RAN/TSGR_91e/Inbox/Drafts/5G-ACIA%20December/Company%20Inputs/INTEL%20-%205G-ACIA%20LS%20-%20Phase%202%20inputs.docx).

[Contribution link – updated](https://www.3gpp.org/ftp/tsg_ran/TSG_RAN/TSGR_91e/Inbox/Drafts/5G-ACIA%20December/Company%20Inputs/INTEL%20-%205G-ACIA%20LS%20-%20Phase%202%20inputs%20-%20updated.docx) with additional density for UL.

Other companies can provide questions and comments in the table below:

|  |  |
| --- | --- |
| Company | Questions and comments |
| ZTE | Do you assume one baseband for all 12 BSs or separate basebands for different BSs. Is there any coordination among different BSs? |
| Nokia | How was the scheduling done? I.e. was SPS/CG used, or was each TB scheduled independently? If SPS/CG was used, what are the modelling assumptions for the SPS/CG scheduling? E.g. is the FDRA fixed for each UE for the entire simulation? Is the MCS selected per UE or is it the same for all the UEs? Are there any adjustments of MCS/FDRA during the simulation? |
| Qualcomm | What is the exact number for the % of Ues satisfying 10-4 PER for the DL simulation with 30 UE/cell? |
| Ericsson | * + In Table 1 evaluation assumptions, it has Handover margin of 1 dB. Is handover simulated? (But the agreement was ‘No explicit UE mobility (nor handovers) are modeled in the evaluations.’)   + In Table 1 evaluation assumptions, was there special reason to use BS transmit power of 30 dBm? The agreement was to follow 38.824: “24 dBm per 20 MHz”, which gives 31 dBm.   + For Figure 1(a), why was channel path gain presented? Other companies tend to show coupling loss. It’s easier for calibration if coupling loss is shown instead.   + For Figure 1(b), what configuration the geometry shown for? For example, BS antenna configuration is 4Tx/4Rx or 8Tx/8Rx?   + Regarding BLER target of 1e-5: is this a bit of overkill? With CSA=99.9999%, and survival time = 1ms, one packet error is acceptable. CSA is for two or more consecutive packet errors. BLER around 1e-3 should be adequate. |
| vivo | * For TDD DL-UL configuration, 1:1 DL-to-UL 7 OS DL - 7 OS UL was used. Whether DL to UL gap was considered or not? * Since latency was one of the service requriement in 5G-ACIA LS, whether/how physical layer processing delay was modeled in you simulation? |
| HW/HiSi | Q1: In the paper it is said that 720 UEs are simulated in total. Is the understanding correct that for 10 UEs per service area, there are 6 drops, for 20 UEs, 3 drops, for 30 UEs there are 2 drops? How about the case of 40 UEs per service area?  Q2: Is it correctly understood that no cell-cooperation and no MU MIMO is assumed in the simulations?  Q3: On the number of packets that are generated to evaluate the CSA. In our view 100k packets are not sufficient to accurately estimate the CSA for these low BLERs. Not enough errors would be observed. |

## 2.3 ITRI

[Contribution link](https://www.3gpp.org/ftp/tsg_ran/TSG_RAN/TSGR_91e/Inbox/Drafts/5G-ACIA%20December/Company%20Inputs/ITRI_5G%20ACIA%20Simulation%20Result%20for%20InF-DH%204GHz.docx).

Other companies can provide questions and comments in the table below:

|  |  |
| --- | --- |
| Company | Questions and comments |
| ZTE | Do you assume one baseband for all 12 BSs or separate basebands for different BSs. Is there any coordination among different BSs?  ITRI response: all 12 BS are separate and independent, there is no coordination among the BSs. |
| Nokia | What are the modelling assumptions for the SPS/CG scheduling? E.g. is the FDRA fixed for each UE for the entire simulation? Is the MCS selected per UE or is it the same for all the UEs? Are there any adjustments of MCS/FDRA during the simulation?  ITRI response: The FDRA is fixed for each UE. We use the MCS index table 2 in TS38.214, and choose the lowest MCS index to satisfy the traffic load for each UE.  The performance in terms of supported number of UEs seems significantly worse than what is reported by other companies. It was not immediately clear why this is the case, but it would be a good to understand the reasons before considering including these in the 5G-ACIA response LS. Would ITRI be able to indicate the potential reason for such low number of UEs supported?  ITRI response: When the traffic arrival just misses the pre-allocated (configured) transmission opportunity, it is postponed to the next chance, and hence the reception exceeds 1ms deadline, as shown in the following figure. |
| Qualcomm | What is the exact PER requirement?  ITRI response: We setting PER requirement 0.01% (10-4) |
| Ericsson | For section 3 simulation results table, it’s puzzling why Percentage of UEs satisfying requirements is only at the level of 70+%, while other companies‘ results for 10 UE per service area show 99+%. Some explanation text was provided about the configuration, but not very easy to understand the details.  ITRI response: When the traffic arrival just misses the pre-allocated (configured) transmission opportunity, it is postponed to the next chance, and hence the reception exceeds 1ms deadline, as shown in the following figure. |
| vivo | Both BS and UE processing delay were taken into account latency statistic or not?  ITRI response: We recently don’t take processing delay into account. |
| HW/HiSi | Q1: What MCS is used for the configured resources?  ITRI response: We use the MCS index table 2 in TS38.214, and choose the lowest MCS index that satisfies the traffic load for each UE.  Q2 In the table showing the simulation results, it is said CSA = 100%, also it is shown that slightly more than 70% of the UEs satisfy the requirements. Is it correctly understood that UE not satisfying the requirements here means that these UEs have too large latency?  ITRI response: Yes, latency of some UEs is too large, resulting in that these UEs are not satifyied.  Q3: Is the following correctly understood by us: It seems that the latency problem could be overcome with choosing a proper and more realistic traffic model (as it was discussed during the previous email discussion)? That means if the data arrival would not be random, then the configuration could be aligned with the data arrival and the latency could be reduced significantly? Prior to this simulation round we discussed the applicable traffic model. Would  Q4: For the frame structure SU with S including 12D and 2S, and the TTI of 7OS, it cannot be guaranteed that there is sufficient time to transmit and process every packet if the packet arrives randomly in the 1 ms CT. Could this be a reason for the long latency that is observed?.  ITRI response for Q3 and Q4: Yes. The main reason for the long latency is as follows. When the traffic arrival just misses the pre-allocated (configured) transmission opportunity, it is postponed to the next chance, and hence the reception exceeds 1ms deadline, as shown in the following figure.    Q5: The simulation time is 5 s, i.e., 5000 packets for each UE. We think that this is too small for the given error rates.  ITRI response: Yes it is too small for the given error rates. We will simulate more packets in the feature.  Q6: Is cell cooperation and MU is considered?  ITRI response: No, neither cooperation nor MU are considered. |
| Intel | How the latency distribution is obtained: per packet or per UE? If it is per UE, then how a single value from all the packets of a UE is calculated?  ITRI response: Per packet. |

## 2.4 Nokia

[Contribution link](https://www.3gpp.org/ftp/tsg_ran/TSG_RAN/TSGR_91e/Inbox/Drafts/5G-ACIA%20December/Company%20Inputs/NOKIA%20-%205G-ACIA%20First%20round%20of%20simulation%20results.zip).

Other companies can provide questions and comments in the table below:

|  |  |
| --- | --- |
| Company | Questions and comments |
| ZTE | Do you assume one baseband for all 12 BSs or separate basebands for different BSs. Is there any coordination among different BSs?  Nokia response: all 12 BS are separate and independent, there is no coordination among the BSs. |
| Qualcomm | * Please clarify the number of samples per UE (is it 2\*106)? * Regarding the statement “This is because the latency performance is impacted not only by queuing delay and interference but also by limitations to user multiplexing imposed by beamforming operation itself. )”, is the difference between DL and UL in FR 2 due to beamforming capability, i.e. fewer opportunities for scheduling UEs? * It would be great if the following quantities could be clarified: gNB processing delay, UE processing Delay, PUSCH preparation time |
| Ericsson | * + In Appendix B simulation assumption of FR1,     - what’s the number of UE Tx antennas and configuration? Same as Rx antennas?     - Any reason that BS Tx power is 27 dBm? The agreement was to follow 38.824 (24 dBm per 20 MHz), which gives 31 dBm.   + In Appendix B simulation assumption of FR2, UE antenna configuration mentions “2 UE panels facing opposite directions”. Was the panel selection static? |
| Vivo | * What does ‘queuing delay’ means? * Whether BS and UE processing delay were considered in latency performance or not? |
| HW/HiSi | Q1: According to our understanding the number of packets that are simulated in each drop is 20k in FR1 and 10k in FR2. Given the low target error rates, this seems not sufficient to produce accurate results.  Q6: Is cell cooperation and MU is considered? |
| Intel | Do you think per-packet latency CDF provides sufficient understanding of the system performance? The more interest is in understanding each UE quality of service, which can rather be characterized by per-UE metrics, not necessarily latency. |

## 2.5 Qualcomm

Contribution links for [FR1](https://www.3gpp.org/ftp/tsg_ran/TSG_RAN/TSGR_91e/Inbox/Drafts/5G-ACIA%20December/Company%20Inputs/QUALCOMM-5G-ACIA_URLLC_simulation_results_1st_round_FR1.docx) and [FR2](https://www.3gpp.org/ftp/tsg_ran/TSG_RAN/TSGR_91e/Inbox/Drafts/5G-ACIA%20December/Company%20Inputs/Qualcomm5G-ACIA_URLLCResultsRound1_FR2.docx).

Other companies can provide questions and comments in the table below:

|  |  |
| --- | --- |
| Company | Questions and comments |
| ZTE | * For FR1 evaluation, is it a correct understanding that you allocated some time domain resources dedicated for re-transmission? To allow one re-transmission, do you assume 3 symbols or 4.5 symbols for processing SPS PDSCH and preparing HARQ-ACK?   Qualcomm response: 3 symbols are needed for processing SPS PDSCH. Also, 3 symbols are needed betwen receiving a PUSCH and sending a retransmission on PDSCH. They are all compliant with TR 37.910, TS 38.2124 and TS 38211 as explained below.   * Do you assume one baseband for all 12 BSs or separate basebands for different BSs. Is there any coordination among different BSs?   Qualcomm response: All 12 BS are separate and independent, and there is no coordination among the BSs except for the orthogonal retransmission phase.  In addition, it’s our understanding that assuming only 2.8 symbols for gNB processing especially for decoding PUCCH plus scheduling re-transmission is challenging.  Qualcomm response: Our calculation is based on Table 5.7.1.1.1-1 and Table 5.7.1.1.2-1 in TR 37.910 and Table 6.4-2 in TS 38.214 (Note that for 30 kHz SCS from TS 38.211 Table 4.2.1). Note that the retransmitted packet is already available in the buffer when the retransmission decision is made. Therefore, it seems practical to allow 2.8 symbols for gNB to process the PUCCH + retransmission. |
| Nokia | FR1:   * As also commented by ZTE, UE and gNB processing times seem more optimistic than what has been assumed by other companies. For instance, 2.8 symbols are assumed from PUCCH transmission (with HARQ) to PDSCH retransmission, whereas we assume 5.5 symbols (corresponding to N2). Also, minimum DL/UL latency in the CDF (Figs. 11 and 12) is ~80 us, which also doesn’t seem very realistic.   Qualcomm response: Our calculation is based on Table 5.7.1.1.1-1 and Table 5.7.1.1.2-1 in TR 37.910 and Table 6.4-2 in TS 38.214 (Note that for 30 kHz SCS from TS 38.211 Table 4.2.1). The labels of the latency plot are indeed incorrect because the 2 SPS data symbols were not taken into account. The correct plot is attached here, where the DL and UL latencies are nearly identical:     * Section 4.1: On the CSA distribution, does the reported ‘end-to-end error’ corresponds to single-error or two consecutive error case?   Qualcomm response: It’s the single-error case.   * Are UEs dropped per service area or per BS? Section 4 and 5 seem to imply that there is a fixed amount of UEs in each BS.   Qualcomm response: Fixed number of UEs are dropped to each service area, while each BS may have different loads due to the pathloss association rule.   * The proposed TDD configuration seems to require UE capability 5-1b of multiple DL/UL switches per slot. Should this be included in the feature list to be evaluated?   Qualcomm response: From Table 11.1.1-1 in TS 38.213, multiple DL/UL switches per slots are permitted. We think the multiple DL/UL switches per slot should be included in the feature list.  FR2:   * Is it correct that you assume up to 4 UEs FDM? Is this opportunistic, depending on how many UEs happen to be reachable with the same beam, or is there some sort of multi-beam transmission allowing this to happen always?  1. Qualcomm response: Multi-beam transmission is assumed so that 4 Ues FDM always happens – provided there is traffic available.   What are the modelling assumptions for the SPS/CG scheduling? E.g. is the FDRA fixed for each UE for the entire simulation? Is the MCS selected per UE or is it the same for all the Ues? Are there any adjustments of MCS/FDRA during the simulation?  Qualcomm response: FDRA and MCS for a UE could be modified by the SPS/CG overriding symbol in each slot. More precisely, FDRA can be updated upon need for radio link adaptation. MCS is selected per UE. There are adjustments of both MCS and FDRA when the radio link adaptation indicates MCS or FDRA change-upon NACK reception. Given that not many Ues need FDRA or MCS update per slot under our SPS/CG scheme, the resources provided for SPS overriding should be sufficient. Our preliminary investigation indicated that the difference between SPS/CG and dynamic schedulings are minimal for 20 UE/cell under our SPS/CG strategy. More detailed investigation on the overhead required for effective SPS/CG is ongoing. |
| Ericsson | FR1:   * + For simulation parameters in section 2, why was the periodicity=2ms? Use case #2 has transfer interval of 1ms, which means periodicity=1ms in our understanding.   + Could you please explain why retx BLER target is 10-4/6? How to ensure orthogonal **retx** throughout the network? No orthogonal tx if initial transmission?   FR2:   * + For FR1 study, the timing was worked out to allow 1 retx, hence a high BLER target of 1e-2 could be used for initial tx. For FR2, presumably the latency requirement is easier to achieve than FR1. Isn’t it more feasible to use HARQ retx in FR2 (hence higher BLER target than 1e-4)? What’s the considerations for the setup of FR2 as compared to FR1?   + In Section 2.1.5, UE Tx power is set to 11 dBm. Any special reason to use such low value? 38.824 used 23 dBm. |
| Vivo | * What does ‘At most one retransmission occurs at any time throughout the network‘ mean? Was that mean BS have to reserve resources for retransmission at any time? Meanwhile, initial transmission can not be allocated to the reserved resources. * For ‘A node always transmits at full power if it is performing a retransmission‘, was it only for UL? |
| HW/HiSi | **Q1:** In the figure 4, is the following understanding correct: If the PDSCH would be decoded correctly after the first transmission already, then the latency calculation would be terminated there, right? There is no need to wait for the PUCCH to send the ACK. Thus, in case of one-transmission, the latency would be 2.8 symbols plus PDSCH transmission time and the PDSCH decoding time, right?  **Q2:** On Figure 4   * For DL processing: It seems that the signal processing (PUCCH decoding) at the gNB receiver as well as the scheduling delay at the gNB are not considered. This combined time should be larger than the given 2.8 symbols. If we have understood correctly, then we think it is not feasible to have time-budget for 1 re-TX. Also, in our understanding, it seems that in the assumed frame structure, the gap between PDSCH to PUCCH of ACK/NACK is at most 4 symbols, this is smaller than 4.5 OS that shown in Figure 4. Are we missing here? * For UL processing: It seems that the signal processing (data decoding) at the gNB receiver as well as the scheduling delay at gNB are not considered. This time should be larger than the given 4.5OS. Similarly, the PDCCH to PUSCH time is set as 2.8 OS, smaller than 5.5OS required for UE capability #2. We don’t think that the time budget for a re-TX is feasible in the UL direction. Could you please clarify? * For retransmission, the decoding time at receiver for the second transmission is not considered   **Q3**: Is it correctly understood that the results are not obtained directly from simulations but are calculated based on intermediate results which are obtained the simulation? Could you please clarify?  **Q4**: Is it correctly understood that the results are showing the probability that the requirements are not satisfied for all UEs, but not the percentages of UEs satisfying the requirements?  **Q5**: For FR2, we think that 100s simulation time is too short to accurately capture the results.  **Q6**: Is cell cooperation and/or MU considered (it seems that no cooperation is adopted)? |
| Intel | Did you evaluate uncoordinated operation and how it would compare to the coordinated scheduling?  Is that correct understanding, that interference is not modelled/accounted under the assumption of fully orthogonal scheduling? If that is correct, then what is potential source of different performance between different UE densities? |

## 2.6 vivo

[Contribution link](https://www.3gpp.org/ftp/tsg_ran/TSG_RAN/TSGR_91e/Inbox/Drafts/5G-ACIA%20December/Company%20Inputs/vivo-5G-ACIA%201st%20round%20URLLC%20evaluation%20results.DOCX).

Other companies can provide questions and comments in the table below:

|  |  |
| --- | --- |
| Company | Questions and comments |
| ZTE | Do you assume one baseband for all 12 BSs or separate basebands for different BSs. Is there any coordination among different BSs? |
| Nokia | How was the scheduling done? I.e. was SPS/CG used, or was each TB scheduled independently? If SPS/CG was used, what are the modelling assumptions for the SPS/CG scheduling? E.g. is the FDRA fixed for each UE for the entire simulation? Is the MCS selected per UE or is it the same for all the UEs? Are there any adjustments of MCS/FDRA during the simulation? |
| Qualcomm | What is the CSA assumption? |
| HW/HiSi | **Q1:** Table one says “satisfying the requirements” What requirements are meant, is it latency, CSA or both om them? Could you please clarify?  **Q2**: IS cell cooperation and/or MU considered (it seems that no cooperation is adopted)? |
| Intel | How the latency distribution is obtained: per packet or per UE? If it is per UE, then how a single value from all the packets of a UE is calculated? |

## 2.7 Ericsson

[Contribution link](https://www.3gpp.org/ftp/tsg_ran/TSG_RAN/TSGR_91e/Inbox/Drafts/5G-ACIA%20December/Company%20Inputs/Ericsson%205G-ACIA%20Simulation%20Results%20Round1.zip).

Other companies can provide questions and comments in the table below:

|  |  |
| --- | --- |
| Company | Questions and comments |
| ZTE | Do you assume one baseband for all 12 BSs or separate basebands for different BSs. Is there any coordination among different BSs? |
| Nokia | * What are the modelling assumptions for the SPS/CG scheduling? E.g. is the FDRA fixed for each UE for the entire simulation? Is the MCS selected per UE or is it the same for all the UEs? Are there any adjustments of MCS/FDRA during the simulation? * Minimum latency is ~100 us, which is a bit too low for 2 OS TTI and 30 kHz SCS. Are realistic processing times taken into account? |
| Qualcomm | * Please elaborate the sentence “Since packet arrival is known by gNB, allocation in time and periodicity is optimized so that the alignment delay is minimized.” What quantities are optimized to minimize alignment delay? |
| vivo | How to define the maximum supported UEs per service area? Define as the maximum number of UEs per service area achieve 99.9999% CSA for more than 99% of users? |
| HW/HiSi | Is cell cooperation and/or MU MIMO adopted? |
| Intel | How to interpret the latency CDF point with ‘0 ms’ value?  How the latency distribution is obtained: per packet or per UE? If it is per UE, then how a single value from all the packets of a UE is calculated? |

## 2.8 ZTE

[Contribution link](https://www.3gpp.org/ftp/tsg_ran/TSG_RAN/TSGR_91e/Inbox/Drafts/5G-ACIA%20December/Company%20Inputs/ZTE-5G-ACIA%20evaluations%20-%201st%20round%20of%20simulation%20results.docx).

Other companies can provide questions and comments in the table below:

|  |  |
| --- | --- |
| Company | Questions and comments |
| Nokia | * What are the modelling assumptions for the SPS/CG scheduling? E.g. is the FDRA fixed for each UE for the entire simulation? Is the MCS selected per UE or is it the same for all the UEs? Are there any adjustments done to the MCS/FDRA during the simulation? * Minimum latency is ~250 us, which is a bit too low for 5 OS TTI and 30 kHZ SCS. Are realistic processing times taken into account? |
| Qualcomm | * How do the base stations coordinate together? What are the technologies involved, such as mTRP, ICIC or other features as well? |
| Ericsson | * + For latency figures (Fig 3 and 4), why are the CDF curves in staircase shape? Is it related to packet arrival being generated with symbol granularity?   + For Table 1 RU results, it is puzzling why RU is so low. Our back of envelope estimate is, using RU=10.08% for 50 UEs per service area as in ZTE Table 1, most UEs are allocated with 1 PRB. This seems very low. For example, as a reference point, Intel’s RU results (Table 2 and Table 3) are approximately 4 times as high as ZTE’s for both DL and UL. |
| vivo | Whether BS and UE processing delay were considered in latency performance or not? |
| HW/HiSi | Q1: Which MCS table is used? Is it correctly understood that a packet only occupies one RB, and hence the 256QAM table is used and the highest MCS index is used?  Q2: Since cell cooperation is adopted, why is the RU shown in Table 1 is so low? For 40 UEs per area, about 40\*12/2 = 240 RBs should be used in each TTI in the factory. Hence the RU should be 240/273? Is there something that we are missing?  Q3: It seems that SU MIMO is adopted in the simulation, is this a correct understanding? If yes, then for the case of of 50 UEs per service area (i.e., 600 UEs in the factory), the resources would be overloaded, then how is it decided which UE would be served? |
| Intel | What number of samples was used to conclude that CSA is 100% for certain scenarios? |

# 3 Updates of simulations assumptions and missing simulations

In the table below, companies can provide inputs on need for changes in simulation assumptions and what additional simulations that should be performed for the second round of simulations.

|  |  |
| --- | --- |
| Company | Input |
| Nokia | The antenna assumption for FR2 may be too restrictive, with only 2 Tx/Rx antenna ports in the BS. When analogue BF is used, this limits the multiplexing capability and leads to starvation of slots to transmit while a lot of PRBs are unused. We should consider an antenna system that allows for larger number of Ues to be multiplexed to transmit/receive at the same time.  Not exactly related to further simulation needs, but realizing that some companies are simulating a single gNB with 12 RRUs having some sort of joint scheduler, while others are simulating 12 independent gNBs without coordination it would seem important to categorize the results in the final output so that it is possible to understand that different network setups lead to different performance. |
| Qualcomm | Our proposed scheme assumes the following: CSA metric with no consecutive errors; gNBs do not coordinate except for orthogonalized retransmissions, which is a special form of mTRP; Multiple uplink/downlink switchings in a slot; Processing delays compliant with TR 37.910, TS 38.2124 and TS 38211 are used for our HARQ strategy.  Most companies use the same CSA metric where no consecutive errors are permitted. It seems appropriate to make this CSA assumption mandatory in the 2nd round.  Also, the set of permitted coordination strategies among gNBs should be made more specific. The companies that assume gNB coordination should elaborate the coordination strategies being used because different coordination strategies have different processing requirements. |
| Ericsson | There is a need to calibrate the simulator among companies. We noticed at least two sets of DL geometry curves for 4GHz:   1. E/// and QC have very similar DL geometry curves; 2. HW and Intel have very similar DL geometry curves;   But (b) are significantly worse than (a), for example, about 4 dB worse at CDF=50% and 80%. It would be good to align this basic setup first. |
| Vivo | We need to align physical layer processing delay modeling. In our understanding, in addition to alignment delay and transmission duration, BS and UE processing delay also should be taken into account. Otherwise, the simulation results cannot reflect real deployment.  Since different companies have different understanding on the maximum supported Ues per service area, we need to further clarified. |
| HW/HiSi | **E2E latency**: One critical factor is how we define the E2E latency. One company reports that there is enough time for one re-TX whereas all other companies think that there is only time for a one-shot. But even among those companies, there seems to be a different understanding what is factors are contributing to the E2E latency. Maybe we should spend some more effort on this to align our view?  **Cell cooperation:** Two companies assumed cell cooperation whereas all other companies did not assume cell cooperation. For the given deployment scenario, it seems that cell cooperation can be easily achieved and the performance is very good and practicable since interference can be avoided. We would encourage more companies to study cell-cooperation.  **MU-MIMO**: Only one company studied MU-MIMO. For the given scenario, it is a very good feature to further increase the performance. It would be nice if more results also could be presented. Maybe it would be possible to define some 3 cases for simulations and to compare the results?   * No cell cooperation, SU MIMO * Cell cooperation, SU MIMO * Cell cooperation, MU-MIMO   **Calibration:** Intel, ITRI, E///, QC and HW/HiSi present results (mainly coupling loss and geometry) for calibration. There are some small differences in the results. Also, it seems that there are small differences in the simulation assumptions of different companies. Maybe we could try to prepare a table in order to compare the detailed simulation parameters from different companies, and then generate the calibration figure to compare the calibration results?  **Number of samples that should be generated:** It seems that many results are based on few samples. It would good to give a target for the produced number of packets depending on the error rate, e.g. 100/error\_rate? |
| Intel | Agree with Ericsson that it would be great to have more alignment on basic geometry curves.  Clearer categorization between coordinated and uncoordinated scheduling results is desirable.  We can now narrow down to InF-DH explicitly. Also, can confirm options of traffic arrival for DL and UL, since no company proposes to revise it in this phase.  May be good to agree on a minimum number of packets per UE and minimum number of UEs / network drops modelled for more confidence in results. |
| ITRI | **Calibration:** Agree with Ericsson and HW/HiSi.  There is a need to calibrate the simulator among companies, and prepare a table in order to compare the detailed simulation parameters. |

# 4 Conclusions

# References

1. [RP-202069](https://protect2.fireeye.com/v1/url?k=41a5db26-1f051960-41a59bbd-86fc6812c361-73f443258ff773bf&q=1&e=bc078f84-983d-45f3-ab31-19e60d911036&u=https%3A%2F%2Fwww.3gpp.org%2Fftp%2Ftsg_ran%2FTSG_RAN%2FTSGR_89e%2FDocs%2FRP-202069.zip), “Way forward on RAN work for 5G ACIA requested simulations“, Ericsson
2. “[Simulation results for 5G-ACIA in the first round](https://www.3gpp.org/ftp/tsg_ran/TSG_RAN/TSGR_91e/Inbox/Drafts/5G-ACIA%20December/Company%20Inputs/HwHiSi%20-%20Simulation%20results%20for%205G-ACIA%20in%20the%20first%20round.docx) Huawei, HiSilicon
3. “[5G-ACIA LS – Phase 2 input](https://www.3gpp.org/ftp/tsg_ran/TSG_RAN/TSGR_91e/Inbox/Drafts/5G-ACIA%20December/Company%20Inputs/INTEL%20-%205G-ACIA%20LS%20-%20Phase%202%20inputs.docx)”, Intel Corporation
4. “[Simulation Assumptions and URLLC Performance Evaluations for 5G-ACIA Performance Evaluation Round 1](https://www.3gpp.org/ftp/tsg_ran/TSG_RAN/TSGR_91e/Inbox/Drafts/5G-ACIA%20December/Company%20Inputs/ITRI_5G%20ACIA%20Simulation%20Result%20for%20InF-DH%204GHz.docx)”, ITRI
5. “[First round of simulation results for 5G-ACIA evaluation](https://www.3gpp.org/ftp/tsg_ran/TSG_RAN/TSGR_91e/Inbox/Drafts/5G-ACIA%20December/Company%20Inputs/NOKIA%20-%205G-ACIA%20First%20round%20of%20simulation%20results.zip)”, Nokia, Nokia Shanghai Bell
6. “[First round of FR1 simulation results for 5G ACIA URLLC LS response](https://www.3gpp.org/ftp/tsg_ran/TSG_RAN/TSGR_91e/Inbox/Drafts/5G-ACIA%20December/Company%20Inputs/QUALCOMM-5G-ACIA_URLLC_simulation_results_1st_round_FR1.docx) ”, Qualcomm CDMA Technologies
7. “[Simulation Assumptions and URLLC Performance Evaluations for 5G-ACIA Performance Evaluation Round 1](https://www.3gpp.org/ftp/tsg_ran/TSG_RAN/TSGR_91e/Inbox/Drafts/5G-ACIA%20December/Company%20Inputs/Qualcomm5G-ACIA_URLLCResultsRound1_FR2.docx)”, Qualcomm CDMA Technologies
8. “[5G-ACIA 1st round URLLC evaluation results](https://www.3gpp.org/ftp/tsg_ran/TSG_RAN/TSGR_91e/Inbox/Drafts/5G-ACIA%20December/Company%20Inputs/vivo-5G-ACIA%201st%20round%20URLLC%20evaluation%20results.DOCX)”, vivo
9. “[Simulation Results for 5G-ACIA (First round)](https://www.3gpp.org/ftp/tsg_ran/TSG_RAN/TSGR_91e/Inbox/Drafts/5G-ACIA%20December/Company%20Inputs/Ericsson%205G-ACIA%20Simulation%20Results%20Round1.zip)”, Ericsson
10. “[ZTE-5G-ACIA evaluations - 1st round of simulation results](https://www.3gpp.org/ftp/tsg_ran/TSG_RAN/TSGR_91e/Inbox/Drafts/5G-ACIA%20December/Company%20Inputs/ZTE-5G-ACIA%20evaluations%20-%201st%20round%20of%20simulation%20results.docx)”, ZTE
11. “[INTEL - 5G-ACIA LS - Phase 2 inputs - updated](https://www.3gpp.org/ftp/tsg_ran/TSG_RAN/TSGR_91e/Inbox/Drafts/5G-ACIA%20December/Company%20Inputs/INTEL%20-%205G-ACIA%20LS%20-%20Phase%202%20inputs%20-%20updated.docx)”, Intel