TSG-RAN Meeting #26
Athen, Greece, 08-10 December 2004

RP-040481
Agenda item 7.3.5

Source: TSG-RAN WG2.

Title: CRs to 25.331 Rel-5 (1) (and Rel-6)

The following CRs are in RP-040481:

Spec	CR	Rev	Phase	Subject	Cat	Version-Current	Version-New	Doc-2nd-Level	Workitem
25.331	2433	-	Rel-5	Correction to measured results on RACH	F	5.10.0	5.11.0	R2-042214	TEI5
25.331	2434	-	Rel-6	Correction to measured results on RACH	A	6.3.0	6.4.0	R2-042215	TEI5
25.331	2435	-	Rel-5	T305 handling upon a state transition	F	5.10.0	5.11.0	R2-042253	TEI5
25.331	2436	-	Rel-6	T305 handling upon a state transition	A	6.3.0	6.4.0	R2-042254	TEI5
25.331	2437	-	Rel-5	Handling of pending AM RLC unrecoverable errors signalled by cell update	F	5.10.0	5.11.0	R2-042268	TEI5
25.331	2438	-	Rel-6	Handling of pending AM RLC unrecoverable errors signalled by cell update	A	6.3.0	6.4.0	R2-042269	TEI5
25.331	2443	-	Rel-5	TPC step size in default configurations	F	5.10.0	5.11.0	R2-042585	TEI5
25.331	2444	-	Rel-6	TPC step size in default configurations	A	6.3.0	6.4.0	R2-042586	TEI5
25.331	2452	-	Rel-5	Correction to HS-DSCH reception conditions	F	5.10.0	5.11.0	R2-042595	HSDPA-L23
25.331	2453	-	Rel-6	Correction to HS-DSCH reception conditions	A	6.3.0	6.4.0	R2-042596	HSDPA-L23
25.331	2454	1	Rel-5	MAC-hs Reset procedure	F	5.10.0	5.11.0	R2-042685	HSDPA-L23
25.331	2455	1	Rel-6	MAC-hs Reset procedure	A	6.3.0	6.4.0	R2-042686	HSDPA-L23

3GPP TSG RAN2#44 **R2-042214**
Sophia Antipolis, France, France, 4-8 October 2004

CR-Form-v7.1

CHANGE REQUEST

⌘ **25.331** **CR** **2433** ⌘ **rev** **-** ⌘ Current version: **5.a.0** ⌘

*For **HELP** on using this form, see bottom of this page or look at the pop-up text over the ⌘ symbols.*

Proposed change affects: UICC apps⌘ ☐ ME ☒ Radio Access Network ☒ Core Network ☐

Title:	⌘	Correction to measured results on RACH
Source:	⌘	RAN WG2
Work item code:	⌘	TEI5
Date:	⌘	September 2004
Category:	⌘	**F**
Release:	⌘	Rel-5

Use one of the following categories:
F *(correction)*
A *(corresponds to a correction in an earlier release)*
B *(addition of feature),*
C *(functional modification of feature)*
D *(editorial modification)*
Detailed explanations of the above categories can be found in 3GPP TR 21.900.

Use one of the following releases:
Ph2 *(GSM Phase 2)*
R96 *(Release 1996)*
R97 *(Release 1997)*
R98 *(Release 1998)*
R99 *(Release 1999)*
Rel-4 *(Release 4)*
Rel-5 *(Release 5)*
Rel-6 *(Release 6)*
Rel-7 *(Release 7)*

Reason for change:	⌘	The following inconsistency related to the “measured results on RACH” was detected in 25.331: • Section 8.1.1.6.12: read and use the IE "Intra-frequency measurement quantity", "Intra-frequency reporting quantity for RACH reporting", "Maximum number of reported cells on RACH" or "Reporting information for state CELL_DCH" in SIB 11 if not included in SIB12. • Section 8.5.23: use SIB 11 values only if SIB 12 is not broadcast.
Summary of change:	⌘	It is assumed that the behaviour from section 8.1.1.6.12 is generally understood to be correct. Therefore section 8.5.23 is proposed to be aligned to section 8.1.1.6.12.
Consequences if not approved:	⌘	Inconsistency remains in the specification.

Clauses affected:	⌘	8.5.23

Other specs affected: ⌘	Y	N		⌘
	☐	☐	Other core specifications	
	☐	☐	Test specifications	
	☐	☐	O&M Specifications	

Other comments: ⌘

How to create CRs using this form:
Comprehensive information and tips about how to create CRs can be found at http://www.3gpp.org/specs/CR.htm. Below is a brief summary:

1) Fill out the above form. The symbols above marked ⌘ contain pop-up help information about the field that they are closest to.

2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under ftp://ftp.3gpp.org/specs/ For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.

3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

8.5.23 Measured results on RACH

When transmitting an uplink RRC message, the UE shall:

1> if the uplink RRC message is an RRC CONNECTION REQUEST message:

2> if the IE "Intra-frequency reporting quantity for RACH reporting" and the IE "Maximum number of reported cells on RACH" are included in System Information Block type 11:

3> include a measurement report in the IE "Measured results on RACH", as specified in the IE "Intra-frequency reporting quantity for RACH reporting" and the IE "Maximum number of reported cells on RACH" in System Information Block type 11.

1> for any other uplink RRC message which optionally includes the IE "Measured results on RACH":

2> if the IE "Intra-frequency reporting quantity for RACH reporting" and the IE "Maximum number of reported cells on RACH" are included in System Information Block type 12 (or "System Information Block Type 11", if these IEs are not included in the broadcast "System Information Block Type 12" or "System Information Block Type 12" is not being broadcast).

3> include a measurement report in the IE "Measured results on RACH", as specified in the IE "Intra-frequency reporting quantity for RACH reporting" and the IE "Maximum number of reported cells on RACH" in System Information Block type 12 (or "System Information Block Type 11" if these IEs are not included in the broadcast "System Information Block Type 12" or "System Information Block Type 12 is not being broadcast).

1> include in the IE "Measured results on RACH" all requested reporting quantities for cells for which measurements are reported.

NOTE: The UE only includes measurement results for neighbour cells for which valid measurements are available at the time the message is sent. At cell access following selection or reselection to a cell, the UE may not have had sufficient time to obtain valid measurement results for neighbour cells.

1> for messages transmitted on CCCH, take care that the maximum allowed message size is not exceeded when forming the IE "Measured results on RACH", i.e. limit the number of included neighbour cells or if required omit the IE "Measured results on RACH" altogether.

If the IE "Measured results on RACH" is present in the message, the UTRAN should extract the contents to be used for radio resource control.

3GPP TSG RAN2#44 **R2-042215**
Sophia Antipolis, France, France, 4-8 October 2004

CR-Form-v7.1

CHANGE REQUEST

⌘ **25.331** **CR** **2434** ⌘ **rev** **-** ⌘ Current version: **6.3.0** ⌘

*For **HELP** on using this form, see bottom of this page or look at the pop-up text over the ⌘ symbols.*

Proposed change affects: UICC apps⌘ ☐ ME ☒ Radio Access Network ☒ Core Network ☐

Title:	⌘	Correction to measured results on RACH
Source:	⌘	RAN WG2
Work item code:	⌘	TEI5
Date:	⌘	September 2004
Category:	⌘	**A**
Release:	⌘	Rel-6

Use one of the following categories:
F *(correction)*
A *(corresponds to a correction in an earlier release)*
B *(addition of feature),*
C *(functional modification of feature)*
D *(editorial modification)*
Detailed explanations of the above categories can be found in 3GPP TR 21.900.

Use one of the following releases:
Ph2 *(GSM Phase 2)*
R96 *(Release 1996)*
R97 *(Release 1997)*
R98 *(Release 1998)*
R99 *(Release 1999)*
Rel-4 *(Release 4)*
Rel-5 *(Release 5)*
Rel-6 *(Release 6)*
Rel-7 *(Release 7)*

Reason for change:	⌘	The following inconsistency related to the “measured results on RACH” was detected in 25.331: • Section 8.1.1.6.12: read and use the IE "Intra-frequency measurement quantity", "Intra-frequency reporting quantity for RACH reporting", "Maximum number of reported cells on RACH" or "Reporting information for state CELL_DCH" in SIB 11 if not included in SIB12. • Section 8.5.23: use SIB 11 values only if SIB 12 is not broadcast.
Summary of change:	⌘	It is assumed that the behaviour from section 8.1.1.6.12 is generally understood to be correct. Therefore section 8.5.23 is proposed to be aligned to section 8.1.1.6.12.
Consequences if not approved:	⌘	Inconsistency remains in the specification.

Clauses affected:	⌘	8.5.23

Other specs affected: ⌘	Y	N		⌘
	☐	☐	Other core specifications	
	☐	☐	Test specifications	
	☐	☐	O&M Specifications	

Other comments: ⌘

How to create CRs using this form:
Comprehensive information and tips about how to create CRs can be found at http://www.3gpp.org/specs/CR.htm. Below is a brief summary:

1) Fill out the above form. The symbols above marked ⌘ contain pop-up help information about the field that they are closest to.

2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under ftp://ftp.3gpp.org/specs/ For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.

3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

8.5.23 Measured results on RACH

When transmitting an uplink RRC message, the UE shall:

1> if the uplink RRC message is an RRC CONNECTION REQUEST message:

2> if the IE "Intra-frequency reporting quantity for RACH reporting" and the IE "Maximum number of reported cells on RACH" are included in System Information Block type 11:

3> include a measurement report in the IE "Measured results on RACH", as specified in the IE "Intra-frequency reporting quantity for RACH reporting" and the IE "Maximum number of reported cells on RACH" in System Information Block type 11.

1> for any other uplink RRC message which optionally includes the IE "Measured results on RACH":

2> if the IE "Intra-frequency reporting quantity for RACH reporting" and the IE "Maximum number of reported cells on RACH" are included in System Information Block type 12 (or "System Information Block Type 11", if these IEs are not included in the broadcast "System Information Block Type 12" or "System Information Block Type 12" is not being broadcast).

3> include a measurement report in the IE "Measured results on RACH", as specified in the IE "Intra-frequency reporting quantity for RACH reporting" and the IE "Maximum number of reported cells on RACH" in System Information Block type 12 (or "System Information Block Type 11" if these IEs are not included in the broadcast "System Information Block Type 12" or "System Information Block Type 12 is not being broadcast).

1> include in the IE "Measured results on RACH" all requested reporting quantities for cells for which measurements are reported.

NOTE: The UE only includes measurement results for neighbour cells for which valid measurements are available at the time the message is sent. At cell access following selection or reselection to a cell, the UE may not have had sufficient time to obtain valid measurement results for neighbour cells.

1> for messages transmitted on CCCH, take care that the maximum allowed message size is not exceeded when forming the IE "Measured results on RACH", i.e. limit the number of included neighbour cells or if required omit the IE "Measured results on RACH" altogether.

If the IE "Measured results on RACH" is present in the message, the UTRAN should extract the contents to be used for radio resource control.

3GPP TSG-RAN2 Meeting #44
Sophia-Antipolis, France, 4-8 October 2004

Tdoc ⌘ R2-042253

CR-Form-v7.1

CHANGE REQUEST

⌘ **25.331** **CR** **2435** ⌘ **rev** **-** ⌘ Current version: **5.a.0** ⌘

*For **HELP** on using this form, see bottom of this page or look at the pop-up text over the ⌘ symbols.*

Proposed change affects: UICC apps⌘ ☐ ME ☒ Radio Access Network ☐ Core Network ☐

Title:	⌘	T305 handling upon a state transition
Source:	⌘	RAN WG2
Work item code:	⌘	TEI5
Date:	⌘	8/10/2004
Category:	⌘	**F**
Release:	⌘	Rel-5

Use one of the following categories:
F *(correction)*
A *(corresponds to a correction in an earlier release)*
B *(addition of feature),*
C *(functional modification of feature)*
D *(editorial modification)*
Detailed explanations of the above categories can be found in 3GPP TR 21.900.

Use one of the following releases:
Ph2 (GSM Phase 2)
R96 (Release 1996)
R97 (Release 1997)
R98 (Release 1998)
R99 (Release 1999)
Rel-4 (Release 4)
Rel-5 (Release 5)
Rel-6 (Release 6)
Rel-7 (Release 7)

Reason for change: ⌘

In the current specification it is not completely clear whether the UE should re-start T305 in case of a state transition from CELL_FACH state to CELL_PCH/URA_PCH state.

In section 13.1 it is indicated that the UE should stop T305 when “entering another state”. Section 8 only mentions the UE stopping of T305 at the initiation of CELL UPDATE/URA UPDATE procedure.

See R2-042030 for more detail.

Summary of change: ⌘

It is proposed to specify that the UE shall stop T305 when it leaves CELL_FACH state.

Implementation of this CR by a R99/Rel-4 UE will not cause compatibility issues.

Consequences if not approved: ⌘

Two possibilites of UE implementation remain in the specification.

Isolated impact analysis:
This CR only affects UE implementation.

Impact on test specifications:
No impact. The test case 8.3.1.4 in 34.123-1 already assumes the UE re-starts T305 upon a state transition from CELL_FACH to CELL_PCH.

Clauses affected: ⌘ 8.2.2.3

		Y	N			
Other specs affected:	⌘		X	Other core specifications	⌘	
			X	Test specifications		
			X	O&M Specifications		
Other comments:	⌘					

How to create CRs using this form:

Comprehensive information and tips about how to create CRs can be found at http://www.3gpp.org/specs/CR.htm. Below is a brief summary:

1) Fill out the above form. The symbols above marked ⌘ contain pop-up help information about the field that they are closest to.

2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under ftp://ftp.3gpp.org/specs/ For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.

3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

8.2.2.3 Reception of RADIO BEARER SETUP or RADIO BEARER RECONFIGURATION or RADIO BEARER RELEASE or TRANSPORT CHANNEL RECONFIGURATION or PHYSICAL CHANNEL RECONFIGURATION message by the UE

The UE shall:

1> be able to receive any of the following messages:

2> RADIO BEARER SETUP message; or

2> RADIO BEARER RECONFIGURATION message; or

2> RADIO BEARER RELEASE message; or

2> TRANSPORT CHANNEL RECONFIGURATION message; or

2> PHYSICAL CHANNEL RECONFIGURATION message;

1> be able to perform a hard handover and apply physical layer synchronisation procedure A as specified in [29], even if no prior UE measurements have been performed on the target cell and/or frequency.

In case the reconfiguration procedure is used to remove all existing RL(s) in the active set while new RL(s) are established the UE shall:

1> if the UE has a pending "TGPS reconfiguration CFN" at the activation time received in the reconfiguration message and the reconfiguration requests a timing re-initialised hard handover (see subclause 8.3.5.1), the UE may:

2> abort the pending CM activation;

2> set the CM_PATTERN_ACTIVATION_ABORTED to TRUE.

1> otherwise:

2> set the CM_PATTERN_ACTIVATION_ABORTED to FALSE.

If the UE receives:

- a RADIO BEARER SETUP message; or
- a RADIO BEARER RECONFIGURATION message; or
- a RADIO BEARER RELEASE message; or
- a TRANSPORT CHANNEL RECONFIGURATION message; or
- a PHYSICAL CHANNEL RECONFIGURATION message:

it shall:

1> set the variable ORDERED_RECONFIGURATION to TRUE;

1> if the UE will enter the CELL_DCH state from any state other than CELL_DCH state at the conclusion of this procedure:

2> perform the physical layer synchronisation procedure A as specified in [29] (FDD only).

1> act upon all received information elements as specified in subclause 8.6, unless specified in the following and perform the actions below.

The UE may:

1> maintain a list of the set of cells to which the UE has Radio Links if the IE "Cell ID" is present.

The UE may first release the physical channel configuration used at reception of the reconfiguration message. The UE shall then:

1> in FDD, if the IE "PDSCH code mapping" is included but the IE "PDSCH with SHO DCH Info" is not included and if the DCH has only one link in its active set:

2> act upon the IE "PDSCH code mapping" as specified in subclause 8.6; and

2> infer that the PDSCH will be transmitted from the cell from which the downlink DPCH is transmitted.

1> enter a state according to subclause 8.6.3.3.

In case the UE receives a RADIO BEARER RECONFIGURATION message including the IE "RB information to reconfigure" that only includes the IE "RB identity", the UE shall:

1> handle the message as if IE "RB information to reconfigure" was absent.

NOTE: The RADIO BEARER RECONFIGURATION message always includes the IE "RB information to reconfigure". UTRAN has to include it even if it does not require the reconfiguration of any RB.

If after state transition the UE enters CELL_DCH state, the UE shall, after the state transition:

1> in FDD; or

1> in TDD when "Primary CCPCH Info" is included indicating a new target cell and "New C-RNTI" is not specified:

2> remove any C-RNTI from MAC;

2> clear the variable C_RNTI.

If after state transition the UE leaves CELL_DCH state, the UE shall, after the state transition:

1> clear any stored IE "Downlink HS-PDSCH information";

1> determine the value for the HS_DSCH_RECEPTION variable and take the corresponding actions as described in subclause 8.5.25.

In FDD, if after state transition the UE leaves CELL_DCH state, the UE shall, after the state transition:

1> remove any DSCH-RNTI from MAC;

1> clear the variable DSCH_RNTI.

If the UE was in CELL_DCH state upon reception of the reconfiguration message and remains in CELL_DCH state, the UE shall:

1> if the IE "Uplink DPCH Info" is absent, not change its current UL Physical channel configuration;

1> in TDD:

2> if "Primary CCPCH Info" is included indicating a new target cell and "New C-RNTI" is not specified:

3> remove any C-RNTI from MAC;

3> clear the variable C_RNTI.

1> if "DPCH frame offset" is included for one or more RLs in the active set:

2> use its value to determine the beginning of the DPCH frame in accordance with the following:

3> if the received IE "DPCH frame offset" is across the value range border compared to the DPCH frame offset currently used by the UE:

4> consider it to be a request to adjust the timing with 256 chips across the frame border (e.g. if the UE receives value 0 while the value currently used is 38144 consider this as a request to adjust the timing with +256 chips).

3> if after taking into account value range borders, the received IE "DPCH frame offset" corresponds to a request to adjust the timing with a step exceeding 256 chips:

4> set the variable INVALID_CONFIGURATION to TRUE.

3> and the procedure ends.

2> adjust the radio link timing accordingly.

If after state transition the UE enters CELL_FACH state, the UE shall, after the state transition:

1> if the IE "Frequency info" is included in the received reconfiguration message:

2> select a suitable UTRA cell according to [4] on that frequency;

2> if the UE finds a suitable UTRA cell on that frequency:

3> if the received reconfiguration message included the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD), and the UE selects another cell than indicated by this IE or the received reconfiguration message did not include the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD):

4> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";

4> when the cell update procedure completed successfully:

5> if the UE is in CELL_PCH or URA_PCH state, initiate a cell update procedure according to subclause 8.3.1 using the cause "Uplink data transmission" and proceed as below.

2> else, if the UE can not find a suitable UTRA cell on that frequency but it finds a suitable UTRA cell on another frequency:

3> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";

3> when the cell update procedure completed successfully:

4> if the UE is in CELL_PCH or URA_PCH state, initiate a cell update procedure according to subclause 8.3.1 using the cause "Uplink data transmission" and proceed as below.

1> if the IE "Frequency info" is not included in the received reconfiguration message:

2> select a suitable UTRA cell according to [4];

2> if the UE finds a suitable UTRA cell on the current frequency:

3> if the received reconfiguration message included the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD), and the UE selects another cell than indicated by this IE or the received reconfiguration message did not include the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD):

4> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";

4> when the cell update procedure completed successfully:

5> if the UE is in CELL_PCH or URA_PCH state, initiate a cell update procedure according to subclause 8.3.1 using the cause "Uplink data transmission" and proceed as below.

2> else, if the UE can not find a suitable UTRA cell on the current frequency but it finds a suitable UTRA cell on another frequency:

3> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";

3> when the cell update procedure completed successfully:

4> if the UE is in CELL_PCH or URA_PCH state, initiate a cell update procedure according to subclause 8.3.1 using the cause "Uplink data transmission" and proceed as below.

1> start timer T305 using its initial value if timer T305 is not running and if periodical update has been configured by T305 in the IE "UE Timers and constants in connected mode" set to any other value than "infinity" in the variable TIMERS_AND_CONSTANTS;

1> select PRACH according to subclause 8.5.17;

1> select Secondary CCPCH according to subclause 8.5.19;

1> use the transport format set given in system information;

1> if the IE "UTRAN DRX cycle length coefficient" is included in the same message:

2> ignore that IE and stop using DRX.

1> if the contents of the variable C_RNTI is empty:

2> perform a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";

2> when the cell update procedure completed successfully:

3> if the UE is in CELL_PCH or URA_PCH state:

4> initiate a cell update procedure according to subclause 8.3.1 using the cause "Uplink data transmission";

4> proceed as below.

If the UE was in CELL_FACH state upon reception of the reconfiguration message and remains in CELL_FACH state, the UE shall:

1> if the IE "Frequency info" is included in the received reconfiguration message:

2> select a suitable UTRA cell according to [4] on that frequency;

2> if the UE finds a suitable UTRA cell on that frequency:

3> if the received reconfiguration message included the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD), and the UE selected another cell than indicated by this IE or the received reconfiguration message did not include the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD):

4> initiate a cell update procedure according to subclause 8.3.1 using the cause "cell reselection";

4> when the cell update procedure completed successfully:

5> if the UE is in CELL_PCH or URA_PCH state, initiate a cell update procedure according to subclause 8.3.1 using the cause "Uplink data transmission" and proceed as below.

2> else, if the UE can not find a suitable UTRA cell on that frequency but it finds a suitable UTRA cell on another frequency:

3> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";

3> when the cell update procedure completed successfully:

4> if the UE is in CELL_PCH or URA_PCH state, initiate a cell update procedure according to subclause 8.3.1 using the cause "Uplink data transmission" and proceed as below.

1> if the IE "Frequency info" is not included in the received reconfiguration message:

2> if the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD) is included the UE shall either:

3> ignore the content of the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD) and proceed as below;

2> or:

3> if the received reconfiguration message included the IE "Primary CPICH info" (for FDD) or "Primary CPCH info" (for TDD), and it is different from the current cell:

4> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";

4> when the cell update procedure completed successfully:

5> if the UE is in CELL_PCH or URA_PCH state, initiate a cell update procedure according to subclause 8.3.1 using the cause "Uplink data transmission" and proceed as below.

If after state transition the UE leaves CELL_FACH state, the UE shall:

1> stop timer T305;

If after state transition the UE enters CELL_PCH or URA_PCH state, the UE shall:

1> if the IE "UTRAN DRX cycle length coefficient" is not included in the same message:

2> set the variable INVALID_CONFIGURATION to TRUE.

The UE shall transmit a response message as specified in subclause 8.2.2.4, setting the information elements as specified below. The UE shall:

1> if the received reconfiguration message included the IE "Downlink counter synchronisation info"; or

1> if the received reconfiguration message is a RADIO BEARER RECONFIGURATION and the IE "New U-RNTI" is included:

2> if the variable PDCP_SN_INFO is empty:

3> configure the corresponding RLC entity for all AM and UM radio bearers and AM and UM signalling radio bearers except RB2 to "stop".

2> else:

3> configure the RLC entity for signalling radio bearers RB1, RB3 and RB4 to "stop";

3> configure the RLC entity for UM and AM radio bearers for which the IE "PDCP SN Info" is not included to "stop".

2> re-establish the RLC entity for RB2;

2> for the downlink and the uplink, apply the ciphering configuration as follows:

3> if the received re-configuation message included the IE "Ciphering Mode Info":

4> use the ciphering configuration in the received message when transmitting the response message.

3> if the ciphering configuration for RB2 from a previously received SECURITY MODE COMMAND has not yet been applied because the activation times not having been reached:

4> if the previous SECURITY MODE COMMAND was received due to new keys being received:

5> consider the new ciphering configuration to include the received new keys;

5> initialise the HFN component of the uplink COUNT-C and downlink COUNT-C of SRB2 as indicated in subclause 8.1.12.3.1.

4> if the ciphering configuration for RB2 from a previously received SECURITY MODE COMMAND has not yet been applied because of the corresponding activation times not having been reached and the previous SECURITY MODE COMMAND caused a change in LATEST_CONFIGURED_CN_DOMAIN:

5> consider the new ciphering configuration to include the keys associated with the LATEST_CONFIGURED_CN_DOMAIN;

5> initialise the HFN component of the uplink COUNT-C and downlink COUNT-C of SRB2 to the most recently transmitted IE "START list" or IE "START" for the

LATEST_CONFIGURED_CN_DOMAIN at the reception of the previous SECURITY MODE COMMAND.

4> apply the new ciphering configuration immediately following RLC re-establishment.

3> else:

4> continue using the current ciphering configuration.

2> set the new uplink and downlink HFN component of COUNT-C of RB2 to MAX(uplink HFN component of COUNT-C of RB2, downlink HFN component of COUNT-C of RB2);

2> increment by one the downlink and uplink values of the HFN of COUNT-C for RB2;

2> calculate the START value according to subclause 8.5.9;

2> include the calculated START values for each CN domain in the IE "START list" in the IE "Uplink counter synchronisation info".

1> if the received reconfiguration message did not include the IE "Downlink counter synchronisation info":

2> if the variable START_VALUE_TO_TRANSMIT is set:

3> include and set the IE "START" to the value of that variable.

2> if the variable START_VALUE_TO_TRANSMIT is not set and the IE "New U-RNTI" is included:

3> calculate the START value according to subclause 8.5.9;

3> include the calculated START values for each CN domain in the IE "START list" in the IE "Uplink counter synchronisation info".

2> if the received reconfiguration message caused a change in the RLC size for any RB using RLC-AM:

3> calculate the START value according to subclause 8.5.9;

3> include the calculated START values for the CN domain associated with the corresponding RB identity in the IE "START list" in the IE "Uplink counter synchronisation info".

1> if the received reconfiguration message contained the IE "Ciphering mode info" or contained the IE "Integrity protection mode info":

2> set the IE "Status" in the variable SECURITY_MODIFICATION for all the CN domains in the variable SECURITY_MODIFICATION to "Affected".

1> if the received reconfiguration message contained the IE "Ciphering mode info":

2> if the reconfiguration message is not used to perform SRNS relocation with change of ciphering algorithm:

3> the UE behaviour is not specified.

2> if the message is used to perform a timing re-initialised hard handover:

3> if IE "Ciphering activation time for DPCH" is included:

4> the UE behaviour is not specified.

2> else:

3> if the reconfiguration message is used to setup radio bearer(s) using RLC-TM; or

3> if radio bearer(s) using RLC-TM already exist:

4> if IE "Ciphering activation time for DPCH" is not included:

5> the UE behaviour is not specified.

2> include and set the IE "Radio bearer uplink ciphering activation time info" to the value of the variable RB_UPLINK_CIPHERING_ACTIVATION_TIME_INFO.

1> if the received reconfiguration message did not contain the IE "Ciphering activation time for DPCH" in IE "Ciphering mode info":

2> if prior to this procedure there exist no transparent mode RLC radio bearers:

3> if, at the conclusion of this procedure, the UE will be in CELL_DCH state; and

3> if, at the conclusion of this procedure, at least one transparent mode RLC radio bearer exists:

4> include the IE "COUNT-C activation time" and specify a CFN value for this IE that is a multiple of 8 frames (CFN mod 8 = 0) and lies at least 200 frames ahead of the CFN in which the response message is first transmitted.

NOTE: UTRAN should not include the IE "Ciphering mode info" in any reconfiguration message unless it is also used to perform an SRNS relocation with change of ciphering algorithm.

1> set the IE "RRC transaction identifier" to the value of "RRC transaction identifier" in the entry for the received message in the table "Accepted transactions" in the variable TRANSACTIONS; and

1> clear that entry;

1> if the variable PDCP_SN_INFO is not empty:

2> include the IE "RB with PDCP information list" and set it to the value of the variable PDCP_SN_INFO.

1> in TDD, if the procedure is used to perform a handover to a cell where timing advance is enabled, and the UE can calculate the timing advance value in the new cell (i.e. in a synchronous TDD network):

2> set the IE "Uplink Timing Advance" according to subclause 8.6.6.26.

1> if the IE "Integrity protection mode info" was present in the received reconfiguration message:

2> start applying the new integrity protection configuration in the uplink for signalling radio bearer RB2 from and including the transmitted response message.

If after state transition the UE enters URA_PCH state, the UE shall, after the state transition and transmission of the response message:

1> if the IE "Frequency info" is included in the received reconfiguration message:

2> select a suitable UTRA cell according to [4] on that frequency.

2> if the UE can not find a suitable UTRA cell on that frequency but it finds a suitable UTRA cell on another frequency:

3> proceed as below.

1> if the IE "Frequency info" is not included in the received reconfiguration message:

2> select a suitable UTRA cell according to [4].

1> prohibit periodical status transmission in RLC;

1> remove any C-RNTI from MAC;

1> clear the variable C_RNTI;

1> start timer T305 using its initial value if timer T305 is not running and if periodical update has been configured by T305 in the IE "UE Timers and constants in connected mode" set to any other value than "infinity" in the variable TIMERS_AND_CONSTANTS;

1> select Secondary CCPCH according to subclause 8.5.19;

1> if the IE "UTRAN DRX cycle length coefficient" is included in the same message:

2> use the value in the IE "UTRAN DRX Cycle length coefficient" for calculating Paging occasion and PICH Monitoring Occasion as specified in subclause 8.6.3.2.

1> if the criteria for URA update caused by "URA reselection" according to subclause 8.3.1 are fulfilled after cell selection:

2> initiate a URA update procedure according to subclause 8.3.1 using the cause "URA reselection";

2> when the URA update procedure is successfully completed:

3> the procedure ends.

If after state transition the UE enters CELL_PCH state from CELL_DCH state, the UE shall, after the state transition and transmission of the response message:

1> if the IE "Frequency info" is included in the received reconfiguration message:

2> select a suitable UTRA cell according to [4] on that frequency.

2> if the UE finds a suitable UTRA cell on that frequency:

3> if the received reconfiguration message included the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD), and the UE selects another cell than indicated by this IE or the received reconfiguration message did not include the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD):

4> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";

4> proceed as below.

2> else, if the UE can not find a suitable UTRA cell on that frequency but it finds a suitable UTRA cell on another frequency:

3> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";

3> proceed as below.

1> if the IE "Frequency info" is not included in the received reconfiguration message:

2> select a suitable UTRA cell according to [4].

2> if the UE finds a suitable UTRA cell on the current frequency:

3> if the received reconfiguration message included the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD), and the UE selects another cell than indicated by this IE or the received reconfiguration message did not include the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD):

4> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";

4> proceed as below.

2> else, if the UE can not find a suitable UTRA cell on the current frequency but it finds a suitable UTRA cell on another frequency:

3> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";

3> proceed as below.

1> prohibit periodical status transmission in RLC;

1> remove any C-RNTI from MAC;

1> clear the variable C_RNTI;

1> start timer T305 using its initial value if timer T305 is not running and if periodical update has been configured by T305 in the IE "UE Timers and constants in connected mode" set to any other value than "infinity" in the variable TIMERS_AND_CONSTANTS;

1> select Secondary CCPCH according to subclause 8.5.19;

1> if the IE "UTRAN DRX cycle length coefficient" is included in the same message:

2> use the value in the IE "UTRAN DRX Cycle length coefficient" for calculating Paging occasion and PICH Monitoring Occasion as specified in subclause 8.6.3.2.

1> the procedure ends.

If after state transition the UE enters CELL_PCH state from CELL_FACH state, the UE shall, after the state transition and transmission of the response message:

1> if the IE "Frequency info" is included in the received reconfiguration message:

2> select a suitable UTRA cell according to [4] on that frequency.

2> if the UE finds a suitable UTRA cell on that frequency:

3> if the received reconfiguration message included the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD), and the UE selected another cell than indicated by this IE or the received reconfiguration message did not include the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD):

4> initiate a cell update procedure according to subclause 8.3.1 using the cause "cell reselection";

4> proceed as below.

2> else, if the UE can not find a suitable UTRA cell on that frequency but it finds a suitable UTRA cell on another frequency:

3> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";

3> proceed as below.

1> if the IE "Frequency info" is not included in the received reconfiguration message:

2> if the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD) is included the UE shall either:

3> ignore the content of the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD) and proceed as below;

2> or:

3> if the received reconfiguration message included the IE "Primary CPICH info" (for FDD) or "Primary CPCH info" (for TDD), and it is different from the current cell:

4> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";

4> proceed as below.

1> prohibit periodical status transmission in RLC;

1> remove any C-RNTI from MAC;

1> clear the variable C_RNTI;

1> start timer T305 using its initial value if timer T305 is not running and if periodical update has been configured by T305 in the IE "UE Timers and constants in connected mode" set to any other value than "infinity" in the variable TIMERS_AND_CONSTANTS;

1> select Secondary CCPCH according to subclause 8.5.19;

1> if the IE "UTRAN DRX cycle length coefficient" is included in the same message:

2> use the value in the IE "UTRAN DRX Cycle length coefficient" for calculating Paging occasion and PICH Monitoring Occasion as specified in subclause 8.6.3.2.

1> the procedure ends.

3GPP TSG-RAN2 Meeting #44
Sophia-Antipolis, France, 4-8 October 2004

Tdoc ⌘ R2-042254

CR-Form-v7.1

CHANGE REQUEST

⌘ **25.331** **CR** **2436** ⌘ **rev** **-** ⌘ Current version: **6.3.0** ⌘

*For **HELP** on using this form, see bottom of this page or look at the pop-up text over the ⌘ symbols.*

Proposed change affects: UICC apps⌘ ☐ ME ☒ Radio Access Network ☐ Core Network ☐

Title:	⌘	T305 handling upon a state transition
Source:	⌘	RAN WG2
Work item code:	⌘	TEI5
Date:	⌘	8/10/2004
Category:	⌘	**A**
Release:	⌘	Rel-6

Use one of the following categories:
F *(correction)*
A *(corresponds to a correction in an earlier release)*
B *(addition of feature),*
C *(functional modification of feature)*
D *(editorial modification)*
Detailed explanations of the above categories can be found in 3GPP TR 21.900.

Use one of the following releases:
Ph2 (GSM Phase 2)
R96 (Release 1996)
R97 (Release 1997)
R98 (Release 1998)
R99 (Release 1999)
Rel-4 (Release 4)
Rel-5 (Release 5)
Rel-6 (Release 6)
Rel-7 (Release 7)

Reason for change: ⌘

In the current specification it is not completely clear whether the UE should re-start T305 in case of a state transition from CELL_FACH state to CELL_PCH/URA_PCH state.

In section 13.1 it is indicated that the UE should stop T305 when “entering another state”. Section 8 only mentions the UE stopping of T305 at the initiation of CELL UPDATE/URA UPDATE procedure.

See R2-042030 for more detail.

Summary of change: ⌘

It is proposed to specify that the UE shall stop T305 when it leaves CELL_FACH state.

Consequences if not approved: ⌘

Two possibilites of UE implementation remain in the specification.

Isolated impact analysis:
This CR only affects UE implementation.

Impact on test specifications:
No impact. The test case 8.3.1.4 in 34.123-1 already assumes the UE re-starts T305 upon a state transition from CELL_FACH to CELL_PCH.

Clauses affected: ⌘ 8.2.2.3

Y | N

Other specs affected:	⌘		**X**	Other core specifications	⌘
			X	Test specifications	
			X	O&M Specifications	
Other comments:	⌘				

How to create CRs using this form:

Comprehensive information and tips about how to create CRs can be found at http://www.3gpp.org/specs/CR.htm. Below is a brief summary:

1) Fill out the above form. The symbols above marked ⌘ contain pop-up help information about the field that they are closest to.

2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under ftp://ftp.3gpp.org/specs/ For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.

3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

8.2.2.3 Reception of RADIO BEARER SETUP or RADIO BEARER RECONFIGURATION or RADIO BEARER RELEASE or TRANSPORT CHANNEL RECONFIGURATION or PHYSICAL CHANNEL RECONFIGURATION message by the UE

The UE shall:

1> be able to receive any of the following messages:

2> RADIO BEARER SETUP message; or

2> RADIO BEARER RECONFIGURATION message; or

2> RADIO BEARER RELEASE message; or

2> TRANSPORT CHANNEL RECONFIGURATION message; or

2> PHYSICAL CHANNEL RECONFIGURATION message;

1> be able to perform a hard handover and apply physical layer synchronisation procedure A as specified in [29], even if no prior UE measurements have been performed on the target cell and/or frequency.

In case the reconfiguration procedure is used to remove all existing RL(s) in the active set while new RL(s) are established the UE shall:

1> if the UE has a pending "TGPS reconfiguration CFN" at the activation time received in the reconfiguration message and the reconfiguration requests a timing re-initialised hard handover (see subclause 8.3.5.1), the UE may:

2> abort the pending CM activation;

2> set the CM_PATTERN_ACTIVATION_ABORTED to TRUE.

1> otherwise:

2> set the CM_PATTERN_ACTIVATION_ABORTED to FALSE.

If the UE receives:

- a RADIO BEARER SETUP message; or
- a RADIO BEARER RECONFIGURATION message; or
- a RADIO BEARER RELEASE message; or
- a TRANSPORT CHANNEL RECONFIGURATION message; or
- a PHYSICAL CHANNEL RECONFIGURATION message:

it shall:

1> set the variable ORDERED_RECONFIGURATION to TRUE;

1> if the UE will enter the CELL_DCH state from any state other than CELL_DCH state at the conclusion of this procedure:

2> perform the physical layer synchronisation procedure A as specified in [29] (FDD only).

1> act upon all received information elements as specified in subclause 8.6, unless specified in the following and perform the actions below.

The UE may:

1> maintain a list of the set of cells to which the UE has Radio Links if the IE "Cell ID" is present.

The UE may first release the physical channel configuration used at reception of the reconfiguration message. The UE shall then:

1> in FDD, if the IE "PDSCH code mapping" is included but the IE "PDSCH with SHO DCH Info" is not included and if the DCH has only one link in its active set:

2> act upon the IE "PDSCH code mapping" as specified in subclause 8.6; and

2> infer that the PDSCH will be transmitted from the cell from which the downlink DPCH is transmitted.

1> enter a state according to subclause 8.6.3.3.

In case the UE receives a RADIO BEARER RECONFIGURATION message including the IE "RB information to reconfigure" that only includes the IE "RB identity", the UE shall:

1> handle the message as if IE "RB information to reconfigure" was absent.

NOTE: The RADIO BEARER RECONFIGURATION message always includes the IE "RB information to reconfigure". UTRAN has to include it even if it does not require the reconfiguration of any RB.

If after state transition the UE enters CELL_DCH state, the UE shall, after the state transition:

1> in FDD; or

1> in TDD when "Primary CCPCH Info" is included indicating a new target cell and "New C-RNTI" is not specified:

2> remove any C-RNTI from MAC;

2> clear the variable C_RNTI.

If after state transition the UE leaves CELL_DCH state, the UE shall, after the state transition:

1> clear any stored IE "Downlink HS-PDSCH information";

1> determine the value for the HS_DSCH_RECEPTION variable and take the corresponding actions as described in subclause 8.5.25.

In FDD, if after state transition the UE leaves CELL_DCH state, the UE shall, after the state transition:

1> remove any DSCH-RNTI from MAC;

1> clear the variable DSCH_RNTI.

If the UE was in CELL_DCH state upon reception of the reconfiguration message and remains in CELL_DCH state, the UE shall:

1> if the IE "Uplink DPCH Info" is absent, not change its current UL Physical channel configuration;

1> in TDD:

2> if "Primary CCPCH Info" is included indicating a new target cell and "New C-RNTI" is not specified:

3> remove any C-RNTI from MAC;

3> clear the variable C_RNTI.

1> if "DPCH frame offset" is included for one or more RLs in the active set:

2> use its value to determine the beginning of the DPCH frame in accordance with the following:

3> if the received IE "DPCH frame offset" is across the value range border compared to the DPCH frame offset currently used by the UE:

4> consider it to be a request to adjust the timing with 256 chips across the frame border (e.g. if the UE receives value 0 while the value currently used is 38144 consider this as a request to adjust the timing with +256 chips).

3> if after taking into account value range borders, the received IE "DPCH frame offset" corresponds to a request to adjust the timing with a step exceeding 256 chips:

4> set the variable INVALID_CONFIGURATION to TRUE.

3> and the procedure ends.

2> adjust the radio link timing accordingly.

If after state transition the UE enters CELL_FACH state, the UE shall, after the state transition:

1> if the IE "Frequency info" is included in the received reconfiguration message:

2> select a suitable UTRA cell according to [4] on that frequency;

2> if the UE finds a suitable UTRA cell on that frequency:

3> if the received reconfiguration message included the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD), and the UE selects another cell than indicated by this IE or the received reconfiguration message did not include the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD):

4> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";

4> when the cell update procedure completed successfully:

5> if the UE is in CELL_PCH or URA_PCH state, initiate a cell update procedure according to subclause 8.3.1 using the cause "Uplink data transmission" and proceed as below.

2> else, if the UE can not find a suitable UTRA cell on that frequency but it finds a suitable UTRA cell on another frequency:

3> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";

3> when the cell update procedure completed successfully:

4> if the UE is in CELL_PCH or URA_PCH state, initiate a cell update procedure according to subclause 8.3.1 using the cause "Uplink data transmission" and proceed as below.

1> if the IE "Frequency info" is not included in the received reconfiguration message:

2> select a suitable UTRA cell according to [4];

2> if the UE finds a suitable UTRA cell on the current frequency:

3> if the received reconfiguration message included the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD), and the UE selects another cell than indicated by this IE or the received reconfiguration message did not include the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD):

4> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";

4> when the cell update procedure completed successfully:

5> if the UE is in CELL_PCH or URA_PCH state, initiate a cell update procedure according to subclause 8.3.1 using the cause "Uplink data transmission" and proceed as below.

2> else, if the UE can not find a suitable UTRA cell on the current frequency but it finds a suitable UTRA cell on another frequency:

3> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";

3> when the cell update procedure completed successfully:

4> if the UE is in CELL_PCH or URA_PCH state, initiate a cell update procedure according to subclause 8.3.1 using the cause "Uplink data transmission" and proceed as below.

1> start timer T305 using its initial value if timer T305 is not running and if periodical update has been configured by T305 in the IE "UE Timers and constants in connected mode" set to any other value than "infinity" in the variable TIMERS_AND_CONSTANTS;

1> select PRACH according to subclause 8.5.17;

1> select Secondary CCPCH according to subclause 8.5.19;

1> use the transport format set given in system information;

1> if the IE "UTRAN DRX cycle length coefficient" is included in the same message:

2> ignore that IE and stop using DRX.

1> if the contents of the variable C_RNTI is empty:

2> perform a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";

2> when the cell update procedure completed successfully:

3> if the UE is in CELL_PCH or URA_PCH state:

4> initiate a cell update procedure according to subclause 8.3.1 using the cause "Uplink data transmission";

4> proceed as below.

If the UE was in CELL_FACH state upon reception of the reconfiguration message and remains in CELL_FACH state, the UE shall:

1> if the IE "Frequency info" is included in the received reconfiguration message:

2> select a suitable UTRA cell according to [4] on that frequency;

2> if the UE finds a suitable UTRA cell on that frequency:

3> if the received reconfiguration message included the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD), and the UE selected another cell than indicated by this IE or the received reconfiguration message did not include the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD):

4> initiate a cell update procedure according to subclause 8.3.1 using the cause "cell reselection";

4> when the cell update procedure completed successfully:

5> if the UE is in CELL_PCH or URA_PCH state, initiate a cell update procedure according to subclause 8.3.1 using the cause "Uplink data transmission" and proceed as below.

2> else, if the UE can not find a suitable UTRA cell on that frequency but it finds a suitable UTRA cell on another frequency:

3> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";

3> when the cell update procedure completed successfully:

4> if the UE is in CELL_PCH or URA_PCH state, initiate a cell update procedure according to subclause 8.3.1 using the cause "Uplink data transmission" and proceed as below.

1> if the IE "Frequency info" is not included in the received reconfiguration message:

2> if the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD) is included the UE shall either:

3> ignore the content of the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD) and proceed as below;

2> or:

3> if the received reconfiguration message included the IE "Primary CPICH info" (for FDD) or "Primary CPCH info" (for TDD), and it is different from the current cell:

4> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";

4> when the cell update procedure completed successfully:

5> if the UE is in CELL_PCH or URA_PCH state, initiate a cell update procedure according to subclause 8.3.1 using the cause "Uplink data transmission" and proceed as below.

If after state transition the UE leaves CELL_FACH state, the UE shall:

1> stop timer T305;

If after state transition the UE enters CELL_PCH or URA_PCH state, the UE shall:

1> if the IE "UTRAN DRX cycle length coefficient" is not included in the same message:

2> set the variable INVALID_CONFIGURATION to TRUE.

The UE shall transmit a response message as specified in subclause 8.2.2.4, setting the information elements as specified below. The UE shall:

1> if the received reconfiguration message included the IE "Downlink counter synchronisation info"; or

1> if the received reconfiguration message is a RADIO BEARER RECONFIGURATION and the IE "New U-RNTI" is included:

2> if the variable PDCP_SN_INFO is empty:

3> configure the corresponding RLC entity for all AM and UM radio bearers and AM and UM signalling radio bearers except RB2 to "stop".

2> else:

3> configure the RLC entity for signalling radio bearers RB1, RB3 and RB4 to "stop";

3> configure the RLC entity for UM and AM radio bearers for which the IE "PDCP SN Info" is not included to "stop".

2> re-establish the RLC entity for RB2;

2> for the downlink and the uplink, apply the ciphering configuration as follows:

3> if the received re-configuation message included the IE "Ciphering Mode Info":

4> use the ciphering configuration in the received message when transmitting the response message.

3> if the ciphering configuration for RB2 from a previously received SECURITY MODE COMMAND has not yet been applied because the activation times not having been reached:

4> if the previous SECURITY MODE COMMAND was received due to new keys being received:

5> consider the new ciphering configuration to include the received new keys;

5> initialise the HFN component of the uplink COUNT-C and downlink COUNT-C of SRB2 as indicated in subclause 8.1.12.3.1.

4> if the ciphering configuration for RB2 from a previously received SECURITY MODE COMMAND has not yet been applied because of the corresponding activation times not having been reached and the previous SECURITY MODE COMMAND caused a change in LATEST_CONFIGURED_CN_DOMAIN:

5> consider the new ciphering configuration to include the keys associated with the LATEST_CONFIGURED_CN_DOMAIN;

5> initialise the HFN component of the uplink COUNT-C and downlink COUNT-C of SRB2 to the most recently transmitted IE "START list" or IE "START" for the

LATEST_CONFIGURED_CN_DOMAIN at the reception of the previous SECURITY MODE COMMAND.

4> apply the new ciphering configuration immediately following RLC re-establishment.

3> else:

4> continue using the current ciphering configuration.

2> set the new uplink and downlink HFN component of COUNT-C of RB2 to MAX(uplink HFN component of COUNT-C of RB2, downlink HFN component of COUNT-C of RB2);

2> increment by one the downlink and uplink values of the HFN of COUNT-C for RB2;

2> calculate the START value according to subclause 8.5.9;

2> include the calculated START values for each CN domain in the IE "START list" in the IE "Uplink counter synchronisation info".

1> if the received reconfiguration message did not include the IE "Downlink counter synchronisation info":

2> if the variable START_VALUE_TO_TRANSMIT is set:

3> include and set the IE "START" to the value of that variable.

2> if the variable START_VALUE_TO_TRANSMIT is not set and the IE "New U-RNTI" is included:

3> calculate the START value according to subclause 8.5.9;

3> include the calculated START values for each CN domain in the IE "START list" in the IE "Uplink counter synchronisation info".

2> if the received reconfiguration message caused a change in the RLC size for any RB using RLC-AM:

3> calculate the START value according to subclause 8.5.9;

3> include the calculated START values for the CN domain associated with the corresponding RB identity in the IE "START list" in the IE "Uplink counter synchronisation info".

1> if the received reconfiguration message contained the IE "Ciphering mode info" or contained the IE "Integrity protection mode info":

2> set the IE "Status" in the variable SECURITY_MODIFICATION for all the CN domains in the variable SECURITY_MODIFICATION to "Affected".

1> if the received reconfiguration message contained the IE "Ciphering mode info":

2> if the reconfiguration message is not used to perform SRNS relocation with change of ciphering algorithm:

3> the UE behaviour is not specified.

2> if the message is used to perform a timing re-initialised hard handover:

3> if IE "Ciphering activation time for DPCH" is included:

4> the UE behaviour is not specified.

2> else:

3> if the reconfiguration message is used to setup radio bearer(s) using RLC-TM; or

3> if radio bearer(s) using RLC-TM already exist:

4> if IE "Ciphering activation time for DPCH" is not included:

5> the UE behaviour is not specified.

2> include and set the IE "Radio bearer uplink ciphering activation time info" to the value of the variable RB_UPLINK_CIPHERING_ACTIVATION_TIME_INFO.

1> if the received reconfiguration message did not contain the IE "Ciphering activation time for DPCH" in IE "Ciphering mode info":

2> if prior to this procedure there exist no transparent mode RLC radio bearers:

3> if, at the conclusion of this procedure, the UE will be in CELL_DCH state; and

3> if, at the conclusion of this procedure, at least one transparent mode RLC radio bearer exists:

4> include the IE "COUNT-C activation time" and specify a CFN value for this IE that is a multiple of 8 frames (CFN mod 8 = 0) and lies at least 200 frames ahead of the CFN in which the response message is first transmitted.

NOTE: UTRAN should not include the IE "Ciphering mode info" in any reconfiguration message unless it is also used to perform an SRNS relocation with change of ciphering algorithm.

1> set the IE "RRC transaction identifier" to the value of "RRC transaction identifier" in the entry for the received message in the table "Accepted transactions" in the variable TRANSACTIONS; and

1> clear that entry;

1> if the variable PDCP_SN_INFO is not empty:

2> include the IE "RB with PDCP information list" and set it to the value of the variable PDCP_SN_INFO.

1> in TDD, if the procedure is used to perform a handover to a cell where timing advance is enabled, and the UE can calculate the timing advance value in the new cell (i.e. in a synchronous TDD network):

2> set the IE "Uplink Timing Advance" according to subclause 8.6.6.26.

1> if the IE "Integrity protection mode info" was present in the received reconfiguration message:

2> start applying the new integrity protection configuration in the uplink for signalling radio bearer RB2 from and including the transmitted response message.

If after state transition the UE enters URA_PCH state, the UE shall, after the state transition and transmission of the response message:

1> if the IE "Frequency info" is included in the received reconfiguration message:

2> select a suitable UTRA cell according to [4] on that frequency.

2> if the UE can not find a suitable UTRA cell on that frequency but it finds a suitable UTRA cell on another frequency:

3> proceed as below.

1> if the IE "Frequency info" is not included in the received reconfiguration message:

2> select a suitable UTRA cell according to [4].

1> prohibit periodical status transmission in RLC;

1> remove any C-RNTI from MAC;

1> clear the variable C_RNTI;

1> start timer T305 using its initial value if timer T305 is not running and if periodical update has been configured by T305 in the IE "UE Timers and constants in connected mode" set to any other value than "infinity" in the variable TIMERS_AND_CONSTANTS;

1> select Secondary CCPCH according to subclause 8.5.19;

1> if the IE "UTRAN DRX cycle length coefficient" is included in the same message:

2> use the value in the IE "UTRAN DRX Cycle length coefficient" for calculating Paging occasion and PICH Monitoring Occasion as specified in subclause 8.6.3.2.

1> if the criteria for URA update caused by "URA reselection" according to subclause 8.3.1 are fulfilled after cell selection:

2> initiate a URA update procedure according to subclause 8.3.1 using the cause "URA reselection";

2> when the URA update procedure is successfully completed:

3> the procedure ends.

If after state transition the UE enters CELL_PCH state from CELL_DCH state, the UE shall, after the state transition and transmission of the response message:

1> if the IE "Frequency info" is included in the received reconfiguration message:

2> select a suitable UTRA cell according to [4] on that frequency.

2> if the UE finds a suitable UTRA cell on that frequency:

3> if the received reconfiguration message included the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD), and the UE selects another cell than indicated by this IE or the received reconfiguration message did not include the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD):

4> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";

4> proceed as below.

2> else, if the UE can not find a suitable UTRA cell on that frequency but it finds a suitable UTRA cell on another frequency:

3> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";

3> proceed as below.

1> if the IE "Frequency info" is not included in the received reconfiguration message:

2> select a suitable UTRA cell according to [4].

2> if the UE finds a suitable UTRA cell on the current frequency:

3> if the received reconfiguration message included the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD), and the UE selects another cell than indicated by this IE or the received reconfiguration message did not include the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD):

4> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";

4> proceed as below.

2> else, if the UE can not find a suitable UTRA cell on the current frequency but it finds a suitable UTRA cell on another frequency:

3> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";

3> proceed as below.

1> prohibit periodical status transmission in RLC;

1> remove any C-RNTI from MAC;

1> clear the variable C_RNTI;

1> start timer T305 using its initial value if timer T305 is not running and if periodical update has been configured by T305 in the IE "UE Timers and constants in connected mode" set to any other value than "infinity" in the variable TIMERS_AND_CONSTANTS;

1> select Secondary CCPCH according to subclause 8.5.19;

1> if the IE "UTRAN DRX cycle length coefficient" is included in the same message:

2> use the value in the IE "UTRAN DRX Cycle length coefficient" for calculating Paging occasion and PICH Monitoring Occasion as specified in subclause 8.6.3.2.

1> the procedure ends.

If after state transition the UE enters CELL_PCH state from CELL_FACH state, the UE shall, after the state transition and transmission of the response message:

1> if the IE "Frequency info" is included in the received reconfiguration message:

2> select a suitable UTRA cell according to [4] on that frequency.

2> if the UE finds a suitable UTRA cell on that frequency:

3> if the received reconfiguration message included the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD), and the UE selected another cell than indicated by this IE or the received reconfiguration message did not include the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD):

4> initiate a cell update procedure according to subclause 8.3.1 using the cause "cell reselection";

4> proceed as below.

2> else, if the UE can not find a suitable UTRA cell on that frequency but it finds a suitable UTRA cell on another frequency:

3> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";

3> proceed as below.

1> if the IE "Frequency info" is not included in the received reconfiguration message:

2> if the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD) is included the UE shall either:

3> ignore the content of the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD) and proceed as below;

2> or:

3> if the received reconfiguration message included the IE "Primary CPICH info" (for FDD) or "Primary CPCH info" (for TDD), and it is different from the current cell:

4> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";

4> proceed as below.

1> prohibit periodical status transmission in RLC;

1> remove any C-RNTI from MAC;

1> clear the variable C_RNTI;

1> start timer T305 using its initial value if timer T305 is not running and if periodical update has been configured by T305 in the IE "UE Timers and constants in connected mode" set to any other value than "infinity" in the variable TIMERS_AND_CONSTANTS;

1> select Secondary CCPCH according to subclause 8.5.19;

1> if the IE "UTRAN DRX cycle length coefficient" is included in the same message:

2> use the value in the IE "UTRAN DRX Cycle length coefficient" for calculating Paging occasion and PICH Monitoring Occasion as specified in subclause 8.6.3.2.

1> the procedure ends.

3GPP TSG-RAN2 #44 **Tdoc ⌘R2-042268**
Sophia Antipolis, France, 4-8 October, 2004

CR-Form-v7

CHANGE REQUEST

⌘ **25.331** **CR** **2437** ⌘ **rev** **-** ⌘ Current version: **5.10.0** ⌘

*For **HELP** on using this form, see bottom of this page or look at the pop-up text over the ⌘ symbols.*

Proposed change affects: UICC apps⌘ ☐ ME ☒ Radio Access Network ☐ Core Network ☐

Title:	⌘	Handling of pending AM RLC unrecoverable errors signalled by cell update
Source:	⌘	RAN WG2
Work item code:	⌘	TEI5
Date:	⌘	October 7, 2004
Category:	⌘	**F**
Release:	⌘	Rel-5

Use one of the following categories:
F *(correction)*
A *(corresponds to a correction in an earlier release)*
B *(addition of feature),*
C *(functional modification of feature)*
D *(editorial modification)*
Detailed explanations of the above categories can be found in 3GPP TR 21.900.

Use one of the following releases:
2 *(GSM Phase 2)*
R96 *(Release 1996)*
R97 *(Release 1997)*
R98 *(Release 1998)*
R99 *(Release 1999)*
Rel-4 *(Release 4)*
Rel-5 *(Release 5)*
Rel-6 *(Release 6)*

Reason for change:	⌘	If the first CELL UPDATE message indicating an AM RLC unrecoverable error is lost, the UE should continue indicating the error in a subsequent CELL UPDATE message. This behaviour is not clearly specified in the current version of the specification.
Summary of change:	⌘	New RRC variables AM_RLC_ERROR_PENDING_RB234 and AM_RLC_ERROR_PENDING_RB5_AND_UP created to indicate whether an unrecoverable AM RLC error should be signalled in future CELL UPDATE messages. CELL UPDATE message contents updated to include error indication if these variables are set to TRUE. Added requirement for UE to set the new variables to FALSE on reception of a CELL UPDATE CONFIRM message.
Consequences if not approved:	⌘	Connection could be lost due to UE failing to notify UTRAN of the RLC error.

Clauses affected:	⌘	8.3.1.3, 8.3.1.6, 13.4.new1, 13.4.new2

Other specs affected: ⌘	Y	N	
		X	Other core specifications ⌘
		X	Test specifications
		X	O&M Specifications

Other comments:	⌘	Revision of (part of) R2-042037.

How to create CRs using this form:
Comprehensive information and tips about how to create CRs can be found at http://www.3gpp.org/specs/CR.htm. Below is a brief summary:

1) Fill out the above form. The symbols above marked ⌘ contain pop-up help information about the field that they are closest to.

2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under ftp://ftp.3gpp.org/specs/ For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.

3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

8.3.1.3 CELL UPDATE / URA UPDATE message contents to set

In case of cell update procedure the UE shall transmit a CELL UPDATE message.

In case of URA update procedure the UE shall transmit a URA UPDATE message.

The UE shall set the IEs in the CELL UPDATE message as follows:

1> set the IE "Cell update cause" corresponding to the cause specified in subclause 8.3.1.2 that is valid when the CELL UPDATE message is submitted to lower layers for transmission;

NOTE: During the time period starting from when a cell update procedure is initiated by the UE until when the procedure ends, additional CELL UPDATE messages may be transmitted by the UE with different causes.

1> set the IE "U-RNTI" to the value of the variable U_RNTI;

1> if the value of the variable PROTOCOL_ERROR_INDICATOR is TRUE:

2> include the IE "RRC transaction identifier"; and

3> set it to the value of "RRC transaction identifier" in the entry for the CELL UPDATE CONFIRM message in the table "Rejected transactions" in the variable TRANSACTIONS.

2> include and set the IE "failure cause" to the cause value "protocol error";

2> set the IE "Protocol error information" set to the value of the variable PROTOCOL_ERROR_INFORMATION.

1> if the value of the variable FAILURE_INDICATOR is TRUE:

2> include the IE "RRC transaction identifier"; and

3> set it to the value of "RRC transaction identifier" in the entry for the CELL UPDATE CONFIRM message in the table "Accepted transactions" in the variable TRANSACTIONS.

2> include and set the IE "failure cause" to the value of the variable FAILURE_CAUSE.

1> include the START values for each CN domain, calculated according to subclause 8.5.9;

1> if an unrecoverable error [16] in any of the AM RLC entities for the signalling radio bearers RB2, RB3 or RB4 is detected:

2> set the IE "AM_RLC error indication (RB2, RB3 or RB4)" to TRUE;~~.~~

2> set the variable AM_RLC_ERROR_PENDING_RB234 to TRUE.

1> otherwise:

2> if the value of the variable AM_RLC_ERROR_PENDING_RB234 is TRUE:

3> set the IE “AM_RLC error indication (RB2, RB3 or RB4)” to TRUE.

2> otherwise:

~~2~~3> set the IE "AM_RLC error indication (RB2, RB3 or RB4)" to FALSE.

1> if an unrecoverable error [16] in any of the AM RLC entities for the RB5 or upward is detected:

2> set the IE "AM_RLC error indication (RB>4)" to TRUE;~~.~~

2> set the variable AM_RLC_ERROR_PENDING_RB5_AND_UP to TRUE.

1> otherwise:

2> if the value of the variable AM_RLC_ERROR_PENDING_RB5_AND_UP is TRUE:

3> set the IE “AM_RLC error indication (RB>4)” to TRUE.

2> otherwise:

3~~2~~> set the IE "AM_RLC error indication (RB>4)" to FALSE.

1> set the IE "RB Timer indicator" to the value of the variable RB_TIMER_INDICATOR;

1> if the variable ESTABLISHMENT_CAUSE is initialised:

2> include the IE "Establishment cause" and set it to the value of the variable ESTABLISHMENT_CAUSE.

The UE shall set the IEs in the URA UPDATE message as follows:

1> set the IE "U-RNTI" to the value of the variable U_RNTI;

1> set the IE "URA update cause" corresponding to which cause as specified in subclause 8.3.1.2 that is valid when the URA UPDATE message is submitted to lower layers for transmission;

NOTE: During the time period starting from when a URA update procedure is initiated by the UE until when the procedure ends, additional URA UPDATE messages may be transmitted by the UE with different causes, depending on which causes are valid for the respective URA UPDATE message.

2> if the value of the variable PROTOCOL_ERROR_INDICATOR is TRUE:

3> include the IE "RRC transaction identifier"; and

4> set it to the value of "RRC transaction identifier" in the entry for the URA UPDATE CONFIRM message in the table "Rejected transactions" in the variable TRANSACTIONS;

3> set the IE "Protocol error indicator" to TRUE;

3> include the IE "Protocol error information" set to the value of the variable PROTOCOL_ERROR_INFORMATION.

2> if the value of the variable PROTOCOL_ERROR_INDICATOR is FALSE:

3> if the value of the variable INVALID_CONFIGURATION is TRUE:

4> include the IE "RRC transaction identifier"; and

4> set it to the value of "RRC transaction identifier" in the entry for the URA UPDATE CONFIRM message in the table "Accepted transactions" in the variable TRANSACTIONS;

4> set the IE "Protocol error indicator" to TRUE;

4> include the IE "Protocol error information" set to "Information element value not comprehended";

3> if the value of the variable INVALID_CONFIGURATION is FALSE:

4> set the IE "Protocol error indicator" to FALSE.

[…]

8.3.1.6 Reception of the CELL UPDATE CONFIRM/URA UPDATE CONFIRM message by the UE

When the UE receives a CELL UPDATE CONFIRM/URA UPDATE CONFIRM message; and

- if the message is received on the CCCH, and IE "U-RNTI" is present and has the same value as the variable U_RNTI; or
- if the message is received on DCCH:

the UE may:

1> maintain a list of the set of cells to which the UE has Radio Links if the IE "Cell ID" is present.

the UE shall:

1> stop timer T302;

1> in case of a cell update procedure and the CELL UPDATE CONFIRM message:

2> includes "RB information elements"; and/or

2> includes "Transport channel information elements"; and/or

2> includes "Physical channel information elements"; and

2> if the variable ORDERED_RECONFIGURATION is set to FALSE:

3> set the variable ORDERED_RECONFIGURATION to TRUE.

1> act upon all received information elements as specified in subclause 8.6, unless specified otherwise in the following:

2> if the IE "Frequency info" is included in the message:

3> if the IE "RRC State Indicator" is set to the value "CELL_FACH" or "CELL_PCH" or URA_PCH":

4> select a suitable UTRA cell according to [4] on that frequency;

4> act as specified in subclause 8.3.1.12.

3> if the IE "RRC State Indicator" is set to the value "CELL_DCH":

4> act on the IE "Frequency info" as specified in subclause 8.6.6.1.

2> use the transport channel(s) applicable for the physical channel types that is used; and

2> if the IE "TFS" is neither included nor previously stored in the UE for that transport channel(s):

3> use the TFS given in system information.

2> if none of the TFS stored is compatible with the physical channel:

3> delete the stored TFS;

3> use the TFS given in system information.

2> if the IE "RLC re-establish indicator (RB2, RB3 and RB4)" in the CELL UPDATE CONFIRM message is set to TRUE:

3> re-establish the RLC entities for signalling radio bearer RB2, signalling radio bearer RB3 and signalling radio bearer RB4 (if established);

3> if the value of the IE "Status" in the variable CIPHERING_STATUS of the CN domain stored in the variable LATEST_CONFIGURED_CN_DOMAIN is set to "Started":

4> set the HFN component of the respective COUNT-C values for AM RLC entities with RB identity 2,RB identity 3 and RB identity 4 (if established) equal to the START value included in the latest transmitted CELL UPDATE message for the CN domain stored in the variable LATEST_CONFIGURED_CN_DOMAIN.

2> if the IE "RLC re-establish indicator (RB5 and upwards)" in the CELL UPDATE CONFIRM message is set to TRUE:

3> for radio bearers with RB identity 5 and upwards:

4> re-establish the AM RLC entities;

4> if the value of the IE "Status" in the variable CIPHERING_STATUS of the CN domain as indicated in the IE "CN domain identity" in the IE "RAB info" in the variable ESTABLISHED_RABS is set to "Started":

5> set the HFN component of the respective COUNT-C values for AM RLC entities equal to the START value included in this CELL UPDATE message for the CN domain as indicated in the IE "CN domain identity" in the IE "RAB info" in the variable ESTABLISHED_RABS.

NOTE: UE actions, in case IE "Downlink counter synchronisation info" is included and either IE "RLC re-establish indicator (RB2, RB3 and RB4)" or IE "RLC re-establish indicator (RB5 and upwards)" are set to TRUE, are not defined.

1> if the CELL UPDATE CONFIRM / URA UPDATE CONFIRM message contained the IE "Ciphering mode info" or contained the IE "Integrity protection mode info":

2> set the IE "Status" in the variable SECURITY_MODIFICATION for all the CN domains in the variable SECURITY_MODIFICATION to "Affected".

1> if the variable ESTABLISHMENT_CAUSE is set:

2> clear the variable ESTABLISHMENT_CAUSE.

1> enter a state according to subclause 8.6.3.3 applied on the CELL UPDATE CONFIRM / URA UPDATE CONFIRM message.

If the UE after state transition enters CELL_DCH state, it shall:

1> perform the physical layer synchronisation procedure A as specified in [29] (FDD only);

1> not prohibit periodical status transmission in RLC.

If the UE after state transition remains in CELL_FACH state, it shall

1> start the timer T305 using its initial value if timer T305 is not running and periodical cell update has been configured by T305 in the IE "UE Timers and constants in connected mode" set to any other value than "infinity";

1> select PRACH according to subclause 8.5.17;

1> select Secondary CCPCH according to subclause 8.5.19;

1> not prohibit periodical status transmission in RLC;

1> if the IE "UTRAN DRX cycle length coefficient" is included in the same message:

2> ignore that IE and stop using DRX.

If the UE after state transition enters URA_PCH or CELL_PCH state, it shall:

1> prohibit periodical status transmission in RLC;

1> clear the variable C_RNTI;

1> stop using that C_RNTI just cleared from the variable C_RNTI in MAC;

1> start the timer T305 using its initial value if timer T305 is not running and periodical update has been configured by T305 in the IE "UE Timers and constants in connected mode" set to any other value than "infinity";

1> select Secondary CCPCH according to subclause 8.5.19;

1> if the IE "UTRAN DRX cycle length coefficient" is included in the same message:

2> use the value in the IE "UTRAN DRX Cycle length coefficient" for calculating Paging Occasion and PICH Monitoring Occasion as specified in subclause 8.6.3.2.

1> if the IE "UTRAN DRX cycle length coefficient" is not included in the same message:

2> set the variable INVALID_CONFIGURATION to TRUE.

If the UE after the state transition remains in CELL_FACH state; and

1> the contents of the variable C_RNTI are empty:

it shall check the value of V302; and:

1> if V302 is equal to or smaller than N302:

2> if, caused by the received CELL UPDATE CONFIRM or URA UPDATE CONFIRM message:

3> the IE "Reconfiguration" in the variable CIPHERING_STATUS is set to TRUE; and/or

3> the IE "Reconfiguration" in the variable INTEGRITY_PROTECTION_INFO is set to TRUE:

4> abort the ongoing integrity and/or ciphering reconfiguration;

4> if the received CELL UPDATE CONFIRM or URA UPDATE CONFIRM message contained the IE "Ciphering mode info":

5> set the IE "Reconfiguration" in the variable CIPHERING_STATUS to FALSE; and

5> clear the variable RB_UPLINK_CIPHERING_ACTIVATION_TIME_INFO.

4> if the received CELL UPDATE CONFIRM or URA UPDATE CONFIRM message contained the IE "Integrity protection mode info":

5> set the IE "Reconfiguration" in the variable INTEGRITY_PROTECTION_INFO to FALSE; and

5> clear the variable INTEGRITY_PROTECTION_ACTIVATION_INFO.

2> in case of a URA update procedure:

3> stop the URA update procedure;

3> clear any entry for the URA UPDATE CONFIRM message in the table "Accepted transactions" in the variable TRANSACTIONS; and

3> continue with a cell update procedure.

2> set the contents of the CELL UPDATE message according to subclause 8.3.1.3, except for the IE "Cell update cause" which shall be set to "cell reselection";

2> submit the CELL UPDATE message for transmission on the uplink CCCH;

2> increment counter V302;

2> restart timer T302 when the MAC layer indicates success or failure to transmit the message.

1> if V302 is greater than N302:

2> clear the variable RB_UPLINK_CIPHERING_ACTIVATION_TIME_INFO;

2> clear the variable INTEGRITY_PROTECTION_ACTIVATION_INFO;

2> in case of a cell update procedure:

3> clear the entry for the CELL UPDATE CONFIRM message in the table "Rejected transactions" in the variable TRANSACTIONS.

2> in case of a URA update procedure:

3> clear the entry for the URA UPDATE CONFIRM message in the table "Rejected transactions" in the variable TRANSACTIONS.

2> release all its radio resources;

2> indicate release (abort) of the established signalling connections (as stored in the variable ESTABLISHED_SIGNALLING_CONNECTIONS) and established radio access bearers (as stored in the variable ESTABLISHED_RABS) to upper layers;

2> clear the variable ESTABLISHED_SIGNALLING_CONNECTIONS;

2> clear the variable ESTABLISHED_RABS;

2> enter idle mode;

2> other actions the UE shall perform when entering idle mode from connected mode are specified in subclause 8.5.2;

2> and the procedure ends.

If the UE after the state transition remains in CELL_FACH state; and

- a C-RNTI is stored in the variable C_RNTI;

or

- the UE after the state transition moves to another state than the CELL_FACH state:

the UE shall:

1> if the CELL UPDATE CONFIRM / URA UPDATE CONFIRM message contained the IE "Ciphering mode info":

2> include and set the IE "Radio bearer uplink ciphering activation time info" in any response message transmitted below to the value of the variable RB_UPLINK_CIPHERING_ACTIVATION_TIME_INFO.

1> in case cell reselection interrupted an ongoing cell update procedure and a CELL UPDATE CONFIRM/URA UPDATE CONFIRM was received with the IE "Downlink counter synchronisation info" present and the response to which was not submitted to the lower layers due to the cell re-selection:

2> include the IE "START list" in the response message transmitted according to subclause 8.3.1.7;

2> if the CELL UPDATE CONFIRM/URA UPDATE CONFIRM, the response to which was not delivered to the lower layers, due to the cell re-selection, included the IE "RB with PDCP information list":

3> include the IE "RB with PDCP information list" in the response message transmitted according to subclause 8.3.1.7.

1> in case of a cell update procedure:

2> set the IE "RRC transaction identifier" in any response message transmitted below to the value of "RRC transaction identifier" in the entry for the CELL UPDATE CONFIRM message in the table "Accepted transactions" in the variable TRANSACTIONS; and

2> clear that entry.

1> in case of a URA update procedure:

2> set the IE "RRC transaction identifier" in any response message transmitted below to the value of "RRC transaction identifier" in the entry for the URA UPDATE CONFIRM message in the table "Accepted transactions" in the variable TRANSACTIONS; and

2> clear that entry;

1> if the variable PDCP_SN_INFO is non-empty:

2> include the IE "RB with PDCP information list" in any response message transmitted below and set it to the value of the variable PDCP_SN_INFO.

1> if the received CELL UPDATE CONFIRM or URA UPDATE CONFIRM message included the IE "Downlink counter synchronisation info":

2> if the variable PDCP_SN_INFO is empty:

3> configure the corresponding RLC entity for all AM and UM radio bearers and AM and UM signalling radio bearers except RB2 to "stop".

2> else:

3> configure the RLC entity for signalling radio bearers RB1, RB3 and RB4 to "stop";

3> configure the RLC entity for UM and AM radio bearers for which the IE "PDCP SN Info" is not included to "stop".

2> re-establish the RLC entity for RB2;

2> for the downlink and the uplink, apply the ciphering configuration as follows:

3> if the received re-configuation message included the IE "Ciphering Mode Info":

4> use the ciphering configuration in the received message when transmitting the response message.

3> if the ciphering configuration for RB2 from a previously received SECURITY MODE COMMAND has not yet been applied because the activation times not having been reached:

4> if the previous SECURITY MODE COMMAND was received due to new keys being received:

5> consider the new ciphering configuration to include the received new keys;

5> initialise the HFN component of the uplink COUNT-C and downlink COUNT-C of SRB2 as indicated in subclause 8.1.12.3.1.

4> if the ciphering configuration for RB2 from a previously received SECURITY MODE COMMAND has not yet been applied because of the corresponding activation times not having been reached and the previous SECURITY MODE COMMAND caused a change in LATEST_CONFIGURED_CN_DOMAIN:

5> consider the new ciphering configuration to include the keys associated with the LATEST_CONFIGURED_CN_DOMAIN;

5> initialise the HFN component of the uplink COUNT-C and downlink COUNT-C of SRB2 to the most recently transmitted IE "START list" or IE "START" for the LATEST_CONFIGURED_CN_DOMAIN at the reception of the previous SECURITY MODE COMMAND.

4> apply the new ciphering configuration immediately following RLC re-establishment.

3> else:

4> continue using the current ciphering configuration.

2> set the new uplink and downlink HFN component of the COUNT-C of RB2 to MAX(uplink HFN component of the COUNT-C of RB2, downlink HFN component of the COUNT-C of RB2);

2> increment by one the downlink and uplink values of the HFN component of the COUNT-C for RB2;

2> calculate the START value according to subclause 8.5.9;

2> include the calculated START values for each CN domain in the IE "START list" in the IE "Uplink counter synchronisation info" in any response message transmitted below.

1> transmit a response message as specified in subclause 8.3.1.7;

1> if the IE "Integrity protection mode info" was present in the CELL UPDATE CONFIRM or URA UPDATE CONFIRM message:

2> start applying the new integrity protection configuration in the uplink for signalling radio bearer RB2 from and including the transmitted response message.

1> if the variable ORDERED_RECONFIGURATION is set to TRUE caused by the received CELL UPDATE CONFIRM message in case of a cell update procedure:

2> set the variable ORDERED_RECONFIGURATION to FALSE.

1> clear the variable PDCP_SN_INFO;

1> when the response message transmitted per subclause 8.3.1.7 to the UTRAN has been confirmed by RLC:

2> if the CELL UPDATE CONFIRM / URA UPDATE CONFIRM message contained the IE "Ciphering mode info":

3> resume data transmission on any suspended radio bearer and signalling radio bearer mapped on RLC-AM or RLC-UM;

3> set the IE "Reconfiguration" in the variable CIPHERING_STATUS to FALSE; and

3> clear the variable RB_UPLINK_CIPHERING_ACTIVATION_TIME_INFO.

2> if the CELL UPDATE CONFIRM / URA UPDATE CONFIRM message contained the IE "Integrity protection mode info":

3> set "Uplink RRC Message sequence number" for signalling radio bearer RB0 in the variable INTEGRITY_PROTECTION_INFO to a value such that next RRC message to be sent on uplink RB0 will use the new integrity protection configuration;

3> allow the transmission of RRC messages on all signalling radio bearers with any RRC SN;

3> set the IE "Reconfiguration" in the variable INTEGRITY_PROTECTION_INFO to FALSE.

2> clear the variable INTEGRITY_PROTECTION_ACTIVATION_INFO.

1> in case of a cell update procedure:

2> clear the entry for the CELL UPDATE CONFIRM message in the table "Rejected transactions" in the variable TRANSACTIONS.

1> in case of a URA update procedure:

2> clear the entry for the URA UPDATE CONFIRM message in the table "Rejected transactions" in the variable TRANSACTIONS.

1> set the variable CELL_UPDATE_STARTED to FALSE;

1> if the variable AM_RLC_ERROR_PENDING_RB234 is set to TRUE:

2> set the variable AM_RLC_ERROR_PENDING_RB234 to FALSE;

1> if the variable AM_RLC_ERROR_PENDING_RB5_AND_UP is set to TRUE:

2> set the variable AM_RLC_ERROR_PENDING_RB5_AND_UP to FALSE;

1> clear the variable SECURITY_MODIFICATION.

The procedure ends.

[…]

13.4 UE variables

[…]

13.4.new1 AM_RLC_ERROR_PENDING_RB234

This variable indicates whether an AM RLC unrecoverable error has been detected during the current cell update procedure on RB 2, 3, or 4.

Information Element/Group name	Need	Multi	Type and reference	Semantics description
AM RLC error pending	MP		Boolean	TRUE means an unrecoverable error was detected on AM RLC during the current cell update procedure.. Set to FALSE when the cell update procedure is completed.

13.4.new2 AM_RLC_ERROR_PENDING_RB5_AND_UP

This variable indicates whether an AM RLC unrecoverable error has been detected during the current cell update procedure on RB 5 or above.

Information Element/Group name	Need	Multi	Type and reference	Semantics description
AM RLC error pending	MP		Boolean	TRUE means an unrecoverable error was detected on AM RLC during the current cell update procedure.. Set to FALSE when the cell update procedure is completed.

3GPP TSG-RAN2 #44 **Tdoc ⌘R2-042269**
Sophia Antipolis, France, 4-8 October, 2004

CR-Form-v7

CHANGE REQUEST

⌘ **25.331** **CR** **2438** ⌘**rev** **-** ⌘ Current version: **6.3.0** ⌘

*For **HELP** on using this form, see bottom of this page or look at the pop-up text over the ⌘ symbols.*

Proposed change affects: UICC apps⌘ ☐ ME ☒ Radio Access Network ☐ Core Network ☐

Title:	⌘	Handling of pending AM RLC unrecoverable errors signalled by cell update
Source:	⌘	RAN WG2
Work item code:	⌘	TEI5
Date:	⌘	October 7, 2004
Category:	⌘	**A**
Release:	⌘	Rel-6

Use one of the following categories:
F *(correction)*
A *(corresponds to a correction in an earlier release)*
B *(addition of feature),*
C *(functional modification of feature)*
D *(editorial modification)*
Detailed explanations of the above categories can be found in 3GPP TR 21.900.

Use one of the following releases:
2 *(GSM Phase 2)*
R96 *(Release 1996)*
R97 *(Release 1997)*
R98 *(Release 1998)*
R99 *(Release 1999)*
Rel-4 *(Release 4)*
Rel-5 *(Release 5)*
Rel-6 *(Release 6)*

Reason for change:	⌘	If the first CELL UPDATE message indicating an AM RLC unrecoverable error is lost, the UE should continue indicating the error in a subsequent CELL UPDATE message. This behaviour is not clearly specified in the current version of the specification.
Summary of change:	⌘	New RRC variables AM_RLC_ERROR_PENDING_RB234 and AM_RLC_ERROR_PENDING_RB5_AND_UP created to indicate whether an unrecoverable AM RLC error should be signalled in future CELL UPDATE messages. CELL UPDATE message contents updated to include error indication if these variables are set to TRUE. Added requirement for UE to set the new variables to FALSE on reception of a CELL UPDATE CONFIRM message.
Consequences if not approved:	⌘	Connection could be lost due to UE failing to notify UTRAN of the RLC error.

Clauses affected:	⌘	8.3.1.3, 8.3.1.6, 13.4.new1, 13.4.new2

Other specs affected: ⌘	Y	N	
		X	Other core specifications ⌘
		X	Test specifications
		X	O&M Specifications

Other comments:	⌘	Revision of (part of) R2-042037.

How to create CRs using this form:
Comprehensive information and tips about how to create CRs can be found at http://www.3gpp.org/specs/CR.htm. Below is a brief summary:

1) Fill out the above form. The symbols above marked ⌘ contain pop-up help information about the field that they are closest to.

2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under ftp://ftp.3gpp.org/specs/ For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.

3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

8.3.1.3 CELL UPDATE / URA UPDATE message contents to set

In case of cell update procedure the UE shall transmit a CELL UPDATE message.

In case of URA update procedure the UE shall transmit a URA UPDATE message.

The UE shall set the IEs in the CELL UPDATE message as follows:

1> set the IE "Cell update cause" corresponding to the cause specified in subclause 8.3.1.2 that is valid when the CELL UPDATE message is submitted to lower layers for transmission;

NOTE: During the time period starting from when a cell update procedure is initiated by the UE until when the procedure ends, additional CELL UPDATE messages may be transmitted by the UE with different causes.

1> set the IE "U-RNTI" to the value of the variable U_RNTI;

1> if the value of the variable PROTOCOL_ERROR_INDICATOR is TRUE:

2> include the IE "RRC transaction identifier"; and

3> set it to the value of "RRC transaction identifier" in the entry for the CELL UPDATE CONFIRM message in the table "Rejected transactions" in the variable TRANSACTIONS.

2> include and set the IE "failure cause" to the cause value "protocol error";

2> set the IE "Protocol error information" set to the value of the variable PROTOCOL_ERROR_INFORMATION.

1> if the value of the variable FAILURE_INDICATOR is TRUE:

2> include the IE "RRC transaction identifier"; and

3> set it to the value of "RRC transaction identifier" in the entry for the CELL UPDATE CONFIRM message in the table "Accepted transactions" in the variable TRANSACTIONS.

2> include and set the IE "failure cause" to the value of the variable FAILURE_CAUSE.

1> include the START values for each CN domain, calculated according to subclause 8.5.9;

1> if an unrecoverable error [16] in any of the AM RLC entities for the signalling radio bearers RB2, RB3 or RB4 is detected:

2> set the IE "AM_RLC error indication (RB2, RB3 or RB4)" to TRUE;~~.~~

2> set the variable AM_RLC_ERROR_PENDING_RB234 to TRUE.

1> otherwise:

2> if the value of the variable AM_RLC_ERROR_PENDING_RB234 is TRUE:

3> set the IE “AM_RLC error indication (RB2, RB3 or RB4)” to TRUE.

2> otherwise:

~~2~~3> set the IE "AM_RLC error indication (RB2, RB3 or RB4)" to FALSE.

1> if an unrecoverable error [16] in any of the AM RLC entities for the RB5 or upward is detected:

2> set the IE "AM_RLC error indication (RB>4)" to TRUE;~~.~~

2> set the variable AM_RLC_ERROR_PENDING_RB5_AND_UP to TRUE.

1> otherwise:

2> if the value of the variable AM_RLC_ERROR_PENDING_RB5_AND_UP is TRUE:

3> set the IE “AM_RLC error indication (RB>4)” to TRUE.

2> otherwise:

3~~2~~> set the IE "AM_RLC error indication (RB>4)" to FALSE.

1> set the IE "RB Timer indicator" to the value of the variable RB_TIMER_INDICATOR;

1> if the variable ESTABLISHMENT_CAUSE is initialised:

2> include the IE "Establishment cause" and set it to the value of the variable ESTABLISHMENT_CAUSE.

The UE shall set the IEs in the URA UPDATE message as follows:

1> set the IE "U-RNTI" to the value of the variable U_RNTI;

1> set the IE "URA update cause" corresponding to which cause as specified in subclause 8.3.1.2 that is valid when the URA UPDATE message is submitted to lower layers for transmission;

NOTE: During the time period starting from when a URA update procedure is initiated by the UE until when the procedure ends, additional URA UPDATE messages may be transmitted by the UE with different causes, depending on which causes are valid for the respective URA UPDATE message.

2> if the value of the variable PROTOCOL_ERROR_INDICATOR is TRUE:

3> include the IE "RRC transaction identifier"; and

4> set it to the value of "RRC transaction identifier" in the entry for the URA UPDATE CONFIRM message in the table "Rejected transactions" in the variable TRANSACTIONS;

3> set the IE "Protocol error indicator" to TRUE;

3> include the IE "Protocol error information" set to the value of the variable PROTOCOL_ERROR_INFORMATION.

2> if the value of the variable PROTOCOL_ERROR_INDICATOR is FALSE:

3> if the value of the variable INVALID_CONFIGURATION is TRUE:

4> include the IE "RRC transaction identifier"; and

4> set it to the value of "RRC transaction identifier" in the entry for the URA UPDATE CONFIRM message in the table "Accepted transactions" in the variable TRANSACTIONS;

4> set the IE "Protocol error indicator" to TRUE;

4> include the IE "Protocol error information" set to "Information element value not comprehended";

3> if the value of the variable INVALID_CONFIGURATION is FALSE:

4> set the IE "Protocol error indicator" to FALSE.

[…]

8.3.1.6 Reception of the CELL UPDATE CONFIRM/URA UPDATE CONFIRM message by the UE

When the UE receives a CELL UPDATE CONFIRM/URA UPDATE CONFIRM message; and

- if the message is received on the CCCH, and IE "U-RNTI" is present and has the same value as the variable U_RNTI; or
- if the message is received on DCCH:

the UE may:

1> maintain a list of the set of cells to which the UE has Radio Links if the IE "Cell ID" is present.

the UE shall:

1> stop timer T302;

1> in case of a cell update procedure and the CELL UPDATE CONFIRM message:

2> includes "RB information elements"; and/or

2> includes "Transport channel information elements"; and/or

2> includes "Physical channel information elements"; and

2> if the variable ORDERED_RECONFIGURATION is set to FALSE:

3> set the variable ORDERED_RECONFIGURATION to TRUE.

1> act upon all received information elements as specified in subclause 8.6, unless specified otherwise in the following:

2> if the IE "Frequency info" is included in the message:

3> if the IE "RRC State Indicator" is set to the value "CELL_FACH" or "CELL_PCH" or URA_PCH":

4> select a suitable UTRA cell according to [4] on that frequency;

4> act as specified in subclause 8.3.1.12.

3> if the IE "RRC State Indicator" is set to the value "CELL_DCH":

4> act on the IE "Frequency info" as specified in subclause 8.6.6.1.

2> use the transport channel(s) applicable for the physical channel types that is used; and

2> if the IE "TFS" is neither included nor previously stored in the UE for that transport channel(s):

3> use the TFS given in system information.

2> if none of the TFS stored is compatible with the physical channel:

3> delete the stored TFS;

3> use the TFS given in system information.

2> if the IE "RLC re-establish indicator (RB2, RB3 and RB4)" in the CELL UPDATE CONFIRM message is set to TRUE:

3> re-establish the RLC entities for signalling radio bearer RB2, signalling radio bearer RB3 and signalling radio bearer RB4 (if established);

3> if the value of the IE "Status" in the variable CIPHERING_STATUS of the CN domain stored in the variable LATEST_CONFIGURED_CN_DOMAIN is set to "Started":

4> set the HFN component of the respective COUNT-C values for AM RLC entities with RB identity 2,RB identity 3 and RB identity 4 (if established) equal to the START value included in the latest transmitted CELL UPDATE message for the CN domain stored in the variable LATEST_CONFIGURED_CN_DOMAIN.

2> if the IE "RLC re-establish indicator (RB5 and upwards)" in the CELL UPDATE CONFIRM message is set to TRUE:

3> for radio bearers with RB identity 5 and upwards:

4> re-establish the AM RLC entities;

4> if the value of the IE "Status" in the variable CIPHERING_STATUS of the CN domain as indicated in the IE "CN domain identity" in the IE "RAB info" in the variable ESTABLISHED_RABS is set to "Started":

5> set the HFN component of the respective COUNT-C values for AM RLC entities equal to the START value included in this CELL UPDATE message for the CN domain as indicated in the IE "CN domain identity" in the IE "RAB info" in the variable ESTABLISHED_RABS.

NOTE: UE actions, in case IE "Downlink counter synchronisation info" is included and either IE "RLC re-establish indicator (RB2, RB3 and RB4)" or IE "RLC re-establish indicator (RB5 and upwards)" are set to TRUE, are not defined.

1> if the CELL UPDATE CONFIRM / URA UPDATE CONFIRM message contained the IE "Ciphering mode info" or contained the IE "Integrity protection mode info":

2> set the IE "Status" in the variable SECURITY_MODIFICATION for all the CN domains in the variable SECURITY_MODIFICATION to "Affected".

1> if the variable ESTABLISHMENT_CAUSE is set:

2> clear the variable ESTABLISHMENT_CAUSE.

1> enter a state according to subclause 8.6.3.3 applied on the CELL UPDATE CONFIRM / URA UPDATE CONFIRM message.

If the UE after state transition enters CELL_DCH state, it shall:

1> perform the physical layer synchronisation procedure A as specified in [29] (FDD only);

1> not prohibit periodical status transmission in RLC.

If the UE after state transition remains in CELL_FACH state, it shall

1> start the timer T305 using its initial value if timer T305 is not running and periodical cell update has been configured by T305 in the IE "UE Timers and constants in connected mode" set to any other value than "infinity";

1> select PRACH according to subclause 8.5.17;

1> select Secondary CCPCH according to subclause 8.5.19;

1> not prohibit periodical status transmission in RLC;

1> if the IE "UTRAN DRX cycle length coefficient" is included in the same message:

2> ignore that IE and stop using DRX.

If the UE after state transition enters URA_PCH or CELL_PCH state, it shall:

1> prohibit periodical status transmission in RLC;

1> clear the variable C_RNTI;

1> stop using that C_RNTI just cleared from the variable C_RNTI in MAC;

1> start the timer T305 using its initial value if timer T305 is not running and periodical update has been configured by T305 in the IE "UE Timers and constants in connected mode" set to any other value than "infinity";

1> select Secondary CCPCH according to subclause 8.5.19;

1> if the IE "UTRAN DRX cycle length coefficient" is included in the same message:

2> use the value in the IE "UTRAN DRX Cycle length coefficient" for calculating Paging Occasion and PICH Monitoring Occasion as specified in subclause 8.6.3.2.

1> if the IE "UTRAN DRX cycle length coefficient" is not included in the same message:

2> set the variable INVALID_CONFIGURATION to TRUE.

If the UE after the state transition remains in CELL_FACH state; and

1> the contents of the variable C_RNTI are empty:

it shall check the value of V302; and:

1> if V302 is equal to or smaller than N302:

2> if, caused by the received CELL UPDATE CONFIRM or URA UPDATE CONFIRM message:

3> the IE "Reconfiguration" in the variable CIPHERING_STATUS is set to TRUE; and/or

3> the IE "Reconfiguration" in the variable INTEGRITY_PROTECTION_INFO is set to TRUE:

4> abort the ongoing integrity and/or ciphering reconfiguration;

4> if the received CELL UPDATE CONFIRM or URA UPDATE CONFIRM message contained the IE "Ciphering mode info":

5> set the IE "Reconfiguration" in the variable CIPHERING_STATUS to FALSE; and

5> clear the variable RB_UPLINK_CIPHERING_ACTIVATION_TIME_INFO.

4> if the received CELL UPDATE CONFIRM or URA UPDATE CONFIRM message contained the IE "Integrity protection mode info":

5> set the IE "Reconfiguration" in the variable INTEGRITY_PROTECTION_INFO to FALSE; and

5> clear the variable INTEGRITY_PROTECTION_ACTIVATION_INFO.

2> in case of a URA update procedure:

3> stop the URA update procedure;

3> clear any entry for the URA UPDATE CONFIRM message in the table "Accepted transactions" in the variable TRANSACTIONS; and

3> continue with a cell update procedure.

2> set the contents of the CELL UPDATE message according to subclause 8.3.1.3, except for the IE "Cell update cause" which shall be set to "cell reselection";

2> submit the CELL UPDATE message for transmission on the uplink CCCH;

2> increment counter V302;

2> restart timer T302 when the MAC layer indicates success or failure to transmit the message.

1> if V302 is greater than N302:

2> clear the variable RB_UPLINK_CIPHERING_ACTIVATION_TIME_INFO;

2> clear the variable INTEGRITY_PROTECTION_ACTIVATION_INFO;

2> in case of a cell update procedure:

3> clear the entry for the CELL UPDATE CONFIRM message in the table "Rejected transactions" in the variable TRANSACTIONS.

2> in case of a URA update procedure:

3> clear the entry for the URA UPDATE CONFIRM message in the table "Rejected transactions" in the variable TRANSACTIONS.

2> release all its radio resources;

2> indicate release (abort) of the established signalling connections (as stored in the variable ESTABLISHED_SIGNALLING_CONNECTIONS) and established radio access bearers (as stored in the variable ESTABLISHED_RABS) to upper layers;

2> clear the variable ESTABLISHED_SIGNALLING_CONNECTIONS;

2> clear the variable ESTABLISHED_RABS;

2> enter idle mode;

2> other actions the UE shall perform when entering idle mode from connected mode are specified in subclause 8.5.2;

2> and the procedure ends.

If the UE after the state transition remains in CELL_FACH state; and

- a C-RNTI is stored in the variable C_RNTI;

or

- the UE after the state transition moves to another state than the CELL_FACH state:

the UE shall:

1> if the CELL UPDATE CONFIRM / URA UPDATE CONFIRM message contained the IE "Ciphering mode info":

2> include and set the IE "Radio bearer uplink ciphering activation time info" in any response message transmitted below to the value of the variable RB_UPLINK_CIPHERING_ACTIVATION_TIME_INFO.

1> in case cell reselection interrupted an ongoing cell update procedure and a CELL UPDATE CONFIRM/URA UPDATE CONFIRM was received with the IE "Downlink counter synchronisation info" present and the response to which was not submitted to the lower layers due to the cell re-selection:

2> include the IE "START list" in the response message transmitted according to subclause 8.3.1.7;

2> if the CELL UPDATE CONFIRM/URA UPDATE CONFIRM, the response to which was not delivered to the lower layers, due to the cell re-selection, included the IE "RB with PDCP information list":

3> include the IE "RB with PDCP information list" in the response message transmitted according to subclause 8.3.1.7.

1> in case of a cell update procedure:

2> set the IE "RRC transaction identifier" in any response message transmitted below to the value of "RRC transaction identifier" in the entry for the CELL UPDATE CONFIRM message in the table "Accepted transactions" in the variable TRANSACTIONS; and

2> clear that entry.

1> in case of a URA update procedure:

2> set the IE "RRC transaction identifier" in any response message transmitted below to the value of "RRC transaction identifier" in the entry for the URA UPDATE CONFIRM message in the table "Accepted transactions" in the variable TRANSACTIONS; and

2> clear that entry;

1> if the variable PDCP_SN_INFO is non-empty:

2> include the IE "RB with PDCP information list" in any response message transmitted below and set it to the value of the variable PDCP_SN_INFO.

1> if the received CELL UPDATE CONFIRM or URA UPDATE CONFIRM message included the IE "Downlink counter synchronisation info":

2> if the variable PDCP_SN_INFO is empty:

3> configure the corresponding RLC entity for all AM and UM radio bearers and AM and UM signalling radio bearers except RB2 to "stop".

2> else:

3> configure the RLC entity for signalling radio bearers RB1, RB3 and RB4 to "stop";

3> configure the RLC entity for UM and AM radio bearers for which the IE "PDCP SN Info" is not included to "stop".

2> re-establish the RLC entity for RB2;

2> for the downlink and the uplink, apply the ciphering configuration as follows:

3> if the received re-configuation message included the IE "Ciphering Mode Info":

4> use the ciphering configuration in the received message when transmitting the response message.

3> if the ciphering configuration for RB2 from a previously received SECURITY MODE COMMAND has not yet been applied because the activation times not having been reached:

4> if the previous SECURITY MODE COMMAND was received due to new keys being received:

5> consider the new ciphering configuration to include the received new keys;

5> initialise the HFN component of the uplink COUNT-C and downlink COUNT-C of SRB2 as indicated in subclause 8.1.12.3.1.

4> if the ciphering configuration for RB2 from a previously received SECURITY MODE COMMAND has not yet been applied because of the corresponding activation times not having been reached and the previous SECURITY MODE COMMAND caused a change in LATEST_CONFIGURED_CN_DOMAIN:

5> consider the new ciphering configuration to include the keys associated with the LATEST_CONFIGURED_CN_DOMAIN;

5> initialise the HFN component of the uplink COUNT-C and downlink COUNT-C of SRB2 to the most recently transmitted IE "START list" or IE "START" for the LATEST_CONFIGURED_CN_DOMAIN at the reception of the previous SECURITY MODE COMMAND.

4> apply the new ciphering configuration immediately following RLC re-establishment.

3> else:

4> continue using the current ciphering configuration.

2> set the new uplink and downlink HFN component of the COUNT-C of RB2 to MAX(uplink HFN component of the COUNT-C of RB2, downlink HFN component of the COUNT-C of RB2);

2> increment by one the downlink and uplink values of the HFN component of the COUNT-C for RB2;

2> calculate the START value according to subclause 8.5.9;

2> include the calculated START values for each CN domain in the IE "START list" in the IE "Uplink counter synchronisation info" in any response message transmitted below.

1> transmit a response message as specified in subclause 8.3.1.7;

1> if the IE "Integrity protection mode info" was present in the CELL UPDATE CONFIRM or URA UPDATE CONFIRM message:

2> start applying the new integrity protection configuration in the uplink for signalling radio bearer RB2 from and including the transmitted response message.

1> if the variable ORDERED_RECONFIGURATION is set to TRUE caused by the received CELL UPDATE CONFIRM message in case of a cell update procedure:

2> set the variable ORDERED_RECONFIGURATION to FALSE.

1> clear the variable PDCP_SN_INFO;

1> when the response message transmitted per subclause 8.3.1.7 to the UTRAN has been confirmed by RLC:

2> if the CELL UPDATE CONFIRM / URA UPDATE CONFIRM message contained the IE "Ciphering mode info":

3> resume data transmission on any suspended radio bearer and signalling radio bearer mapped on RLC-AM or RLC-UM;

3> set the IE "Reconfiguration" in the variable CIPHERING_STATUS to FALSE; and

3> clear the variable RB_UPLINK_CIPHERING_ACTIVATION_TIME_INFO.

2> if the CELL UPDATE CONFIRM / URA UPDATE CONFIRM message contained the IE "Integrity protection mode info":

3> set "Uplink RRC Message sequence number" for signalling radio bearer RB0 in the variable INTEGRITY_PROTECTION_INFO to a value such that next RRC message to be sent on uplink RB0 will use the new integrity protection configuration;

3> allow the transmission of RRC messages on all signalling radio bearers with any RRC SN;

3> set the IE "Reconfiguration" in the variable INTEGRITY_PROTECTION_INFO to FALSE.

2> clear the variable INTEGRITY_PROTECTION_ACTIVATION_INFO.

1> in case of a cell update procedure:

2> clear the entry for the CELL UPDATE CONFIRM message in the table "Rejected transactions" in the variable TRANSACTIONS.

1> in case of a URA update procedure:

2> clear the entry for the URA UPDATE CONFIRM message in the table "Rejected transactions" in the variable TRANSACTIONS.

1> set the variable CELL_UPDATE_STARTED to FALSE;

1> if the variable AM_RLC_ERROR_PENDING_RB234 is set to TRUE:

2> set the variable AM_RLC_ERROR_PENDING_RB234 to FALSE;

1> if the variable AM_RLC_ERROR_PENDING_RB5_AND_UP is set to TRUE:

2> set the variable AM_RLC_ERROR_PENDING_RB5_AND_UP to FALSE;

1> clear the variable SECURITY_MODIFICATION.

The procedure ends.

[…]

13.4 UE variables

[…]

13.4.new1 AM_RLC_ERROR_PENDING_RB234

This variable indicates whether an AM RLC unrecoverable error has been detected during the current cell update procedure on RB 2, 3, or 4.

Information Element/Group name	Need	Multi	Type and reference	Semantics description
AM RLC error pending	MP		Boolean	TRUE means an unrecoverable error was detected on AM RLC during the current cell update procedure.. Set to FALSE when the cell update procedure is completed.

13.4.new2 AM_RLC_ERROR_PENDING_RB5_AND_UP

This variable indicates whether an AM RLC unrecoverable error has been detected during the current cell update procedure on RB 5 or above.

Information Element/Group name	Need	Multi	Type and reference	Semantics description
AM RLC error pending	MP		Boolean	TRUE means an unrecoverable error was detected on AM RLC during the current cell update procedure.. Set to FALSE when the cell update procedure is completed.

3GPP TSG-RAN-WG2 Meeting #45
Shin-Yokohama, Japan, 15-19 October 2004

Tdoc ⌘R2-042585

CR-Form-v7.1

CHANGE REQUEST

⌘ **25.331** **CR** **2443** ⌘ **rev** **-** ⌘ Current version: **5.a.0** ⌘

*For **HELP** on using this form, see bottom of this page or look at the pop-up text over the ⌘ symbols.*

Proposed change affects: UICC apps⌘ [ ] ME [X] Radio Access Network [X] Core Network [ ]

Title:	⌘	TPC step size in default configurations
Source:	⌘	RAN WG2
Work item code:	⌘	TEI5
Date:	⌘	Nov 2004
Category:	⌘	**F**
Release:	⌘	Rel-5

Use one of the following categories:
F *(correction)*
A *(corresponds to a correction in an earlier release)*
B *(addition of feature),*
C *(functional modification of feature)*
D *(editorial modification)*
Detailed explanations of the above categories can be found in 3GPP TR 21.900.

Use one of the following releases:
Ph2 (GSM Phase 2)
R96 (Release 1996)
R97 (Release 1997)
R98 (Release 1998)
R99 (Release 1999)
Rel-4 (Release 4)
Rel-5 (Release 5)
Rel-6 (Release 6)
Rel-7 (Release 7)

Reason for change: ⌘ In section 13.7 of 25.331, the TPC Step size (in the power control algorithm) is shown as 1. It is unclear if this value reflect 1dB (asn1 value of '0') or asn1 value of '1' (which means 2dB).

----- From TS 25.331 Section 13.7------
>ul-DPCH-PowerControlInfo
>>powerControlAlgorithm Algorithm 1 Algorithm 1 Algorithm 1 Algorithm 1
>>>tpcStepSize 1 1 1 1
------- From TS 25.331 Section 11.3 -------
-- Actual value TPC-StepSizeFDD = IE value + 1
TPC-StepSizeFDD ::= INTEGER (0..1)

Summary of change: ⌘ Clarified that all IEs in the default configurations tables are shown in terms of their real value and not ASN.1.
Also added “dB” to the TPC step size.

Implementation of this CR by R99/Rel-4 UEs will not cause backwards compatibility issues

Impact Analysis:
A UE/UTRAN that has not implemented according to this CR, will require a modification.

Consequences if not approved:	⌘	Due to a larger TPC step size there would typically be larger UL power spiking and could worsen the UL synchronization times of the UTRAN. This could cause additional UL interference problems to other users.

Clauses affected:	⌘	13.7			
		Y	**N**		
Other specs affected:	⌘			Other core specifications	⌘
				Test specifications	
				O&M Specifications	
Other comments:	⌘				

How to create CRs using this form:

Comprehensive information and tips about how to create CRs can be found at http://www.3gpp.org/specs/CR.htm. Below is a brief summary:

1) Fill out the above form. The symbols above marked ⌘ contain pop-up help information about the field that they are closest to.

2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under ftp://ftp.3gpp.org/specs/ For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.

3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

13.7 Parameter values for default radio configurations

The UE shall support the use of the default radio configurations that are specified in the following.

NOTE 1: These configurations are based on [41] and cover a number of RAB and signalling connection configurations.

In the table that is used to specify the parameter values for these default configurations, the following principles are used:

- Optional IEs that are not used are omitted;
- In case no parameter value is specified in a column, this means the value given the previous (left side) column applies.

NOTE 2: If needed, signalling radio bearer RB4 is established after the completion of handover.

NOTE 3: For each default configuration, the value of FDD, 3.84 Mcps TDD and 1.28 Mcps TDD parameters are specified. All parameters apply to FDD, 3.84 Mcps TDD and 1.28 Mcps TDD modes, unless explicitly stated otherwise. It should be noted that in this respect default configurations differ from pre-defined configurations, which only include parameter values for one mode.

NOTE 4: The transport format sizes, indicated in the following table, concern the RLC PDU size, since all configurations concern dedicated channels. The transport block sizes indicated in TS 34.108 are different since these include the size of the MAC header.

NOTE 5: The tabular values included in this section, represent the actual IE values as in section 10, and not the ASN.1 representation of these values.

Configuration	**3.4 kbps signalling**	**13.6 kbps signalling**	**7.95 kbps speech + 3.4 kbps signalling**	**12.2 kbps speech + 3.4 kbps signalling**
Ref 34.108	2	3	6	4
Default configuration identity	0	1	2	3
RB INFORMATION				
rb-Identity	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3, RB5: 5, RB6: 6	RB1: 1, RB2: 2, RB3: 3, RB5: 5, RB6: 6, RB7: 7
rlc-InfoChoice	Rlc-info	Rlc-info	Rlc-info	Rlc-info
>ul-RLC-Mode	RB1: UM RB2- RB3: AM	RB1: UM RB2- RB3: AM	RB1: UM RB2- RB3: AM RB5-RB6: TM	RB1: UM RB2- RB3: AM RB5-RB7: TM
>>transmissionRLC-DiscardMode	RB1: N/A RB2- RB3: NoDiscard	RB1: N/A RB2- RB3: NoDiscard	RB1: N/A RB2- RB3: NoDiscard RB5- RB6: N/A	RB1: N/A RB2- RB3: NoDiscard RB5- RB7: N/A
>>>maxDat	RB1: N/A RB2- RB3: 15	RB1: N/A RB2- RB3: 15	RB1: N/A RB2- RB3: 15 RB5- RB6: N/A	RB1: N/A RB2- RB3: 15 RB5- RB7: N/A
>>transmissionWindowSize	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB6: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB7: N/A
>>timerRST	RB1: N/A RB2- RB3: 300	RB1: N/A RB2- RB3: 300	RB1: N/A RB2- RB3: 300 RB5- RB6: N/A	RB1: N/A RB2- RB3: 300 RB5- RB7: N/A
>>max-RST	RB1: N/A RB2- RB3: 1	RB1: N/A RB2- RB3: 1	RB1: N/A RB2- RB3: 1 RB5- RB6: N/A	RB1: N/A RB2- RB3: 1 RB5- RB7: N/A

Configuration	3.4 kbps signalling	13.6 kbps signalling	7.95 kbps speech + 3.4 kbps signalling	12.2 kbps speech + 3.4 kbps signalling
>>pollingInfo	RB1: N/A RB2- RB3: as below	RB1: N/A RB2- RB3: as below	RB1: N/A RB2- RB3: as below RB5- RB6: N/A	RB1: N/A RB2- RB3: as below RB5- RB7: N/A
>>>lastTransmissionPDU-Poll	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>lastRetransmissionPDU-Poll	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerPollPeriodic	RB2- RB3: 300	RB2- RB3: 100	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A	RB1- RB3: N/A	RB1- RB3: N/A RB5- RB6: FALSE	RB1- RB3: N/A RB5- RB7: FALSE
>dl-RLC-Mode	RB1: UM RB2- RB3: AM	RB1: UM RB2- RB3: AM	RB1: UM RB2- RB3: AM RB5- RB6: TM	RB1: UM RB2- RB3: AM RB5- RB7: TM
>>inSequenceDelivery	RB1: N/A RB2- RB3: TRUE	RB1: N/A RB2- RB3: TRUE	RB1: N/A RB2- RB3: TRUE RB5- RB6: N/A	RB1: N/A RB2- RB3: TRUE RB5- RB7: N/A
>>receivingWindowSize	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB6: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB7: N/A
>>dl-RLC-StatusInfo	RB1: N/A RB2- RB3: as below	RB1: N/A RB2- RB3: as below	RB1: N/A RB2- RB3: as below RB5- RB6: N/A	RB1: N/A RB2- RB3: as below RB5- RB7: N/A
>>>timerStatusProhibit	RB2- RB3: 100	RB2- RB3: 100	RB2- RB3: 100	RB2- RB3: 100
>>>missingPDU-Indicator	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerStatusPeriodic	RB2- RB3: 300	RB2- RB3: 100	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A	RB1- RB3: N/A	RB1- RB3: N/A RB5- RB6: FALSE	RB1- RB3: N/A RB5- RB7: FALSE
rb-MappingInfo				
>UL-LogicalChannelMappings	OneLogicalChannel	OneLogicalChannel	OneLogicalChannel	OneLogicalChannel
>>ul-TransportChannelType	Dch	Dch	Dch	Dch
>>>transportChannelIdentity	RB1- RB3: 1	RB1- RB3: 1	RB1- RB3: 3 RB5: 1, RB6: 2	RB1- RB3: 4 RB5: 1, RB6: 2, RB7: 3
>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3 RB5- RB6: N/A	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: N/A
>>rlc-SizeList	RB1- RB3: configured	RB1- RB3: configured	RB1- RB3: configured RB5- RB6: N/A	RB1- RB3: configured RB5- RB7: N/A
>>mac-LogicalChannelPriority	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3 RB5- RB6: 5	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: 5
>DL-logicalChannelMappingList				
>>Mapping option 1	One mapping option	One mapping option	One mapping option	One mapping option
>>>dl-TransportChannelType	Dch	Dch	Dch	Dch
>>>>transportChannelIdentity	RB1- RB3: 1	RB1- RB3: 1	RB1- RB3: 3 RB5: 1, RB6: 2	RB1- RB3: 4 RB5: 1, RB6: 2, RB7: 3
>>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3 RB5- RB6: N/A	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: N/A
TrCH INFORMATION PER TrCH				

Configuration	3.4 kbps signalling	13.6 kbps signalling	7.95 kbps speech + 3.4 kbps signalling	12.2 kbps speech + 3.4 kbps signalling
UL-AddReconfTransChInfoList				
>Uplink transport channel type	dch	dch	dch	dch
>transportChannelIdentity	TrCH1: 1	TrCH1: 1	TrCH1: 1, TrCH2: 2, TrCH3: 3	TrCH1: 1, TrCH2: 2, TrCH3: 3, TrCH4: 4
>transportFormatSet	DedicatedTransChTFS	DedicatedTransChTFS	DedicatedTransChTFS	DedicatedTransChTFS
>>dynamicTF-information				
>>>tf0/ tf0,1	TrCH1: (0x144, 1x144)	TrCH1: (0x144, 1x144)	TrCH1: (0x75) TrCH2: (0x 84 1x84) TrCH3: (0x144, 1x144)	TrCH1: (0x81) TrCH2: (0x 103, 1x103) TrCH3: (0x 60, 1x60) TrCH4: (0x144, 1x144)
>>>>rlcSize	BitMode	BitMode	BitMode	BitMode
>>>>>sizeType	TrCH1: type 2, part1= 2, part2= 0 (144)	TrCH1: type 2, part1= 2, part2= 0 (144)	TrCH1: type 1: 75 TrCH2: type 1: 84 TrCH3: 2: type 2, part1= 2, part2= 0 (144)	TrCH1: type 1: 81 TrCH2: type 1: 103 TrCH3: type 1: 60 TrCH4: 2: type 2, part1= 2, part2= 0 (144)
>>>>numberOfTbSizeList	TrCH1: Zero, one	TrCH1: Zero, one	TrCH1: Zero TrCH2-3: Zero, one	TrCH1: Zero TrCH2-4: Zero, one
>>>>logicalChannelList	All	All	All	All
>>>tf 1	N/A	N/A	TrCH1: (1x39) TrCH2- TrCH4: N/A	TrCH1: (1x39) TrCH2- TrCH4: N/A
>>>>numberOfTransportBlocks			TrCH1: One	TrCH1: One
>>>>rlc-Size			TrCH1: BitMode	TrCH1: BitMode
>>>>>sizeType			TrCH1: 1: 39	TrCH1: 1: 39
>>>>numberOfTbSizeList			TrCH1: One	TrCH1: One
>>>>logicalChannelList			TrCH1: all	TrCH1: all
>>>tf 2	N/A	N/A	TrCH1: (1x75) TrCH2- TrCH3: N/A	TrCH1: (1x81) TrCH2- TrCH4: N/A
>>>>numberOfTransportBlocks			TrCH1: One	TrCH1: One
>>>>rlc-Size			TrCH1: BitMode	TrCH1: BitMode
>>>>>sizeType			TrCH1: type 1: 75	TrCH1: type 1: 81
>>>>numberOfTbSizeList			TrCH1: One	TrCH1: One
>>>>logicalChannelList			TrCH1: all	TrCH1: all
>>semistaticTF-Information				
>>>tti	TrCH1: 40	TrCH1: 10	TrCH1- TrCH2: 20 TrCH3: 40	TrCH1- TrCH3: 20 TrCH4: 40
>>>channelCodingType	Convolutional	Convolutional	Convolutional	Convolutional
>>>>codingRate	TrCH1: Third	TrCH1: Third	TrCH1- TrCH2: Third TrCH3: Third	TrCH1- TrCH2: Third TrCH3: Half TrCH4: Third
>>>rateMatchingAttribute	TrCH1: 160	TrCH1: 160	TrCH1: 200 TrCH2: 190 TrCH3: 160	TrCH1: 200 TrCH2: 190 TrCH3: 235 TrCH4: 160
>>>crc-Size	TrCH1: 16	TrCH1: 16	TrCH1: 12 TrCH2: 0 TrCH3: 16	TrCH1: 12 TrCH2- TrCH3: 0 TrCH4: 16
DL-AddReconfTransChInfoList				
>Downlink transport channel type	dch	dch	dch	dch

Configuration	3.4 kbps signalling	13.6 kbps signalling	7.95 kbps speech + 3.4 kbps signalling	12.2 kbps speech + 3.4 kbps signalling
>dl-TransportChannelIdentity (should be as for UL)	TrCH1: 1	TrCH1: 1	TrCH1: 1, TrCH2: 2, TrCH3: 3	TrCH1: 1, TrCH2: 2, TrCH3: 3, TrCH4: 4
>tfs-SignallingMode	SameAsUL	SameAsUL	Explicit <Only tf0 on TrCH1 is different and shown below>	Explicit <Only tf0 on TrCH1 is different and shown below>
>>transportFormatSet			DedicatedTransChTFS	DedicatedTransChTFS
>>>dynamicTF-information				
>>>>tf0/ tf0,1			TrCH1: (1x0)	TrCH1: (1x0)
>>>>rlcSize			BitMode	bitMode
>>>>>sizeType			TrCH1: type 1: 0	TrCH1: type 1: 0
>>>>numberOfTbSizeList			TrCH1: One	TrCH1: One
>>>>logicalChannelList			All	All
>>ULTrCH-Id	TrCH1: 1	TrCH1: 1	TrCH1: 1, TrCH2: 2, TrCH3: 3	TrCH1: 1, TrCH2: 2, TrCH3: 3, TrCH4: 4
>dch-QualityTarget				
>>bler-QualityValue	TrCH1: $5x10^{-2}$	TrCH1: $5x10^{-2}$	TrCH1: $7x10^{-3}$ TrCH2- TrCH3: Absent	TrCH1: $7x10^{-3}$ TrCH2- TrCH4: Absent
TrCH INFORMATION, COMMON				
ul-CommonTransChInfo				
>tfcs-ID (TDD only)	1	1	1	1
>sharedChannelIndicator (TDD only)	FALSE	FALSE	FALSE	FALSE
>tfc-Subset	Absent, not required	Absent, not required	Absent, not required	Absent, not required
>ul-TFCS	Normal TFCI signalling	Normal TFCI signalling	Normal TFCI signalling	Normal TFCI signalling
>>explicitTFCS-ConfigurationMode	Complete	Complete	Complete	Complete
>>>ctfcSize	Ctfc2Bit	Ctfc2Bit	Ctfc4Bit	Ctfc6Bit
>>>>TFCS representation	Addition	Addition	Addition	Addition
>>>>>TFCS list				
>>>>>>TFCS 1	(TF0)	(TF0)	(TF0, TF0, TF0)	(TF0, TF0, TF0, TF0)
>>>>>>>ctfc	0	0	0	0
>>>>>>>gainFactorInformation	Computed	Computed	Computed	Computed
>>>>>>>>referenceTFCId	0	0	0	0
>>>>>>TFCS 2	(TF1)	(TF1)	(TF1, TF0, TF0)	(TF1, TF0, TF0, TF0)
>>>>>>>ctfc	1	1	1	1
>>>>>>>gainFactorInformation	Signalled	Signalled	Computed	Computed
>>>>>>>>βc (FDD only)	11	11	N/A	N/A
>>>>>>>>βd	15	15	N/A	N/A
>>>>>>>>referenceTFCId	0	0	0	0
>>>>>>TFCS 3			(TF2, TF1, TF0)	(TF2, TF1, TF1, TF0)
>>>>>>>ctfc			5	11
>>>>>>>gainFactorInformation			Computed	Computed
>>>>>>>>referenceTFCId			0	0
>>>>>>TFCS 4			(TF0, TF0, TF1)	(TF0, TF0, TF0, TF1)
>>>>>>>ctfc			6	12
>>>>>>>gainFactorInformation			Computed	Computed
>>>>>>>>βc (FDD only)			N/A	N/A

Configuration	3.4 kbps signalling	13.6 kbps signalling	7.95 kbps speech + 3.4 kbps signalling	12.2 kbps speech + 3.4 kbps signalling
>>>>>>>>βd			N/A	N/A
>>>>>>>>referenceTFCId			0	0
>>>>>>TFCS 5			(TF1, TF0, TF1)	(TF1, TF0, TF0, TF1)
>>>>>>>ctfc			7	13
>>>>>>>gainFactorInformation			Computed	Computed
>>>>>>>>referenceTFCId			0	0
>>>>>>TFCS 6			(TF2, TF1, TF1)	(TF2, TF1, TF1, TF1)
>>>>>>>ctfc			11	23
>>>>>>>gainFactorInformation			Signalled	Signalled
>>>>>>>>βc (FDD only)			11	11
>>>>>>>>βd			15	15
>>>>>>>>referenceTFCId			0	0
dl-CommonTransChInfo				
>tfcs-SignallingMode	Same as UL	Same as UL	Same as UL	Same as UL
PhyCH INFORMATION FDD				
UL-DPCH-InfoPredef				
>ul-DPCH-PowerControlInfo				
>>powerControlAlgorithm	Algorithm 1	Algorithm 1	Algorithm 1	Algorithm 1
>>>tpcStepSize	1 dB	1 dB	1 dB	1 dB
>tfci-Existence	TRUE	TRUE	TRUE	TRUE
>puncturingLimit	1	1	1	0.88
DL-CommonInformationPredef				
>dl-DPCH-InfoCommon				
>>spreadingFactor	256	128	128	128
>>tfci-Existence	FALSE	FALSE	FALSE	FALSE
>>pilotBits	4	4	4	4
>>positionFixed	N/A	N/A	Fixed	Fixed
PhyCH INFORMATION 3.84 Mcps TDD				
UL-DPCH-InfoPredef				
>ul-DPCH-PowerControlInfo				
>>dpch-ConstantValue	0	0	0	0
>commonTimeslotInfo				
>>secondInterleavingMode	frameRelated	frameRelated	frameRelated	frameRelated
>>tfci-Coding	4	4	16	16
>>puncturingLimit	1	0.92	0.52	0.88
>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
DL-CommonInformationPredef				
>dl-DPCH-InfoCommon				
>>commonTimeslotInfo				
>>>secondInterleavingMode	frameRelated	frameRelated	frameRelated	frameRelated
>>>tfci-Coding	4	4	16	16
>>>puncturingLimit	1	0.92	0.52	0.92
>>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
PhyCH INFORMATION 1.28 Mcps TDD				
UL-DPCH-InfoPredef				

Configuration	3.4 kbps signalling	13.6 kbps signalling	7.95 kbps speech + 3.4 kbps signalling	12.2 kbps speech + 3.4 kbps signalling
>commonTimeslotInfo				
>>secondInterleavingMode	frameRelated	frameRelated	frameRelated	frameRelated
>>tfci-Coding	4	4	16	16
>>puncturingLimit	1	0.64	0.80	0.60
>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
DL-CommonInformationPredef				
>dl-DPCH-InfoCommon				
>>commonTimeslotInfo				
>>>secondInterleavingMode	frameRelated	frameRelated	frameRelated	frameRelated
>>>tfci-Coding	4	4	16	16
>>>puncturingLimit	1	0.64	0.80	0.60
>>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1

Configuration	28.8 kbps conv. CS- data + 3.4 kbps signalling	32 kbps conv. CS-data + 3.4 kbps signalling	64kbps conv. CS-data + 3.4 kbps signalling	14.4 kbps streaming CS-data + 3.4 kbps signalling
Ref 34.108	12	14	13	15
Default configuration identity	4	5	6	7
RB INFORMATION				
rb-Identity	RB1: 1, RB2: 2, RB3: 3, RB5: 5	RB1: 1, RB2: 2, RB3: 3, RB5: 5	RB1: 1, RB2: 2, RB3: 3, RB5: 5	RB1: 1, RB2: 2, RB3: 3, RB5: 5
rlc-InfoChoice	Rlc-info	Rlc-info	Rlc-info	Rlc-info
>ul-RLC-Mode	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5: TM
>>transmissionRLC-DiscardMode	RB1: N/A RB2- RB3: NoDiscard RB5: N/A	RB1: N/A RB2- RB3: NoDiscard RB5: N/A	RB1: N/A RB2- RB3: NoDiscard RB5: N/A	RB1: N/A RB2- RB3: NoDiscard RB5: N/A
>>>maxDat	RB1: N/A RB2- RB3: 15 RB5: N/A	RB1: N/A RB2- RB3: 15 RB5: N/A	RB1: N/A RB2- RB3: 15 RB5: N/A	RB1: N/A RB2- RB3: 15 RB5: N/A
>>transmissionWindowSize	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A
>>timerRST	RB1: N/A RB2- RB3: 300 RB5: N/A	RB1: N/A RB2- RB3: 300 RB5: N/A	RB1: N/A RB2- RB3: 300 RB5: N/A	RB1: N/A RB2- RB3: 300 RB5: N/A
>>max-RST	RB1: N/A RB2- RB3: 1 RB5: N/A	RB1: N/A RB2- RB3: 1 RB5: N/A	RB1: N/A RB2- RB3: 1 RB5: N/A	RB1: N/A RB2- RB3: 1 RB5: N/A
>>pollingInfo	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5: N/A
>>>lastTransmissionPDU-Poll	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>lastRetransmissionPDU-Poll	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerPollPeriodic	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A RB5: FALSE	RB1- RB3: N/A RB5: FALSE	RB1- RB3: N/A RB5: FALSE	RB1- RB3: N/A RB5: FALSE

Configuration	28.8 kbps conv. CS- data + 3.4 kbps signalling	32 kbps conv. CS-data + 3.4 kbps signalling	64kbps conv. CS-data + 3.4 kbps signalling	14.4 kbps streaming CS-data + 3.4 kbps signalling
>dl-RLC-Mode	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5: TM
>>inSequenceDelivery	RB1: N/A RB2- RB3: TRUE RB5: N/A	RB1: N/A RB2- RB3: TRUE RB5: N/A	RB1: N/A RB2- RB3: TRUE RB5: N/A	RB1: N/A RB2- RB3: TRUE RB5: N/A
>>receivingWindowSize	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A
>>dl-RLC-StatusInfo	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5: N/A
>>>timerStatusProhibit	RB2- RB3: 100	RB2- RB3: 100	RB2- RB3: 100	RB2- RB3: 100
>>>missingPDU-Indicator	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerStatusPeriodic	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A RB5: FALSE	RB1- RB3: N/A RB5: FALSE	RB1- RB3: N/A RB5: FALSE	RB1- RB3: N/A RB5: FALSE
rb-MappingInfo				
>UL-LogicalChannelMappings	OneLogicalChannel	OneLogicalChannel	OneLogicalChannel	OneLogicalChannel
>>ul-TransportChannelType	Dch	Dch	Dch	Dch
>>>transportChannelIdentity	RB1- RB3: 2 RB5: 1	RB1- RB3: 2 RB5: 1	RB1- RB3: 2 RB5: 1	RB1- RB3: 2 RB5: 1
>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5: N/A
>>rlc-SizeList	RB1- RB3: configured RB5: N/A	RB1- RB3: configured RB5: N/A	RB1- RB3: configured RB5: N/A	RB1- RB3: configured RB5: N/A
>>mac-LogicalChannelPriority	RB1: 1, RB2: 2, RB3: 3 RB5: 5	RB1: 1, RB2: 2, RB3: 3 RB5: 5	RB1: 1, RB2: 2, RB3: 3 RB5: 5	RB1: 1, RB2: 2, RB3: 3 RB5: 5
>DL-logicalChannelMappingList				
>>Mapping option 1	One mapping option	One mapping option	One mapping option	One mapping option
>>>dl-TransportChannelType	Dch	Dch	Dch	Dch
>>>>transportChannelIdentity	RB1- RB3: 2 RB5: 1	RB1- RB3: 2 RB5: 1	RB1- RB3: 2 RB5: 1	RB1- RB3: 2 RB5: 1
>>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5: N/A
TrCH INFORMATION PER TrCH				
UL-AddReconfTransChInfoList				
>Uplink transport channel type	dch	dch	dch	dch
>transportChannelIdentity	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2
>transportFormatSet	DedicatedTransChTFS	DedicatedTransChTFS	DedicatedTransChTFS	DedicatedTransChTFS
>>dynamicTF-information				
>>>tf0/ tf0,1	TrCH1: (0x576, 1x576, 2x576) TrCH2: (0x144, 1x144)	TrCH1: (0x640, 1x640) TrCH2: (0x144, 1x144)	TrCH1: (0x640, 2x640) TrCH2: (0x144, 1x144)	TrCH1: (0x576, 1x576) TrCH2: (0x144, 1x144)

Configuration	28.8 kbps conv. CS- data + 3.4 kbps signalling	32 kbps conv. CS-data + 3.4 kbps signalling	64kbps conv. CS-data + 3.4 kbps signalling	14.4 kbps streaming CS-data + 3.4 kbps signalling
>>>>rlcSize	TrCH1: OctetMode TrCH2:BitMode	TrCH1: OctetMode TrCH2:BitMode	TrCH1: OctetMode TrCH2:BitMode	TrCH1: OctetMode TrCH2:BitMode
>>>>>sizeType	TrCH1: type 2, part1= 9, part2= 2 (576) TrCH2: type 2, part1= 2, part2= 0 (144)	TrCH1: type 2, part1= 11, part2= 2 (640) TrCH2: type 2, part1= 2, part2= 0 (144)	TrCH1: type 2, part1= 11, part2= 2 (640) TrCH2: type 2, part1= 2, part2= 0 (144)	TrCH1: type 2, part1= 9, part2= 2 (576) TrCH2: type 2, part1= 2, part2= 0 (144)
>>>>numberOfTbSizeList	TrCH1: Zero,1, 2 TrCH2: Zero, one	TrCH1: Zero, one TrCH2: Zero, one	TrCH1: Zero, 2 TrCH2: Zero, one	TrCH1: Zero, one, TrCH2: Zero, one
>>>>logicalChannelList	All	All	All	All
>>semiStaticTF-Information				
>>>tti	TrCH1: 40 TrCH2: 40	TrCH1: 20 TrCH2: 40	TrCH1: 20 TrCH2: 40	TrCH1: 40 TrCH2: 40
>>>channelCodingType	TrCH1: Turbo TrCH2: Convolutional	TrCH1: Turbo TrCH2: Convolutional	TrCH1: Turbo TrCH2: Convolutional	TrCH1: Turbo TrCH2: Convolutional
>>>>codingRate	TrCH1: N/A TrCH2: Third	TrCH1: N/A TrCH2: Third	TrCH1: N/A TrCH2: Third	TrCH1: N/A TrCH2: Third
>>>rateMatchingAttribute	TrCH1: 180 TrCH2: 160	TrCH1: 185 TrCH2: 160	TrCH1: 170 TrCH2: 160	TrCH1: 165 TrCH2: 160
>>>crc-Size	TrCH1: 16 TrCH2: 16	TrCH1: 16 TrCH2: 16	TrCH1: 16 TrCH2: 16	TrCH1: 16 TrCH2: 16
DL-AddReconfTransChInfoList				
>Downlink transport channel type	dch	dch	dch	dch
>dl-TransportChannelIdentity (should be as for UL)	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2
>tfs-SignallingMode	SameAsUL	SameAsUL	SameAsUL	SameAsUL
>>transportFormatSet				
>>>dynamicTF-information				
>>>>tf0/ tf0,1				
>>>>rlcSize				
>>>>>sizeType				
>>>>numberOfTbSizeList				
>>>>logicalChannelList				
>>ULTrCH-Id	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2
>dch-QualityTarget				
>>bler-QualityValue	TrCH1: $2x10^{-3}$ TrCH2: Absent	TrCH1: $2x10^{-3}$ TrCH2: Absent	TrCH1: $2x10^{-3}$ TrCH2: Absent	TrCH1: $1x10^{-2}$ TrCH2: Absent
TrCH INFORMATION, COMMON				
ul-CommonTransChInfo				
>tfcs-ID (TDD only)	1	1	1	1
>sharedChannelIndicator (TDD only)	FALSE	FALSE	FALSE	FALSE
>tfc-Subset	Absent, not required	Absent, not required	Absent, not required	Absent, not required
>ul-TFCS	Normal TFCI signalling	Normal TFCI signalling	Normal TFCI signalling	Normal TFCI signalling
>>explicitTFCS-ConfigurationMode	Complete	Complete	Complete	Complete
>>>ctfcSize	Ctfc4Bit	Ctfc2Bit	Ctfc2Bit	Ctfc4Bit
>>>>TFCS representation	Addition	Addition	Addition	Addition
>>>>>TFCS list				
>>>>>>TFCS 1	(TF0, TF0)	(TF0, TF0)	(TF0, TF0)	(TF0, TF0)
>>>>>>>ctfc	0	0	0	0
>>>>>>>gainFactorInformation	Computed	Computed	Computed	Computed

Configuration	28.8 kbps conv. CS- data + 3.4 kbps signalling	32 kbps conv. CS-data + 3.4 kbps signalling	64kbps conv. CS-data + 3.4 kbps signalling	14.4 kbps streaming CS-data + 3.4 kbps signalling
>>>>>>>>referenceTFCId	0	0	0	0
>>>>>>TFCS 2	(TF1, TF0)	(TF1, TF0)	(TF1, TF0)	(TF1, TF0)
>>>>>>>ctfc	1	1	1	1
>>>>>>>gainFactorInformation	Computed	Computed	Computed	Computed
>>>>>>>>βc (FDD only)	N/A	N/A	N/A	N/A
>>>>>>>>βd	N/A	N/A	N/A	N/A
>>>>>>>>referenceTFCId	0	0	0	0
>>>>>>TFCS 3	(TF2, TF0)	(TF0, TF1)	(TF0, TF1)	(TF0, TF1)
>>>>>>>ctfc	2	2	2	2
>>>>>>>gainFactorInformation	Computed	Computed	Computed	Computed
>>>>>>>>referenceTFCId	0	0	0	0
>>>>>>TFCS 4	(TF0, TF1)	(TF1, TF1)	(TF1, TF1)	(TF1, TF1)
>>>>>>>ctfc	3	3	3	3
>>>>>>>gainFactorInformation	Computed	Signalled	Signalled	Signalled
>>>>>>>>βc (FDD only)	N/A	8	8	11
>>>>>>>>βd	N/A	15	15	15
>>>>>>>>referenceTFCId	0	0	0	0
>>>>>>TFCS 5	(TF1, TF1)	N/A	N/A	
>>>>>>>ctfc	4			
>>>>>>>gainFactorInformation	Computed			
>>>>>>>>referenceTFCId	0			
>>>>>>TFCS 6	(TF2, TF1)	N/A	N/A	
>>>>>>>ctfc	5			
>>>>>>>gainFactorInformation	Signalled			
>>>>>>>>βc (FDD only)	8			
>>>>>>>>βd	15			
>>>>>>>>referenceTFCId	0			
>>>>>>TFCS 7				
>>>>>>>ctfc				
>>>>>>>gainFactorInformation				
>>>>>>>>referenceTFCId				
>>>>>>TFCS 8				
>>>>>>>ctfc				
>>>>>>>gainFactorInformation				
>>>>>>>>referenceTFCId				
>>>>>>TFCS 9				
>>>>>>>ctfc				
>>>>>>>gainFactorInformation				
>>>>>>>>referenceTFCId				
>>>>>>TFCS 10				
>>>>>>>ctfc				
>>>>>>>gainFactorInformation				
>>>>>>>>βc (FDD only)				
>>>>>>>>βd				
>>>>>>>>referenceTFCId				
dl-CommonTransChInfo				
>tfcs-SignallingMode	Same as UL	Same as UL	Same as UL	Same as UL
PhyCH INFORMATION FDD				

Configuration	28.8 kbps conv. CS- data + 3.4 kbps signalling	32 kbps conv. CS-data + 3.4 kbps signalling	64kbps conv. CS-data + 3.4 kbps signalling	14.4 kbps streaming CS-data + 3.4 kbps signalling
UL-DPCH-InfoPredef				
>ul-DPCH-PowerControlInfo				
>>powerControlAlgorithm	Algorithm 1	Algorithm 1	Algorithm 1	Algorithm 1
>>>tpcStepSize	1 dB	1 dB	1 dB	1 dB
>tfci-Existence	TRUE	TRUE	TRUE	TRUE
>puncturingLimit	0.92	0.8	0.92	1
DL-CommonInformationPredef				
>dl-DPCH-InfoCommon				
>>spreadingFactor	64	64	32	128
>>tfci-Existence	TRUE	TRUE	TRUE	TRUE
>>pilotBits	8	8	8	8
>>positionFixed	Flexible	Flexible	Flexible	Flexible
PhyCH INFORMATION 3.84 Mcps TDD				
UL-DPCH-InfoPredef				
>ul-DPCH-PowerControlInfo				
>>dpch-ConstantValue	0	0	0	0
>commonTimeslotInfo				
>>secondInterleavingMode	frameRelated	frameRelated	frameRelated	frameRelated
>>tfci-Coding	16	8	8	8
>>puncturingLimit	0.44	0.8	0.56	0.8
>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
DL-CommonInformationPredef				
>dl-DPCH-InfoCommon				
>>commonTimeslotInfo				
>>>secondInterleavingMode	frameRelated	frameRelated	frameRelated	frameRelated
>>>tfci-Coding	16	8	8	8
>>>puncturingLimit	0.44	0.64	0.56	0.8
>>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
PhyCH INFORMATION 1.28 Mcps TDD				
UL-DPCH-InfoPredef				
>commonTimeslotInfo				
>>secondInterleavingMode	frameRelated	frameRelated	frameRelated	frameRelated
>>tfci-Coding	16	8	8	8
>>puncturingLimit	0.64	0.60	0.64	1
>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
DL-CommonInformationPredef				
>dl-DPCH-InfoCommon				
>>commonTimeslotInfo				
>>>secondInterleavingMode	frameRelated	frameRelated	frameRelated	frameRelated
>>>tfci-Coding	16	8	8	8
>>>puncturingLimit	0.64	0.60	0.64	0.88
>>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1

Configuration	28.8 kbps streaming CS-data + 3.4 kbps signalling	57.6 kbps streaming CS-data + 3.4 kbps signalling	12.2 kbps speech(multimode) + 3.4 kbps signalling
Ref 34.108	16	17	1a
Default configuration identity	8	9	10
RB INFORMATION			
rb-Identity	RB1: 1, RB2: 2, RB3: 3, RB5: 5	RB1: 1, RB2: 2, RB3: 3, RB5: 5	RB1: 1, RB2: 2, RB3: 3, RB5: 5, RB6: 6, RB7: 7
rlc-InfoChoice	Rlc-info	Rlc-info	Rlc-info
>ul-RLC-Mode	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5-RB7: TM
>>transmissionRLC-DiscardMode	RB1: N/A RB2- RB3: NoDiscard RB5: N/A	RB1: N/A RB2- RB3: NoDiscard RB5: N/A	RB1: N/A RB2- RB3: NoDiscard RB5- RB7: N/A
>>>maxDat	RB1: N/A RB2- RB3: 15 RB5: N/A	RB1: N/A RB2- RB3: 15 RB5: N/A	RB1: N/A RB2- RB3: 15 RB5- RB7: N/A
>>transmissionWindowSize	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB7: N/A
>>timerRST	RB1: N/A RB2- RB3: 300 RB5: N/A	RB1: N/A RB2- RB3: 300 RB5: N/A	RB1: N/A RB2- RB3: 300 RB5- RB7: N/A
>>max-RST	RB1: N/A RB2- RB3: 1 RB5: N/A	RB1: N/A RB2- RB3: 1 RB5: N/A	RB1: N/A RB2- RB3: 1 RB5- RB7: N/A
>>pollingInfo	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5- RB7: N/A
>>>lastTransmissionPDU-Poll	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>lastRetransmissionPDU-Poll	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerPollPeriodic	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A RB5: FALSE	RB1- RB3: N/A RB5: FALSE	RB1- RB3: N/A RB5- RB7: FALSE
>dl-RLC-Mode	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5- RB7: TM
>>inSequenceDelivery	RB1: N/A RB2- RB3: TRUE RB5: N/A	RB1: N/A RB2- RB3: TRUE RB5: N/A	RB1: N/A RB2- RB3: TRUE RB5- RB7: N/A
>>receivingWindowSize	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB7: N/A
>>dl-RLC-StatusInfo	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5- RB7: N/A
>>>timerStatusProhibit	RB2- RB3: 100	RB2- RB3: 100	RB2- RB3: 100
>>>missingPDU-Indicator	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerStatusPeriodic	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A RB5: FALSE	RB1- RB3: N/A RB5: FALSE	RB1- RB3: N/A RB5- RB7: FALSE

Configuration	28.8 kbps streaming CS-data + 3.4 kbps signalling	57.6 kbps streaming CS-data + 3.4 kbps signalling	12.2 kbps speech(multimode) + 3.4 kbps signalling
rb-MappingInfo			
>UL-LogicalChannelMappings	OneLogicalChannel	OneLogicalChannel	OneLogicalChannel
>>ul-TransportChannelType	Dch	Dch	Dch
>>>transportChannelIdentity	RB1- RB3: 2 RB5: 1	RB1- RB3: 2 RB5: 1	RB1- RB3: 4 RB5: 1, RB6: 2, RB7: 3
>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: N/A
>>rlc-SizeList	RB1- RB3: configured RB5: N/A	RB1- RB3: configured RB5: N/A	RB1- RB3: configured RB5- RB7: N/A
>>mac-LogicalChannelPriority	RB1: 1, RB2: 2, RB3: 3 RB5: 5	RB1: 1, RB2: 2, RB3: 3 RB5: 5	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: 5
>DL-logicalChannelMappingList			
>>Mapping option 1	One mapping option	One mapping option	One mapping option
>>>dl-TransportChannelType	Dch	Dch	Dch
>>>>transportChannelIdentity	RB1- RB3: 2 RB5: 1	RB1- RB3: 2 RB5: 1	RB1- RB3: 4 RB5: 1, RB6: 2, RB7: 3
>>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: N/A
TrCH INFORMATION PER TrCH			
UL-AddReconfTransChInfoList			
>Uplink transport channel type	dch	dch	dch
>transportChannelIdentity	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2, TrCH3: 3, TrCH4: 4
>transportFormatSet	DedicatedTransChTFS	DedicatedTransChTFS	DedicatedTransChTFS
>>dynamicTF-information			
>>>tf0/ tf0,1	TrCH1: (0x576, 1x576, 2x576) TrCH2: (0x144, 1x144)	TrCH1: (0x576, 1x576, 2x576, 3x576, 4x576) TrCH2: (0x144, 1x144)	TrCH1: (0x81) TrCH2: (0x 103 TrCH3: (0x 60) TrCH4: (0x144)
>>>>rlcSize	TrCH1: OctetMode TrCH2:BitMode	TrCH1: OctetMode TrCH2:BitMode	BitMode
>>>>>sizeType	TrCH1: type 2, part1= 9, part2= 2 (576) TrCH2: type 2, part1= 2, part2= 0 (144)	TrCH1: type 2, part1= 9, part2= 2 (576) TrCH2: type 2, part1= 2, part2= 0 (144)	TrCH1: type 1: 81 TrCH2: type 1: 103 TrCH3: type 1: 60 TrCH4: 2: type 2, part1= 2, part2= 0 (144)
>>>>numberOfTbSizeList	TrCH1: Zero, one, 2 TrCH2: Zero, one	TrCH1: Zero, one, 2, 3, 4 TrCH2: Zero, one	TrCH1-4: Zero
>>>>logicalChannelList	All	All	All
>>>tf 1			TrCH1: (1x39) TrCH2: (1x53) TrCH3: (1x60) TrCH4: (1x144)

Configuration	28.8 kbps streaming CS-data + 3.4 kbps signalling	57.6 kbps streaming CS-data + 3.4 kbps signalling	12.2 kbps speech(multimode) + 3.4 kbps signalling
>>>>numberOfTransportBlocks			TrCH1-3: One
>>>>rlc-Size			TrCH1-3: BitMode
>>>>>sizeType			TrCH1: 1: 39 TrCH2: 1: 53 TrCH3: 1: 60
>>>>numberOfTbSizeList			TrCH1-3: One
>>>>logicalChannelList			TrCH1-3: all
>>>tf 2			TrCH1: (1x42) TrCH2: (1x63) TrCH3- TrCH4: N/A
>>>>numberOfTransportBlocks			TrCH1-2: One
>>>>rlc-Size			TrCH1: BitMode
>>>>>sizeType			TrCH1: type 1: 42 TrCH2: type 1: 63
>>>>numberOfTbSizeList			TrCH1-2: One
>>>>logicalChannelList			TrCH1: all
>>>tf 3			TrCH1: (1x55) TrCH2: (1x84) TrCH3- TrCH4: N/A
>>>>numberOfTransportBlocks			TrCH1-2: One
>>>>rlc-Size			TrCH1: BitMode
>>>>>sizeType			TrCH1: type 1: 55 TrCH2: type 1: 84
>>>>numberOfTbSizeList			TrCH1-2: One
>>>>logicalChannelList			TrCH1: all
>>>tf 4			TrCH1: (1x75) TrCH2: (1x103) TrCH3- TrCH4: N/A
>>>>numberOfTransportBlocks			TrCH1-2: One
>>>>rlc-Size			TrCH1: BitMode
>>>>>sizeType			TrCH1: type 1: 75 TrCH2: type 1: 103
>>>>numberOfTbSizeList			TrCH1-2: One
>>>>logicalChannelList			TrCH1: all
>>>tf 5			TrCH1: (1x81) TrCH2- TrCH4: N/A
>>>>numberOfTransportBlocks			TrCH1: One
>>>>rlc-Size			TrCH1: BitMode
>>>>>sizeType			TrCH1: type 1: 81
>>>>numberOfTbSizeList			TrCH1: One
>>>>logicalChannelList			TrCH1: all
>>semiStaticTF-Information			
>>>tti	TrCH1: 40 TrCH2: 40	TrCH1: 40 TrCH2: 40	TrCH1- TrCH3: 20 TrCH4: 40
>>>channelCodingType	TrCH1: Turbo TrCH2: Convolutional	TrCH1: Turbo TrCH2: Convolutional	Convolutional
>>>>codingRate	TrCH1: N/A TrCH2: Third	TrCH1: N/A TrCH2: Third	TrCH1- TrCH2: Third TrCH3: Half TrCH4: Third
>>>rateMatchingAttribute	TrCH1: 155 TrCH2: 160	TrCH1: 145 TrCH2: 160	TrCH1: 200 TrCH2: 190 TrCH3: 235 TrCH4: 160

Configuration	28.8 kbps streaming CS-data + 3.4 kbps signalling	57.6 kbps streaming CS-data + 3.4 kbps signalling	12.2 kbps speech(multimode) + 3.4 kbps signalling
>>>crc-Size	TrCH1: 16 TrCH2: 16	TrCH1: 16 TrCH2: 16	TrCH1: 12 TrCH2- TrCH3: 0 TrCH4: 16
DL-AddReconfTransChInfoList			
>Downlink transport channel type	dch	dch	dch
>dl-TransportChannelIdentity (should be as for UL)	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2, TrCH3: 3, TrCH4: 4
>tfs-SignallingMode	SameAsUL	SameAsUL	Independent <Only tf0 on TrCH1 is different and shown below>
>>transportFormatSet			DedicatedTransChTFS
>>>dynamicTF-information			
>>>>tf0/ tf0,1			TrCH1: (1x0)
>>>>rlcSize			bitMode
>>>>>sizeType			TrCH1: type 1: 0
>>>>numberOfTbSizeList			TrCH1: One
>>>>logicalChannelList			All
>>ULTrCH-Id	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2, TrCH3: 3, TrCH4: 4
>dch-QualityTarget			
>>bler-QualityValue	TrCH1: $1x10^{-2}$ TrCH2: Absent	TrCH1: $1x10^{-2}$ TrCH2: Absent	TrCH1: $7x10^{-3}$ TrCH2- TrCH4: Absent
TrCH INFORMATION, COMMON			
ul-CommonTransChInfo			
>tfcs-ID (TDD only)	1	1	1
>sharedChannelIndicator (TDD only)	FALSE	FALSE	FALSE
>tfc-Subset	Absent, not required	Absent, not required	Absent, not required
>ul-TFCS	Normal TFCI signalling	Normal TFCI signalling	Normal TFCI signalling
>>explicitTFCS-ConfigurationMode	Complete	Complete	Complete
>>>ctfcSize	Ctfc4Bit	Ctfc4Bit	Ctfc8Bit
>>>>TFCS representation	Addition	Addition	Addition
>>>>>TFCS list			
>>>>>>TFCS 1	(TF0, TF0)	(TF0, TF0)	(TF0, TF0, TF0, TF0)
>>>>>>>ctfc	0	0	0
>>>>>>>gainFactorInformation	Computed	Computed	Computed
>>>>>>>>referenceTFCId	0	0	0
>>>>>>TFCS 2	(TF1, TF0)	(TF1, TF0)	(TF1, TF0, TF0, TF0)
>>>>>>>ctfc	1	1	1
>>>>>>>gainFactorInformation	Computed	Computed	Computed
>>>>>>>>βc (FDD only)	N/A	N/A	N/A
>>>>>>>>βd	N/A	N/A	N/A
>>>>>>>>referenceTFCId	0	0	0
>>>>>>TFCS 3	(TF2, TF0)	(TF2, TF0)	(TF2, TF1, TF0, TF0)
>>>>>>>ctfc	2	2	8

Configuration	28.8 kbps streaming CS-data + 3.4 kbps signalling	57.6 kbps streaming CS-data + 3.4 kbps signalling	12.2 kbps speech(multimode) + 3.4 kbps signalling
>>>>>>>gainFactorInformation	Computed	Computed	Computed
>>>>>>>>referenceTFCId	0	0	0
>>>>>>TFCS 4	(TF0, TF1)	(TF3, TF0)	(TF3, TF2, TF0, TF0)
>>>>>>>ctfc	3	3	15
>>>>>>>gainFactorInformation	Computed	Computed	Computed
>>>>>>>>βc (FDD only)	N/A	N/A	N/A
>>>>>>>>βd	N/A	N/A	N/A
>>>>>>>>referenceTFCId	0	0	0
>>>>>>TFCS 5	(TF1, TF1)	(TF4, TF0)	(TF4, TF3, TF0, TF0)
>>>>>>>ctfc	4	4	22
>>>>>>>gainFactorInformation	Computed	Computed	Computed
>>>>>>>>referenceTFCId	0	0	0
>>>>>>TFCS 6	(TF2, TF1)	(TF0, TF1)	(TF5, TF4, TF1, TF0)
>>>>>>>ctfc	5	5	59
>>>>>>>gainFactorInformation	Signalled	Computed	Computed
>>>>>>>>βc (FDD only)	8	N/A	N/A
>>>>>>>>βd	15	N/A	N/A
>>>>>>>>referenceTFCId	0	0	0
>>>>>>TFCS 7		(TF1, TF1)	(TF0,TF0,TF0,TF1)
>>>>>>>ctfc		6	60
>>>>>>>gainFactorInformation		Computed	Computed
>>>>>>>>referenceTFCId		0	0
>>>>>>TFCS 8		(TF2, TF1)	(TF1,TF0,TF0,TF1)
>>>>>>>ctfc		7	61
>>>>>>>gainFactorInformation		Computed	Computed
>>>>>>>>referenceTFCId		0	0
>>>>>>TFCS 9		(TF3, TF1)	(TF2,TF1,TF0,TF1)
>>>>>>>ctfc		8	68
>>>>>>>gainFactorInformation		Computed	Computed
>>>>>>>>referenceTFCId		0	0
>>>>>>TFCS 10		(TF4, TF1)	(TF3,TF2,TF0,TF1)
>>>>>>>ctfc		9	75
>>>>>>>gainFactorInformation		Signalled	Computed
>>>>>>>>βc (FDD only)		8	N/A
>>>>>>>>βd		15	N/A
>>>>>>>>referenceTFCId		0	0
>>>>>>TFCS 11			(TF4,TF3,TF0,TF1)
>>>>>>>ctfc			82
>>>>>>>gainFactorInformation			Computed
>>>>>>>>referenceTFCId			0
>>>>>>TFCS 12			(TF5,TF4,TF1,TF1)
>>>>>>>ctfc			119
>>>>>>>gainFactorInformation			Signalled
>>>>>>>>βc (FDD only)			11
>>>>>>>>βd			15
>>>>>>>>referenceTFCId			0

Configuration	28.8 kbps streaming CS-data + 3.4 kbps signalling	57.6 kbps streaming CS-data + 3.4 kbps signalling	12.2 kbps speech(multimode) + 3.4 kbps signalling
dl-CommonTransChInfo			
>tfcs-SignallingMode	Same as UL	Same as UL	Same as UL
PhyCH INFORMATION FDD			
UL-DPCH-InfoPredef			
>ul-DPCH-PowerControlInfo			
>>powerControlAlgorithm	Algorithm 1	Algorithm 1	Algorithm 1
>>>tpcStepSize	1 dB	1 dB	1 dB
>tfci-Existence	TRUE	TRUE	TRUE
>puncturingLimit	1	1	0.88
DL-CommonInformationPredef			
>dl-DPCH-InfoCommon			
>>spreadingFactor	64	32	128
>>tfci-Existence	TRUE	TRUE	FALSE
>>pilotBits	8	8	4
>>positionFixed	Flexible	Flexible	Fixed
PhyCH INFORMATION 3.84 Mcps TDD			
UL-DPCH-InfoPredef			
>ul-DPCH-PowerControlInfo			
>>dpch-ConstantValue	0	0	0
>commonTimeslotInfo			
>>secondInterleavingMode	frameRelated	frameRelated	frameRelated
>>tfci-Coding	16	16	16
>>puncturingLimit	0.44	0.48	0.88
>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
DL-CommonInformationPredef			
>dl-DPCH-InfoCommon			
>>commonTimeslotInfo			
>>>secondInterleavingMode	frameRelated	frameRelated	frameRelated
>>>tfci-Coding	16	16	16
>>>puncturingLimit	0.44	0.48	0.92
>>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
PhyCH INFORMATION 1.28 Mcps TDD			
UL-DPCH-InfoPredef			
>commonTimeslotInfo			
>>secondInterleavingMode	frameRelated	frameRelated	
>>tfci-Coding	16	16	
>>puncturingLimit	0.64	0.72	
>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	
DL-CommonInformationPredef			
>dl-DPCH-InfoCommon			
>>commonTimeslotInfo			
>>>secondInterleavingMode	frameRelated	frameRelated	frameRelated

Configuration	28.8 kbps streaming CS-data + 3.4 kbps signalling	57.6 kbps streaming CS-data + 3.4 kbps signalling	12.2 kbps speech(multimode) + 3.4 kbps signalling
>>>tfci-Coding	16	16	16
>>>puncturingLimit	0.64	0.72	0.92
>>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1

Configuration	10.2/6.7/5.9/4.75 kbps speech + 3.4 kbps signalling	7.4/6.7/5.9/4.75 kbps speech + 3.4 kbps signalling
Ref 34.108	N/A	N/A
Default configuration identity	11	12
RB INFORMATION		
rb-Identity	RB1: 1, RB2: 2, RB3: 3, RB5: 5, RB6: 6, RB7: 7, RB8: 8	RB1: 1, RB2: 2, RB3: 3, RB5: 5, RB6: 6, RB7: 7
rlc-InfoChoice	Rlc-info	Rlc-info
>ul-RLC-Mode	RB1: UM RB2- RB3: AM RB5-RB7: TM	RB1: UM RB2- RB3: AM RB5-RB6: TM
>>transmissionRLC-DiscardMode	RB1: N/A RB2- RB3: NoDiscard RB5- RB7: N/A	RB1: N/A RB2- RB3: NoDiscard RB5- RB6: N/A
>>>maxDat	RB1: N/A RB2- RB3: 15 RB5- RB7: N/A	RB1: N/A RB2- RB3: 15 RB5- RB6: N/A
>>transmissionWindowSize	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB7: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB6: N/A
>>timerRST	RB1: N/A RB2- RB3: 300 RB5- RB7: N/A	RB1: N/A RB2- RB3: 300 RB5- RB6: N/A
>>max-RST	RB1: N/A RB2- RB3: 1 RB5- RB7: N/A	RB1: N/A RB2- RB3: 1 RB5- RB6: N/A
>>pollingInfo	RB1: N/A RB2- RB3: as below RB5- RB7: N/A	RB1: N/A RB2- RB3: as below RB5- RB6: N/A
>>>lastTransmissionPDU-Poll	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>lastRetransmissionPDU-Poll	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerPollPeriodic	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A RB5- RB7: FALSE	RB1- RB3: N/A RB5- RB6: FALSE
>dl-RLC-Mode	RB1: UM RB2- RB3: AM RB5- RB7: TM RB8: TM	RB1: UM RB2- RB3: AM RB5- RB6: TM RB7: TM
>>inSequenceDelivery	RB1: N/A RB2- RB3: TRUE RB5- RB8: N/A	RB1: N/A RB2- RB3: TRUE RB5- RB7: N/A
>>receivingWindowSize	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB8: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB7: N/A
>>dl-RLC-StatusInfo	RB1: N/A RB2- RB3: as below RB5- RB8: N/A	RB1: N/A RB2- RB3: as below RB5- RB7: N/A
>>>timerStatusProhibit	RB2- RB3: 100	RB2- RB3: 100
>>>missingPDU-Indicator	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerStatusPeriodic	RB2- RB3: 300	RB2- RB3: 300

>>segmentationIndication	RB1- RB3: N/A RB5- RB8: FALSE	RB1- RB3: N/A RB5- RB7: FALSE
rb-MappingInfo		
>UL-LogicalChannelMappings	OneLogicalChannel	OneLogicalChannel
>>ul-TransportChannelType	Dch	Dch
>>>transportChannelIdentity	RB1- RB3: 4 RB5: 1, RB6: 2, RB7: 3,	RB1- RB3: 3 RB5: 1, RB6: 2
>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: N/A	RB1: 1, RB2: 2, RB3: 3 RB5- RB6: N/A
>>rlc-SizeList	RB1- RB3: configured RB5- RB7: N/A	RB1- RB3: configured RB5- RB6: N/A
>>mac-LogicalChannelPriority	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: 5	RB1: 1, RB2: 2, RB3: 3 RB5- RB6: 5
>DL-logicalChannelMappingList		
>>Mapping option 1	One mapping option	One mapping option
>>>dl-TransportChannelType	Dch	Dch
>>>>transportChannelIdentity	RB1- RB3: 4 RB5: 1, RB6: 2, RB7: 3, RB8: 5	RB1- RB3: 3 RB5: 1, RB6: 2, RB7:4
>>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3 RB5- RB8: N/A	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: N/A
TrCH INFORMATION PER TrCH		
UL-AddReconfTransChInfoList		
>Uplink transport channel type	dch	dch
>transportChannelIdentity	TrCH1: 1, TrCH2: 2, TrCH3: 3, TrCH4: 4	TrCH1: 1, TrCH2: 2, TrCH3: 3
>transportFormatSet	DedicatedTransChTFS	DedicatedTransChTFS
>>dynamicTF-information		
>>>tf0/ tf0,1	TrCH1: (0x65) TrCH2: (0x 99) TrCH3: (0x 40, 1x40) TrCH4: (0x144, 1x144)	TrCH1: (0x61) TrCH2: (0x 87) TrCH3: (0x 144, 1x144)
>>>>rlcSize	BitMode	BitMode
>>>>>sizeType	TrCH1: type 1: 65 TrCH2: type 1: 99 TrCH3: type 1: 40 TrCH4: 2: type 2, part1= 2, part2= 0 (144)	TrCH1: type 1: 61 TrCH2: type 1: 87 TrCH3: 2: type 2, part1= 2, part2= 0 (144)
>>>>numberOfTbSizeList	TrCH1-2: Zero TrCH3-4: Zero, one	TrCH1-2: Zero TrCH3: Zero, one
>>>>logicalChannelList	All	All
>>>tf 1	TrCH1: (1x39) TrCH2: (1x 53) TrCH3- TrCH4: N/A	TrCH1: (1x39) TrCH2: (1x53) TrCH3: N/A
>>>>numberOfTransportBlocks	TrCH1: One TrCH2: One	TrCH1: One TrCH2: One
>>>>rlc-Size	TrCH1-2: BitMode	TrCH1-2: BitMode
>>>>>sizeType	TrCH1: 1: 39 TrCH2: 1: 53	TrCH1: 1: 39 TrCH1: 1: 53
>>>>numberOfTbSizeList	TrCH1-2: One	TrCH1-2: One

>>>>logicalChannelList	TrCH1: all	TrCH1: all
>>>tf 2	TrCH1: (1x42) TrCH2: (1x63) TrCH3- TrCH4: N/A	TrCH1: (1x42) TrCH2: (1x63) TrCH3: N/A
>>>>numberOfTransportBlocks	TrCH1: One TrCh2: One	TrCH1: One TrCh2: One
>>>>rlc-Size	TrCH1: BitMode	TrCH1: BitMode
>>>>>sizeType	TrCH1: type 1: 42 TrCH2: type 1: 63	TrCH1: type 1: 42 TrCH2: type 1: 63
>>>>numberOfTbSizeList	TrCH1: One TrCH2: One	TrCH1: One TrCH2: One
>>>>logicalChannelList	TrCH1: all TrCH2: all	TrCH1: all TrCH2: all
>>>tf 3	TrCH1: (1x55) TrCH2: (1x76) TrCH3- TrCH4: N/A	TrCH1: (1x55) TrCH2: (1x76) TrCH3: N/A
>>>>numberOfTransportBlocks	TrCH1: One TrCh2: One	TrCH1: One TrCh2: One
>>>>rlc-Size	TrCH1: BitMode	TrCH1: BitMode
>>>>>sizeType	TrCH1: type 1: 55 TrCH2: type 1: 76	TrCH1: type 1: 55 TrCH2: type 1: 76
>>>>numberOfTbSizeList	TrCH1: One TrCH2: One	TrCH1: One TrCH2: One
>>>>logicalChannelList	TrCH1: all TrCH2: all	TrCH1: all TrCH2: all
>>>tf 4	TrCH1: (1x58) TrCH2: (1x99) TrCH3- TrCH4: N/A	TrCH1: (1x58) TrCH2: (1x87) TrCH3: N/A
>>>>numberOfTransportBlocks	TrCH1: One TrCh2: One	TrCH1: One TrCh2: One
>>>>rlc-Size	TrCH1: BitMode	TrCH1: BitMode
>>>>>sizeType	TrCH1: type 1: 58 TrCH2: type 1: 99	TrCH1: type 1: 58 TrCH2: type 1: 87
>>>>numberOfTbSizeList	TrCH1: One TrCH2: One	TrCH1: One TrCH2: One
>>>>logicalChannelList	TrCH1: all TrCH2: all	TrCH1: all TrCH2: all
>>>tf 5	TrCH1: (1x65) TrCH2- TrCH4: N/A	TrCH1: (1x61) TrCH2- TrCH4: N/A
>>>>numberOfTransportBlocks	TrCH1: One	TrCH1: One
>>>>rlc-Size	TrCH1: BitMode	TrCH1: BitMode
>>>>>sizeType	TrCH1: type 1: 65	TrCH1: type 1: 61
>>>>numberOfTbSizeList	TrCH1: One	TrCH1: One
>>>>logicalChannelList	TrCH1: all	TrCH1: all
>>semistaticTF-Information		
>>>tti	TrCH1- TrCH3: 20 TrCH4: 40	TrCH1- TrCH2: 20 TrCH3: 40
>>>channelCodingType	Convolutional	Convolutional
>>>>codingRate	TrCH1- TrCH2: Third TrCH3: Half TrCH4: Third	TrCH1- TrCH2: Third TrCH3: Third
>>>rateMatchingAttribute	TrCH1: 200 TrCH2: 190 TrCH3: 235 TrCH4: 160	TrCH1: 200 TrCH2: 190 TrCH3: 160
>>>crc-Size	TrCH1: 12 TrCH2- TrCH3: 0 TrCH4: 16	TrCH1: 12 TrCH2: 0 TrCH3: 16
DL-AddReconfTransChInfoList		
>Downlink transport channel type	dch	dch
>dl-TransportChannelIdentity		

>tfs-SignallingMode	Independent <Only tf0 on TrCH1 and tf0/tf1 on TrCH5 are different and shown below>	Independent <Only tf0 on TrCH1 and tf0/tf1 on TrCH4 are different and shown below>
>>transportFormatSet		
>>>dynamicTF-information		
>>>>tf0/ tf0,1	TrCH1: (1x0) TrCH5: (0x3, 1x3)	TrCH1: (1x0) TrCH4: (0x3, 1x3)
>>>>rlcSize	BitMode	bitMode
>>>>>sizeType	TrCH1: type 1: 0 TrCH5: type 1: 3	TrCH1: type 1: 0 TrCH4: type 1: 3
>>>>numberOfTbSizeList	TrCH1: One TrCH5: Zero, one	TrCH1: One TrCH4: Zero, one
>>>>logicalChannelList	All	All
>>>semistaticTF-Information	same as UL except for TrCH5	same as UL except for TrCH4
>>>>tti	TrCH5: 20	TrCH4: 20
>>>>channelCodingType	Convolutional	Convolutional
>>>>>codingRate	TrCH5: Third	TrCH4: Third
>>>>rateMatchingAttribute	TrCH5: 200	TrCH4: 200
>>>>crc-Size	TrCH5: 12	TrCH4: 12
>>ULTrCH-Id	TrCH1: 1, TrCH2: 2, TrCH3: 3, TrCH4: 4,	TrCH1: 1, TrCH2: 2, TrCH3: 3
>dch-QualityTarget		
>>bler-QualityValue	TrCH1: $7x10^{-3}$ TrCH2- TrCH5: Absent	TrCH1: $7x10^{-3}$ TrCH2- TrCH4: Absent
TrCH INFORMATION, COMMON		
ul-CommonTransChInfo		
>tfcs-ID (TDD only)	1	1
>sharedChannelIndicator (TDD only)	FALSE	FALSE
> tfc-Subset	Absent, not required	Absent, not required
>ul-TFCS	Normal TFCI signalling	Normal TFCI signalling
>>explicitTFCS-ConfigurationMode	Complete	Complete
>>>ctfcSize	Ctfc8Bit	Ctfc6Bit
>>>>TFCS representation	Addition	Addition
>>>>>TFC list		
>>>>>>TFC 1	(TF0, TF0, TF0, TF0)	(TF0, TF0, TF0)
>>>>>>>ctfc	0	0
>>>>>>>gainFactorInformation	Computed	Computed
>>>>>>>referenceTFCId	0	0
>>>>>>TFC 2	(TF1, TF0, TF0, TF0)	(TF1, TF0, TF0)
>>>>>>>ctfc	1	1
>>>>>>>gainFactorInformation	Computed	Computed
>>>>>>>βc (FDD only)	N/A	N/A
>>>>>>>βd	N/A	N/A
>>>>>>>referenceTFCId	0	0
>>>>>>TFC 3	(TF2, TF1, TF0, TF0)	(TF2, TF1, TF0)
>>>>>>>ctfc	8	8
>>>>>>>gainFactorInformation	Computed	Computed
>>>>>>>referenceTFCId	0	0
>>>>>>TFC 4	(TF3, TF2, TF0, TF0)	(TF3, TF2, TF0)
>>>>>>>ctfc	15	15

>>>>>>>gainFactorInformation	Computed	Computed
>>>>>>>>βc (FDD only)		
>>>>>>>>βd		
>>>>>>>referenceTFCId	0	0
>>>>>>TFC 5	(TF4, TF3, TF0, TF0)	(TF4, TF3, TF0)
>>>>>>>ctfc	22	22
>>>>>>>gainFactorInformation	Computed	Computed
>>>>>>>>referenceTFCId	0	0
>>>>>>TFC 6	(TF5, TF4, TF1, TF0)	(TF5, TF4, TF0)
>>>>>>>ctfc	59	29
>>>>>>>gainFactorInformation	Computed	Computed
>>>>>>>>βc (FDD only)		
>>>>>>>>βd		
>>>>>>>>referenceTFCId	0	0
>>>>>>TFC 7	(TF0, TF0, TF0, TF1)	(TF0, TF0, TF1)
>>>>>>>ctfc	60	30
>>>>>>>gainFactorInformation	Computed	Computed
>>>>>>>>referenceTFCId	0	0
>>>>>>TFC 8	(TF1, TF0, TF0, TF1)	(TF1, TF0, TF1)
>>>>>>>ctfc	61	31
>>>>>>>gainFactorInformation	computed	computed
>>>>>>>>βc (FDD only)		
>>>>>>>>βd		
>>>>>>>>referenceTFCId	0	0
>>>>>>TFC 9	(TF2, TF1, TF0, TF1)	(TF2, TF1, TF1)
>>>>>>>ctfc	68	38
>>>>>>>gainFactorInformation	computed	computed
>>>>>>>>referenceTFCId	0	0
>>>>>>TFC 10	(TF3, TF2, TF0, TF1)	(TF3, TF2, TF1)
>>>>>>>ctfc	75	45
>>>>>>>gainFactorInformation	computed	computed
>>>>>>>>βc (FDD only)		
>>>>>>>>βd		
>>>>>>>>referenceTFCId	0	0
>>>>>>TFC 11	(TF4, TF3, TF0, TF1)	(TF4, TF3, TF1)
>>>>>>>ctfc	82	52
>>>>>>>gainFactorInformation	computed	computed
>>>>>>>>referenceTFCId	0	0
>>>>>>TFC 12	(TF5, TF4, TF1, TF1)	(TF5, TF4, TF1)
>>>>>>>ctfc	97	59
>>>>>>>gainFactorInformation	signalled	signalled
>>>>>>>>βc (FDD only)	11	11
>>>>>>>>βd	15	15
>>>>>>>>referenceTFCId	0	0
> TFC subset list		
>>TFC subset 1	(speech rate 10.2)	(speech rate 7.4)

>>> Allowed transport format combination list	(TFC1, TFC2, TFC7, TFC8, TFC6, TFC12)	(TFC1, TFC2, TFC7, TFC8, TFC6, TFC12)
>>TFC subset 2	(speech rate 6.7)	(speech rate 6.7)
>>> Allowed transport format combination list	(TFC1, TFC2, TFC7, TFC8, TFC5, TFC11)	(TFC1, TFC2, TFC7, TFC8, TFC5, TFC11)
>>TFC subset 3	(speech rate 5.9)	(speech rate 5.9)
>>> Allowed transport format combination list	(TFC1, TFC2, TFC7, TFC8, TFC4, TFC10)	(TFC1, TFC2, TFC7, TFC8, TFC4, TFC10)
>>TFC subset 4	(speech rate 4.75)	(speech rate 4.75)
>>> Allowed transport format combination list	(TFC1, TFC2, TFC7, TFC8, TFC3, TFC9)	(TFC1, TFC2, TFC7, TFC8, TFC3, TFC9)
dl-CommonTransChInfo		
>tfcs-SignallingMode	Independent	Independent
>tfcs-ID (TDD only)	1	1
>sharedChannelIndicator (TDD only)	FALSE	FALSE
> tfc-Subset	Absent, not required	Absent, not required
>dl-TFCS	Normal TFCI signalling	Normal TFCI signalling
>>explicitTFCS-ConfigurationMode	Complete	Complete
>>>ctfcSize	Ctfc8Bit	Ctfc8Bit
>>>>TFCS representation	Addition	Addition
>>>>>TFCS list		
>>>>>>TFC 1	(TF0, TF0, TF0, TF0, TF0)	(TF0, TF0, TF0, TF0)
>>>>>>>ctfc	0	0
>>>>>>TFC 2	(TF1, TF0, TF0, TF0, TF0)	(TF1, TF0, TF0, TF0)
>>>>>>>ctfc	1	1
>>>>>>TFC 3	(TF2, TF1, TF0, TF0, TF0)	(TF2, TF1, TF0, TF0)
>>>>>>>ctfc	8	8
>>>>>>TFC 4	(TF3, TF2, TF0, TF0, TF0)	(TF3, TF2, TF0, TF0)
>>>>>>>ctfc	15	15
>>>>>>TFC 5	(TF4, TF3, TF0, TF0, TF0)	(TF4, TF3, TF0, TF0)
>>>>>>>ctfc	22	22
>>>>>>TFC 6	(TF5, TF4, TF1, TF0, TF0)	(TF5, TF4, TF0, TF0)
>>>>>>>ctfc	59	29
>>>>>>TFC 7	(TF0, TF0, TF0, TF1, TF0)	(TF0, TF0, TF1, TF0)
>>>>>>>ctfc	60	30
>>>>>>TFC 8	(TF1, TF0, TF0, TF1, TF0)	(TF1, TF0, TF1, TF0)
>>>>>>>ctfc	61	31
>>>>>>TFC 9	(TF2, TF1, TF0, TF1, TF0)	(TF2, TF1, TF1, TF0)
>>>>>>>ctfc	68	37
>>>>>>TFC 10	(TF3, TF2, TF0, TF1, TF0)	(TF3, TF2, TF1, TF0)
>>>>>>>ctfc	75	55
>>>>>>TFC 11	(TF4, TF3, TF0, TF1, TF0)	(TF4, TF3, TF1, TF0)
>>>>>>>ctfc	82	52
>>>>>>TFC 12	(TF5, TF4, TF1, TF1, TF0)	(TF5, TF4, TF1, TF0)
>>>>>>>ctfc	119	59

>>>>>>TFC 13	(TF0, TF0, TF0, TF0, TF1)	(TF0, TF0, TF0, TF1)
>>>>>>>ctfc	120	60
>>>>>>TFC 14	(TF1, TF0, TF0, TF0, TF1)	(TF1, TF0, TF0, TF1)
>>>>>>>ctfc	121	61
>>>>>>TFC 15	(TF2, TF1, TF0, TF0, TF1)	(TF2, TF1, TF0, TF1)
>>>>>>>ctfc	128	68
>>>>>>TFC 16	(TF3, TF2, TF0, TF0, TF1)	(TF3, TF2, TF0, TF1)
>>>>>>>ctfc	135	75
>>>>>>TFC 17	(TF4, TF3, TF0, TF0, TF1)	(TF4, TF3, TF0, TF1)
>>>>>>>ctfc	152	82
>>>>>>TFC 18	(TF5, TF4, TF1, TF0, TF1)	(TF5, TF4, TF0, TF1)
>>>>>>>ctfc	189	89
>>>>>>TFC 19	(TF0, TF0, TF0, TF1, TF1)	(TF0, TF0, TF1, TF1)
>>>>>>>ctfc	180	90
>>>>>>TFC 20	(TF1, TF0, TF0, TF1, TF1)	(TF1, TF0, TF1, TF1)
>>>>>>>ctfc	181	91
>>>>>>TFC 21	(TF2, TF1, TF0, TF1, TF1)	(TF2, TF1, TF1, TF1)
>>>>>>>ctfc	188	98
>>>>>>TFC 22	(TF3, TF2, TF0, TF1, TF1)	(TF3, TF2, TF1, TF1)
>>>>>>>ctfc	195	105
>>>>>>TFC 23	(TF4, TF3, TF0, TF1, TF1)	(TF4, TF3, TF1, TF1)
>>>>>>>ctfc	239	112
>>>>>>TFC 24	(TF5, TF4, TF1, TF1, TF1)	(TF5, TF4, TF1, TF1)
>>>>>>>ctfc	218	119
PhyCH INFORMATION FDD		
UL-DPCH-InfoPredef		
>ul-DPCH-PowerControlInfo		
>>powerControlAlgorithm	Algorithm 1	Algorithm 1
>>>tpcStepSize	1 dB	1 dB
>tfci-Existence	TRUE	TRUE
>puncturingLimit	0.88	0.88
DL-CommonInformationPredef		
>dl-DPCH-InfoCommon		
>>spreadingFactor	128	128
>>tfci-Existence	FALSE	FALSE
>>pilotBits	4	4
>>positionFixed	Fixed	Fixed
PhyCH INFORMATION 3.84 Mcps TDD		
UL-DPCH-InfoPredef		
>ul-DPCH-PowerControlInfo		
>>dpch-ConstantValue	0	0
>commonTimeslotInfo		
>>secondInterleavingMode	frameRelated	frameRelated
>>tfci-Coding	16	16
>>puncturingLimit	0.60	0.60
>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1

DL-CommonInformationPredef		
>dl-DPCH-InfoCommon		
>>commonTimeslotInfo		
>>>secondInterleavingMode	frameRelated	frameRelated
>>>tfci-Coding	16	16
>>>puncturingLimit	0.60	0.60
>>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1
PhyCH INFORMATION 1.28 Mcps TDD		
UL-DPCH-InfoPredef		
>commonTimeslotInfo		
>>secondInterleavingMode	frame Related	frame Related
>>tfci-Coding	16	16
>>puncturingLimit	0.64	0.64
>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1
DL-CommonInformationPredef		
>dl-DPCH-InfoCommon		
>>commonTimeslotInfo		
>>>secondInterleavingMode	frame Related	frame Related
>>>tfci-Coding	16	16
>>>puncturingLimit	0.64	0.64
>>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1

Configuration	12.65/8.85/6.6 kbps speech + 3.4 kbps signalling
Ref 34.108	62
Default configuration identity	13
RB INFORMATION	
rb-Identity	RB1: 1, RB2: 2, RB3: 3, RB5: 5, RB6: 6, RB8: 8
rlc-InfoChoice	Rlc-info
>ul-RLC-Mode	RB1: UM RB2- RB3: AM RB5-RB6: TM
>>transmissionRLC-DiscardMode	RB1: N/A RB2- RB3: NoDiscard RB5- RB6: N/A
>>>maxDat	RB1: N/A RB2- RB3: 15 RB5- RB6: N/A
>>transmissionWindowSize	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB6: N/A
>>timerRST	RB1: N/A RB2- RB3: 300 RB5- RB6: N/A
>>max-RST	RB1: N/A RB2- RB3: 1 RB5- RB6: N/A
>>pollingInfo	RB1: N/A RB2- RB3: as below RB5- RB6: N/A
>>>lastTransmissionPDU-Poll	RB2- RB3: FALSE
>>>lastRetransmissionPDU-Poll	RB2- RB3: FALSE
>>>timerPollPeriodic	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A RB5- RB6: FALSE
>dl-RLC-Mode	RB1: UM RB2- RB3: AM RB5- RB6: TM RB8: TM
>>inSequenceDelivery	RB1: N/A RB2- RB3: TRUE RB5- RB6: N/A RB8: N/A
>>receivingWindowSize	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB6: N/A RB8: N/A
>>dl-RLC-StatusInfo	RB1: N/A RB2- RB3: as below RB5- RB6: N/A RB8: N/A
>>>timerStatusProhibit	RB2- RB3: 100
>>>missingPDU-Indicator	RB2- RB3: FALSE

>>>timerStatusPeriodic	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A RB5- RB6: FALSE RB8: FALSE
rb-MappingInfo	
>UL-LogicalChannelMappings	OneLogicalChannel
>>ul-TransportChannelType	Dch
>>>transportChannelIdentity	RB1- RB3: 4 RB5: 1, RB6: 2.
>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3 RB5- RB6: N/A
>>rlc-SizeList	RB1- RB3: configured RB5- RB6: N/A
>>mac-LogicalChannelPriority	RB1: 1, RB2: 2, RB3: 3 RB5- RB6: 5
>DL-logicalChannelMappingList	
>>Mapping option 1	One mapping option
>>>dl-TransportChannelType	Dch
>>>>transportChannelIdentity	RB1- RB3: 4 RB5: 1, RB6: 2, RB8: 5
>>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3 RB5- RB6: N/A RB8: N/A
TrCH INFORMATION PER TrCH	
UL-AddReconfTransChInfoList	
>Uplink transport channel type	dch
>transportChannelIdentity	TrCH1: 1, TrCH2: 2, TrCH4: 4
>transportFormatSet	DedicatedTransChTFS
>>dynamicTF-information	
>>>tf0/ tf0,1	TrCH1: (0x72) TrCH2: (0x 181) TrCH4: (0x144, 1x144)
>>>>rlcSize	BitMode
>>>>>sizeType	TrCH1: type 1: 72 TrCH2: type 1: 181 TrCH4: 2: type 2, part1= 2, part2= 0 (144)
>>>>numberOfTbSizeList	TrCH1-2: Zero TrCH4: Zero, one
>>>>logicalChannelList	All
>>>tf 1	TrCH1: (1x40) TrCH2: (1x 78) TrCH4: N/A
>>>>numberOfTransportBlocks	TrCH1: One TrCH2: One
>>>>rlc-Size	TrCH1-2: BitMode
>>>>>sizeType	TrCH1: 1: 40 TrCH2: 1: 78
>>>>numberOfTbSizeList	TrCH1-2: One
>>>>logicalChannelList	TrCH1: all

>>>tf 2	TrCH1: (1x54) TrCH2: (1x113) TrCH4: N/A
>>>>numberOfTransportBlocks	TrCH1: One TrCh2: One
>>>>rlc-Size	TrCH1: BitMode
>>>>>sizeType	TrCH1: type 1: 54 TrCH2: type 1: 113
>>>>numberOfTbSizeList	TrCH1: One TrCH2: One
>>>>logicalChannelList	TrCH1: all TrCH2: all
>>>tf 3	TrCH1: (1x64) TrCH2: (1x181) TrCH4: N/A
>>>>numberOfTransportBlocks	TrCH1: One TrCh2: One
>>>>rlc-Size	TrCH1: BitMode
>>>>>sizeType	TrCH1: type 1: 64 TrCH2: type 1: 181
>>>>numberOfTbSizeList	TrCH1: One TrCH2: One
>>>>logicalChannelList	TrCH1: all TrCH2: all
>>>tf 4	TrCH1: (1x72) TrCH2: N/A TrCH4: N/A
>>>>numberOfTransportBlocks	TrCH1: One
>>>>rlc-Size	TrCH1: BitMode
>>>>>sizeType	TrCH1: type 1: 72
>>>>numberOfTbSizeList	TrCH1: One
>>>>logicalChannelList	TrCH1: all
>>semistaticTF-Information	
>>>tti	TrCH1- TrCH2: 20 TrCH4: 40
>>>channelCodingType	Convolutional
>>>>codingRate	TrCH1- TrCH2: Third TrCH4: Third
>>>rateMatchingAttribute	TrCH1: 200 TrCH2: 190 TrCH4: 170
>>>crc-Size	TrCH1: 12 TrCH2: 0 TrCH4: 16
DL-AddReconfTransChInfoList	
>Downlink transport channel type	dch
>dl-TransportChannelIdentity	
>tfs-SignallingMode	Independent <Only tf0 on TrCH1 and tf0/tf1 on TrCH5 are different and shown below>
>>transportFormatSet	
>>>dynamicTF-information	
>>>>tf0/ tf0,1	TrCH1: (1x0) TrCH5: (0x3, 1x3)
>>>>rlcSize	BitMode
>>>>>sizeType	TrCH1: type 1: 0 TrCH5: type 1: 3
>>>>numberOfTbSizeList	TrCH1: One TrCH5: Zero, one

>>>>logicalChannelList	All
>>>semistaticTF-Information	same as UL except for TrCH5
>>>>tti	TrCH5: 20
>>>>channelCodingType	Convolutional
>>>>>codingRate	TrCH5: Third
>>>>rateMatchingAttribute	TrCH5: 205
>>>>crc-Size	TrCH5: 8
>>ULTrCH-Id	TrCH1: 1, TrCH2: 2, TrCH4: 4,
>dch-QualityTarget	
>>bler-QualityValue	TrCH1: $7x10^{-3}$ TrCH2: Absent TrCH4- TrCH5: Absent
TrCH INFORMATION, COMMON	
ul-CommonTransChInfo	
>tfcs-ID (TDD only)	1
>sharedChannelIndicator (TDD only)	FALSE
> tfc-Subset	Absent, not required
>ul-TFCS	Normal TFCI signalling
>>explicitTFCS-ConfigurationMode	Complete
>>>ctfcSize	Ctfc6Bit
>>>>TFCS representation	Addition
>>>>>TFC list	
>>>>>>TFC 1	(TF0, TF0, TF0)
>>>>>>>ctfc	0
>>>>>>>gainFactorInformation	Computed
>>>>>>>>referenceTFCId	0
>>>>>>TFC 2	(TF1, TF0, TF0)
>>>>>>>ctfc	1
>>>>>>>gainFactorInformation	Computed
>>>>>>>>βc (FDD only)	N/A
>>>>>>>>βd	N/A
>>>>>>>>referenceTFCId	0
>>>>>>TFC 3	(TF2, TF1, TF0)
>>>>>>>ctfc	7
>>>>>>>gainFactorInformation	Computed
>>>>>>>>referenceTFCId	0
>>>>>>TFC 4	(TF3, TF2, TF0)
>>>>>>>ctfc	13
>>>>>>>gainFactorInformation	Computed
>>>>>>>>βc (FDD only)	
>>>>>>>>βd	
>>>>>>>>referenceTFCId	0
>>>>>>TFC 5	(TF4, TF3, TF0)
>>>>>>>ctfc	19
>>>>>>>gainFactorInformation	Computed
>>>>>>>>referenceTFCId	0
>>>>>>TFC 6	(TF0, TF0, TF1)
>>>>>>>ctfc	20
>>>>>>>gainFactorInformation	Computed
>>>>>>>>βc (FDD only)	
>>>>>>>>βd	

>>>>>>>>referenceTFCId	0
>>>>>>TFC 7	(TF1, TF0, TF1)
>>>>>>>ctfc	21
>>>>>>>gainFactorInformation	Computed
>>>>>>>>referenceTFCId	0
>>>>>>TFC 8	(TF2, TF1, TF1)
>>>>>>>ctfc	27
>>>>>>>gainFactorInformation	computed
>>>>>>>>βc (FDD only)	
>>>>>>>>βd	
>>>>>>>>referenceTFCId	0
>>>>>>TFC 9	(TF3, TF2, TF1)
>>>>>>>ctfc	33
>>>>>>>gainFactorInformation	computed
>>>>>>>>referenceTFCId	0
>>>>>>TFC 10	(TF4, TF3, TF1)
>>>>>>>ctfc	39
>>>>>>>gainFactorInformation	signalled
>>>>>>>>βc (FDD only)	11
>>>>>>>>βd	15
>>>>>>>>referenceTFCId	0
> TFC subset list	
>>TFC subset 1	(speech rate 6.6)
>>> Allowed transport format combination list	(TFC1, TFC2, TFC3, TFC6, TFC7, TFC8)
>>TFC subset 2	(speech rate 8.85)
>>> Allowed transport format combination list	(TFC1, TFC2, TFC3, TFC4, TFC6, TFC7, TFC8, TFC9)
>>TFC subset 3	(speech rate 12.65)
>>> Allowed transport format combination list	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10)
dl-CommonTransChInfo	
>tfcs-SignallingMode	Independent
ul-CommonTransChInfo	
>tfcs-ID (TDD only)	1
>sharedChannelIndicator (TDD only)	FALSE
> tfc-Subset	Absent, not required
>dl-TFCS	Normal TFCI signalling
>>explicitTFCS-ConfigurationMode	Complete
>>>ctfcSize	Ctfc8Bit
>>>>TFCS representation	Addition
>>>>>TFCS list	
>>>>>>TFC 1	(TF0, TF0, TF0, TF0)
>>>>>>>ctfc	0
>>>>>>TFC 2	(TF1, TF0, TF0, TF0)
>>>>>>>ctfc	1
>>>>>>TFC 3	(TF2, TF1, TF0, TF0)
>>>>>>>ctfc	7
>>>>>>TFC 4	(TF3, TF2, TF0, TF0)

>>>>>>>ctfc	13
>>>>>>TFC 5	(TF4, TF3, TF0, TF0)
>>>>>>>ctfc	19
>>>>>>TFC 6	(TF0, TF0, TF1, TF0)
>>>>>>>ctfc	20
>>>>>>TFC 7	(TF1, TF0, TF1, TF0)
>>>>>>>ctfc	21
>>>>>>TFC 8	(TF2, TF1, TF1, TF0)
>>>>>>>ctfc	27
>>>>>>TFC 9	(TF3, TF2, TF1, TF0)
>>>>>>>ctfc	33
>>>>>>TFC 10	(TF4, TF3, TF1, TF0)
>>>>>>>ctfc	39
>>>>>>TFC 11	(TF0, TF0, TF0, TF1)
>>>>>>>ctfc	40
>>>>>>TFC 12	(TF1, TF0, TF0, TF1)
>>>>>>>ctfc	41
>>>>>>TFC 13	(TF2, TF1, TF0, TF1)
>>>>>>>ctfc	47
>>>>>>TFC 14	(TF3, TF2, TF0, TF1)
>>>>>>>ctfc	53
>>>>>>TFC 15	(TF4, TF3, TF0, TF1)
>>>>>>>ctfc	59
>>>>>>TFC 16	(TF0, TF0, TF1, TF1)
>>>>>>>ctfc	60
>>>>>>TFC 17	(TF1, TF0, TF1, TF1)
>>>>>>>ctfc	61
>>>>>>TFC 18	(TF2, TF1, TF1, TF1)
>>>>>>>ctfc	67
>>>>>>TFC 19	(TF3, TF2, TF1, TF1)
>>>>>>>ctfc	73
>>>>>>TFC 20	(TF4, TF3, TF1, TF1)
>>>>>>>ctfc	79
PhyCH INFORMATION FDD	
UL-DPCH-InfoPredef	
>ul-DPCH-PowerControlInfo	
>>powerControlAlgorithm	Algorithm 1
>>>tpcStepSize	1 dB
>tfci-Existence	TRUE
>puncturingLimit	0.84
DL-CommonInformationPredef	
>dl-DPCH-InfoCommon	
>>spreadingFactor	128
>>tfci-Existence	FALSE
>>pilotBits	4
>>positionFixed	Fixed

3GPP TSG-RAN-WG2 Meeting #45
Shin-Yokohama, Japan, 15-19 October 2004

Tdoc ⌘R2-042586

CR-Form-v7.1

CHANGE REQUEST

⌘ **25.331** **CR** **2444** ⌘ **rev** **-** ⌘ Current version: **6.3.0** ⌘

*For **HELP** on using this form, see bottom of this page or look at the pop-up text over the ⌘ symbols.*

Proposed change affects: UICC apps⌘ [ ] ME [X] Radio Access Network [X] Core Network [ ]

Title:	⌘	TPC step size in default configurations
Source:	⌘	RAN WG2
Work item code:	⌘	TEI5
Date:	⌘	Nov 2004
Category:	⌘	**A**
Release:	⌘	Rel-6

Use one of the following categories:
F *(correction)*
A *(corresponds to a correction in an earlier release)*
B *(addition of feature),*
C *(functional modification of feature)*
D *(editorial modification)*
Detailed explanations of the above categories can be found in 3GPP TR 21.900.

Use one of the following releases:
Ph2 (GSM Phase 2)
R96 (Release 1996)
R97 (Release 1997)
R98 (Release 1998)
R99 (Release 1999)
Rel-4 (Release 4)
Rel-5 (Release 5)
Rel-6 (Release 6)
Rel-7 (Release 7)

Reason for change: ⌘ In section 13.7 of 25.331, the TPC Step size (in the power control algorithm) is shown as 1. It is unclear if this value reflect 1dB (asn1 value of '0') or asn1 value of '1' (which means 2dB).

----- From TS 25.331 Section 13.7------
>ul-DPCH-PowerControlInfo
>>powerControlAlgorithm Algorithm 1 Algorithm 1 Algorithm 1 Algorithm 1
>>>tpcStepSize 1 1 1 1
------- From TS 25.331 Section 11.3 -------
-- Actual value TPC-StepSizeFDD = IE value + 1
TPC-StepSizeFDD ::= INTEGER (0..1)

Summary of change: ⌘ Clarified that all IEs in the default configurations tables are shown in terms of their real value and not ASN.1.
Also added “dB” to the TPC step size.

Implementation of this CR by R99/Rel-4 UEs will not cause backwards compatibility issues

Impact Analysis:
A UE/UTRAN that has not implemented according to this CR, will require a modification.

Consequences if not approved:	⌘	Due to a larger TPC step size there would typically be larger UL power spiking and could worsen the UL synchronization times of the UTRAN. This could cause additional UL interference problems to other users.

Clauses affected:	⌘	13.7			
		Y	**N**		
Other specs affected:	⌘			Other core specifications	⌘
				Test specifications	
				O&M Specifications	
Other comments:	⌘				

How to create CRs using this form:

Comprehensive information and tips about how to create CRs can be found at http://www.3gpp.org/specs/CR.htm. Below is a brief summary:

1) Fill out the above form. The symbols above marked ⌘ contain pop-up help information about the field that they are closest to.

2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under ftp://ftp.3gpp.org/specs/ For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.

3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

13.7 Parameter values for default radio configurations

The UE shall support the use of the default radio configurations that are specified in the following.

NOTE 1: These configurations are based on [41] and cover a number of RAB and signalling connection configurations.

In the table that is used to specify the parameter values for these default configurations, the following principles are used:

- Optional IEs that are not used are omitted;
- In case no parameter value is specified in a column, this means the value given the previous (left side) column applies.

NOTE 2: If needed, signalling radio bearer RB4 is established after the completion of handover.

NOTE 3: For each default configuration, the value of FDD, 3.84 Mcps TDD and 1.28 Mcps TDD parameters are specified. All parameters apply to FDD, 3.84 Mcps TDD and 1.28 Mcps TDD modes, unless explicitly stated otherwise. It should be noted that in this respect default configurations differ from pre-defined configurations, which only include parameter values for one mode.

NOTE 4: The transport format sizes, indicated in the following table, concern the RLC PDU size, since all configurations concern dedicated channels. The transport block sizes indicated in TS 34.108 are different since these include the size of the MAC header.

NOTE 5: The tabular values included in this section, represent the actual IE values as in section 10, and not the ASN.1 representation of these values.

Configuration	**3.4 kbps signalling**	**13.6 kbps signalling**	**7.95 kbps speech + 3.4 kbps signalling**	**12.2 kbps speech + 3.4 kbps signalling**
Ref 34.108	2	3	6	4
Default configuration identity	0	1	2	3
RB INFORMATION				
rb-Identity	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3, RB5: 5, RB6: 6	RB1: 1, RB2: 2, RB3: 3, RB5: 5, RB6: 6, RB7: 7
rlc-InfoChoice	Rlc-info	Rlc-info	Rlc-info	Rlc-info
>ul-RLC-Mode	RB1: UM RB2- RB3: AM	RB1: UM RB2- RB3: AM	RB1: UM RB2- RB3: AM RB5-RB6: TM	RB1: UM RB2- RB3: AM RB5-RB7: TM
>>transmissionRLC-DiscardMode	RB1: N/A RB2- RB3: NoDiscard	RB1: N/A RB2- RB3: NoDiscard	RB1: N/A RB2- RB3: NoDiscard RB5- RB6: N/A	RB1: N/A RB2- RB3: NoDiscard RB5- RB7: N/A
>>>maxDat	RB1: N/A RB2- RB3: 15	RB1: N/A RB2- RB3: 15	RB1: N/A RB2- RB3: 15 RB5- RB6: N/A	RB1: N/A RB2- RB3: 15 RB5- RB7: N/A
>>transmissionWindowSize	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB6: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB7: N/A
>>timerRST	RB1: N/A RB2- RB3: 300	RB1: N/A RB2- RB3: 300	RB1: N/A RB2- RB3: 300 RB5- RB6: N/A	RB1: N/A RB2- RB3: 300 RB5- RB7: N/A
>>max-RST	RB1: N/A RB2- RB3: 1	RB1: N/A RB2- RB3: 1	RB1: N/A RB2- RB3: 1 RB5- RB6: N/A	RB1: N/A RB2- RB3: 1 RB5- RB7: N/A

Configuration	3.4 kbps signalling	13.6 kbps signalling	7.95 kbps speech + 3.4 kbps signalling	12.2 kbps speech + 3.4 kbps signalling
>>pollingInfo	RB1: N/A RB2- RB3: as below	RB1: N/A RB2- RB3: as below	RB1: N/A RB2- RB3: as below RB5- RB6: N/A	RB1: N/A RB2- RB3: as below RB5- RB7: N/A
>>>lastTransmissionPDU-Poll	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>lastRetransmissionPDU-Poll	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerPollPeriodic	RB2- RB3: 300	RB2- RB3: 100	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A	RB1- RB3: N/A	RB1- RB3: N/A RB5- RB6: FALSE	RB1- RB3: N/A RB5- RB7: FALSE
>dl-RLC-Mode	RB1: UM RB2- RB3: AM	RB1: UM RB2- RB3: AM	RB1: UM RB2- RB3: AM RB5- RB6: TM	RB1: UM RB2- RB3: AM RB5- RB7: TM
>>inSequenceDelivery	RB1: N/A RB2- RB3: TRUE	RB1: N/A RB2- RB3: TRUE	RB1: N/A RB2- RB3: TRUE RB5- RB6: N/A	RB1: N/A RB2- RB3: TRUE RB5- RB7: N/A
>>receivingWindowSize	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB6: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB7: N/A
>>dl-RLC-StatusInfo	RB1: N/A RB2- RB3: as below	RB1: N/A RB2- RB3: as below	RB1: N/A RB2- RB3: as below RB5- RB6: N/A	RB1: N/A RB2- RB3: as below RB5- RB7: N/A
>>>timerStatusProhibit	RB2- RB3: 100	RB2- RB3: 100	RB2- RB3: 100	RB2- RB3: 100
>>>missingPDU-Indicator	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerStatusPeriodic	RB2- RB3: 300	RB2- RB3: 100	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A	RB1- RB3: N/A	RB1- RB3: N/A RB5- RB6: FALSE	RB1- RB3: N/A RB5- RB7: FALSE
rb-MappingInfo				
>UL-LogicalChannelMappings	OneLogicalChannel	OneLogicalChannel	OneLogicalChannel	OneLogicalChannel
>>ul-TransportChannelType	Dch	Dch	Dch	Dch
>>>transportChannelIdentity	RB1- RB3: 1	RB1- RB3: 1	RB1- RB3: 3 RB5: 1, RB6: 2	RB1- RB3: 4 RB5: 1, RB6: 2, RB7: 3
>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3 RB5- RB6: N/A	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: N/A
>>rlc-SizeList	RB1- RB3: configured	RB1- RB3: configured	RB1- RB3: configured RB5- RB6: N/A	RB1- RB3: configured RB5- RB7: N/A
>>mac-LogicalChannelPriority	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3 RB5- RB6: 5	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: 5
>DL-logicalChannelMappingList				
>>Mapping option 1	One mapping option	One mapping option	One mapping option	One mapping option
>>>dl-TransportChannelType	Dch	Dch	Dch	Dch
>>>>transportChannelIdentity	RB1- RB3: 1	RB1- RB3: 1	RB1- RB3: 3 RB5: 1, RB6: 2	RB1- RB3: 4 RB5: 1, RB6: 2, RB7: 3
>>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3 RB5- RB6: N/A	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: N/A
TrCH INFORMATION PER TrCH				

Configuration	3.4 kbps signalling	13.6 kbps signalling	7.95 kbps speech + 3.4 kbps signalling	12.2 kbps speech + 3.4 kbps signalling
UL-AddReconfTransChInfoList				
>Uplink transport channel type	dch	dch	dch	dch
>transportChannelIdentity	TrCH1: 1	TrCH1: 1	TrCH1: 1, TrCH2: 2, TrCH3: 3	TrCH1: 1, TrCH2: 2, TrCH3: 3, TrCH4: 4
>transportFormatSet	DedicatedTransChTFS	DedicatedTransChTFS	DedicatedTransChTFS	DedicatedTransChTFS
>>dynamicTF-information				
>>>tf0/ tf0,1	TrCH1: (0x144, 1x144)	TrCH1: (0x144, 1x144)	TrCH1: (0x75) TrCH2: (0x 84 1x84) TrCH3: (0x144, 1x144)	TrCH1: (0x81) TrCH2: (0x 103, 1x103) TrCH3: (0x 60, 1x60) TrCH4: (0x144, 1x144)
>>>>rlcSize	BitMode	BitMode	BitMode	BitMode
>>>>>sizeType	TrCH1: type 2, part1= 2, part2= 0 (144)	TrCH1: type 2, part1= 2, part2= 0 (144)	TrCH1: type 1: 75 TrCH2: type 1: 84 TrCH3: 2: type 2, part1= 2, part2= 0 (144)	TrCH1: type 1: 81 TrCH2: type 1: 103 TrCH3: type 1: 60 TrCH4: 2: type 2, part1= 2, part2= 0 (144)
>>>>numberOfTbSizeList	TrCH1: Zero, one	TrCH1: Zero, one	TrCH1: Zero TrCH2-3: Zero, one	TrCH1: Zero TrCH2-4: Zero, one
>>>>logicalChannelList	All	All	All	All
>>>tf 1	N/A	N/A	TrCH1: (1x39) TrCH2- TrCH4: N/A	TrCH1: (1x39) TrCH2- TrCH4: N/A
>>>>numberOfTransportBlocks			TrCH1: One	TrCH1: One
>>>>rlc-Size			TrCH1: BitMode	TrCH1: BitMode
>>>>>sizeType			TrCH1: 1: 39	TrCH1: 1: 39
>>>>numberOfTbSizeList			TrCH1: One	TrCH1: One
>>>>logicalChannelList			TrCH1: all	TrCH1: all
>>>tf 2	N/A	N/A	TrCH1: (1x75) TrCH2- TrCH3: N/A	TrCH1: (1x81) TrCH2- TrCH4: N/A
>>>>numberOfTransportBlocks			TrCH1: One	TrCH1: One
>>>>rlc-Size			TrCH1: BitMode	TrCH1: BitMode
>>>>>sizeType			TrCH1: type 1: 75	TrCH1: type 1: 81
>>>>numberOfTbSizeList			TrCH1: One	TrCH1: One
>>>>logicalChannelList			TrCH1: all	TrCH1: all
>>semistaticTF-Information				
>>>tti	TrCH1: 40	TrCH1: 10	TrCH1- TrCH2: 20 TrCH3: 40	TrCH1- TrCH3: 20 TrCH4: 40
>>>channelCodingType	Convolutional	Convolutional	Convolutional	Convolutional
>>>>codingRate	TrCH1: Third	TrCH1: Third	TrCH1- TrCH2: Third TrCH3: Third	TrCH1- TrCH2: Third TrCH3: Half TrCH4: Third
>>>rateMatchingAttribute	TrCH1: 160	TrCH1: 160	TrCH1: 200 TrCH2: 190 TrCH3: 160	TrCH1: 200 TrCH2: 190 TrCH3: 235 TrCH4: 160
>>>crc-Size	TrCH1: 16	TrCH1: 16	TrCH1: 12 TrCH2: 0 TrCH3: 16	TrCH1: 12 TrCH2- TrCH3: 0 TrCH4: 16
DL-AddReconfTransChInfoList				
>Downlink transport channel type	dch	dch	dch	dch

Configuration	3.4 kbps signalling	13.6 kbps signalling	7.95 kbps speech + 3.4 kbps signalling	12.2 kbps speech + 3.4 kbps signalling
>dl-TransportChannelIdentity (should be as for UL)	TrCH1: 1	TrCH1: 1	TrCH1: 1, TrCH2: 2, TrCH3: 3	TrCH1: 1, TrCH2: 2, TrCH3: 3, TrCH4: 4
>tfs-SignallingMode	SameAsUL	SameAsUL	Explicit <Only tf0 on TrCH1 is different and shown below>	Explicit <Only tf0 on TrCH1 is different and shown below>
>>transportFormatSet			DedicatedTransChTFS	DedicatedTransChTFS
>>>dynamicTF-information				
>>>>tf0/ tf0,1			TrCH1: (1x0)	TrCH1: (1x0)
>>>>rlcSize			BitMode	bitMode
>>>>>sizeType			TrCH1: type 1: 0	TrCH1: type 1: 0
>>>>numberOfTbSizeList			TrCH1: One	TrCH1: One
>>>>logicalChannelList			All	All
>>ULTrCH-Id	TrCH1: 1	TrCH1: 1	TrCH1: 1, TrCH2: 2, TrCH3: 3	TrCH1: 1, TrCH2: 2, TrCH3: 3, TrCH4: 4
>dch-QualityTarget				
>>bler-QualityValue	TrCH1: $5x10^{-2}$	TrCH1: $5x10^{-2}$	TrCH1: $7x10^{-3}$ TrCH2- TrCH3: Absent	TrCH1: $7x10^{-3}$ TrCH2- TrCH4: Absent
TrCH INFORMATION, COMMON				
ul-CommonTransChInfo				
>tfcs-ID (TDD only)	1	1	1	1
>sharedChannelIndicator (TDD only)	FALSE	FALSE	FALSE	FALSE
>tfc-Subset	Absent, not required	Absent, not required	Absent, not required	Absent, not required
>ul-TFCS	Normal TFCI signalling	Normal TFCI signalling	Normal TFCI signalling	Normal TFCI signalling
>>explicitTFCS-ConfigurationMode	Complete	Complete	Complete	Complete
>>>ctfcSize	Ctfc2Bit	Ctfc2Bit	Ctfc4Bit	Ctfc6Bit
>>>>TFCS representation	Addition	Addition	Addition	Addition
>>>>>TFCS list				
>>>>>>TFCS 1	(TF0)	(TF0)	(TF0, TF0, TF0)	(TF0, TF0, TF0, TF0)
>>>>>>>ctfc	0	0	0	0
>>>>>>>gainFactorInformation	Computed	Computed	Computed	Computed
>>>>>>>>referenceTFCId	0	0	0	0
>>>>>>TFCS 2	(TF1)	(TF1)	(TF1, TF0, TF0)	(TF1, TF0, TF0, TF0)
>>>>>>>ctfc	1	1	1	1
>>>>>>>gainFactorInformation	Signalled	Signalled	Computed	Computed
>>>>>>>>βc (FDD only)	11	11	N/A	N/A
>>>>>>>>βd	15	15	N/A	N/A
>>>>>>>>referenceTFCId	0	0	0	0
>>>>>>TFCS 3			(TF2, TF1, TF0)	(TF2, TF1, TF1, TF0)
>>>>>>>ctfc			5	11
>>>>>>>gainFactorInformation			Computed	Computed
>>>>>>>>referenceTFCId			0	0
>>>>>>TFCS 4			(TF0, TF0, TF1)	(TF0, TF0, TF0, TF1)
>>>>>>>ctfc			6	12
>>>>>>>gainFactorInformation			Computed	Computed
>>>>>>>>βc (FDD only)			N/A	N/A

Configuration	3.4 kbps signalling	13.6 kbps signalling	7.95 kbps speech + 3.4 kbps signalling	12.2 kbps speech + 3.4 kbps signalling
>>>>>>>>βd			N/A	N/A
>>>>>>>>referenceTFCId			0	0
>>>>>>TFCS 5			(TF1, TF0, TF1)	(TF1, TF0, TF0, TF1)
>>>>>>>ctfc			7	13
>>>>>>>gainFactorInformation			Computed	Computed
>>>>>>>>referenceTFCId			0	0
>>>>>>TFCS 6			(TF2, TF1, TF1)	(TF2, TF1, TF1, TF1)
>>>>>>>ctfc			11	23
>>>>>>>gainFactorInformation			Signalled	Signalled
>>>>>>>>βc (FDD only)			11	11
>>>>>>>>βd			15	15
>>>>>>>>referenceTFCId			0	0
dl-CommonTransChInfo				
>tfcs-SignallingMode	Same as UL	Same as UL	Same as UL	Same as UL
PhyCH INFORMATION FDD				
UL-DPCH-InfoPredef				
>ul-DPCH-PowerControlInfo				
>>powerControlAlgorithm	Algorithm 1	Algorithm 1	Algorithm 1	Algorithm 1
>>>tpcStepSize	1 dB	1 dB	1 dB	1 dB
>tfci-Existence	TRUE	TRUE	TRUE	TRUE
>puncturingLimit	1	1	1	0.88
DL-CommonInformationPredef				
>dl-DPCH-InfoCommon				
>>spreadingFactor	256	128	128	128
>>tfci-Existence	FALSE	FALSE	FALSE	FALSE
>>pilotBits	4	4	4	4
>>positionFixed	N/A	N/A	Fixed	Fixed
PhyCH INFORMATION 3.84 Mcps TDD				
UL-DPCH-InfoPredef				
>ul-DPCH-PowerControlInfo				
>>dpch-ConstantValue	0	0	0	0
>commonTimeslotInfo				
>>secondInterleavingMode	frameRelated	frameRelated	frameRelated	frameRelated
>>tfci-Coding	4	4	16	16
>>puncturingLimit	1	0.92	0.52	0.88
>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
DL-CommonInformationPredef				
>dl-DPCH-InfoCommon				
>>commonTimeslotInfo				
>>>secondInterleavingMode	frameRelated	frameRelated	frameRelated	frameRelated
>>>tfci-Coding	4	4	16	16
>>>puncturingLimit	1	0.92	0.52	0.92
>>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
PhyCH INFORMATION 1.28 Mcps TDD				
UL-DPCH-InfoPredef				

Configuration	3.4 kbps signalling	13.6 kbps signalling	7.95 kbps speech + 3.4 kbps signalling	12.2 kbps speech + 3.4 kbps signalling
>commonTimeslotInfo				
>>secondInterleavingMode	frameRelated	frameRelated	frameRelated	frameRelated
>>tfci-Coding	4	4	16	16
>>puncturingLimit	1	0.64	0.80	0.60
>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
DL-CommonInformationPredef				
>dl-DPCH-InfoCommon				
>>commonTimeslotInfo				
>>>secondInterleavingMode	frameRelated	frameRelated	frameRelated	frameRelated
>>>tfci-Coding	4	4	16	16
>>>puncturingLimit	1	0.64	0.80	0.60
>>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1

Configuration	28.8 kbps conv. CS- data + 3.4 kbps signalling	32 kbps conv. CS-data + 3.4 kbps signalling	64kbps conv. CS-data + 3.4 kbps signalling	14.4 kbps streaming CS-data + 3.4 kbps signalling
Ref 34.108	12	14	13	15
Default configuration identity	4	5	6	7
RB INFORMATION				
rb-Identity	RB1: 1, RB2: 2, RB3: 3, RB5: 5	RB1: 1, RB2: 2, RB3: 3, RB5: 5	RB1: 1, RB2: 2, RB3: 3, RB5: 5	RB1: 1, RB2: 2, RB3: 3, RB5: 5
rlc-InfoChoice	Rlc-info	Rlc-info	Rlc-info	Rlc-info
>ul-RLC-Mode	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5: TM
>>transmissionRLC-DiscardMode	RB1: N/A RB2- RB3: NoDiscard RB5: N/A	RB1: N/A RB2- RB3: NoDiscard RB5: N/A	RB1: N/A RB2- RB3: NoDiscard RB5: N/A	RB1: N/A RB2- RB3: NoDiscard RB5: N/A
>>>maxDat	RB1: N/A RB2- RB3: 15 RB5: N/A	RB1: N/A RB2- RB3: 15 RB5: N/A	RB1: N/A RB2- RB3: 15 RB5: N/A	RB1: N/A RB2- RB3: 15 RB5: N/A
>>transmissionWindowSize	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A
>>timerRST	RB1: N/A RB2- RB3: 300 RB5: N/A	RB1: N/A RB2- RB3: 300 RB5: N/A	RB1: N/A RB2- RB3: 300 RB5: N/A	RB1: N/A RB2- RB3: 300 RB5: N/A
>>max-RST	RB1: N/A RB2- RB3: 1 RB5: N/A	RB1: N/A RB2- RB3: 1 RB5: N/A	RB1: N/A RB2- RB3: 1 RB5: N/A	RB1: N/A RB2- RB3: 1 RB5: N/A
>>pollingInfo	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5: N/A
>>>lastTransmissionPDU-Poll	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>lastRetransmissionPDU-Poll	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerPollPeriodic	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A RB5: FALSE	RB1- RB3: N/A RB5: FALSE	RB1- RB3: N/A RB5: FALSE	RB1- RB3: N/A RB5: FALSE

Configuration	28.8 kbps conv. CS- data + 3.4 kbps signalling	32 kbps conv. CS-data + 3.4 kbps signalling	64kbps conv. CS-data + 3.4 kbps signalling	14.4 kbps streaming CS-data + 3.4 kbps signalling
>dl-RLC-Mode	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5: TM
>>inSequenceDelivery	RB1: N/A RB2- RB3: TRUE RB5: N/A	RB1: N/A RB2- RB3: TRUE RB5: N/A	RB1: N/A RB2- RB3: TRUE RB5: N/A	RB1: N/A RB2- RB3: TRUE RB5: N/A
>>receivingWindowSize	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A
>>dl-RLC-StatusInfo	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5: N/A
>>>timerStatusProhibit	RB2- RB3: 100	RB2- RB3: 100	RB2- RB3: 100	RB2- RB3: 100
>>>missingPDU-Indicator	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerStatusPeriodic	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A RB5: FALSE	RB1- RB3: N/A RB5: FALSE	RB1- RB3: N/A RB5: FALSE	RB1- RB3: N/A RB5: FALSE
rb-MappingInfo				
>UL-LogicalChannelMappings	OneLogicalChannel	OneLogicalChannel	OneLogicalChannel	OneLogicalChannel
>>ul-TransportChannelType	Dch	Dch	Dch	Dch
>>>transportChannelIdentity	RB1- RB3: 2 RB5: 1	RB1- RB3: 2 RB5: 1	RB1- RB3: 2 RB5: 1	RB1- RB3: 2 RB5: 1
>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5: N/A
>>rlc-SizeList	RB1- RB3: configured RB5: N/A	RB1- RB3: configured RB5: N/A	RB1- RB3: configured RB5: N/A	RB1- RB3: configured RB5: N/A
>>mac-LogicalChannelPriority	RB1: 1, RB2: 2, RB3: 3 RB5: 5	RB1: 1, RB2: 2, RB3: 3 RB5: 5	RB1: 1, RB2: 2, RB3: 3 RB5: 5	RB1: 1, RB2: 2, RB3: 3 RB5: 5
>DL-logicalChannelMappingList				
>>Mapping option 1	One mapping option	One mapping option	One mapping option	One mapping option
>>>dl-TransportChannelType	Dch	Dch	Dch	Dch
>>>>transportChannelIdentity	RB1- RB3: 2 RB5: 1	RB1- RB3: 2 RB5: 1	RB1- RB3: 2 RB5: 1	RB1- RB3: 2 RB5: 1
>>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5: N/A
TrCH INFORMATION PER TrCH				
UL-AddReconfTransChInfoList				
>Uplink transport channel type	dch	dch	dch	dch
>transportChannelIdentity	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2
>transportFormatSet	DedicatedTransChTFS	DedicatedTransChTFS	DedicatedTransChTFS	DedicatedTransChTFS
>>dynamicTF-information				
>>>tf0/ tf0,1	TrCH1: (0x576, 1x576, 2x576) TrCH2: (0x144, 1x144)	TrCH1: (0x640, 1x640) TrCH2: (0x144, 1x144)	TrCH1: (0x640, 2x640) TrCH2: (0x144, 1x144)	TrCH1: (0x576, 1x576) TrCH2: (0x144, 1x144)

Configuration	28.8 kbps conv. CS- data + 3.4 kbps signalling	32 kbps conv. CS-data + 3.4 kbps signalling	64kbps conv. CS-data + 3.4 kbps signalling	14.4 kbps streaming CS-data + 3.4 kbps signalling
>>>>rlcSize	TrCH1: OctetMode TrCH2:BitMode	TrCH1: OctetMode TrCH2:BitMode	TrCH1: OctetMode TrCH2:BitMode	TrCH1: OctetMode TrCH2:BitMode
>>>>>sizeType	TrCH1: type 2, part1= 9, part2= 2 (576) TrCH2: type 2, part1= 2, part2= 0 (144)	TrCH1: type 2, part1= 11, part2= 2 (640) TrCH2: type 2, part1= 2, part2= 0 (144)	TrCH1: type 2, part1= 11, part2= 2 (640) TrCH2: type 2, part1= 2, part2= 0 (144)	TrCH1: type 2, part1= 9, part2= 2 (576) TrCH2: type 2, part1= 2, part2= 0 (144)
>>>>numberOfTbSizeList	TrCH1: Zero,1, 2 TrCH2: Zero, one	TrCH1: Zero, one TrCH2: Zero, one	TrCH1: Zero, 2 TrCH2: Zero, one	TrCH1: Zero, one, TrCH2: Zero, one
>>>>logicalChannelList	All	All	All	All
>>semiStaticTF-Information				
>>>tti	TrCH1: 40 TrCH2: 40	TrCH1: 20 TrCH2: 40	TrCH1: 20 TrCH2: 40	TrCH1: 40 TrCH2: 40
>>>channelCodingType	TrCH1: Turbo TrCH2: Convolutional	TrCH1: Turbo TrCH2: Convolutional	TrCH1: Turbo TrCH2: Convolutional	TrCH1: Turbo TrCH2: Convolutional
>>>>codingRate	TrCH1: N/A TrCH2: Third	TrCH1: N/A TrCH2: Third	TrCH1: N/A TrCH2: Third	TrCH1: N/A TrCH2: Third
>>>rateMatchingAttribute	TrCH1: 180 TrCH2: 160	TrCH1: 185 TrCH2: 160	TrCH1: 170 TrCH2: 160	TrCH1: 165 TrCH2: 160
>>>crc-Size	TrCH1: 16 TrCH2: 16	TrCH1: 16 TrCH2: 16	TrCH1: 16 TrCH2: 16	TrCH1: 16 TrCH2: 16
DL-AddReconfTransChInfoList				
>Downlink transport channel type	dch	dch	dch	dch
>dl-TransportChannelIdentity (should be as for UL)	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2
>tfs-SignallingMode	SameAsUL	SameAsUL	SameAsUL	SameAsUL
>>transportFormatSet				
>>>dynamicTF-information				
>>>>tf0/ tf0,1				
>>>>rlcSize				
>>>>>sizeType				
>>>>numberOfTbSizeList				
>>>>logicalChannelList				
>>ULTrCH-Id	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2
>dch-QualityTarget				
>>bler-QualityValue	TrCH1: $2x10^{-3}$ TrCH2: Absent	TrCH1: $2x10^{-3}$ TrCH2: Absent	TrCH1: $2x10^{-3}$ TrCH2: Absent	TrCH1: $1x10^{-2}$ TrCH2: Absent
TrCH INFORMATION, COMMON				
ul-CommonTransChInfo				
>tfcs-ID (TDD only)	1	1	1	1
>sharedChannelIndicator (TDD only)	FALSE	FALSE	FALSE	FALSE
>tfc-Subset	Absent, not required	Absent, not required	Absent, not required	Absent, not required
>ul-TFCS	Normal TFCI signalling	Normal TFCI signalling	Normal TFCI signalling	Normal TFCI signalling
>>explicitTFCS-ConfigurationMode	Complete	Complete	Complete	Complete
>>>ctfcSize	Ctfc4Bit	Ctfc2Bit	Ctfc2Bit	Ctfc4Bit
>>>>TFCS representation	Addition	Addition	Addition	Addition
>>>>>TFCS list				
>>>>>>TFCS 1	(TF0, TF0)	(TF0, TF0)	(TF0, TF0)	(TF0, TF0)
>>>>>>>ctfc	0	0	0	0
>>>>>>>gainFactorInformation	Computed	Computed	Computed	Computed

Configuration	28.8 kbps conv. CS- data + 3.4 kbps signalling	32 kbps conv. CS-data + 3.4 kbps signalling	64kbps conv. CS-data + 3.4 kbps signalling	14.4 kbps streaming CS-data + 3.4 kbps signalling
>>>>>>>>referenceTFCId	0	0	0	0
>>>>>>TFCS 2	(TF1, TF0)	(TF1, TF0)	(TF1, TF0)	(TF1, TF0)
>>>>>>>ctfc	1	1	1	1
>>>>>>>gainFactorInformation	Computed	Computed	Computed	Computed
>>>>>>>>βc (FDD only)	N/A	N/A	N/A	N/A
>>>>>>>>βd	N/A	N/A	N/A	N/A
>>>>>>>>referenceTFCId	0	0	0	0
>>>>>>TFCS 3	(TF2, TF0)	(TF0, TF1)	(TF0, TF1)	(TF0, TF1)
>>>>>>>ctfc	2	2	2	2
>>>>>>>gainFactorInformation	Computed	Computed	Computed	Computed
>>>>>>>>referenceTFCId	0	0	0	0
>>>>>>TFCS 4	(TF0, TF1)	(TF1, TF1)	(TF1, TF1)	(TF1, TF1)
>>>>>>>ctfc	3	3	3	3
>>>>>>>gainFactorInformation	Computed	Signalled	Signalled	Signalled
>>>>>>>>βc (FDD only)	N/A	8	8	11
>>>>>>>>βd	N/A	15	15	15
>>>>>>>>referenceTFCId	0	0	0	0
>>>>>>TFCS 5	(TF1, TF1)	N/A	N/A	
>>>>>>>ctfc	4			
>>>>>>>gainFactorInformation	Computed			
>>>>>>>>referenceTFCId	0			
>>>>>>TFCS 6	(TF2, TF1)	N/A	N/A	
>>>>>>>ctfc	5			
>>>>>>>gainFactorInformation	Signalled			
>>>>>>>>βc (FDD only)	8			
>>>>>>>>βd	15			
>>>>>>>>referenceTFCId	0			
>>>>>>TFCS 7				
>>>>>>>ctfc				
>>>>>>>gainFactorInformation				
>>>>>>>>referenceTFCId				
>>>>>>TFCS 8				
>>>>>>>ctfc				
>>>>>>>gainFactorInformation				
>>>>>>>>referenceTFCId				
>>>>>>TFCS 9				
>>>>>>>ctfc				
>>>>>>>gainFactorInformation				
>>>>>>>>referenceTFCId				
>>>>>>TFCS 10				
>>>>>>>ctfc				
>>>>>>>gainFactorInformation				
>>>>>>>>βc (FDD only)				
>>>>>>>>βd				
>>>>>>>>referenceTFCId				
dl-CommonTransChInfo				
>tfcs-SignallingMode	Same as UL	Same as UL	Same as UL	Same as UL
PhyCH INFORMATION FDD				

Configuration	28.8 kbps conv. CS- data + 3.4 kbps signalling	32 kbps conv. CS-data + 3.4 kbps signalling	64kbps conv. CS-data + 3.4 kbps signalling	14.4 kbps streaming CS-data + 3.4 kbps signalling
UL-DPCH-InfoPredef				
>ul-DPCH-PowerControlInfo				
>>powerControlAlgorithm	Algorithm 1	Algorithm 1	Algorithm 1	Algorithm 1
>>>tpcStepSize	1 dB	1 dB	1 dB	1 dB
>tfci-Existence	TRUE	TRUE	TRUE	TRUE
>puncturingLimit	0.92	0.8	0.92	1
DL-CommonInformationPredef				
>dl-DPCH-InfoCommon				
>>spreadingFactor	64	64	32	128
>>tfci-Existence	TRUE	TRUE	TRUE	TRUE
>>pilotBits	8	8	8	8
>>positionFixed	Flexible	Flexible	Flexible	Flexible
PhyCH INFORMATION 3.84 Mcps TDD				
UL-DPCH-InfoPredef				
>ul-DPCH-PowerControlInfo				
>>dpch-ConstantValue	0	0	0	0
>commonTimeslotInfo				
>>secondInterleavingMode	frameRelated	frameRelated	frameRelated	frameRelated
>>tfci-Coding	16	8	8	8
>>puncturingLimit	0.44	0.8	0.56	0.8
>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
DL-CommonInformationPredef				
>dl-DPCH-InfoCommon				
>>commonTimeslotInfo				
>>>secondInterleavingMode	frameRelated	frameRelated	frameRelated	frameRelated
>>>tfci-Coding	16	8	8	8
>>>puncturingLimit	0.44	0.64	0.56	0.8
>>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
PhyCH INFORMATION 1.28 Mcps TDD				
UL-DPCH-InfoPredef				
>commonTimeslotInfo				
>>secondInterleavingMode	frameRelated	frameRelated	frameRelated	frameRelated
>>tfci-Coding	16	8	8	8
>>puncturingLimit	0.64	0.60	0.64	1
>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
DL-CommonInformationPredef				
>dl-DPCH-InfoCommon				
>>commonTimeslotInfo				
>>>secondInterleavingMode	frameRelated	frameRelated	frameRelated	frameRelated
>>>tfci-Coding	16	8	8	8
>>>puncturingLimit	0.64	0.60	0.64	0.88
>>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1

Configuration	28.8 kbps streaming CS-data + 3.4 kbps signalling	57.6 kbps streaming CS-data + 3.4 kbps signalling	12.2 kbps speech(multimode) + 3.4 kbps signalling
Ref 34.108	16	17	1a
Default configuration identity	8	9	10
RB INFORMATION			
rb-Identity	RB1: 1, RB2: 2, RB3: 3, RB5: 5	RB1: 1, RB2: 2, RB3: 3, RB5: 5	RB1: 1, RB2: 2, RB3: 3, RB5: 5, RB6: 6, RB7: 7
rlc-InfoChoice	Rlc-info	Rlc-info	Rlc-info
>ul-RLC-Mode	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5-RB7: TM
>>transmissionRLC-DiscardMode	RB1: N/A RB2- RB3: NoDiscard RB5: N/A	RB1: N/A RB2- RB3: NoDiscard RB5: N/A	RB1: N/A RB2- RB3: NoDiscard RB5- RB7: N/A
>>>maxDat	RB1: N/A RB2- RB3: 15 RB5: N/A	RB1: N/A RB2- RB3: 15 RB5: N/A	RB1: N/A RB2- RB3: 15 RB5- RB7: N/A
>>transmissionWindowSize	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB7: N/A
>>timerRST	RB1: N/A RB2- RB3: 300 RB5: N/A	RB1: N/A RB2- RB3: 300 RB5: N/A	RB1: N/A RB2- RB3: 300 RB5- RB7: N/A
>>max-RST	RB1: N/A RB2- RB3: 1 RB5: N/A	RB1: N/A RB2- RB3: 1 RB5: N/A	RB1: N/A RB2- RB3: 1 RB5- RB7: N/A
>>pollingInfo	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5- RB7: N/A
>>>lastTransmissionPDU-Poll	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>lastRetransmissionPDU-Poll	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerPollPeriodic	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A RB5: FALSE	RB1- RB3: N/A RB5: FALSE	RB1- RB3: N/A RB5- RB7: FALSE
>dl-RLC-Mode	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5- RB7: TM
>>inSequenceDelivery	RB1: N/A RB2- RB3: TRUE RB5: N/A	RB1: N/A RB2- RB3: TRUE RB5: N/A	RB1: N/A RB2- RB3: TRUE RB5- RB7: N/A
>>receivingWindowSize	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB7: N/A
>>dl-RLC-StatusInfo	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5- RB7: N/A
>>>timerStatusProhibit	RB2- RB3: 100	RB2- RB3: 100	RB2- RB3: 100
>>>missingPDU-Indicator	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerStatusPeriodic	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A RB5: FALSE	RB1- RB3: N/A RB5: FALSE	RB1- RB3: N/A RB5- RB7: FALSE

Configuration	28.8 kbps streaming CS-data + 3.4 kbps signalling	57.6 kbps streaming CS-data + 3.4 kbps signalling	12.2 kbps speech(multimode) + 3.4 kbps signalling
rb-MappingInfo			
>UL-LogicalChannelMappings	OneLogicalChannel	OneLogicalChannel	OneLogicalChannel
>>ul-TransportChannelType	Dch	Dch	Dch
>>>transportChannelIdentity	RB1- RB3: 2 RB5: 1	RB1- RB3: 2 RB5: 1	RB1- RB3: 4 RB5: 1, RB6: 2, RB7: 3
>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: N/A
>>rlc-SizeList	RB1- RB3: configured RB5: N/A	RB1- RB3: configured RB5: N/A	RB1- RB3: configured RB5- RB7: N/A
>>mac-LogicalChannelPriority	RB1: 1, RB2: 2, RB3: 3 RB5: 5	RB1: 1, RB2: 2, RB3: 3 RB5: 5	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: 5
>DL-logicalChannelMappingList			
>>Mapping option 1	One mapping option	One mapping option	One mapping option
>>>dl-TransportChannelType	Dch	Dch	Dch
>>>>transportChannelIdentity	RB1- RB3: 2 RB5: 1	RB1- RB3: 2 RB5: 1	RB1- RB3: 4 RB5: 1, RB6: 2, RB7: 3
>>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: N/A
TrCH INFORMATION PER TrCH			
UL-AddReconfTransChInfoList			
>Uplink transport channel type	dch	dch	dch
>transportChannelIdentity	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2, TrCH3: 3, TrCH4: 4
>transportFormatSet	DedicatedTransChTFS	DedicatedTransChTFS	DedicatedTransChTFS
>>dynamicTF-information			
>>>tf0/ tf0,1	TrCH1: (0x576, 1x576, 2x576) TrCH2: (0x144, 1x144)	TrCH1: (0x576, 1x576, 2x576, 3x576, 4x576) TrCH2: (0x144, 1x144)	TrCH1: (0x81) TrCH2: (0x 103 TrCH3: (0x 60) TrCH4: (0x144)
>>>>rlcSize	TrCH1: OctetMode TrCH2:BitMode	TrCH1: OctetMode TrCH2:BitMode	BitMode
>>>>>sizeType	TrCH1: type 2, part1= 9, part2= 2 (576) TrCH2: type 2, part1= 2, part2= 0 (144)	TrCH1: type 2, part1= 9, part2= 2 (576) TrCH2: type 2, part1= 2, part2= 0 (144)	TrCH1: type 1: 81 TrCH2: type 1: 103 TrCH3: type 1: 60 TrCH4: 2: type 2, part1= 2, part2= 0 (144)
>>>>numberOfTbSizeList	TrCH1: Zero, one, 2 TrCH2: Zero, one	TrCH1: Zero, one, 2, 3, 4 TrCH2: Zero, one	TrCH1-4: Zero
>>>>logicalChannelList	All	All	All
>>>tf 1			TrCH1: (1x39) TrCH2: (1x53) TrCH3: (1x60) TrCH4: (1x144)

Configuration	28.8 kbps streaming CS-data + 3.4 kbps signalling	57.6 kbps streaming CS-data + 3.4 kbps signalling	12.2 kbps speech(multimode) + 3.4 kbps signalling
>>>>numberOfTransportBlocks			TrCH1-3: One
>>>>rlc-Size			TrCH1-3: BitMode
>>>>>sizeType			TrCH1: 1: 39 TrCH2: 1: 53 TrCH3: 1: 60
>>>>numberOfTbSizeList			TrCH1-3: One
>>>>logicalChannelList			TrCH1-3: all
>>>tf 2			TrCH1: (1x42) TrCH2: (1x63) TrCH3- TrCH4: N/A
>>>>numberOfTransportBlocks			TrCH1-2: One
>>>>rlc-Size			TrCH1: BitMode
>>>>>sizeType			TrCH1: type 1: 42 TrCH2: type 1: 63
>>>>numberOfTbSizeList			TrCH1-2: One
>>>>logicalChannelList			TrCH1: all
>>>tf 3			TrCH1: (1x55) TrCH2: (1x84) TrCH3- TrCH4: N/A
>>>>numberOfTransportBlocks			TrCH1-2: One
>>>>rlc-Size			TrCH1: BitMode
>>>>>sizeType			TrCH1: type 1: 55 TrCH2: type 1: 84
>>>>numberOfTbSizeList			TrCH1-2: One
>>>>logicalChannelList			TrCH1: all
>>>tf 4			TrCH1: (1x75) TrCH2: (1x103) TrCH3- TrCH4: N/A
>>>>numberOfTransportBlocks			TrCH1-2: One
>>>>rlc-Size			TrCH1: BitMode
>>>>>sizeType			TrCH1: type 1: 75 TrCH2: type 1: 103
>>>>numberOfTbSizeList			TrCH1-2: One
>>>>logicalChannelList			TrCH1: all
>>>tf 5			TrCH1: (1x81) TrCH2- TrCH4: N/A
>>>>numberOfTransportBlocks			TrCH1: One
>>>>rlc-Size			TrCH1: BitMode
>>>>>sizeType			TrCH1: type 1: 81
>>>>numberOfTbSizeList			TrCH1: One
>>>>logicalChannelList			TrCH1: all
>>semiStaticTF-Information			
>>>tti	TrCH1: 40 TrCH2: 40	TrCH1: 40 TrCH2: 40	TrCH1- TrCH3: 20 TrCH4: 40
>>>channelCodingType	TrCH1: Turbo TrCH2: Convolutional	TrCH1: Turbo TrCH2: Convolutional	Convolutional
>>>>codingRate	TrCH1: N/A TrCH2: Third	TrCH1: N/A TrCH2: Third	TrCH1- TrCH2: Third TrCH3: Half TrCH4: Third
>>>rateMatchingAttribute	TrCH1: 155 TrCH2: 160	TrCH1: 145 TrCH2: 160	TrCH1: 200 TrCH2: 190 TrCH3: 235 TrCH4: 160

Configuration	28.8 kbps streaming CS-data + 3.4 kbps signalling	57.6 kbps streaming CS-data + 3.4 kbps signalling	12.2 kbps speech(multimode) + 3.4 kbps signalling
>>>crc-Size	TrCH1: 16 TrCH2: 16	TrCH1: 16 TrCH2: 16	TrCH1: 12 TrCH2- TrCH3: 0 TrCH4: 16
DL-AddReconfTransChInfoList			
>Downlink transport channel type	dch	dch	dch
>dl-TransportChannelIdentity (should be as for UL)	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2, TrCH3: 3, TrCH4: 4
>tfs-SignallingMode	SameAsUL	SameAsUL	Independent <Only tf0 on TrCH1 is different and shown below>
>>transportFormatSet			DedicatedTransChTFS
>>>dynamicTF-information			
>>>>tf0/ tf0,1			TrCH1: (1x0)
>>>>rlcSize			bitMode
>>>>>sizeType			TrCH1: type 1: 0
>>>>numberOfTbSizeList			TrCH1: One
>>>>logicalChannelList			All
>>ULTrCH-Id	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2, TrCH3: 3, TrCH4: 4
>dch-QualityTarget			
>>bler-QualityValue	TrCH1: $1x10^{-2}$ TrCH2: Absent	TrCH1: $1x10^{-2}$ TrCH2: Absent	TrCH1: $7x10^{-3}$ TrCH2- TrCH4: Absent
TrCH INFORMATION, COMMON			
ul-CommonTransChInfo			
>tfcs-ID (TDD only)	1	1	1
>sharedChannelIndicator (TDD only)	FALSE	FALSE	FALSE
>tfc-Subset	Absent, not required	Absent, not required	Absent, not required
>ul-TFCS	Normal TFCI signalling	Normal TFCI signalling	Normal TFCI signalling
>>explicitTFCS-ConfigurationMode	Complete	Complete	Complete
>>>ctfcSize	Ctfc4Bit	Ctfc4Bit	Ctfc8Bit
>>>>TFCS representation	Addition	Addition	Addition
>>>>>TFCS list			
>>>>>>TFCS 1	(TF0, TF0)	(TF0, TF0)	(TF0, TF0, TF0, TF0)
>>>>>>>ctfc	0	0	0
>>>>>>>gainFactorInformation	Computed	Computed	Computed
>>>>>>>>referenceTFCId	0	0	0
>>>>>>TFCS 2	(TF1, TF0)	(TF1, TF0)	(TF1, TF0, TF0, TF0)
>>>>>>>ctfc	1	1	1
>>>>>>>gainFactorInformation	Computed	Computed	Computed
>>>>>>>>βc (FDD only)	N/A	N/A	N/A
>>>>>>>>βd	N/A	N/A	N/A
>>>>>>>>referenceTFCId	0	0	0
>>>>>>TFCS 3	(TF2, TF0)	(TF2, TF0)	(TF2, TF1, TF0, TF0)
>>>>>>>ctfc	2	2	8

Configuration	28.8 kbps streaming CS-data + 3.4 kbps signalling	57.6 kbps streaming CS-data + 3.4 kbps signalling	12.2 kbps speech(multimode) + 3.4 kbps signalling
>>>>>>>gainFactorInformation	Computed	Computed	Computed
>>>>>>>>referenceTFCId	0	0	0
>>>>>>TFCS 4	(TF0, TF1)	(TF3, TF0)	(TF3, TF2, TF0, TF0)
>>>>>>>ctfc	3	3	15
>>>>>>>gainFactorInformation	Computed	Computed	Computed
>>>>>>>>βc (FDD only)	N/A	N/A	N/A
>>>>>>>>βd	N/A	N/A	N/A
>>>>>>>>referenceTFCId	0	0	0
>>>>>>TFCS 5	(TF1, TF1)	(TF4, TF0)	(TF4, TF3, TF0, TF0)
>>>>>>>ctfc	4	4	22
>>>>>>>gainFactorInformation	Computed	Computed	Computed
>>>>>>>>referenceTFCId	0	0	0
>>>>>>TFCS 6	(TF2, TF1)	(TF0, TF1)	(TF5, TF4, TF1, TF0)
>>>>>>>ctfc	5	5	59
>>>>>>>gainFactorInformation	Signalled	Computed	Computed
>>>>>>>>βc (FDD only)	8	N/A	N/A
>>>>>>>>βd	15	N/A	N/A
>>>>>>>>referenceTFCId	0	0	0
>>>>>>TFCS 7		(TF1, TF1)	(TF0,TF0,TF0,TF1)
>>>>>>>ctfc		6	60
>>>>>>>gainFactorInformation		Computed	Computed
>>>>>>>>referenceTFCId		0	0
>>>>>>TFCS 8		(TF2, TF1)	(TF1,TF0,TF0,TF1)
>>>>>>>ctfc		7	61
>>>>>>>gainFactorInformation		Computed	Computed
>>>>>>>>referenceTFCId		0	0
>>>>>>TFCS 9		(TF3, TF1)	(TF2,TF1,TF0,TF1)
>>>>>>>ctfc		8	68
>>>>>>>gainFactorInformation		Computed	Computed
>>>>>>>>referenceTFCId		0	0
>>>>>>TFCS 10		(TF4, TF1)	(TF3,TF2,TF0,TF1)
>>>>>>>ctfc		9	75
>>>>>>>gainFactorInformation		Signalled	Computed
>>>>>>>>βc (FDD only)		8	N/A
>>>>>>>>βd		15	N/A
>>>>>>>>referenceTFCId		0	0
>>>>>>TFCS 11			(TF4,TF3,TF0,TF1)
>>>>>>>ctfc			82
>>>>>>>gainFactorInformation			Computed
>>>>>>>>referenceTFCId			0
>>>>>>TFCS 12			(TF5,TF4,TF1,TF1)
>>>>>>>ctfc			119
>>>>>>>gainFactorInformation			Signalled
>>>>>>>>βc (FDD only)			11
>>>>>>>>βd			15
>>>>>>>>referenceTFCId			0

Configuration	28.8 kbps streaming CS-data + 3.4 kbps signalling	57.6 kbps streaming CS-data + 3.4 kbps signalling	12.2 kbps speech(multimode) + 3.4 kbps signalling
dl-CommonTransChInfo			
>tfcs-SignallingMode	Same as UL	Same as UL	Same as UL
PhyCH INFORMATION FDD			
UL-DPCH-InfoPredef			
>ul-DPCH-PowerControlInfo			
>>powerControlAlgorithm	Algorithm 1	Algorithm 1	Algorithm 1
>>>tpcStepSize	1 dB	1 dB	1 dB
>tfci-Existence	TRUE	TRUE	TRUE
>puncturingLimit	1	1	0.88
DL-CommonInformationPredef			
>dl-DPCH-InfoCommon			
>>spreadingFactor	64	32	128
>>tfci-Existence	TRUE	TRUE	FALSE
>>pilotBits	8	8	4
>>positionFixed	Flexible	Flexible	Fixed
PhyCH INFORMATION 3.84 Mcps TDD			
UL-DPCH-InfoPredef			
>ul-DPCH-PowerControlInfo			
>>dpch-ConstantValue	0	0	0
>commonTimeslotInfo			
>>secondInterleavingMode	frameRelated	frameRelated	frameRelated
>>tfci-Coding	16	16	16
>>puncturingLimit	0.44	0.48	0.88
>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
DL-CommonInformationPredef			
>dl-DPCH-InfoCommon			
>>commonTimeslotInfo			
>>>secondInterleavingMode	frameRelated	frameRelated	frameRelated
>>>tfci-Coding	16	16	16
>>>puncturingLimit	0.44	0.48	0.92
>>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
PhyCH INFORMATION 1.28 Mcps TDD			
UL-DPCH-InfoPredef			
>commonTimeslotInfo			
>>secondInterleavingMode	frameRelated	frameRelated	
>>tfci-Coding	16	16	
>>puncturingLimit	0.64	0.72	
>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	
DL-CommonInformationPredef			
>dl-DPCH-InfoCommon			
>>commonTimeslotInfo			
>>>secondInterleavingMode	frameRelated	frameRelated	frameRelated

Configuration	28.8 kbps streaming CS-data + 3.4 kbps signalling	57.6 kbps streaming CS-data + 3.4 kbps signalling	12.2 kbps speech(multimode) + 3.4 kbps signalling
>>>tfci-Coding	16	16	16
>>>puncturingLimit	0.64	0.72	0.92
>>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1

Configuration	10.2/6.7/5.9/4.75 kbps speech + 3.4 kbps signalling	7.4/6.7/5.9/4.75 kbps speech + 3.4 kbps signalling
Ref 34.108	N/A	N/A
Default configuration identity	11	12
RB INFORMATION		
rb-Identity	RB1: 1, RB2: 2, RB3: 3, RB5: 5, RB6: 6, RB7: 7, RB8: 8	RB1: 1, RB2: 2, RB3: 3, RB5: 5, RB6: 6, RB7: 7
rlc-InfoChoice	Rlc-info	Rlc-info
>ul-RLC-Mode	RB1: UM RB2- RB3: AM RB5-RB7: TM	RB1: UM RB2- RB3: AM RB5-RB6: TM
>>transmissionRLC-DiscardMode	RB1: N/A RB2- RB3: NoDiscard RB5- RB7: N/A	RB1: N/A RB2- RB3: NoDiscard RB5- RB6: N/A
>>>maxDat	RB1: N/A RB2- RB3: 15 RB5- RB7: N/A	RB1: N/A RB2- RB3: 15 RB5- RB6: N/A
>>transmissionWindowSize	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB7: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB6: N/A
>>timerRST	RB1: N/A RB2- RB3: 300 RB5- RB7: N/A	RB1: N/A RB2- RB3: 300 RB5- RB6: N/A
>>max-RST	RB1: N/A RB2- RB3: 1 RB5- RB7: N/A	RB1: N/A RB2- RB3: 1 RB5- RB6: N/A
>>pollingInfo	RB1: N/A RB2- RB3: as below RB5- RB7: N/A	RB1: N/A RB2- RB3: as below RB5- RB6: N/A
>>>lastTransmissionPDU-Poll	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>lastRetransmissionPDU-Poll	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerPollPeriodic	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A RB5- RB7: FALSE	RB1- RB3: N/A RB5- RB6: FALSE
>dl-RLC-Mode	RB1: UM RB2- RB3: AM RB5- RB7: TM RB8: TM	RB1: UM RB2- RB3: AM RB5- RB6: TM RB7: TM
>>inSequenceDelivery	RB1: N/A RB2- RB3: TRUE RB5- RB8: N/A	RB1: N/A RB2- RB3: TRUE RB5- RB7: N/A
>>receivingWindowSize	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB8: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB7: N/A
>>dl-RLC-StatusInfo	RB1: N/A RB2- RB3: as below RB5- RB8: N/A	RB1: N/A RB2- RB3: as below RB5- RB7: N/A
>>>timerStatusProhibit	RB2- RB3: 100	RB2- RB3: 100
>>>missingPDU-Indicator	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerStatusPeriodic	RB2- RB3: 300	RB2- RB3: 300

>>segmentationIndication	RB1- RB3: N/A RB5- RB8: FALSE	RB1- RB3: N/A RB5- RB7: FALSE
rb-MappingInfo		
>UL-LogicalChannelMappings	OneLogicalChannel	OneLogicalChannel
>>ul-TransportChannelType	Dch	Dch
>>>transportChannelIdentity	RB1- RB3: 4 RB5: 1, RB6: 2, RB7: 3,	RB1- RB3: 3 RB5: 1, RB6: 2
>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: N/A	RB1: 1, RB2: 2, RB3: 3 RB5- RB6: N/A
>>rlc-SizeList	RB1- RB3: configured RB5- RB7: N/A	RB1- RB3: configured RB5- RB6: N/A
>>mac-LogicalChannelPriority	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: 5	RB1: 1, RB2: 2, RB3: 3 RB5- RB6: 5
>DL-logicalChannelMappingList		
>>Mapping option 1	One mapping option	One mapping option
>>>dl-TransportChannelType	Dch	Dch
>>>>transportChannelIdentity	RB1- RB3: 4 RB5: 1, RB6: 2, RB7: 3, RB8: 5	RB1- RB3: 3 RB5: 1, RB6: 2, RB7:4
>>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3 RB5- RB8: N/A	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: N/A
TrCH INFORMATION PER TrCH		
UL-AddReconfTransChInfoList		
>Uplink transport channel type	dch	dch
>transportChannelIdentity	TrCH1: 1, TrCH2: 2, TrCH3: 3, TrCH4: 4	TrCH1: 1, TrCH2: 2, TrCH3: 3
>transportFormatSet	DedicatedTransChTFS	DedicatedTransChTFS
>>dynamicTF-information		
>>>tf0/ tf0,1	TrCH1: (0x65) TrCH2: (0x 99) TrCH3: (0x 40, 1x40) TrCH4: (0x144, 1x144)	TrCH1: (0x61) TrCH2: (0x 87) TrCH3: (0x 144, 1x144)
>>>>rlcSize	BitMode	BitMode
>>>>>sizeType	TrCH1: type 1: 65 TrCH2: type 1: 99 TrCH3: type 1: 40 TrCH4: 2: type 2, part1= 2, part2= 0 (144)	TrCH1: type 1: 61 TrCH2: type 1: 87 TrCH3: 2: type 2, part1= 2, part2= 0 (144)
>>>>numberOfTbSizeList	TrCH1-2: Zero TrCH3-4: Zero, one	TrCH1-2: Zero TrCH3: Zero, one
>>>>logicalChannelList	All	All
>>>tf 1	TrCH1: (1x39) TrCH2: (1x 53) TrCH3- TrCH4: N/A	TrCH1: (1x39) TrCH2: (1x53) TrCH3: N/A
>>>>numberOfTransportBlocks	TrCH1: One TrCH2: One	TrCH1: One TrCH2: One
>>>>rlc-Size	TrCH1-2: BitMode	TrCH1-2: BitMode
>>>>>sizeType	TrCH1: 1: 39 TrCH2: 1: 53	TrCH1: 1: 39 TrCH1: 1: 53
>>>>numberOfTbSizeList	TrCH1-2: One	TrCH1-2: One

>>>>logicalChannelList	TrCH1: all	TrCH1: all
>>>tf 2	TrCH1: (1x42) TrCH2: (1x63) TrCH3- TrCH4: N/A	TrCH1: (1x42) TrCH2: (1x63) TrCH3: N/A
>>>>numberOfTransportBlocks	TrCH1: One TrCh2: One	TrCH1: One TrCh2: One
>>>>rlc-Size	TrCH1: BitMode	TrCH1: BitMode
>>>>>sizeType	TrCH1: type 1: 42 TrCH2: type 1: 63	TrCH1: type 1: 42 TrCH2: type 1: 63
>>>>numberOfTbSizeList	TrCH1: One TrCH2: One	TrCH1: One TrCH2: One
>>>>logicalChannelList	TrCH1: all TrCH2: all	TrCH1: all TrCH2: all
>>>tf 3	TrCH1: (1x55) TrCH2: (1x76) TrCH3- TrCH4: N/A	TrCH1: (1x55) TrCH2: (1x76) TrCH3: N/A
>>>>numberOfTransportBlocks	TrCH1: One TrCh2: One	TrCH1: One TrCh2: One
>>>>rlc-Size	TrCH1: BitMode	TrCH1: BitMode
>>>>>sizeType	TrCH1: type 1: 55 TrCH2: type 1: 76	TrCH1: type 1: 55 TrCH2: type 1: 76
>>>>numberOfTbSizeList	TrCH1: One TrCH2: One	TrCH1: One TrCH2: One
>>>>logicalChannelList	TrCH1: all TrCH2: all	TrCH1: all TrCH2: all
>>>tf 4	TrCH1: (1x58) TrCH2: (1x99) TrCH3- TrCH4: N/A	TrCH1: (1x58) TrCH2: (1x87) TrCH3: N/A
>>>>numberOfTransportBlocks	TrCH1: One TrCh2: One	TrCH1: One TrCh2: One
>>>>rlc-Size	TrCH1: BitMode	TrCH1: BitMode
>>>>>sizeType	TrCH1: type 1: 58 TrCH2: type 1: 99	TrCH1: type 1: 58 TrCH2: type 1: 87
>>>>numberOfTbSizeList	TrCH1: One TrCH2: One	TrCH1: One TrCH2: One
>>>>logicalChannelList	TrCH1: all TrCH2: all	TrCH1: all TrCH2: all
>>>tf 5	TrCH1: (1x65) TrCH2- TrCH4: N/A	TrCH1: (1x61) TrCH2- TrCH4: N/A
>>>>numberOfTransportBlocks	TrCH1: One	TrCH1: One
>>>>rlc-Size	TrCH1: BitMode	TrCH1: BitMode
>>>>>sizeType	TrCH1: type 1: 65	TrCH1: type 1: 61
>>>>numberOfTbSizeList	TrCH1: One	TrCH1: One
>>>>logicalChannelList	TrCH1: all	TrCH1: all
>>semistaticTF-Information		
>>>tti	TrCH1- TrCH3: 20 TrCH4: 40	TrCH1- TrCH2: 20 TrCH3: 40
>>>channelCodingType	Convolutional	Convolutional
>>>>codingRate	TrCH1- TrCH2: Third TrCH3: Half TrCH4: Third	TrCH1- TrCH2: Third TrCH3: Third
>>>rateMatchingAttribute	TrCH1: 200 TrCH2: 190 TrCH3: 235 TrCH4: 160	TrCH1: 200 TrCH2: 190 TrCH3: 160
>>>crc-Size	TrCH1: 12 TrCH2- TrCH3: 0 TrCH4: 16	TrCH1: 12 TrCH2: 0 TrCH3: 16
DL-AddReconfTransChInfoList		
>Downlink transport channel type	dch	dch
>dl-TransportChannelIdentity		

>tfs-SignallingMode	Independent <Only tf0 on TrCH1 and tf0/tf1 on TrCH5 are different and shown below>	Independent <Only tf0 on TrCH1 and tf0/tf1 on TrCH4 are different and shown below>
>>transportFormatSet		
>>>dynamicTF-information		
>>>>tf0/ tf0,1	TrCH1: (1x0) TrCH5: (0x3, 1x3)	TrCH1: (1x0) TrCH4: (0x3, 1x3)
>>>>rlcSize	BitMode	bitMode
>>>>>sizeType	TrCH1: type 1: 0 TrCH5: type 1: 3	TrCH1: type 1: 0 TrCH4: type 1: 3
>>>>numberOfTbSizeList	TrCH1: One TrCH5: Zero, one	TrCH1: One TrCH4: Zero, one
>>>>logicalChannelList	All	All
>>>semistaticTF-Information	same as UL except for TrCH5	same as UL except for TrCH4
>>>>tti	TrCH5: 20	TrCH4: 20
>>>>channelCodingType	Convolutional	Convolutional
>>>>>codingRate	TrCH5: Third	TrCH4: Third
>>>>rateMatchingAttribute	TrCH5: 200	TrCH4: 200
>>>>crc-Size	TrCH5: 12	TrCH4: 12
>>ULTrCH-Id	TrCH1: 1, TrCH2: 2, TrCH3: 3, TrCH4: 4,	TrCH1: 1, TrCH2: 2, TrCH3: 3
>dch-QualityTarget		
>>bler-QualityValue	TrCH1: $7x10^{-3}$ TrCH2- TrCH5: Absent	TrCH1: $7x10^{-3}$ TrCH2- TrCH4: Absent
TrCH INFORMATION, COMMON		
ul-CommonTransChInfo		
>tfcs-ID (TDD only)	1	1
>sharedChannelIndicator (TDD only)	FALSE	FALSE
> tfc-Subset	Absent, not required	Absent, not required
>ul-TFCS	Normal TFCI signalling	Normal TFCI signalling
>>explicitTFCS-ConfigurationMode	Complete	Complete
>>>ctfcSize	Ctfc8Bit	Ctfc6Bit
>>>>TFCS representation	Addition	Addition
>>>>>TFC list		
>>>>>>TFC 1	(TF0, TF0, TF0, TF0)	(TF0, TF0, TF0)
>>>>>>>ctfc	0	0
>>>>>>>gainFactorInformation	Computed	Computed
>>>>>>>referenceTFCId	0	0
>>>>>>TFC 2	(TF1, TF0, TF0, TF0)	(TF1, TF0, TF0)
>>>>>>>ctfc	1	1
>>>>>>>gainFactorInformation	Computed	Computed
>>>>>>>βc (FDD only)	N/A	N/A
>>>>>>>βd	N/A	N/A
>>>>>>>referenceTFCId	0	0
>>>>>>TFC 3	(TF2, TF1, TF0, TF0)	(TF2, TF1, TF0)
>>>>>>>ctfc	8	8
>>>>>>>gainFactorInformation	Computed	Computed
>>>>>>>referenceTFCId	0	0
>>>>>>TFC 4	(TF3, TF2, TF0, TF0)	(TF3, TF2, TF0)
>>>>>>>ctfc	15	15

>>>>>>>gainFactorInformation	Computed	Computed
>>>>>>>>βc (FDD only)		
>>>>>>>>βd		
>>>>>>>referenceTFCId	0	0
>>>>>>TFC 5	(TF4, TF3, TF0, TF0)	(TF4, TF3, TF0)
>>>>>>>ctfc	22	22
>>>>>>>gainFactorInformation	Computed	Computed
>>>>>>>>referenceTFCId	0	0
>>>>>>TFC 6	(TF5, TF4, TF1, TF0)	(TF5, TF4, TF0)
>>>>>>>ctfc	59	29
>>>>>>>gainFactorInformation	Computed	Computed
>>>>>>>>βc (FDD only)		
>>>>>>>>βd		
>>>>>>>>referenceTFCId	0	0
>>>>>>TFC 7	(TF0, TF0, TF0, TF1)	(TF0, TF0, TF1)
>>>>>>>ctfc	60	30
>>>>>>>gainFactorInformation	Computed	Computed
>>>>>>>>referenceTFCId	0	0
>>>>>>TFC 8	(TF1, TF0, TF0, TF1)	(TF1, TF0, TF1)
>>>>>>>ctfc	61	31
>>>>>>>gainFactorInformation	computed	computed
>>>>>>>>βc (FDD only)		
>>>>>>>>βd		
>>>>>>>>referenceTFCId	0	0
>>>>>>TFC 9	(TF2, TF1, TF0, TF1)	(TF2, TF1, TF1)
>>>>>>>ctfc	68	38
>>>>>>>gainFactorInformation	computed	computed
>>>>>>>>referenceTFCId	0	0
>>>>>>TFC 10	(TF3, TF2, TF0, TF1)	(TF3, TF2, TF1)
>>>>>>>ctfc	75	45
>>>>>>>gainFactorInformation	computed	computed
>>>>>>>>βc (FDD only)		
>>>>>>>>βd		
>>>>>>>>referenceTFCId	0	0
>>>>>>TFC 11	(TF4, TF3, TF0, TF1)	(TF4, TF3, TF1)
>>>>>>>ctfc	82	52
>>>>>>>gainFactorInformation	computed	computed
>>>>>>>>referenceTFCId	0	0
>>>>>>TFC 12	(TF5, TF4, TF1, TF1)	(TF5, TF4, TF1)
>>>>>>>ctfc	97	59
>>>>>>>gainFactorInformation	signalled	signalled
>>>>>>>>βc (FDD only)	11	11
>>>>>>>>βd	15	15
>>>>>>>>referenceTFCId	0	0
> TFC subset list		
>>TFC subset 1	(speech rate 10.2)	(speech rate 7.4)

>>> Allowed transport format combination list	(TFC1, TFC2, TFC7, TFC8, TFC6, TFC12)	(TFC1, TFC2, TFC7, TFC8, TFC6, TFC12)
>>TFC subset 2	(speech rate 6.7)	(speech rate 6.7)
>>> Allowed transport format combination list	(TFC1, TFC2, TFC7, TFC8, TFC5, TFC11)	(TFC1, TFC2, TFC7, TFC8, TFC5, TFC11)
>>TFC subset 3	(speech rate 5.9)	(speech rate 5.9)
>>> Allowed transport format combination list	(TFC1, TFC2, TFC7, TFC8, TFC4, TFC10)	(TFC1, TFC2, TFC7, TFC8, TFC4, TFC10)
>>TFC subset 4	(speech rate 4.75)	(speech rate 4.75)
>>> Allowed transport format combination list	(TFC1, TFC2, TFC7, TFC8, TFC3, TFC9)	(TFC1, TFC2, TFC7, TFC8, TFC3, TFC9)
dl-CommonTransChInfo		
>tfcs-SignallingMode	Independent	Independent
>tfcs-ID (TDD only)	1	1
>sharedChannelIndicator (TDD only)	FALSE	FALSE
> tfc-Subset	Absent, not required	Absent, not required
>dl-TFCS	Normal TFCI signalling	Normal TFCI signalling
>>explicitTFCS-ConfigurationMode	Complete	Complete
>>>ctfcSize	Ctfc8Bit	Ctfc8Bit
>>>>TFCS representation	Addition	Addition
>>>>>TFCS list		
>>>>>>TFC 1	(TF0, TF0, TF0, TF0, TF0)	(TF0, TF0, TF0, TF0)
>>>>>>>ctfc	0	0
>>>>>>TFC 2	(TF1, TF0, TF0, TF0, TF0)	(TF1, TF0, TF0, TF0)
>>>>>>>ctfc	1	1
>>>>>>TFC 3	(TF2, TF1, TF0, TF0, TF0)	(TF2, TF1, TF0, TF0)
>>>>>>>ctfc	8	8
>>>>>>TFC 4	(TF3, TF2, TF0, TF0, TF0)	(TF3, TF2, TF0, TF0)
>>>>>>>ctfc	15	15
>>>>>>TFC 5	(TF4, TF3, TF0, TF0, TF0)	(TF4, TF3, TF0, TF0)
>>>>>>>ctfc	22	22
>>>>>>TFC 6	(TF5, TF4, TF1, TF0, TF0)	(TF5, TF4, TF0, TF0)
>>>>>>>ctfc	59	29
>>>>>>TFC 7	(TF0, TF0, TF0, TF1, TF0)	(TF0, TF0, TF1, TF0)
>>>>>>>ctfc	60	30
>>>>>>TFC 8	(TF1, TF0, TF0, TF1, TF0)	(TF1, TF0, TF1, TF0)
>>>>>>>ctfc	61	31
>>>>>>TFC 9	(TF2, TF1, TF0, TF1, TF0)	(TF2, TF1, TF1, TF0)
>>>>>>>ctfc	68	37
>>>>>>TFC 10	(TF3, TF2, TF0, TF1, TF0)	(TF3, TF2, TF1, TF0)
>>>>>>>ctfc	75	55
>>>>>>TFC 11	(TF4, TF3, TF0, TF1, TF0)	(TF4, TF3, TF1, TF0)
>>>>>>>ctfc	82	52
>>>>>>TFC 12	(TF5, TF4, TF1, TF1, TF0)	(TF5, TF4, TF1, TF0)
>>>>>>>ctfc	119	59

>>>>>>TFC 13	(TF0, TF0, TF0, TF0, TF1)	(TF0, TF0, TF0, TF1)
>>>>>>>ctfc	120	60
>>>>>>TFC 14	(TF1, TF0, TF0, TF0, TF1)	(TF1, TF0, TF0, TF1)
>>>>>>>ctfc	121	61
>>>>>>TFC 15	(TF2, TF1, TF0, TF0, TF1)	(TF2, TF1, TF0, TF1)
>>>>>>>ctfc	128	68
>>>>>>TFC 16	(TF3, TF2, TF0, TF0, TF1)	(TF3, TF2, TF0, TF1)
>>>>>>>ctfc	135	75
>>>>>>TFC 17	(TF4, TF3, TF0, TF0, TF1)	(TF4, TF3, TF0, TF1)
>>>>>>>ctfc	152	82
>>>>>>TFC 18	(TF5, TF4, TF1, TF0, TF1)	(TF5, TF4, TF0, TF1)
>>>>>>>ctfc	189	89
>>>>>>TFC 19	(TF0, TF0, TF0, TF1, TF1)	(TF0, TF0, TF1, TF1)
>>>>>>>ctfc	180	90
>>>>>>TFC 20	(TF1, TF0, TF0, TF1, TF1)	(TF1, TF0, TF1, TF1)
>>>>>>>ctfc	181	91
>>>>>>TFC 21	(TF2, TF1, TF0, TF1, TF1)	(TF2, TF1, TF1, TF1)
>>>>>>>ctfc	188	98
>>>>>>TFC 22	(TF3, TF2, TF0, TF1, TF1)	(TF3, TF2, TF1, TF1)
>>>>>>>ctfc	195	105
>>>>>>TFC 23	(TF4, TF3, TF0, TF1, TF1)	(TF4, TF3, TF1, TF1)
>>>>>>>ctfc	239	112
>>>>>>TFC 24	(TF5, TF4, TF1, TF1, TF1)	(TF5, TF4, TF1, TF1)
>>>>>>>ctfc	218	119
PhyCH INFORMATION FDD		
UL-DPCH-InfoPredef		
>ul-DPCH-PowerControlInfo		
>>powerControlAlgorithm	Algorithm 1	Algorithm 1
>>>tpcStepSize	1 dB	1 dB
>tfci-Existence	TRUE	TRUE
>puncturingLimit	0.88	0.88
DL-CommonInformationPredef		
>dl-DPCH-InfoCommon		
>>spreadingFactor	128	128
>>tfci-Existence	FALSE	FALSE
>>pilotBits	4	4
>>positionFixed	Fixed	Fixed
PhyCH INFORMATION 3.84 Mcps TDD		
UL-DPCH-InfoPredef		
>ul-DPCH-PowerControlInfo		
>>dpch-ConstantValue	0	0
>commonTimeslotInfo		
>>secondInterleavingMode	frameRelated	frameRelated
>>tfci-Coding	16	16
>>puncturingLimit	0.60	0.60
>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1

DL-CommonInformationPredef		
>dl-DPCH-InfoCommon		
>>commonTimeslotInfo		
>>>secondInterleavingMode	frameRelated	frameRelated
>>>tfci-Coding	16	16
>>>puncturingLimit	0.60	0.60
>>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1
PhyCH INFORMATION 1.28 Mcps TDD		
UL-DPCH-InfoPredef		
>commonTimeslotInfo		
>>secondInterleavingMode	frame Related	frame Related
>>tfci-Coding	16	16
>>puncturingLimit	0.64	0.64
>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1
DL-CommonInformationPredef		
>dl-DPCH-InfoCommon		
>>commonTimeslotInfo		
>>>secondInterleavingMode	frame Related	frame Related
>>>tfci-Coding	16	16
>>>puncturingLimit	0.64	0.64
>>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1

Configuration	12.65/8.85/6.6 kbps speech + 3.4 kbps signalling
Ref 34.108	62
Default configuration identity	13
RB INFORMATION	
rb-Identity	RB1: 1, RB2: 2, RB3: 3, RB5: 5, RB6: 6, RB8: 8
rlc-InfoChoice	Rlc-info
>ul-RLC-Mode	RB1: UM RB2- RB3: AM RB5-RB6: TM
>>transmissionRLC-DiscardMode	RB1: N/A RB2- RB3: NoDiscard RB5- RB6: N/A
>>>maxDat	RB1: N/A RB2- RB3: 15 RB5- RB6: N/A
>>transmissionWindowSize	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB6: N/A
>>timerRST	RB1: N/A RB2- RB3: 300 RB5- RB6: N/A
>>max-RST	RB1: N/A RB2- RB3: 1 RB5- RB6: N/A
>>pollingInfo	RB1: N/A RB2- RB3: as below RB5- RB6: N/A
>>>lastTransmissionPDU-Poll	RB2- RB3: FALSE
>>>lastRetransmissionPDU-Poll	RB2- RB3: FALSE
>>>timerPollPeriodic	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A RB5- RB6: FALSE
>dl-RLC-Mode	RB1: UM RB2- RB3: AM RB5- RB6: TM RB8: TM
>>inSequenceDelivery	RB1: N/A RB2- RB3: TRUE RB5- RB6: N/A RB8: N/A
>>receivingWindowSize	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB6: N/A RB8: N/A
>>dl-RLC-StatusInfo	RB1: N/A RB2- RB3: as below RB5- RB6: N/A RB8: N/A
>>>timerStatusProhibit	RB2- RB3: 100
>>>missingPDU-Indicator	RB2- RB3: FALSE

>>>timerStatusPeriodic	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A RB5- RB6: FALSE RB8: FALSE
rb-MappingInfo	
>UL-LogicalChannelMappings	OneLogicalChannel
>>ul-TransportChannelType	Dch
>>>transportChannelIdentity	RB1- RB3: 4 RB5: 1, RB6: 2.
>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3 RB5- RB6: N/A
>>rlc-SizeList	RB1- RB3: configured RB5- RB6: N/A
>>mac-LogicalChannelPriority	RB1: 1, RB2: 2, RB3: 3 RB5- RB6: 5
>DL-logicalChannelMappingList	
>>Mapping option 1	One mapping option
>>>dl-TransportChannelType	Dch
>>>>transportChannelIdentity	RB1- RB3: 4 RB5: 1, RB6: 2, RB8: 5
>>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3 RB5- RB6: N/A RB8: N/A
TrCH INFORMATION PER TrCH	
UL-AddReconfTransChInfoList	
>Uplink transport channel type	dch
>transportChannelIdentity	TrCH1: 1, TrCH2: 2, TrCH4: 4
>transportFormatSet	DedicatedTransChTFS
>>dynamicTF-information	
>>>tf0/ tf0,1	TrCH1: (0x72) TrCH2: (0x 181) TrCH4: (0x144, 1x144)
>>>>rlcSize	BitMode
>>>>>sizeType	TrCH1: type 1: 72 TrCH2: type 1: 181 TrCH4: 2: type 2, part1= 2, part2= 0 (144)
>>>>numberOfTbSizeList	TrCH1-2: Zero TrCH4: Zero, one
>>>>logicalChannelList	All
>>>tf 1	TrCH1: (1x40) TrCH2: (1x 78) TrCH4: N/A
>>>>numberOfTransportBlocks	TrCH1: One TrCH2: One
>>>>rlc-Size	TrCH1-2: BitMode
>>>>>sizeType	TrCH1: 1: 40 TrCH2: 1: 78
>>>>numberOfTbSizeList	TrCH1-2: One
>>>>logicalChannelList	TrCH1: all

>>>tf 2	TrCH1: (1x54) TrCH2: (1x113) TrCH4: N/A
>>>>numberOfTransportBlocks	TrCH1: One TrCh2: One
>>>>rlc-Size	TrCH1: BitMode
>>>>>sizeType	TrCH1: type 1: 54 TrCH2: type 1: 113
>>>>numberOfTbSizeList	TrCH1: One TrCH2: One
>>>>logicalChannelList	TrCH1: all TrCH2: all
>>>tf 3	TrCH1: (1x64) TrCH2: (1x181) TrCH4: N/A
>>>>numberOfTransportBlocks	TrCH1: One TrCh2: One
>>>>rlc-Size	TrCH1: BitMode
>>>>>sizeType	TrCH1: type 1: 64 TrCH2: type 1: 181
>>>>numberOfTbSizeList	TrCH1: One TrCH2: One
>>>>logicalChannelList	TrCH1: all TrCH2: all
>>>tf 4	TrCH1: (1x72) TrCH2: N/A TrCH4: N/A
>>>>numberOfTransportBlocks	TrCH1: One
>>>>rlc-Size	TrCH1: BitMode
>>>>>sizeType	TrCH1: type 1: 72
>>>>numberOfTbSizeList	TrCH1: One
>>>>logicalChannelList	TrCH1: all
>>semistaticTF-Information	
>>>tti	TrCH1- TrCH2: 20 TrCH4: 40
>>>channelCodingType	Convolutional
>>>>codingRate	TrCH1- TrCH2: Third TrCH4: Third
>>>rateMatchingAttribute	TrCH1: 200 TrCH2: 190 TrCH4: 170
>>>crc-Size	TrCH1: 12 TrCH2: 0 TrCH4: 16
DL-AddReconfTransChInfoList	
>Downlink transport channel type	dch
>dl-TransportChannelIdentity	
>tfs-SignallingMode	Independent <Only tf0 on TrCH1 and tf0/tf1 on TrCH5 are different and shown below>
>>transportFormatSet	
>>>dynamicTF-information	
>>>>tf0/ tf0,1	TrCH1: (1x0) TrCH5: (0x3, 1x3)
>>>>rlcSize	BitMode
>>>>>sizeType	TrCH1: type 1: 0 TrCH5: type 1: 3
>>>>numberOfTbSizeList	TrCH1: One TrCH5: Zero, one

>>>>logicalChannelList	All
>>>semistaticTF-Information	same as UL except for TrCH5
>>>>tti	TrCH5: 20
>>>>channelCodingType	Convolutional
>>>>>codingRate	TrCH5: Third
>>>>rateMatchingAttribute	TrCH5: 205
>>>>crc-Size	TrCH5: 8
>>ULTrCH-Id	TrCH1: 1, TrCH2: 2, TrCH4: 4,
>dch-QualityTarget	
>>bler-QualityValue	TrCH1: $7x10^{-3}$ TrCH2: Absent TrCH4- TrCH5: Absent
TrCH INFORMATION, COMMON	
ul-CommonTransChInfo	
>tfcs-ID (TDD only)	1
>sharedChannelIndicator (TDD only)	FALSE
> tfc-Subset	Absent, not required
>ul-TFCS	Normal TFCI signalling
>>explicitTFCS-ConfigurationMode	Complete
>>>ctfcSize	Ctfc6Bit
>>>>TFCS representation	Addition
>>>>>TFC list	
>>>>>>TFC 1	(TF0, TF0, TF0)
>>>>>>>ctfc	0
>>>>>>>gainFactorInformation	Computed
>>>>>>>>referenceTFCId	0
>>>>>>TFC 2	(TF1, TF0, TF0)
>>>>>>>ctfc	1
>>>>>>>gainFactorInformation	Computed
>>>>>>>>βc (FDD only)	N/A
>>>>>>>>βd	N/A
>>>>>>>>referenceTFCId	0
>>>>>>TFC 3	(TF2, TF1, TF0)
>>>>>>>ctfc	7
>>>>>>>gainFactorInformation	Computed
>>>>>>>>referenceTFCId	0
>>>>>>TFC 4	(TF3, TF2, TF0)
>>>>>>>ctfc	13
>>>>>>>gainFactorInformation	Computed
>>>>>>>>βc (FDD only)	
>>>>>>>>βd	
>>>>>>>>referenceTFCId	0
>>>>>>TFC 5	(TF4, TF3, TF0)
>>>>>>>ctfc	19
>>>>>>>gainFactorInformation	Computed
>>>>>>>>referenceTFCId	0
>>>>>>TFC 6	(TF0, TF0, TF1)
>>>>>>>ctfc	20
>>>>>>>gainFactorInformation	Computed
>>>>>>>>βc (FDD only)	
>>>>>>>>βd	

>>>>>>>>referenceTFCId	0
>>>>>>TFC 7	(TF1, TF0, TF1)
>>>>>>>ctfc	21
>>>>>>>gainFactorInformation	Computed
>>>>>>>>referenceTFCId	0
>>>>>>TFC 8	(TF2, TF1, TF1)
>>>>>>>ctfc	27
>>>>>>>gainFactorInformation	computed
>>>>>>>>βc (FDD only)	
>>>>>>>>βd	
>>>>>>>>referenceTFCId	0
>>>>>>TFC 9	(TF3, TF2, TF1)
>>>>>>>ctfc	33
>>>>>>>gainFactorInformation	computed
>>>>>>>>referenceTFCId	0
>>>>>>TFC 10	(TF4, TF3, TF1)
>>>>>>>ctfc	39
>>>>>>>gainFactorInformation	signalled
>>>>>>>>βc (FDD only)	11
>>>>>>>>βd	15
>>>>>>>>referenceTFCId	0
> TFC subset list	
>>TFC subset 1	(speech rate 6.6)
>>> Allowed transport format combination list	(TFC1, TFC2, TFC3, TFC6, TFC7, TFC8)
>>TFC subset 2	(speech rate 8.85)
>>> Allowed transport format combination list	(TFC1, TFC2, TFC3, TFC4, TFC6, TFC7, TFC8, TFC9)
>>TFC subset 3	(speech rate 12.65)
>>> Allowed transport format combination list	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10)
dl-CommonTransChInfo	
>tfcs-SignallingMode	Independent
ul-CommonTransChInfo	
>tfcs-ID (TDD only)	1
>sharedChannelIndicator (TDD only)	FALSE
> tfc-Subset	Absent, not required
>dl-TFCS	Normal TFCI signalling
>>explicitTFCS-ConfigurationMode	Complete
>>>ctfcSize	Ctfc8Bit
>>>>TFCS representation	Addition
>>>>>TFCS list	
>>>>>>TFC 1	(TF0, TF0, TF0, TF0)
>>>>>>>ctfc	0
>>>>>>TFC 2	(TF1, TF0, TF0, TF0)
>>>>>>>ctfc	1
>>>>>>TFC 3	(TF2, TF1, TF0, TF0)
>>>>>>>ctfc	7
>>>>>>TFC 4	(TF3, TF2, TF0, TF0)

>>>>>>>ctfc	13
>>>>>>TFC 5	(TF4, TF3, TF0, TF0)
>>>>>>>ctfc	19
>>>>>>TFC 6	(TF0, TF0, TF1, TF0)
>>>>>>>ctfc	20
>>>>>>TFC 7	(TF1, TF0, TF1, TF0)
>>>>>>>ctfc	21
>>>>>>TFC 8	(TF2, TF1, TF1, TF0)
>>>>>>>ctfc	27
>>>>>>TFC 9	(TF3, TF2, TF1, TF0)
>>>>>>>ctfc	33
>>>>>>TFC 10	(TF4, TF3, TF1, TF0)
>>>>>>>ctfc	39
>>>>>>TFC 11	(TF0, TF0, TF0, TF1)
>>>>>>>ctfc	40
>>>>>>TFC 12	(TF1, TF0, TF0, TF1)
>>>>>>>ctfc	41
>>>>>>TFC 13	(TF2, TF1, TF0, TF1)
>>>>>>>ctfc	47
>>>>>>TFC 14	(TF3, TF2, TF0, TF1)
>>>>>>>ctfc	53
>>>>>>TFC 15	(TF4, TF3, TF0, TF1)
>>>>>>>ctfc	59
>>>>>>TFC 16	(TF0, TF0, TF1, TF1)
>>>>>>>ctfc	60
>>>>>>TFC 17	(TF1, TF0, TF1, TF1)
>>>>>>>ctfc	61
>>>>>>TFC 18	(TF2, TF1, TF1, TF1)
>>>>>>>ctfc	67
>>>>>>TFC 19	(TF3, TF2, TF1, TF1)
>>>>>>>ctfc	73
>>>>>>TFC 20	(TF4, TF3, TF1, TF1)
>>>>>>>ctfc	79
PhyCH INFORMATION FDD	
UL-DPCH-InfoPredef	
>ul-DPCH-PowerControlInfo	
>>powerControlAlgorithm	Algorithm 1
>>>tpcStepSize	1 dB
>tfci-Existence	TRUE
>puncturingLimit	0.84
DL-CommonInformationPredef	
>dl-DPCH-InfoCommon	
>>spreadingFactor	128
>>tfci-Existence	FALSE
>>pilotBits	4
>>positionFixed	Fixed

3GPP TSG RAN2 #45 **R2-042595**
Shin Yokohama, Japan, 15-19 November

CR-Form-v7.1

CHANGE REQUEST

⌘ **25.331** **CR** **2452** ⌘ **rev** ⌘ Current version: **5.10.0** ⌘

*For **HELP** on using this form, see bottom of this page or look at the pop-up text over the ⌘ symbols.*

Proposed change affects: UICC apps⌘ ☐ ME ☒ Radio Access Network ☒ Core Network ☐

Title:	⌘	Correction to HS-DSCH reception conditions
Source:	⌘	RAN WG2
Work item code:	⌘	HSDPA-L23
Date:	⌘	November 2004
Category:	⌘	**F**
Release:	⌘	Rel-5

Use one of the following categories:
F *(correction)*
A *(corresponds to a correction in an earlier release)*
B *(addition of feature),*
C *(functional modification of feature)*
D *(editorial modification)*
Detailed explanations of the above categories can be found in 3GPP TR 21.900.

Use one of the following releases:
Ph2 (GSM Phase 2)
R96 (Release 1996)
R97 (Release 1997)
R98 (Release 1998)
R99 (Release 1999)
Rel-4 (Release 4)
Rel-5 (Release 5)
Rel-6 (Release 6)
Rel-7 (Release 7)

Reason for change: ⌘ At RAN2 #43 a number of changes relating to HS-DSCH reception were agreed. With respect to the agreed CR one case related to disabling HS-DSCH reception was not included, some aspects related to reception of the of DL HS-PDSCH Info IE should not have been removed, and changes were not complete or correct for TDD.

1. For FDD the serving HS-DSCH RL indicator is now used for enabling and disabling reception. When the serving RL indicator is set to true a new H-RNTI must be signaled otherwise the current H-RNTI is deleted and HS-DSCH reception is disabled. HS-DSCH reception is also disabled when the serving HS-DSCH RL indicator is set to false (see referenced section 8.6.6.4).

In TDD the serving HS-DSCH RL indicator does not exist, but similar actions are necessary for the case of hard handover. It is therefore proposed that upon hard handover when a new target cell is identified by P-CCPCH info the H-RNTI is treated similarly to existing C-RNTI logic and HS-DSCH reception requirements are enforced.

2. Since it is not possible to add/remove many of the required parameters for HS-DSCH reception the description of HS-DSCH reception requirements (8.5.25) includes text describing the methods that may be used for enabling and disabling

	HS-DSCH reception. There are 3 methods defined: add/remove RB mapping, add/remove MAC-d flows and for FDD only add/remove the serving HS-DSCH RL. Unfortunately for TDD with the recent change in HS-DSCH reception criteria the most efficient signaling method for disabling and enabling HS-DSCH reception by setting the serving HS_DSCH RL indicator is not available. Before the agreed change a relatively efficient method existed for enabling/disabling HS-DSCH reception by the existence or not of the HS-PDSCH info IE, but with the agreed change this method is no longer available. To provide an efficient enable/disable signaling method for TDD, and cover the potentially common case upon entry to a cell that does not support R5 HSDPA, it is proposed to include removal/addition of the H-RNTI upon hard handover to the methods described in section 8.5.25. This is quite similar to the FDD method, removal/addition of the RL with serving HS-DSCH RL indicator. 3. The “adding/removing the concerned MAC-d flows” method for enabling and disabling of HS_DSCH reception described in section 8.5.25 requires that when “deleted DL TrCH information” or “Added or Reconfigured DL TrCH information” is received and MAC-d flows are added or removed HS-DSCH reception actions are taken. It is proposed that HS-DSCH reception is checked and actions taken upon reception of “deleted DL TrCH information” and “Added or Reconfigured DL TrCH information”. 4. Also in the agreed CR on HS-DSCH reception the procedural text for IE Downlink HS-PDSCH Information reception (8.6.6.32) was removed. Our understanding for the motivation was mainly changing and clarifying the rules for enabling and disabling HS-DSCH reception (no longer based on the existence of this IE), and providing an improved maintenance of HSDPA parameters. But this section also provided other actions upon reception of the DL HS-PDSCH Info IE that need to be specified. It is proposed the section on reception of Downlink HS-PDSCH Information is returned to the specification, and actions to be taken for TDD timeslot and midamble configuration IE’s, as well as references to sections describing actions to be taken upon reception of other included IE’s are maintained. None of the previous text regarding enabling or disabling HS-DSCH reception is needed since this is now covered in the new section on actions related to HS_DSCH Reception (8.5.25). 5. In the new section describing HS-DSCH reception requirements (8.5.25), text for storing of the IE HARQ info was placed in the FDD only section. Since the IE HARQ info also applied to TDD, it is proposed to move the text to the common FDD&TDD section. Note that corrections 3 and 4 are common for FDD and TDD operation, and corrections 1, 2 and 5 only effect TDD operation.
Summary of change: ⌘	When TDD P-CCPCH info indicates a new target cell (hard handover) and a new H-RNTI is not signaled, the current H-RNTI is deleted and HS-DSCH reception criteria is checked.

		For TDD addition and removal of H-RNTI upon hard handover is added to the methods specified for enabling and disabling HS-DSCH reception. The section on actions to be taken upon reception of Downlink HS-PDSCH Information (8.6.32) is returned to the specification. TDD parameters for timeslot and midamble configuration if included are stored and criteria if HS-DSCH reception is evaluated. Also references to sections describing actions to be taken upon reception of HS-SCCH Info and Measurement Feedback Info IE's are returned to the specification. Other previous text for HS-DSCH reception criteria and enabling/disabling is not included since these aspects are not specified in the new HS-DSCH reception section (8.5.25). In the procedural text for deleted and added or reconfigured TrCH info, HS-DSCH reception is checked in case the MAC-d flows mapped to the HS-DSCH are added or removed. HS-DSCH reception criteria specifies the IE HARQ info is also required for TDD operation.
Consequences if not approved:	⌘	Improper management of the TDD H-RNTI Unnecessary increased signaling requirement for enabling and disabling HS-DSCH reception in TDD Storing of TDD timeslot and midamble configurations will not be specified and the HS-DSCH receprion check specified in section (8.5.25) will fail. HS-DSCH reception check needed upon added or removed MAC-d flows may not be evaulated. Use of HARQ info will not be correctly specified for TDD

Clauses affected:	⌘	8.2.2.3, 8.5.25, 8.6.5.5a, 8.6.5.6, 8.6.5.8 & 8.6.6.32			
Other specs Affected:	⌘	**Y**	**N**	Other core specifications Test specifications O&M Specifications	⌘
Other comments:	⌘				

How to create CRs using this form:

Comprehensive information and tips about how to create CRs can be found at http://www.3gpp.org/specs/CR.htm. Below is a brief summary:

1) Fill out the above form. The symbols above marked ⌘ contain pop-up help information about the field that they are closest to.

2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under ftp://ftp.3gpp.org/specs/ For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.

3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

8.2.2.3 Reception of RADIO BEARER SETUP or RADIO BEARER RECONFIGURATION or RADIO BEARER RELEASE or TRANSPORT CHANNEL RECONFIGURATION or PHYSICAL CHANNEL RECONFIGURATION message by the UE

The UE shall:

1> be able to receive any of the following messages:

2> RADIO BEARER SETUP message; or

2> RADIO BEARER RECONFIGURATION message; or

2> RADIO BEARER RELEASE message; or

2> TRANSPORT CHANNEL RECONFIGURATION message; or

2> PHYSICAL CHANNEL RECONFIGURATION message;

1> be able to perform a hard handover and apply physical layer synchronisation procedure A as specified in [29], even if no prior UE measurements have been performed on the target cell and/or frequency.

In case the reconfiguration procedure is used to remove all existing RL(s) in the active set while new RL(s) are established the UE shall:

1> if the UE has a pending "TGPS reconfiguration CFN" at the activation time received in the reconfiguration message and the reconfiguration requests a timing re-initialised hard handover (see subclause 8.3.5.1), the UE may:

2> abort the pending CM activation;

2> set the CM_PATTERN_ACTIVATION_ABORTED to TRUE.

1> otherwise:

2> set the CM_PATTERN_ACTIVATION_ABORTED to FALSE.

If the UE receives:

- a RADIO BEARER SETUP message; or
- a RADIO BEARER RECONFIGURATION message; or
- a RADIO BEARER RELEASE message; or
- a TRANSPORT CHANNEL RECONFIGURATION message; or
- a PHYSICAL CHANNEL RECONFIGURATION message:

it shall:

1> set the variable ORDERED_RECONFIGURATION to TRUE;

1> if the UE will enter the CELL_DCH state from any state other than CELL_DCH state at the conclusion of this procedure:

2> perform the physical layer synchronisation procedure A as specified in [29] (FDD only).

1> act upon all received information elements as specified in subclause 8.6, unless specified in the following and perform the actions below.

The UE may:

1> maintain a list of the set of cells to which the UE has Radio Links if the IE "Cell ID" is present.

The UE may first release the physical channel configuration used at reception of the reconfiguration message. The UE shall then:

1> in FDD, if the IE "PDSCH code mapping" is included but the IE "PDSCH with SHO DCH Info" is not included and if the DCH has only one link in its active set:

2> act upon the IE "PDSCH code mapping" as specified in subclause 8.6; and

2> infer that the PDSCH will be transmitted from the cell from which the downlink DPCH is transmitted.

1> enter a state according to subclause 8.6.3.3.

In case the UE receives a RADIO BEARER RECONFIGURATION message including the IE "RB information to reconfigure" that only includes the IE "RB identity", the UE shall:

1> handle the message as if IE "RB information to reconfigure" was absent.

NOTE: The RADIO BEARER RECONFIGURATION message always includes the IE "RB information to reconfigure". UTRAN has to include it even if it does not require the reconfiguration of any RB.

If after state transition the UE enters CELL_DCH state, the UE shall, after the state transition:

1> in FDD; or

1> in TDD when "Primary CCPCH Info" is included indicating a new target cell and "New C-RNTI" is not specified:

2> remove any C-RNTI from MAC;

2> clear the variable C_RNTI.

If after state transition the UE leaves CELL_DCH state, the UE shall, after the state transition:

1> clear any stored IE “Downlink HS-PDSCH information”;

1> determine the value for the HS_DSCH_RECEPTION variable and take the corresponding actions as described in subclause 8.5.25.

In FDD, if after state transition the UE leaves CELL_DCH state, the UE shall, after the state transition:

1> remove any DSCH-RNTI from MAC;

1> clear the variable DSCH_RNTI.

If the UE was in CELL_DCH state upon reception of the reconfiguration message and remains in CELL_DCH state, the UE shall:

1> if the IE "Uplink DPCH Info" is absent, not change its current UL Physical channel configuration;

1> in TDD:

2> if "Primary CCPCH Info" is included indicating a new target cell and "New C-RNTI" is not specified:

3> remove any C-RNTI from MAC;

3> clear the variable C_RNTI.

2> if “Primary CCPCH Info” is included indicating a new target cell and “New H-RNTI” is not specified:

3> remove any H-RNTI from MAC;

3> clear the variable H_RNTI;

3> determine the value for the HS_DSCH_RECEPTION variable and take the corresponding actions as described in subclause 8.5.25.

1> if "DPCH frame offset" is included for one or more RLs in the active set:

2> use its value to determine the beginning of the DPCH frame in accordance with the following:

3> if the received IE "DPCH frame offset" is across the value range border compared to the DPCH frame offset currently used by the UE:

4> consider it to be a request to adjust the timing with 256 chips across the frame border (e.g. if the UE receives value 0 while the value currently used is 38144 consider this as a request to adjust the timing with +256 chips).

3> if after taking into account value range borders, the received IE "DPCH frame offset" corresponds to a request to adjust the timing with a step exceeding 256 chips:

4> set the variable INVALID_CONFIGURATION to TRUE.

3> and the procedure ends.

2> adjust the radio link timing accordingly.

- - - - Remaining text in this section is unchanged and omitted - - - -

8.5.25 Actions related to HS_DSCH_RECEPTION variable

The variable HS_DSCH_RECEPTION shall be set to “TRUE” only when all the following conditions are met:

1> the UE is in CELL_DCH state;

1> the variable H_RNTI is set;

1> the UE has a stored IE “HS-SCCH info”;

1> the UE has a stored IE “HARQ info”;

1> for FDD:

2> one of the radio links in the active set is configured as the serving HS-DSCH radio link;

2> the UE has stored the following IEs:

- IE "Measurement Feedback Info";
- IE "Uplink DPCH Power Control Info" including stored $_{ACK,}$ $_{NACK}$ and Ack-NACK Repetition factor;
- ~~IE "HARQ info";~~

1>for 3.84 Mcps TDD, the UE has stored the following IE:

- IE "HS-PDSCH Timeslot Configuration";

1> for 1.28 Mcps TDD, the UE has stored the following IE:

- IE "HS-PDSCH Midamble Configuration";

1> there is at least one RB mapped to HS-DSCH;

1> at least for one of the RB's mapped to HS-DSCH, there is at least one MAC-hs queue (including the IE "MAC-d PDU size Info") configured for the concerning MAC-d flow;

Note: To enable or disable HS-DSCH reception, the UTRAN has the possibility to add/remove the concerning HS-DSCH related RB mapping options, add/remove the concerning MAC-d flows or, for FDD, add/remove the serving HS-DSCH radio link or, for TDD add/remove H-RNTI upon hard handover.

If any of the above conditions is not met and the variable HS_DSCH_RECEPTION is set to TRUE, the UE shall:

1> set the variable HS_DSCH_RECEPTION to FALSE;

1> stop any HS_SCCH reception procedures;

1> stop any HS-DSCH reception procedures;

1> clear the variable H_RNTI and remove any stored H-RNTI;

1> act as if the IE "MAC-hs reset indicator" is received and set to TRUE;

1> release all HARQ resources;

1> no long consider any radio link to be the HS-DSCH serving radio link.

NOTE: If configured for HS-DSCH and not explicitly indicated as being cleared, the UE will have still stored the IEs "HARQ info", "Added or Reconfigured MAC-d flow", "RB mapping Info" and "Downlink HS-PDSCH information".

Whenever the variable HS_DSCH_RECEPTION is set to TRUE, the UE shall:

1> perform HS_SCCH reception procedures according to the stored HS-SCCH configuration as stated in:

2> subclause 8.6.6.33 for the IE "HS-SCCH Info".

1> perform HS-DSCH reception procedures according to the stored HS-PDSCH configuration as stated in:

2> subclause 8.6.3.1b for the IE "H-RNTI";

2> subclause 8.6.5.6b for the IE "HARQ info";

2> subclause 8.6.6.34 for the IE "Measurement Feedback Info".

Whenever the variable HS_DSCH_RECEPTION is set to FALSE, the UE shall:

1> not perform HS_SCCH reception procedures;

1> not perform HS-DSCH reception procedures;

8.6.5.5a Added or reconfigured MAC-d flow

If the IE "Added or reconfigured MAC-d flow" is included, the UE shall:

1> if a MAC-hs queue (identified by the IE "MAC-hs queue Id") is included in both the IE "MAC-hs queue to add or reconfigure list" and the IE "MAC-hs queue to delete list":

2> set the variable INVALID_CONFIGURATION to TRUE.

1> for each MAC-hs queue included in the IE "MAC-hs queue to add or reconfigure" list:

2> if the UE has previously stored a mapping between this MAC-hs queue and a MAC-d flow:

3> delete the old mapping.

2> map the MAC-d flow indicated in the current message to this MAC-hs queue;

2> set the release timer for each of the MAC-hs queues in the MAC-hs entity to the value in the corresponding IE "T1";

2> set the MAC-hs receiver window size for each of the MAC-hs queues in the MAC-hs entity to the value in the corresponding IE "MAC-hs window size"; and

2> configure MAC-hs with the mapping between MAC-d PDU sizes index and allowed MAC-d PDU sizes as follows:

3> if a MAC-d PDU size has been stored for a MAC-d PDU size index for the corresponding MAC-hs queue and no mapping is provided in the current message for this MAC-d PDU index:

4> continue to use this mapping.

3> if a MAC-d PDU size has been stored for a MAC-d PDU size index for the corresponding MAC-hs queue and a mapping is provided in the current message for this MAC-d PDU index:

4> configure the MAC-hs entity with the mapping indicated in the current message.

1> for each MAC-hs queue included in the IE "MAC-hs queue to delete" list:

2> delete any information about the MAC-hs queue identified by the IE "MAC-hs queue Id".

1> if the IE "Added or reconfigured MAC-d flow" is considered valid according to the rules above:

2> perform the actions as specified in subclause 8.5.21.

~~1> determine the value for the HS_DSCH_RECEPTION variable and take the corresponding actions as described in subclause 8.5.25.~~

8.6.5.8 Deleted DL TrCH information

If the IE "Deleted DL TrCH information" is included the UE shall:

1> if a Downlink transport channel is requested to be deleted:

2> delete any information about the transport channel identified by the IE "DL TrCH identity".

1> if a DL MAC-d flow is requested to be deleted:

2> delete any information about the DL HS-DSCH MAC-d flow identified by the IE "MAC-d Flow Identity", i.e. delete any information about MAC-hs queue(s) mapped onto this MAC-d flow.

2> determine the value for the HS_DSCH_RECEPTION variable and take the corresponding actions as described in subclause 8.5.25

8.6.5.6 Added or Reconfigured DL TrCH information

If the IE "Added or Reconfigured DL TrCH information" is included then for the transport channel identified by the IE "DL Transport Channel Identity" the UE shall:

1> if the choice "DL parameters" is set to 'explicit':

2> perform the actions for the IE "Transport Format Set" as specified in subclause 8.6.5.1.

1> if the choice "DL parameters" is set to 'same as uplink':

2> if the IE "UL Transport Channel Identity" indicates an existing or a new UL Transport Channel:

3> store as transport format for this transport channel the transport format associated with the transport channel identified by the IE "UL Transport Channel Identity".

2> else:

3> set the variable INVALID_CONFIGURATION to TRUE.

1> if the choice "DL parameters" is set to 'HSDSCH':

2> if the IE "HARQ Info" is included:

3> perform the actions specified in subclause 8.6.5.6b.

2> if the IE “Added or Reconfigured MAC-d Flow” is included:

3> perform the actions as specified in subclause 8.6.5.5a

3> determine the value for the HS_DSCH_RECEPTION variable and take the corresponding actions as described in subclause 8.5.25.

1> if the IE "DCH quality target" is included:

2> perform the actions specified in subclause 8.6.5.4.

NOTE: The UE stores the DL transport channel configuration until it is explicitly deleted by a message containing the IE "Deleted DL TrCH information" or the UE leaves RRC connected mode.

8.6.6.32 Downlink HS-PDSCH Information

If the IE "Downlink HS-PDSCH Information" is included and the UE would be in CELL_DCH state after completion of this procedure, the UE shall:

1> if the IE "HS-SCCH Info" is included:

2> act as specified in subclause 8.6.6.33.

1> if the IE "Measurement Feedback Info" is included:

2> act as specified in subclause 8.6.6.34.

1> if the IE "HS-DSCH Timeslot Configuration" or "HS-PDSCH Midamble Configuration" is included:

2> store the received configuration.

2> determine the value for the HS_DSCH_RECEPTION variable and take actions as described in subclause 8.5.25.

3GPP TSG RAN2 #45 **R2-042596**
Shin Yokohama, Japan, 15-19 November

CR-Form-v7.1

CHANGE REQUEST

⌘ **25.331** **CR** **2453** ⌘ **rev** ⌘ Current version: **6.3.0** ⌘

*For **HELP** on using this form, see bottom of this page or look at the pop-up text over the ⌘ symbols.*

Proposed change affects: UICC apps⌘ ☐ ME ☒ Radio Access Network ☒ Core Network ☐

Title:	⌘	Correction to HS-DSCH reception conditions
Source:	⌘	RAN WG2
Work item code:	⌘	HSDPA-L23
Date:	⌘	November 2004
Category:	⌘	**A**
Release:	⌘	Rel-6

Use one of the following categories:
F *(correction)*
A *(corresponds to a correction in an earlier release)*
B *(addition of feature),*
C *(functional modification of feature)*
D *(editorial modification)*
Detailed explanations of the above categories can be found in 3GPP TR 21.900.

Use one of the following releases:
Ph2 (GSM Phase 2)
R96 (Release 1996)
R97 (Release 1997)
R98 (Release 1998)
R99 (Release 1999)
Rel-4 (Release 4)
Rel-5 (Release 5)
Rel-6 (Release 6)
Rel-7 (Release 7)

Reason for change: ⌘ At RAN2 #43 a number of changes relating to HS-DSCH reception were agreed. With respect to the agreed CR one case related to disabling HS-DSCH reception was not included, some aspects related to reception of the of DL HS-PDSCH Info IE should not have been removed, and changes were not complete or correct for TDD.

1. For FDD the serving HS-DSCH RL indicator is now used for enabling and disabling reception. When the serving RL indicator is set to true a new H-RNTI must be signaled otherwise the current H-RNTI is deleted and HS-DSCH reception is disabled. HS-DSCH reception is also disabled when the serving HS-DSCH RL indicator is set to false (see referenced section 8.6.6.4).

In TDD the serving HS-DSCH RL indicator does not exist, but similar actions are necessary for the case of hard handover. It is therefore proposed that upon hard handover when a new target cell is identified by P-CCPCH info the H-RNTI is treated similarly to existing C-RNTI logic and HS-DSCH reception requirements are enforced.

2. Since it is not possible to add/remove many of the required parameters for HS-DSCH reception the description of HS-DSCH reception requirements (8.5.25) includes text describing the methods that may be used for enabling and disabling

	HS-DSCH reception. There are 3 methods defined: add/remove RB mapping, add/remove MAC-d flows and for FDD only add/remove the serving HS-DSCH RL. Unfortunately for TDD with the recent change in HS-DSCH reception criteria the most efficient signaling method for disabling and enabling HS-DSCH reception by setting the serving HS_DSCH RL indicator is not available. Before the agreed change a relatively efficient method existed for enabling/disabling HS-DSCH reception by the existence or not of the HS-PDSCH info IE, but with the agreed change this method is no longer available. To provide an efficient enable/disable signaling method for TDD, and cover the potentially common case upon entry to a cell that does not support R5 HSDPA, it is proposed to include removal/addition of the H-RNTI upon hard handover to the methods described in section 8.5.25. This is quite similar to the FDD method, removal/addition of the RL with serving HS-DSCH RL indicator. 3. The “adding/removing the concerned MAC-d flows” method for enabling and disabling of HS_DSCH reception described in section 8.5.25 requires that when “deleted DL TrCH information” or “Added or Reconfigured DL TrCH information” is received and MAC-d flows are added or removed HS-DSCH reception actions are taken. It is proposed that HS-DSCH reception is checked and actions taken upon reception of “deleted DL TrCH information” and “Added or Reconfigured DL TrCH information”. 4. Also in the agreed CR on HS-DSCH reception the procedural text for IE Downlink HS-PDSCH Information reception (8.6.6.32) was removed. Our understanding for the motivation was mainly changing and clarifying the rules for enabling and disabling HS-DSCH reception (no longer based on the existence of this IE), and providing an improved maintenance of HSDPA parameters. But this section also provided other actions upon reception of the DL HS-PDSCH Info IE that need to be specified. It is proposed the section on reception of Downlink HS-PDSCH Information is returned to the specification, and actions to be taken for TDD timeslot and midamble configuration IE’s, as well as references to sections describing actions to be taken upon reception of other included IE’s are maintained. None of the previous text regarding enabling or disabling HS-DSCH reception is needed since this is now covered in the new section on actions related to HS_DSCH Reception (8.5.25). 5. In the new section describing HS-DSCH reception requirements (8.5.25), text for storing of the IE HARQ info was placed in the FDD only section. Since the IE HARQ info also applied to TDD, it is proposed to move the text to the common FDD&TDD section. Note that corrections 3 and 4 are common for FDD and TDD operation, and corrections 1, 2 and 5 only effect TDD operation.
Summary of change: ⌘	When TDD P-CCPCH info indicates a new target cell (hard handover) and a new H-RNTI is not signaled, the current H-RNTI is deleted and HS-DSCH reception criteria is checked.

		For TDD addition and removal of H-RNTI upon hard handover is added to the methods specified for enabling and disabling HS-DSCH reception. The section on actions to be taken upon reception of Downlink HS-PDSCH Information (8.6.32) is returned to the specification. TDD parameters for timeslot and midamble configuration if included are stored and criteria if HS-DSCH reception is evaluated. Also references to sections describing actions to be taken upon reception of HS-SCCH Info and Measurement Feedback Info IE's are returned to the specification. Other previous text for HS-DSCH reception criteria and enabling/disabling is not included since these aspects are not specified in the new HS-DSCH reception section (8.5.25). In the procedural text for deleted and added or reconfigured TrCH info, HS-DSCH reception is checked in case the MAC-d flows mapped to the HS-DSCH are added or removed. HS-DSCH reception criteria specifies the IE HARQ info is also required for TDD operation.
Consequences if not approved:	⌘	Improper management of the TDD H-RNTI Unnecessary increased signaling requirement for enabling and disabling HS-DSCH reception in TDD Storing of TDD timeslot and midamble configurations will not be specified and the HS-DSCH receprion check specified in section (8.5.25) will fail. HS-DSCH reception check needed upon added or removed MAC-d flows may not be evaulated. Use of HARQ info will not be correctly specified for TDD

Clauses affected:	⌘	8.2.2.3, 8.5.25, 8.6.5.5a, 8.6.5.6, 8.6.5.8 & 8.6.6.32			
Other specs Affected:	⌘	**Y**	**N**	Other core specifications	⌘
				Test specifications	
				O&M Specifications	
Other comments:	⌘				

How to create CRs using this form:

Comprehensive information and tips about how to create CRs can be found at http://www.3gpp.org/specs/CR.htm. Below is a brief summary:

1) Fill out the above form. The symbols above marked ⌘ contain pop-up help information about the field that they are closest to.

2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under ftp://ftp.3gpp.org/specs/ For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.

3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

8.2.2.3 Reception of RADIO BEARER SETUP or RADIO BEARER RECONFIGURATION or RADIO BEARER RELEASE or TRANSPORT CHANNEL RECONFIGURATION or PHYSICAL CHANNEL RECONFIGURATION message by the UE

The UE shall:

1> be able to receive any of the following messages:

2> RADIO BEARER SETUP message; or

2> RADIO BEARER RECONFIGURATION message; or

2> RADIO BEARER RELEASE message; or

2> TRANSPORT CHANNEL RECONFIGURATION message; or

2> PHYSICAL CHANNEL RECONFIGURATION message;

1> be able to perform a hard handover and apply physical layer synchronisation procedure A as specified in [29], even if no prior UE measurements have been performed on the target cell and/or frequency.

In case the reconfiguration procedure is used to remove all existing RL(s) in the active set while new RL(s) are established the UE shall:

1> if the UE has a pending "TGPS reconfiguration CFN" at the activation time received in the reconfiguration message and the reconfiguration requests a timing re-initialised hard handover (see subclause 8.3.5.1), the UE may:

2> abort the pending CM activation;

2> set the CM_PATTERN_ACTIVATION_ABORTED to TRUE.

1> otherwise:

2> set the CM_PATTERN_ACTIVATION_ABORTED to FALSE.

If the UE receives:

- a RADIO BEARER SETUP message; or
- a RADIO BEARER RECONFIGURATION message; or
- a RADIO BEARER RELEASE message; or
- a TRANSPORT CHANNEL RECONFIGURATION message; or
- a PHYSICAL CHANNEL RECONFIGURATION message:

it shall:

1> set the variable ORDERED_RECONFIGURATION to TRUE;

1> if the UE will enter the CELL_DCH state from any state other than CELL_DCH state at the conclusion of this procedure:

2> perform the physical layer synchronisation procedure A as specified in [29] (FDD only).

1> act upon all received information elements as specified in subclause 8.6, unless specified in the following and perform the actions below.

The UE may:

1> maintain a list of the set of cells to which the UE has Radio Links if the IE "Cell ID" is present.

The UE may first release the physical channel configuration used at reception of the reconfiguration message. The UE shall then:

1> in FDD, if the IE "PDSCH code mapping" is included but the IE "PDSCH with SHO DCH Info" is not included and if the DCH has only one link in its active set:

2> act upon the IE "PDSCH code mapping" as specified in subclause 8.6; and

2> infer that the PDSCH will be transmitted from the cell from which the downlink DPCH is transmitted.

1> enter a state according to subclause 8.6.3.3.

In case the UE receives a RADIO BEARER RECONFIGURATION message including the IE "RB information to reconfigure" that only includes the IE "RB identity", the UE shall:

1> handle the message as if IE "RB information to reconfigure" was absent.

NOTE: The RADIO BEARER RECONFIGURATION message always includes the IE "RB information to reconfigure". UTRAN has to include it even if it does not require the reconfiguration of any RB.

If after state transition the UE enters CELL_DCH state, the UE shall, after the state transition:

1> in FDD; or

1> in TDD when "Primary CCPCH Info" is included indicating a new target cell and "New C-RNTI" is not specified:

2> remove any C-RNTI from MAC;

2> clear the variable C_RNTI.

If after state transition the UE leaves CELL_DCH state, the UE shall, after the state transition:

1> clear any stored IE “Downlink HS-PDSCH information”;

1> determine the value for the HS_DSCH_RECEPTION variable and take the corresponding actions as described in subclause 8.5.25.

In FDD, if after state transition the UE leaves CELL_DCH state, the UE shall, after the state transition:

1> remove any DSCH-RNTI from MAC;

1> clear the variable DSCH_RNTI.

If the UE was in CELL_DCH state upon reception of the reconfiguration message and remains in CELL_DCH state, the UE shall:

1> if the IE "Uplink DPCH Info" is absent, not change its current UL Physical channel configuration;

1> in TDD:

2> if "Primary CCPCH Info" is included indicating a new target cell and "New C-RNTI" is not specified:

3> remove any C-RNTI from MAC;

3> clear the variable C_RNTI.

2> if “Primary CCPCH Info” is included indicating a new target cell and “New H-RNTI” is not specified:

3> remove any H-RNTI from MAC;

3> clear the variable H_RNTI;

3> determine the value for the HS_DSCH_RECEPTION variable and take the corresponding actions as described in subclause 8.5.25.

1> if "DPCH frame offset" is included for one or more RLs in the active set:

2> use its value to determine the beginning of the DPCH frame in accordance with the following:

3> if the received IE "DPCH frame offset" is across the value range border compared to the DPCH frame offset currently used by the UE:

4> consider it to be a request to adjust the timing with 256 chips across the frame border (e.g. if the UE receives value 0 while the value currently used is 38144 consider this as a request to adjust the timing with +256 chips).

3> if after taking into account value range borders, the received IE "DPCH frame offset" corresponds to a request to adjust the timing with a step exceeding 256 chips:

4> set the variable INVALID_CONFIGURATION to TRUE.

3> and the procedure ends.

2> adjust the radio link timing accordingly.

- - - - Remaining text in this section is unchanged and omitted - - - -

8.5.25 Actions related to HS_DSCH_RECEPTION variable

The variable HS_DSCH_RECEPTION shall be set to “TRUE” only when all the following conditions are met:

1> the UE is in CELL_DCH state;

1> the variable H_RNTI is set;

1> the UE has a stored IE “HS-SCCH info”;

1> the UE has a stored IE “HARQ info”;

1> for FDD:

2> one of the radio links in the active set is configured as the serving HS-DSCH radio link;

2> the UE has stored the following IEs:

- IE "Measurement Feedback Info";
- IE "Uplink DPCH Power Control Info" including stored $_{ACK,}$ $_{NACK}$ and Ack-NACK Repetition factor;
- ~~IE "HARQ info";~~

1>for 3.84 Mcps TDD, the UE has stored the following IE:

- IE "HS-PDSCH Timeslot Configuration";

1> for 1.28 Mcps TDD, the UE has stored the following IE:

- IE "HS-PDSCH Midamble Configuration";

1> there is at least one RB mapped to HS-DSCH;

1> at least for one of the RB's mapped to HS-DSCH, there is at least one MAC-hs queue (including the IE "MAC-d PDU size Info") configured for the concerning MAC-d flow;

Note: To enable or disable HS-DSCH reception, the UTRAN has the possibility to add/remove the concerning HS-DSCH related RB mapping options, add/remove the concerning MAC-d flows or, for FDD, add/remove the serving HS-DSCH radio link or, for TDD add/remove H-RNTI upon hard handover.

If any of the above conditions is not met and the variable HS_DSCH_RECEPTION is set to TRUE, the UE shall:

1> set the variable HS_DSCH_RECEPTION to FALSE;

1> stop any HS_SCCH reception procedures;

1> stop any HS-DSCH reception procedures;

1> clear the variable H_RNTI and remove any stored H-RNTI;

1> act as if the IE "MAC-hs reset indicator" is received and set to TRUE;

1> release all HARQ resources;

1> no long consider any radio link to be the HS-DSCH serving radio link.

NOTE: If configured for HS-DSCH and not explicitly indicated as being cleared, the UE will have still stored the IEs "HARQ info", "Added or Reconfigured MAC-d flow", "RB mapping Info" and "Downlink HS-PDSCH information".

Whenever the variable HS_DSCH_RECEPTION is set to TRUE, the UE shall:

1> perform HS_SCCH reception procedures according to the stored HS-SCCH configuration as stated in:

2> subclause 8.6.6.33 for the IE "HS-SCCH Info".

1> perform HS-DSCH reception procedures according to the stored HS-PDSCH configuration as stated in:

2> subclause 8.6.3.1b for the IE "H-RNTI";

2> subclause 8.6.5.6b for the IE "HARQ info";

2> subclause 8.6.6.34 for the IE "Measurement Feedback Info".

Whenever the variable HS_DSCH_RECEPTION is set to FALSE, the UE shall:

1> not perform HS_SCCH reception procedures;

1> not perform HS-DSCH reception procedures;

8.6.5.5a Added or reconfigured MAC-d flow

If the IE "Added or reconfigured MAC-d flow" is included, the UE shall:

1> if a MAC-hs queue (identified by the IE "MAC-hs queue Id") is included in both the IE "MAC-hs queue to add or reconfigure list" and the IE "MAC-hs queue to delete list":

2> set the variable INVALID_CONFIGURATION to TRUE.

1> for each MAC-hs queue included in the IE "MAC-hs queue to add or reconfigure" list:

2> if the UE has previously stored a mapping between this MAC-hs queue and a MAC-d flow:

3> delete the old mapping.

2> map the MAC-d flow indicated in the current message to this MAC-hs queue;

2> set the release timer for each of the MAC-hs queues in the MAC-hs entity to the value in the corresponding IE "T1";

2> set the MAC-hs receiver window size for each of the MAC-hs queues in the MAC-hs entity to the value in the corresponding IE "MAC-hs window size"; and

2> configure MAC-hs with the mapping between MAC-d PDU sizes index and allowed MAC-d PDU sizes as follows:

3> if a MAC-d PDU size has been stored for a MAC-d PDU size index for the corresponding MAC-hs queue and no mapping is provided in the current message for this MAC-d PDU index:

4> continue to use this mapping.

3> if a MAC-d PDU size has been stored for a MAC-d PDU size index for the corresponding MAC-hs queue and a mapping is provided in the current message for this MAC-d PDU index:

4> configure the MAC-hs entity with the mapping indicated in the current message.

1> for each MAC-hs queue included in the IE "MAC-hs queue to delete" list:

2> delete any information about the MAC-hs queue identified by the IE "MAC-hs queue Id".

1> if the IE "Added or reconfigured MAC-d flow" is considered valid according to the rules above:

2> perform the actions as specified in subclause 8.5.21.

~~1> determine the value for the HS_DSCH_RECEPTION variable and take the corresponding actions as described in subclause 8.5.25.~~

8.6.5.8 Deleted DL TrCH information

If the IE "Deleted DL TrCH information" is included the UE shall:

1> if a Downlink transport channel is requested to be deleted:

2> delete any information about the transport channel identified by the IE "DL TrCH identity".

1> if a DL MAC-d flow is requested to be deleted:

2> delete any information about the DL HS-DSCH MAC-d flow identified by the IE "MAC-d Flow Identity", i.e. delete any information about MAC-hs queue(s) mapped onto this MAC-d flow.

2> determine the value for the HS_DSCH_RECEPTION variable and take the corresponding actions as described in subclause 8.5.25

8.6.5.6 Added or Reconfigured DL TrCH information

If the IE "Added or Reconfigured DL TrCH information" is included then for the transport channel identified by the IE "DL Transport Channel Identity" the UE shall:

1> if the choice "DL parameters" is set to 'explicit':

2> perform the actions for the IE "Transport Format Set" as specified in subclause 8.6.5.1.

1> if the choice "DL parameters" is set to 'same as uplink':

2> if the IE "UL Transport Channel Identity" indicates an existing or a new UL Transport Channel:

3> store as transport format for this transport channel the transport format associated with the transport channel identified by the IE "UL Transport Channel Identity".

2> else:

3> set the variable INVALID_CONFIGURATION to TRUE.

1> if the choice "DL parameters" is set to 'HSDSCH':

2> if the IE "HARQ Info" is included:

3> perform the actions specified in subclause 8.6.5.6b.

2> if the IE "Added or Reconfigured MAC-d Flow" is included:

3> perform the actions as specified in subclause 8.6.5.5a

3> determine the value for the HS_DSCH_RECEPTION variable and take the corresponding actions as described in subclause 8.5.25.

1> if the IE "DCH quality target" is included:

2> perform the actions specified in subclause 8.6.5.4.

NOTE: The UE stores the DL transport channel configuration until it is explicitly deleted by a message containing the IE "Deleted DL TrCH information" or the UE leaves RRC connected mode.

8.6.6.32 Downlink HS-PDSCH Information

If the IE "Downlink HS-PDSCH Information" is included and the UE would be in CELL_DCH state after completion of this procedure, the UE shall:

1> if the IE "HS-SCCH Info" is included:

2> act as specified in subclause 8.6.6.33.

1> if the IE "Measurement Feedback Info" is included:

2> act as specified in subclause 8.6.6.34.

1> if the IE "HS-DSCH Timeslot Configuration" or "HS-PDSCH Midamble Configuration" is included:

2> store the received configuration.

2> determine the value for the HS_DSCH_RECEPTION variable and take actions as described in subclause 8.5.25.

3GPP TSG-RAN2 Meeting #43
Sophia Antepolis, France, 4th October 2004

Tdoc ⌘R2-042685

CR-Form-v7

CHANGE REQUEST

⌘ **25.331** **CR** **2454** ⌘ **rev** **1** ⌘ Current version: **5.10.0** ⌘

*For **HELP** on using this form, see bottom of this page or look at the pop-up text over the ⌘ symbols.*

Proposed change affects: UICC apps⌘ ☐ ME ☐ Radio Access Network ☒ Core Network ☐

Title:	⌘	MAC-hs Reset procedure
Source:	⌘	RAN WG2
Work item code:	⌘	HSDPA-L23
Date:	⌘	4/10/2004
Category:	⌘	**F**
Release:	⌘	Rel-5

Use one of the following categories:
F *(correction)*
A *(corresponds to a correction in an earlier release)*
B *(addition of feature),*
C *(functional modification of feature)*
D *(editorial modification)*
Detailed explanations of the above categories can be found in 3GPP TR 21.900.

Use one of the following releases:

2	*(GSM Phase 2)*
R96	*(Release 1996)*
R97	*(Release 1997)*
R98	*(Release 1998)*
R99	*(Release 1999)*
Rel-4	*(Release 4)*
Rel-5	*(Release 5)*
Rel-6	*(Release 6)*

Reason for change:	⌘	MAC-hs reset was not defined as a standalone feature and was meant to always be used in conjunction with a serving cell change. In case it is used, there will be desynchronization in the MAC-hs protocol between the UE and the Node-B, leading to out-of-sequence delivery of data and ciphering desynchronization.
Summary of change:	⌘	Specified that the UE behaviour is unspecified in the case where a MAC-hs reset is triggered without a change in the serving HS-DSCH cell.
Consequences if not approved:	⌘	UE complexity will be increased un-necessarily for a scheme that is not supported across our specifications. **Isolated impact analysis:** UE does not implement the change: no impact. Network does not implement the change: the UE behaviour will not be predictable in case a Reset is performed without a cell change.

Clauses affected:	⌘	8.6.6.27

Other specs affected: ⌘	Y	N		⌘
	☐	☐	Other core specifications	
	☐	☐	Test specifications	
	☐	☐	O&M Specifications	

Other comments: ⌘

How to create CRs using this form:
Comprehensive information and tips about how to create CRs can be found at http://www.3gpp.org/specs/CR.htm.
Below is a brief summary:

1) Fill out the above form. The symbols above marked ⌘ contain pop-up help information about the field that they are closest to.

2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under ftp://ftp.3gpp.org/specs/ For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.

3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

8.6.6.27 Downlink information common for all radio links

If the IE "Downlink information common for all radio links " is included the UE shall:

1> if the IE "Downlink DPCH info common for all RL" is included:

2> perform actions as specified in subclause 8.6.6.28.

1> if the IE choice "mode" is set to 'FDD':

2> perform actions for the IE "DPCH compressed mode info" as specified in subclause 8.6.6.15;

2> perform actions for the IE "Tx Diversity mode" as specified in subclause 8.6.6.24;

2> if the IE "SSDT information" is included:

3> perform actions as specified in subclause 8.6.6.25.

1> if the IE "Default DPCH Offset value" is included:

2> perform actions as specified in the subclause 8.6.6.21.

1> if the IE "MAC-hs reset indicator" is included:

2> if the serving HS-DSCH radio link is the same radio link as prior to the reception of the message:

3> the UE behaviour is unspecified;

2> reset the MAC-hs entity [15].

<….>

10.3.6.24 Downlink information common for all radio links

Information Element/Group name	Need	Multi	Type and reference	Semantics description	Version
Downlink DPCH info common for all RL	OP		Downlink DPCH info common for all RL 10.3.6.18		
CHOICE *mode*	MP				
>FDD					
>>DPCH compressed mode info	OP		DPCH compressed mode info 10.3.6.33		
>>TX Diversity Mode	MD		TX Diversity Mode 10.3.6.86	Default value is the existing value of TX Diversity mode	
>>SSDT information	OP		SSDT information 10.3.6.77		
>TDD				(no data)	
>>CHOICE *TDD option*	MP				REL-4
>>>3.84 Mcps TDD				(no data)	REL-4
>>>1.28 Mcps TDD					REL-4
>>>>TSTD indicator	MP		TSTD indicator 10.3.6.85a		REL-4
Default DPCH Offset Value	OP		Default		

Information Element/Group name	Need	Multi	Type and reference	Semantics description	Version
			DPCH Offset Value, 10.3.6.16		
MAC-hs reset indicator	CV-*messageType*		Enumerated (true)	TRUE Indicates the MAC-hs entity needs to be reset.	REL-5

Condition	Explanation
MessageType	The IE is not needed in the HANDOVER TO UTRAN COMMAND and the RRC CONNECTION SETUP messages. Otherwise, it is optional.

<…>

10.3.6.27 Downlink information for each radio link

Information Element/Group name	Need	Multi	Type and reference	Semantics description	Version
Choice mode	MP				
>FDD					
>>Primary CPICH info	MP		Primary CPICH info 10.3.6.60		
>>Cell ID	OP		Cell ID 10.3.2.2		REL-4
>>PDSCH with SHO DCH Info	OP		PDSCH with SHO DCH Info 10.3.6.47		
>>PDSCH code mapping	OP		PDSCH code mapping 10.3.6.43		
>>Serving HS-DSCH radio link indicator	CV-*not_rrcConnectionSetup*		Boolean	The value "TRUE" indicates that this radio link is the serving HS-DSCH radio link	REL-5
>TDD					
>>Primary CCPCH info	MP		Primary CCPCH info 10.3.6.57		
Downlink DPCH info for each RL	OP		Downlink DPCH info for each RL 10.3.6.21		
SCCPCH Information for FACH	OP		SCCPCH Information for FACH 10.3.6.70		

Condition	Explanation
not_rrcConnectionSetup	This IE is not needed in the RRC CONNECTION SETUP message. Otherwise it is mandatory present.

3GPP TSG-RAN2 Meeting #43 ***Tdoc ⌘R2-042686***
Sophia Antepolis, France, 4th October 2004

CR-Form-v7

CHANGE REQUEST

⌘ **25.331** **CR** **2455** ⌘ **rev** **1** ⌘ Current version: **6.3.0** ⌘

*For **HELP** on using this form, see bottom of this page or look at the pop-up text over the ⌘ symbols.*

Proposed change affects: UICC apps⌘ ☐ ME ☐ Radio Access Network ☒ Core Network ☐

Title:	⌘	MAC-hs Reset procedure
Source:	⌘	RAN WG2
Work item code:	⌘	HSDPA-L23
Date:	⌘	4/10/2004
Category:	⌘	**A**
Release:	⌘	Rel-6

Use one of the following categories:
F *(correction)*
A *(corresponds to a correction in an earlier release)*
B *(addition of feature),*
C *(functional modification of feature)*
D *(editorial modification)*
Detailed explanations of the above categories can be found in 3GPP TR 21.900.

Use one of the following releases:

2	*(GSM Phase 2)*
R96	*(Release 1996)*
R97	*(Release 1997)*
R98	*(Release 1998)*
R99	*(Release 1999)*
Rel-4	*(Release 4)*
Rel-5	*(Release 5)*
Rel-6	*(Release 6)*

Reason for change:	⌘	MAC-hs reset was not defined as a standalone feature and was meant to always be used in conjunction with a serving cell change. In case it is used, there will be desynchronization in the MAC-hs protocol between the UE and the Node-B, leading to out-of-sequence delivery of data and ciphering desynchronization.
Summary of change:	⌘	Specified that the UE behaviour is unspecified in the case where a MAC-hs reset is triggered without a change in the serving HS-DSCH cell.
Consequences if not approved:	⌘	UE complexity will be increased un-necessarily for a scheme that is not supported across our specifications.

Clauses affected:	⌘	8.6.6.27			
Other specs affected:	⌘	**Y**	**N**		
		☐	☐	Other core specifications	⌘
		☐	☐	Test specifications	
		☐	☐	O&M Specifications	
Other comments:	⌘				

How to create CRs using this form:
Comprehensive information and tips about how to create CRs can be found at http://www.3gpp.org/specs/CR.htm.
Below is a brief summary:

1) Fill out the above form. The symbols above marked ⌘ contain pop-up help information about the field that they are closest to.
2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be

downloaded from the 3GPP server under ftp://ftp.3gpp.org/specs/ For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.

3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

8.6.6.27 Downlink information common for all radio links

If the IE "Downlink information common for all radio links " is included the UE shall:

1> if the IE "Downlink DPCH info common for all RL" is included:

2> perform actions as specified in subclause 8.6.6.28.

1> if the IE choice "mode" is set to 'FDD':

2> perform actions for the IE "DPCH compressed mode info" as specified in subclause 8.6.6.15;

2> perform actions for the IE "Tx Diversity mode" as specified in subclause 8.6.6.24;

2> if the IE "SSDT information" is included:

3> perform actions as specified in subclause 8.6.6.25.

1> if the IE "Default DPCH Offset value" is included:

2> perform actions as specified in the subclause 8.6.6.21.

1> if the IE "MAC-hs reset indicator" is included:

2> if the serving HS-DSCH radio link is the same radio link as prior to the reception of the message:

3> the UE behaviour is unspecified;

2> reset the MAC-hs entity [15].

<….>

10.3.6.24 Downlink information common for all radio links

Information Element/Group name	Need	Multi	Type and reference	Semantics description	Version
Downlink DPCH info common for all RL	OP		Downlink DPCH info common for all RL 10.3.6.18		
CHOICE *mode*	MP				
>FDD					
>>DPCH compressed mode info	OP		DPCH compressed mode info 10.3.6.33		
>>TX Diversity Mode	MD		TX Diversity Mode 10.3.6.86	Default value is the existing value of TX Diversity mode	
>>SSDT information	OP		SSDT information 10.3.6.77		
>TDD				(no data)	
>>CHOICE *TDD option*	MP				REL-4
>>>3.84 Mcps TDD				(no data)	REL-4
>>>1.28 Mcps TDD					REL-4
>>>>TSTD indicator	MP		TSTD indicator 10.3.6.85a		REL-4
Default DPCH Offset Value	OP		Default		

Information Element/Group name	Need	Multi	Type and reference	Semantics description	Version
			DPCH Offset Value, 10.3.6.16		
MAC-hs reset indicator	CV-*messageType*		Enumerated (true)	TRUE Indicates the MAC-hs entity needs to be reset.	REL-5

Condition	Explanation
MessageType	The IE is not needed in the HANDOVER TO UTRAN COMMAND and the RRC CONNECTION SETUP messages. Otherwise, it is optional.

<…>

10.3.6.27 Downlink information for each radio link

Information Element/Group name	Need	Multi	Type and reference	Semantics description	Version
Choice mode	MP				
>FDD					
>>Primary CPICH info	MP		Primary CPICH info 10.3.6.60		
>>Cell ID	OP		Cell ID 10.3.2.2		REL-4
>>PDSCH with SHO DCH Info	OP		PDSCH with SHO DCH Info 10.3.6.47		
>>PDSCH code mapping	OP		PDSCH code mapping 10.3.6.43		
>>Serving HS-DSCH radio link indicator	CV-*not_rrcConnectionSetup*		Boolean	The value "TRUE" indicates that this radio link is the serving HS-DSCH radio link	REL-5
>TDD					
>>Primary CCPCH info	MP		Primary CCPCH info 10.3.6.57		
Downlink DPCH info for each RL	OP		Downlink DPCH info for each RL 10.3.6.21		
SCCPCH Information for FACH	OP		SCCPCH Information for FACH 10.3.6.70		

Condition	Explanation
not_rrcConnectionSetup	This IE is not needed in the RRC CONNECTION SETUP message. Otherwise it is mandatory present.