TSG-RAN Meeting No. 18
Dec. 3-6, 2002 New Orleans, USA

TSGRP-020700

Status Report for WI to TSG

Work Item Name: Improving receiver performance requirements for the FDD UE

SOURCE: Rapporteur (Shimon Moshavi, Intel)   **TSG:** RAN   **WG: 4**

E-mail address rapporteur: Shimon.Moshavi@intel.com

Ref. to WI sheet: RAN_Work_Items.doc, Work Item 66

Progress Report since the last TSG (for all involved WGs):

Additional simulation results were presented in [1,2] regarding the proposal made at RAN WG4 #24 meeting [3] to incorporate power control into the Multipath Fading Demodulation tests of Sec. 8.3 of TS 25.101. Concerns were raised in [4] about the simulation assumptions used for these simulations and about ambiguity in the scope of this work item, (i.e., tightening performance requirements vs. changing the tests to be more realistic). Discussions are ongoing regarding clarifying both of these issues. In addition, a CR was endorsed [5] fixing some minor errors in the technical report on pilot interference mitigation, TR 25.991.

List of Completed elements (for complex work items):

Changes to the Soft Handover DCH demodulation performance requirements (TS 25.101, Sec. 8.7) were approved at RAN #16 according to [6].

List of open issues:

None

Estimates of the level of completion (when possible):

WI completion date review resulting from the discussion at the working group:

Originally December 2002 – additional discussion needed in WG 4.

References to WG's internal documentation and/or TRs:

[1]   3GPP TSGR4-021618, "Simulation results for modified DCH demodulation tests in multi-path fading conditions," Nokia, Nov. 2002.

[2]   3GPP TSGR4-021489, "Simulation Results for DCH demodulation tests with power control," Ericsson, Nov. 2002.

[3]   3GPP TSGR4-021276, "Improvements for DCH Demodulation Tests in Multi-Path Fading Conditions," Nokia, Aug. 2002.

[4]   3GPP TSGR4-021662, "UE performance requirements," Motorola, Nov. 2002.

[5]   3GPP TSGR4-021511, "Correction to Pilot Interference Mitigation Technical Report," Intel, Nov. 2002.

[6]   3GPP TSGR4-020959, "FDD UE Performance Requirements," CR-166, Intel Corp., May. 2002.