

**3GPP TSG-RAN Meeting #15  
Jeju, Korea, 5 – 8, March, 2002**

**RP-020054**

**Title:** Agreed CRs (Rel-5) for the WI of "Enhancement on the DSCH hard split mode"

**Source:** TSG-RAN WG1

**Agenda item:** 9.2.6

No.	Spec	CR	Rev	R1 T-doc	Subject	Release	Cat	Workitem	V_old	V_new
1	25.212	123	4	R1-02-0465	Inclusion of flexible hard split mode TFCI operation	Rel-5	B	RInImp-DSCHhsp	4.3.0	5.0.0
2	25.214	250	1	R1-02-0513	Description of SSDT operation for TFCI power control in hard split mode	Rel-5	B	RInImp-DSCHhsp	4.3.0	5.0.0

## CHANGE REQUEST

⌘ **25.212 CR 123** ⌘ rev **4** ⌘ Current version: **4.3.0** ⌘

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**Proposed change affects:** ⌘ (U)SIM  ME/UE  Radio Access Network  Core Network

<b>Title:</b>	⌘ Inclusion of flexible hard split mode TFCI operation		
<b>Source:</b>	⌘ TSG RAN WG1		
<b>Work item code:</b>	⌘ RinImp-DSCHhsp	<b>Date:</b>	⌘ 15 Feb. 2002
<b>Category:</b>	⌘ <b>B</b>	<b>Release:</b>	⌘ REL-5
	Use <u>one</u> of the following categories:		Use <u>one</u> of the following releases:
	<b>F</b> (correction)	<b>2</b>	(GSM Phase 2)
	<b>A</b> (corresponds to a correction in an earlier release)	<b>R96</b>	(Release 1996)
	<b>B</b> (addition of feature),	<b>R97</b>	(Release 1997)
	<b>C</b> (functional modification of feature)	<b>R98</b>	(Release 1998)
	<b>D</b> (editorial modification)	<b>R99</b>	(Release 1999)
	Detailed explanations of the above categories can be found in 3GPP TR 21.900.		<b>REL-4</b> (Release 4)
			<b>REL-5</b> (Release 5)

<b>Reason for change:</b>	⌘ Changes on coding scheme for TFCI bits are required in TS 25.212 to support the flexible hard split mode operation in Rel-5.
<b>Summary of change:</b>	⌘ It is proposed to include the TFCI coding scheme for the flexible hard split mode in section 4.3.4. Current Rel-99/4 hard split mode operation is included in this scheme, so it's removed for avoiding duplication.
<b>Consequences if not approved:</b>	⌘ Flexible combination of TFCI bits for DCH and DSCH cannot be supported.

<b>Clauses affected:</b>	⌘ 4.3.4		
<b>Other specs affected:</b>	⌘ <input type="checkbox"/> Other core specifications	⌘ <input type="checkbox"/>	
	<input type="checkbox"/> Test specifications		
	<input type="checkbox"/> O&M Specifications		
<b>Other comments:</b>	⌘		

### How to create CRs using this form:

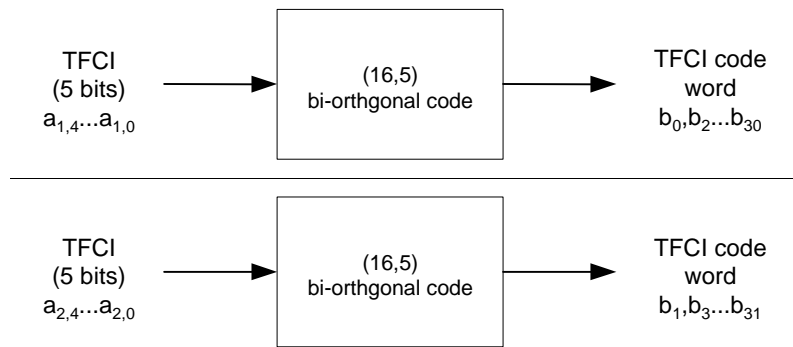
Comprehensive information and tips about how to create CRs can be found at: [http://www.3gpp.org/3G\\_Specs/CRs.htm](http://www.3gpp.org/3G_Specs/CRs.htm). Below is a brief summary:

- 1) Fill out the above form. The symbols above marked ⌘ contain pop-up help information about the field that they are closest to.
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- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

### 4.3.4 Operation of TFCI in Hard Split Mode ~~Operation of Transport-Format-Combination Indicator (TFCI) in Split Mode~~

If one of the DCH is associated with a DSCH, the TFCI code word may be split in such a way that the code word relevant for TFCI activity indication is not transmitted from every cell. The use of such a functionality shall be indicated by higher layer signalling.

The TFCI is encoded using a (16, 5) bi-orthogonal (or first order Reed-Muller) code. The coding procedure is as shown in figure 10.



**Figure 10: Channel coding of split mode TFCI information bits**

The code words of the (16,5) bi-orthogonal code are linear combinations of 5 basis sequences as defined in table 9.

**Table 9: Basis sequences for (16,5) TFCI code**

i	M <sub>i,0</sub>	M <sub>i,1</sub>	M <sub>i,2</sub>	M <sub>i,3</sub>	M <sub>i,4</sub>
0	1	0	0	0	1
1	0	1	0	0	1
2	1	1	0	0	1
3	0	0	1	0	1
4	1	0	1	0	1
5	0	1	1	0	1
6	1	1	1	0	1
7	0	0	0	1	1
8	1	0	0	1	1
9	0	1	0	1	1
10	1	1	0	1	1
11	0	0	1	1	1
12	1	0	1	1	1
13	0	1	1	1	1
14	1	1	1	1	1
15	0	0	0	0	1

The first set of TFCI information bits (a<sub>1,0</sub>, a<sub>1,1</sub>, a<sub>1,2</sub>, a<sub>1,3</sub>, a<sub>1,4</sub> where a<sub>1,0</sub> is LSB and a<sub>1,4</sub> is MSB) shall correspond to the TFC index (expressed in unsigned binary form) defined by the RRC layer to reference the TFC of the DCH CCTrCH in the associated DPCH radio frame.

The second set of TFCI information bits (a<sub>2,0</sub>, a<sub>2,1</sub>, a<sub>2,2</sub>, a<sub>2,3</sub>, a<sub>2,4</sub> where a<sub>2,0</sub> is LSB and a<sub>2,4</sub> is MSB) shall correspond to the TFC index (expressed in unsigned binary form) defined by the RRC layer to reference the TFC of the associated DSCH CCTrCH in the corresponding PDSCH radio frame.

The output code word bits b<sub>k</sub> are given by:

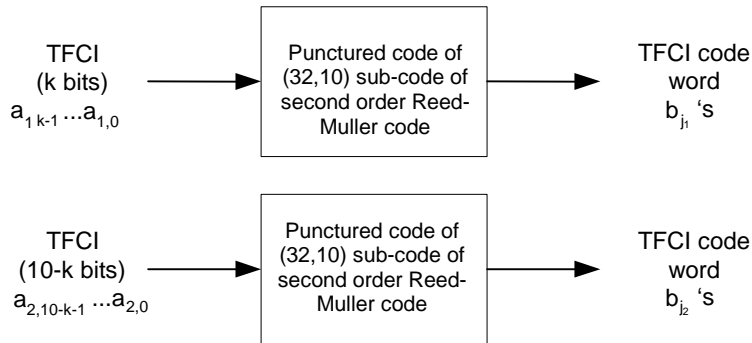
$$b_{2i} = \sum_{n=0}^4 (a_{1,n} \times M_{i,n}) \text{ mod } 2; \quad b_{2i+1} = \sum_{n=0}^4 (a_{2,n} \times M_{i,n}) \text{ mod } 2$$

where i = 0, ..., 15.

The output bits are denoted by  $b_k, k = 0, 1, 2, \dots, 31$ .

If one of the DCH is associated with a DSCH, the TFCI code word may be split in such a way that the code word relevant for TFCI activity indication is not transmitted from every cell. The use of such a functionality shall be indicated by higher layer signalling.

The TFCI is encoded by using punctured code of (32,10) sub-code of second order Reed-Muller code. The coding procedure is as shown in figure 10.



**Figure 10: Channel coding of flexible hard split mode TFCI information bits**

The code words of the punctured code of (32,10) sub-code of second order Reed-Muller code are linear combinations of basis sequences generated by puncturing 10 basis sequences defined in table 8 in section 4.3.3.

The first set of TFCI information bits ( $a_{1,0}, a_{1,1}, a_{1,2}, a_{1,3}, \dots, a_{1,k-1}$  where  $a_{1,0}$  is LSB and  $a_{1,k-1}$  is MSB) shall correspond to the TFC index (expressed in unsigned binary form) defined by the RRC layer to reference the TFC of the DCH CCTrCH in the associated DPCH radio frame.

The second set of TFCI information bits ( $a_{2,0}, a_{2,1}, a_{2,2}, a_{2,3}, \dots, a_{2,10-k-1}$  where  $a_{2,0}$  is LSB and  $a_{2,10-k-1}$  is MSB) shall correspond to the TFC index (expressed in unsigned binary form) defined by the RRC layer to reference the TFC of the associated DSCH CCTrCH in the corresponding PDSCH radio frame.

The output code word bits are given by :

$$b_{j_1} = \sum_{n=0}^{k-1} (a_{1,n} \times M_{\pi_1(k,i_1),\pi_2(k,n)}) \bmod 2; \quad b_{j_2} = \sum_{n=0}^{10-k-1} (a_{2,n} \times M_{\pi_1(10-k,i_2),\pi_2(10-k,n)}) \bmod 2$$

where  $i_1 = 0, \dots, 3 \times k$  and  $i_2 = 0, \dots, 30 - 3 \times k$ .

Then, the relation between  $j_1$  (or  $j_2$ ) and  $i_1$  (or  $i_2$ ) is as follows:

- If  $k \neq 5$ ,

$$j_1 = \left\lfloor \frac{32}{3 \times \min(k, 10 - k) + 1} \times (i_1 + 1) + \frac{1}{2} \right\rfloor - 1; \quad j_2 = i_2 + \left\lfloor \frac{3 \times \min(k, 10 - k) + 1}{32 - (3 \times \min(k, 10 - k) + 1)} \times (i_2 + \frac{1}{2}) \right\rfloor$$

- If  $k = 5$ ,

$$j_1 = 2 \times i_1; \quad j_2 = 2 \times i_2 + 1$$

The functions  $\pi_1, \pi_2$  are defined as shown in the following table 9.

**Table 9.  $\pi_1, \pi_2$  functions**

m	$\pi_1(m, i)$ for $i = 0, \dots, 3 \times m$	$\pi_2(m, n)$ for $n = 0, \dots, m - 1$
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3	0, 1, 2, 3, 4, 5, 6, 8, 9, 11	0, 1, 2
4	3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15	0, 1, 2, 3
5	0, 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 30	0, 1, 2, 3, 5
6	0, 1, 2, 3, 4, 5, 7, 8, 9, 12, 15, 18, 21, 23, 25, 27, 28, 29, 30	0, 1, 2, 3, 4, 5
7	0, 1, 2, 3, 4, 5, 6, 7, 9, 10, 11, 13, 14, 15, 17, 20, 21, 22, 24, 25, 28, 29	0, 1, 2, 3, 4, 6, 7

## CHANGE REQUEST

⌘ **25.214 CR 250** ⌘ rev **1** ⌘ Current version: **4.3.0** ⌘

For **HELP** on using this form, see bottom of this page or look at the pop-up text over the ⌘ symbols.

**Proposed change affects:** ⌘ (U)SIM  ME/UE  Radio Access Network  Core Network

<b>Title:</b>	⌘ Description of SSDD operation for TFCI power control in hard split mode		
<b>Source:</b>	⌘ TSG RAN WG1		
<b>Work item code:</b>	⌘ RInImp-DSCHhsp	<b>Date:</b>	⌘ 2002.02.19.
<b>Category:</b>	⌘ <b>B</b> Use <u>one</u> of the following categories: <b>F</b> (correction) <b>A</b> (corresponds to a correction in an earlier release) <b>B</b> (addition of feature), <b>C</b> (functional modification of feature) <b>D</b> (editorial modification) Detailed explanations of the above categories can be found in 3GPP TR 21.900.	<b>Release:</b>	⌘ REL-5 Use <u>one</u> of the following releases: 2 (GSM Phase 2) R96 (Release 1996) R97 (Release 1997) R98 (Release 1998) R99 (Release 1999) REL-4 (Release 4) REL-5 (Release 5)

<b>Reason for change:</b>	⌘ To describe SSDD operation in section 5.2.1 is required to support the TFCI power control scheme for the flexible hard split mode in Rel'5
<b>Summary of change:</b>	⌘ In section 5.2.1.1, "UTRAN may use the SSDD operation as specified in section 5.2.2 to determine what power offset to use for TFCI in hard split mode with respect to the associated downlink DPDCH.". In section 5.2.1.4, "UTRAN may use the SSDD information for for the PDSCH power control as specified in section 5.2.2 and the TFCI power control in hard split mode."
<b>Consequences if not approved:</b>	⌘ The TFCI power control scheme for the flexible hard split mode cannot be supported

<b>Clauses affected:</b>	⌘ 5.2.1.1, 5.2.1.4.1		
<b>Other specs affected:</b>	⌘ <input type="checkbox"/> Other core specifications	⌘ <input type="checkbox"/>	
	<input type="checkbox"/> Test specifications		
	<input type="checkbox"/> O&M Specifications		
<b>Other comments:</b>	⌘		

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## 5.2.1 DPCCH/DPDCH

### 5.2.1.1 General

The downlink transmit power control procedure controls simultaneously the power of a DPCCH and its corresponding DPDCHs. The power control loop adjusts the power of the DPCCH and DPDCHs with the same amount, i.e. the relative power difference between the DPCCH and DPDCHs is not changed.

The relative transmit power offset between DPCCH fields and DPDCHs is determined by the network. The TFCI, TPC and pilot fields of the DPCCH are offset relative to the DPDCHs power by PO1, PO2 and PO3 dB respectively. The power offsets may vary in time. UTRAN may use the SSdT operation as specified in section 5.2.2 to determine what power offset to use for TFCI in hard split mode with respect to the associated downlink DPDCH. The method for controlling the power offsets within UTRAN is specified in [6].

The power of CCC field in DL DPCCH for CPCH is the same as the power of the pilot field.

### 5.2.1.4 Site selection diversity transmit power control

#### 5.2.1.4.1 General

Site selection diversity transmit power control (SSdT) is another macro diversity method in soft handover mode. This method is optional in UTRAN.

Operation is summarised as follows. The UE selects one of the cells from its active set to be 'primary', all other cells are classed as 'non primary'. The main objective is to transmit on the downlink from the primary cell, thus reducing the interference caused by multiple transmissions in a soft handover mode. A second objective is to achieve fast site selection without network intervention, thus maintaining the advantage of the soft handover. In order to select a primary cell, each cell is assigned a temporary identification (ID) and UE periodically informs a primary cell ID to the connecting cells. The non-primary cells selected by UE switch off the transmission power. The primary cell ID is delivered by UE to the active cells via uplink FBI field. SSdT activation, SSdT termination and ID assignment are all carried out by higher layer signalling.

SSdT can only be used when the P-CPICH is used as the downlink phase reference.

UTRAN may also command UE to use SSdT signalling in the uplink although cells would transmit the downlink as without SSdT active. In case SSdT is used in the uplink direction only, the processing in the UE for the radio links received in the downlink is as with macro diversity in non-SSdT case. The downlink operation mode for SSdT is set by higher layers. UTRAN may use the SSdT information for the PDSCH power control as specified in section 5.2.2 and for the TFCI power control in hard split mode.

NOTE: This feature of SSdT limited to uplink only applies to terminals that are DSCH capable.