TSG-RAN Meeting #14 *RP-010773*
Kyoto, Japan, 11 - 14 December 2001

Title: **Agreed CRs (Rel-4) to TS 25.331**

Source: **TSG-RAN WG2**

Agenda item: **8.2.4**

Doc-1st-	Status-	Spec	CR	Rev	Phase	Subject	Cat	Version	Versio	Workite
R2-012626	agreed	25.331	1096		Rel-4	Usage of UM RLC Special Length Indicator	F	4.2.1	4.3.0	TEI4
R2-012500	agreed	25.331	1120		Rel-4	Corrections to REL-4 LCR Tabular Description and ASN1 Code	F	4.2.1	4.3.0	LCRTD D-L23
R2-012568	agreed	25.331	1199		Rel-4	Correction of FPACH parameter definition for 1.28Mcps TDD	F	4.2.1	4.3.0	LCRTD D-L23
R2-012569	agreed	25.331	1200		Rel-4	Correction of 1.28Mcps TDD	F	4.2.1	4.3.0	LCRTD D-L23
R2-012572	agreed	25.331	1201		Rel-4	Correction and Clarification to Open Loop Power Control in 1.28 Mcps TDD	F	4.2.1	4.3.0	LCRTD D-L23
R2-012630	agreed	25.331	1206		Rel-4	Extensions of IE value ranges in tabular	F	4.2.1	4.3.0	TEI4

3GPP TSG-RAN WG2 Meeting #25
Makuhari, Japan, 26-30 November 2001

Tdoc R2-012626

CR-Form-v4

CHANGE REQUEST

⌘ **25.331** **CR** **1096** ⌘ rev **-** ⌘ Current version: **4.2.1** ⌘

*For **HELP** on using this form, see bottom of this page or look at the pop-up text over the ⌘ symbols.*

Proposed change affects: ⌘ (U)SIM ☐ ME/UE **X** Radio Access Network **X** Core Network ☐

Title:	⌘	Usage of UM RLC Special Length Indicator
Source:	⌘	TSG-RAN WG2
Work item code:	⌘	TEI4
Date:	⌘	26 November 01
Category:	⌘	**F**
Release:	⌘	REL-4

Use one of the following categories:
F *(correction)*
A *(corresponds to a correction in an earlier release)*
B *(addition of feature),*
C *(functional modification of feature)*
D *(editorial modification)*
Detailed explanations of the above categories can be found in 3GPP TR 21.900.

Use one of the following releases:

2	*(GSM Phase 2)*
R96	*(Release 1996)*
R97	*(Release 1997)*
R98	*(Release 1998)*
R99	*(Release 1999)*
REL-4	*(Release 4)*
REL-5	*(Release 5)*

Reason for change:	⌘	As explained in R2-012504, because of the way the concatenation is signalled in UM, if the Special LI is not used and if a RLC UM PDU is lost, then the following correctly received SDU can be discarded by the receiving entity.
Summary of change:	⌘	The usage of the Special LI is extended: - It is no more configured by RRC. It is up to RLC to use it when appropiate.
Consequences if not approved:	⌘	Unefficiant behaviour as described in R2-012504.

Clauses affected:	⌘	6.3		
Other specs affected:	⌘	**X** Other core specifications ☐ Test specifications ☐ O&M Specifications	⌘	25.322
Other comments:	⌘			

How to create CRs using this form:
Comprehensive information and tips about how to create CRs can be found at: http://www.3gpp.org/3G_Specs/CRs.htm. Below is a brief summary:

1) Fill out the above form. The symbols above marked ⌘ contain pop-up help information about the field that they are closest to.
2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under ftp://ftp.3gpp.org/specs/ For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.
3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

6.3 Signalling Radio Bearers

The Radio Bearers (RB) available for transmission of RRC messages are defined as "signalling radio bearers" and are specified in the following. The UE and UTRAN shall select the signalling radio bearers for RRC messages using RLC-TM, RLC-UM or RLC-AM on the DCCH and CCCH, according to the following:

- Signalling radio bearer RB0 shall be used for all messages sent on the CCCH (UL: RLC-TM, DL: RLC-UM).
- Signalling radio bearer RB1 shall be used for all messages sent on the DCCH, when using RLC unacknowledged mode (RLC-UM).
- Signalling radio bearer RB2 shall be used for all messages sent on the DCCH, when using RLC acknowledged mode (RLC-AM), except for the RRC messages carrying higher layer (NAS) signalling.
- Signalling radio bearer RB3 and optionally Signalling radio bearer RB4 shall be used for the RRC messages carrying higher layer (NAS) signalling and sent on the DCCH in RLC acknowledged mode (RLC-AM), as specified in subclauses 8.1.8., 8.1.9 and 8.1.10.
- Additionally, RBs whose identities shall be set between 5 and 32 may be used as signalling radio bearer for the RRC messages on the DCCH sent in RLC transparent mode (RLC-TM).
- RRC messages on the SHCCH are mapped either on RACH or on the USCH in the uplink using TM and either on FACH or on the DSCH using RLC-UM. These messages are only specified for TDD mode.

The Radio Bearer configuration for signalling radio bearer RB0, SHCCH, BCCH on FACH and PCCH on PCH are specified in subclauses 13.6, 13.6a, 13.6b and 13.6c.

~~When an RRC message is transmitted in DL on CCCH or SHCCH using RLC UM, RRC should indicate to RLC that a special RLC length indicator should be used [16]. The UE shall assume that this indication has been given. The special length indicator indicates that an RLC SDU begins in the beginning of an RLC PDU.~~

3GPP TSG-RAN WG2 Meeting #25
Makuhari, Japan, November 26th -30th 2001

Tdoc R2-012500

CR-Form-v4

CHANGE REQUEST

⌘ **25.331 CR 1120** ⌘ ev **-** ⌘ Current version: **4.2.1** ⌘

*For **HELP** on using this form, see bottom of this page or look at the pop-up text over the ⌘ symbols.*

Proposed change affects: ⌘ (U)SIM ☐ ME/UE **X** Radio Access Network **X** Core Network ☐

Title:	⌘	Corrections to REL-4 LCR Tabular Description and ASN1 Code
Source:	⌘	TSG-RAN WG2
Work item code:	⌘	LCRTDD-L23
Date:	⌘	20/11/2001
Category:	⌘	**F**
Release:	⌘	REL-4

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2 (GSM Phase 2)
R96 (Release 1996)
R97 (Release 1997)
R98 (Release 1998)
R99 (Release 1999)
REL-4 (Release 4)
REL-5 (Release 5)

Reason for change: ⌘

UPLINK PHYSICAL CHANNEL CONTROL:
The tabular description of IE "UE positioning related parameters" in clause 10.2.59 does not correspond to the implementation in ASN1. This IE applies only to 3.84 Mcps TDD and the tabular description should be changed accordingly.

SysInfoType5-r3-r4-ext-IEs, SysInfoType6-r3-r4-ext-IEs:
Elements in the IE PRACH-SystemInformationList in the R99 part of the SIB5/6 message are replaced by IEs contained the LCR-r4-extension when describing a 1.28 Mcps TDD system. In the current version only 1 element exists for PRACH-RACH-Info, PRACH-Partitioning and TransportFormatSet to replace an array of these IEs in the R99 Message. Since it is necessary to replace every IE of the R99 array by a corresponding LCR-r4 IE the single IEs where expanded to a corresponding PRACH-SystemInformation-LIST-LCR-r4.

IE PUSCH-SysInfoList-SFN-LCR-r4:
The maximum list index is erronously maxPDSCH for this list, it schould rather be maxPUSCH and should be changed accordingly.

IE CellInfo-r4, CellInfoSI-RSCP LCR-r4, CellInfoSI-ECN0-LCR-r4, CellInfoSI-HCS-RSCP-LCR-r4, CellInfoSI-HCS-ECN0-LCR-r4:
The IE readSFN-Indicator is missing according to tabular description in section 10.3.7.2 Cell info.

IE TimeslotInfo-LCR-r4:
The IE burstType should no be contained in the IE according to tabular description in section 10.2.7.2 Cell info.

Summary of change: ⌘

UplinkPhysicalChannelControl-r4-IEs:
The IE "UE positioning related parameters" is moved to 3.84 Mcps TDD part of description.

		SysInfoType5-r3-r4-ext-IEs, SysInfoType6-r3-r4-ext-IEs: The IE PRACH-SystemInformation-LCR-r4 is replaced by an array PRACH-SystemInformation-LIST-LCR-r4 containing the IEs for a number of RACHs. IE PUSCH-SysInfoList-SFN-LCR-r4: maxPDSCH is replaced be maxPUSCH IE CellInfo-r4, CellInfoSI-RSCP_LCR-r4, CellInfoSI-ECN0-LCR-r4, CellInfoSI-HCS-RSCP-LCR-r4, CellInfoSI-HCS-ECN0-LCR-r4: The IE readSFN-Indicator is inserted at the appropriate location. IE TimeslotInfo-LCR-r4: The IE burstType is deleted in this IE.
Consequences if not approved:	⌘	Information elements of ASN1 code are not aligned with tabular descriptions.

Clauses affected:	⌘	10.2.59, 11.2, 11.3		
Other specs affected:	⌘	☐ Other core specifications ☐ Test specifications ☐ O&M Specifications	⌘	
Other comments:	⌘			

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3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

10.2.59 UPLINK PHYSICAL CHANNEL CONTROL

NOTE: Only for TDD.

This message is used to transfer uplink physical channel parameters to the UE.

RLC-SAP: AM or UM

Logical channel: DCCH

Direction: UTRAN→UE

Information Element/Group name	Need	Multi	Type and Reference	Semantics description	Version
Message Type	MP		Message Type		
UE information elements					
RRC transaction identifier	MP		RRC transaction identifier 10.3.3.36		
Integrity check info	OP		Integrity check info 10.3.3.16		
PhyCH information elements					
CCTrCH power control info	OP		CCTrCH power control info 10.3.6.8	Power control information for one CCTrCH	
CHOICE *TDD option*	MP				REL-4
>3.84 Mcps TDD					REL-4
>>Alpha	OP		Alpha 10.3.6.5		
>>Special Burst Scheduling	OP		Special Burst Scheduling 10.3.6.75a	UL Special Burst generation period in radio frames	
>>Timing Advance Control	OP		UL Timing Advance Control 10.3.6.96		
>>PRACH Constant Value	OP		Constant value 10.3.6.11	Operator controlled PRACH Margin	
>>PUSCH Constant Value	OP		Constant value 10.3.6.11	Operator controlled PUSCH Margin	
>>UE positioning related parameters	CV-*IPDLs*				REL-4
>>>IPDL-Alpha	MP		Alpha 10.3.6.5		REL-4
>>>Max power increase	MP		Integer (0..3)	In db	REL-4
>1.28 Mcps TDD					REL-4
>>Uplink synchronisation parameters	MD			Default: Uplink synchronisation step size 1. Uplink synchronisation frequency 1.	REL-4
>>>Uplink synchronisation step size	MP		Integer(1..8)	This parameter specifies the step size to be used for the adjustment of the uplink transmission timing	REL-4
>>>Uplink synchronisation frequency	MP		Integer(1..8)	This parameter specifies the frequency of the adjustment of the uplink transmission timing	REL-4
~~UE positioning related parameters~~	~~CV-*IPDLs*~~				~~REL-4~~

~~>IPDL-Alpha~~	~~MP~~		~~Alpha 10.3.6.5~~		~~REL-4~~
~~>Max power increase~~	~~MP~~		~~Integer (0..3)~~	~~In db~~	~~REL-4~~

Condition	Explanation
IPDLs	This IE is present only if idle periods are applied

11 Message and Information element abstract syntax (with ASN.1)

```
[...]

PRACH-SystemInformation ::=         SEQUENCE {
    prach-RACH-Info                     PRACH-RACH-Info,
    transportChannelIdentity            TransportChannelIdentity,
    rach-TransportFormatSet             TransportFormatSet                  OPTIONAL,
    rach-TFCS                           TFCS                                OPTIONAL,
    prach-Partitioning                  PRACH-Partitioning                  OPTIONAL,
    persistenceScalingFactorList        PersistenceScalingFactorList        OPTIONAL,
    ac-To-ASC-MappingTable              AC-To-ASC-MappingTable              OPTIONAL,
    modeSpecificInfo                    CHOICE {
        fdd                                 SEQUENCE {
            primaryCPICH-TX-Power               PrimaryCPICH-TX-Power       OPTIONAL,
            constantValue                       ConstantValue               OPTIONAL,
            prach-PowerOffset                   PRACH-PowerOffset           OPTIONAL,
            rach-TransmissionParameters         RACH-TransmissionParameters OPTIONAL,
            aich-Info                           AICH-Info                   OPTIONAL
        },
        tdd                                 NULL
    }
}

PRACH-SystemInformation-LCR-r4 ::=  SEQUENCE {
    prach-RACH-Info-LCR                 PRACH-RACH-Info-LCR-r4,
    rach-TransportFormatSet-LCR         TransportFormatSet-LCR          OPTIONAL,
    prach-Partitioning-LCR              PRACH-Partitioning-LCR-r4       OPTIONAL
}

PRACH-SystemInformationList ::=     SEQUENCE (SIZE (1..maxPRACH)) OF
                                        PRACH-SystemInformation

PRACH-SystemInformationList-LCR-r4 ::=  SEQUENCE (SIZE (1..maxPRACH)) OF
                                            PRACH-SystemInformation-LCR-r4

PreambleRetransMax ::=              INTEGER (1..64)

[...]

PUSCH-SysInfoList-SFN-LCR-r4 ::=        SEQUENCE (SIZE (1..maxPUSCH~~PDSCH~~)) OF
                                        SEQUENCE {
    pusch-SysInfo                           PUSCH-SysInfo-LCR-r4,
    sfn-TimeInfo                            SFN-TimeInfo                    OPTIONAL
}

[...]

CellInfo-r4 ::=                     SEQUENCE {
    cellIndividualOffset                CellIndividualOffset                DEFAULT 0,
    referenceTimeDifferenceToCell       ReferenceTimeDifferenceToCell       OPTIONAL,
    modeSpecificInfo                    CHOICE {
        fdd                                 SEQUENCE {
            primaryCPICH-Info                   PrimaryCPICH-Info           OPTIONAL,
            primaryCPICH-TX-Power               PrimaryCPICH-TX-Power       OPTIONAL,
            readSFN-Indicator                   BOOLEAN,
            tx-DiversityIndicator               BOOLEAN
```

```
        },
        tdd                                 SEQUENCE {
            primaryCCPCH-Info                   PrimaryCCPCH-Info-r4,
            primaryCCPCH-TX-Power               PrimaryCCPCH-TX-Power       OPTIONAL,
            timeslotInfoList                    TimeslotInfoList-r4         OPTIONAL,
            readSFN-Indicator                   BOOLEAN
        }
    }
}

[...]

CellInfoSI-RSCP-LCR-r4 ::=          SEQUENCE {
    cellIndividualOffset                CellIndividualOffset                DEFAULT 0,
    referenceTimeDifferenceToCell       ReferenceTimeDifferenceToCell       OPTIONAL,
    primaryCCPCH-Info                   PrimaryCCPCH-Info-LCR-r4,
    primaryCCPCH-TX-Power               PrimaryCCPCH-TX-Power               OPTIONAL,
    timeslotInfoList                    TimeslotInfoList-LCR-r4             OPTIONAL,
    readSFN-Indicator                   BOOLEAN,
    cellSelectionReselectionInfo        CellSelectReselectInfoSIB-11-12-RSCP        OPTIONAL
}

[...]

CellInfoSI-ECN0-LCR-r4 ::=          SEQUENCE {
    cellIndividualOffset                CellIndividualOffset                DEFAULT 0,
    referenceTimeDifferenceToCell       ReferenceTimeDifferenceToCell       OPTIONAL,
    primaryCCPCH-Info                   PrimaryCCPCH-Info-LCR-r4,
    primaryCCPCH-TX-Power               PrimaryCCPCH-TX-Power               OPTIONAL,
    timeslotInfoList                    TimeslotInfoList-LCR-r4             OPTIONAL,
    readSFN-Indicator                   BOOLEAN,
    cellSelectionReselectionInfo        CellSelectReselectInfoSIB-11-12-ECN0    OPTIONAL
}

[...]

CellInfoSI-HCS-RSCP-LCR-r4 ::=      SEQUENCE {
    cellIndividualOffset                CellIndividualOffset                DEFAULT 0,
    referenceTimeDifferenceToCell       ReferenceTimeDifferenceToCell       OPTIONAL,
    primaryCCPCH-Info                   PrimaryCCPCH-Info-LCR-r4,
    primaryCCPCH-TX-Power               PrimaryCCPCH-TX-Power               OPTIONAL,
    timeslotInfoList                    TimeslotInfoList-LCR-r4             OPTIONAL,
    readSFN-Indicator                   BOOLEAN,
    cellSelectionReselectionInfo        CellSelectReselectInfoSIB-11-12-HCS-RSCP    OPTIONAL
}

[...]

CellInfoSI-HCS-ECN0-LCR-r4 ::=      SEQUENCE {
    cellIndividualOffset                CellIndividualOffset                DEFAULT 0,
    referenceTimeDifferenceToCell       ReferenceTimeDifferenceToCell       OPTIONAL,
    primaryCCPCH-Info                   PrimaryCCPCH-Info-LCR-r4,
    primaryCCPCH-TX-Power               PrimaryCCPCH-TX-Power               OPTIONAL,
    timeslotInfoList                    TimeslotInfoList-LCR-r4             OPTIONAL,
    readSFN-Indicator                   BOOLEAN,
    cellSelectionReselectionInfo        CellSelectReselectInfoSIB-11-12-HCS-ECN0    OPTIONAL
}

[...]

TimeslotInfo-LCR-r4 ::=             SEQUENCE {
    timeslotNumber                      TimeslotNumber-LCR-r4,
    burstType                           BurstType
}

[...]

SysInfoType5-r3-r4-ext-IEs ::= SEQUENCE {
    pNBSCH-Allocation-r4            PNBSCH-Allocation-r4            OPTIONAL,
    -- In case of TDD, the following IE is included instead of the
    -- IE up-IPDL-Parameter in up-OTDOA-AssistanceData.
    openLoopPowerControl-IPDL-TDD   OpenLoopPowerControl-IPDL-TDD-r4    OPTIONAL,
-- If SysInfoType5 is sent to describe a 1.28Mcps TDD cell, the IE PRACH-RACH-Info included in
```

```
-- PRACH-SystemInformationList shall be ignored, and the following IE shall describe
-- the PRACH-RACH-Information.
    prach-RACH-Info-LCR             PRACH-RACH-Info-LCR-r4              OPTIONAL,
-- If SysInfoType5 is sent to describe a 1.28Mcps TDD cell, the IE PRACH-Partitioning in
-- PRACH-SystemInformationList shall be absent, and the following IE shall describe
-- the PRACH-Partitioning.
    prach-Partitioning-LCR          PRACH-Partitioning-LCR-r4           OPTIONAL,
-- If SysInfoType5 is sent to describe a 1.28Mcps TDD cell, the IE rach-TransportFormatSet in
-- PRACH-SystemInformationList shall be absent, and the following IE shall describe
-- the rach-TransportFormatSet.
    rach-TransportFormatSet-LCR     TransportFormatSet-LCR              OPTIONAL,
-- If SysInfoType5 is sent to describe a 1.28Mcps TDD cell, the IE PRACH-RACH-Info included in
-- PRACH-SystemInformationList shall be ignored, the IE PRACH-Partitioning and the
-- IE rach-TransportFormatSet shall be absent and the corresponding IE in the following
-- PRACH-SystemInformationList-LCR-r4 shall be used
    prach-SystemInformationList-LCR-r4  PRACH-SystemInformationList-LCR-r4  OPTIONAL,
    tdd128SpecificInfo              SEQUENCE {
        pusch-SysInfoList-SFN           PUSCH-SysInfoList-SFN-LCR-r4    OPTIONAL,
        pdsch-SysInfoList-SFN           PDSCH-SysInfoList-SFN-LCR-r4    OPTIONAL,
        pCCPCH-LCR-Extensions           PrimaryCCPCH-Info-LCR-r4-ext    OPTIONAL,
        sCCPCH-LCR-ExtensionsList       SCCPCH-SystemInformationList-LCR-r4-ext
    }                                                                   OPTIONAL
}

[...]

SysInfoType6-r3-r4-ext-IEs ::= SEQUENCE {
    -- This IE is present only if IPDLs are applied for TDD
    openLoopPowerControl-IPDL-TDD   OpenLoopPowerControl-IPDL-TDD-r4    OPTIONAL,
-- If SysInfoType6 is sent to describe a 1.28Mcps TDD cell, the IE PRACH-RACH-Info included in
-- PRACH-SystemInformationList shall be ignored, and the following IE shall describe
-- the PRACH-RACH-Information.
    prach-RACH-Info-LCR             PRACH-RACH-Info-LCR-r4              OPTIONAL,
-- If SysInfoType6 is sent to describe a 1.28Mcps TDD cell, the IE PRACH-Partitioning in
-- PRACH-SystemInformationList shall be absent, and the following IE shall describe
-- the PRACH-Partitioning.
    prach-Partitioning-LCR          PRACH-Partitioning-LCR-r4           OPTIONAL,
-- If SysInfoType6 is sent to describe a 1.28Mcps TDD cell, the IE rach-TransportFormatSet in
-- PRACH-SystemInformationList shall be absent, and the following IE shall describe
-- the rach-TransportFormatSet.
    rach-TransportFormatSet-LCR     TransportFormatSet-LCR              OPTIONAL,
-- If SysInfoType6 is sent to describe a 1.28Mcps TDD cell, the IE PRACH-RACH-Info included in
-- PRACH-SystemInformationList shall be ignored, the IE PRACH-Partitioning and the
-- IE rach-TransportFormatSet shall be absent and the corresponding IEs in the following
-- PRACH-SystemInformationList-LCR-r4 shall be used
    prach-SystemInformationList-LCR-r4  PRACH-SystemInformationList-LCR-r4  OPTIONAL,
    tdd128SpecificInfo              SEQUENCE {
        pusch-SysInfoList-SFN           PUSCH-SysInfoList-SFN-LCR-r4    OPTIONAL,
        pdsch-SysInfoList-SFN           PDSCH-SysInfoList-SFN-LCR-r4    OPTIONAL,
        pCCPCH-LCR-Extensions           PrimaryCCPCH-Info-LCR-r4-ext    OPTIONAL,
        sCCPCH-LCR-ExtensionsList       SCCPCH-SystemInformationList-LCR-r4-ext OPTIONAL
    }                                                                   OPTIONAL
}

[...]
```

3GPP TSG-RAN WG2 Meeting#25 ***R2-012568***
Makuhari, Japan, 26-30 November 2001

CR-Form-v3

CHANGE REQUEST

⌘ **25.331** **CR** **1199** ⌘ rev **-** ⌘ Current version: **4.2.1** ⌘

*For **HELP** on using this form, see bottom of this page or look at the pop-up text over the ⌘ symbols.*

Proposed change affects: ⌘ (U)SIM ☐ ME/UE **X** Radio Access Network **X** Core Network ☐

Title:	⌘	Correction of FPACH parameter definition for 1.28Mcps TDD
Source:	⌘	TSG-RAN WG2
Work item code:	⌘	LCRTDD-L23
Date:	⌘	31-10-01
Category:	⌘	**F**
Release:	⌘	REL-4

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R99	*(Release 1999)*
REL-4	*(Release 4)*
REL-5	*(Release 5)*

Reason for change:	⌘	The definition of Timeslot number which FPACH could be used is not clearly defined. For it is inconsistent with WG1, we make this correct.
Summary of change:	⌘	Correct the definition of Timeslot number which FPACH could be used to make it consistent with WG1. Isolated impact: The changes have isolated impact and were corrections to a function where the specification was ambiguous or not sufficiently explicit. They would not affect implementations behaving like indicated in the CR, would affect implementations supporting the corrected functionality otherwise.
Consequences if not approved:	⌘	

Clauses affected:	⌘	10.3.6.35a, 11.3
Other specs affected:	⌘	☐ Other core specifications ⌘ ☐ Test specifications ☐ O&M Specifications
Other comments:	⌘	

10.3.6.35a FPACH info

NOTE: Only for 1.28 Mcps TDD.

Information Element/Group name	Need	Multi	Type and reference	Semantics description	Version
Timeslot number	MP		Integer(~~1~~0..6)		REL-4
Channelisation code	MP		Enumerated((16/1)..(16/16)		REL-4
Midamble Shift and burst type	MP		Midamble shift and burst type 10.3.6.41		REL-4
WT	MP		Integer(1..4)	The number of sub-frames, following the sub-frame in which the SYNC UL is transmitted, in which the FPACH can be transmitted.	REL-4

11.3 Information element definitions

```
-- **************************************************
--
--     PHYSICAL CHANNEL INFORMATION ELEMENTS (10.3.6)
--
-- **************************************************

AC-To-ASC-Mapping ::=               INTEGER (0..7)

AC-To-ASC-MappingTable ::=          SEQUENCE (SIZE (maxASCmap)) OF
                                        AC-To-ASC-Mapping

AccessServiceClass-FDD ::=          SEQUENCE {
    availableSignatureStartIndex        INTEGER (0..15),
    availableSignatureEndIndex          INTEGER (0..15),

    assignedSubChannelNumber            BIT STRING {
                                            b3(0),
                                            b2(1),
                                            b1(2),
                                            b0(3)
                                            }    (SIZE(4))
}

AccessServiceClass-TDD ::=          SEQUENCE {
    channelisationCodeIndices           BIT STRING  {
                                            chCodeIndex7(0),
                                            chCodeIndex6(1),
                                            chCodeIndex5(2),
                                            chCodeIndex4(3),
                                            chCodeIndex3(4),
                                            chCodeIndex2(5),
                                            chCodeIndex1(6),
                                            chCodeIndex0(7)
                                            } (SIZE(8))             OPTIONAL,
    subchannelSize                      CHOICE {
        size1                               NULL,
-- in size2, subch0 means bitstring '01' in the tabular, subch1 means bitsring '10'.
        size2                               SEQUENCE {
            subchannels                         ENUMERATED  { subch0, subch1 }  OPTIONAL
        },
        size4                               SEQUENCE {
```

```
            subchannels                         BIT STRING  {
                                                    subCh3(0),
                                                    subCh2(1),
                                                    subCh1(2),
                                                    subCh0(3)
                                                    } (SIZE(4))     OPTIONAL
        },
        size8                                   SEQUENCE {
            subchannels                         BIT STRING  {
                                                    subCh7(0),
                                                    subCh6(1),
                                                    subCh5(2),
                                                    subCh4(3),
                                                    subCh3(4),
                                                    subCh2(5),
                                                    subCh1(6),
                                                    subCh0(7)
                                                    } (SIZE(8))     OPTIONAL
        }
    }
}

AccessServiceClass-TDD-LCR-r4 ::=   SEQUENCE {
    availableSYNC-UlCodesIndics         BIT STRING {
                                            sulCodeIndex7(0),
                                            sulCodeIndex6(1),
                                            sulCodeIndex5(2),
                                            sulCodeIndex4(3),
                                            sulCodeIndex3(4),
                                            sulCodeIndex2(5),
                                            sulCodeIndex1(6),
                                            sulCodeIndex0(7)
                                            } (SIZE(8))               OPTIONAL,
    subchannelSize                      CHOICE {
        size1                               NULL,
-- in size2, subch0 means bitstring '01' in the tabular, subch1 means bitsring '10'.
        size2                               SEQUENCE {
            subchannels                         ENUMERATED  { subch0, subch1 }  OPTIONAL
        },
        size4                               SEQUENCE {
            subchannels                         BIT STRING  {
                                                    subCh3(0),
                                                    subCh2(1),
                                                    subCh1(2),
                                                    subCh0(3)
                                                    } (SIZE(4))         OPTIONAL
        },
        size8                               SEQUENCE {
            subchannels                         BIT STRING  {
                                                    subCh7(0),
                                                    subCh6(1),
                                                    subCh5(2),
                                                    subCh4(3),
                                                    subCh3(4),
                                                    subCh2(5),
                                                    subCh1(6),
                                                    subCh0(7)
                                                    } (SIZE(8))         OPTIONAL
        }
    }

}

AICH-Info ::=                       SEQUENCE {
    channelisationCode256               ChannelisationCode256,
    sttd-Indicator                      BOOLEAN,
    aich-TransmissionTiming             AICH-TransmissionTiming
}

AICH-PowerOffset ::=                INTEGER (-22..5)

AICH-TransmissionTiming ::=         ENUMERATED {
                                        e0, e1 }

AllocationPeriodInfo ::=            SEQUENCE {
    allocationActivationTime            INTEGER (0..255),
    allocationDuration                  INTEGER (1..256)
}
```

```
-- Actual value = IE value * 0.125
Alpha ::=                           INTEGER (0..8)

AP-AICH-ChannelisationCode ::=      INTEGER (0..255)

AP-PreambleScramblingCode ::=       INTEGER (0..79)

AP-Signature ::=                    INTEGER (0..15)

AP-Signature-VCAM ::=               SEQUENCE {
    ap-Signature                        AP-Signature,
    availableAP-SubchannelList          AvailableAP-SubchannelList OPTIONAL
}

AP-Subchannel ::=                   INTEGER (0..11)

ASCSetting-FDD ::=                      SEQUENCE {
    -- TABULAR: This is MD in tabular description
    -- Default value is previous ASC
    -- If this is the first ASC, the default value is all available signature and sub-channels
    accessServiceClass-FDD                  AccessServiceClass-FDD  OPTIONAL
}

ASCSetting-TDD ::=                      SEQUENCE {
    -- TABULAR: This is MD in tabular description
    -- Default value is previous ASC
    -- If this is the first ASC, the default value is all available channelisation codes and
    -- all available sub-channels with subchannelSize=size1.
    accessServiceClass-TDD                  AccessServiceClass-TDD  OPTIONAL
}

ASCSetting-TDD-LCR-r4 ::=                       SEQUENCE {
    -- TABULAR: This is MD in tabular description
    -- Default value is previous ASC
    -- If this is the first ASC, the default value is all available SYNC_UL codes and
    -- all available sub-channels with subchannelSize=size1.
    accessServiceClass-TDD-LCR                  AccessServiceClass-TDD-LCR-r4   OPTIONAL
}

AvailableAP-Signature-VCAMList ::=  SEQUENCE (SIZE (1..maxPCPCH-APsig)) OF
                                        AP-Signature-VCAM

AvailableAP-SignatureList ::=       SEQUENCE (SIZE (1..maxPCPCH-APsig)) OF
                                        AP-Signature

AvailableAP-SubchannelList ::=      SEQUENCE (SIZE (1..maxPCPCH-APsubCh)) OF
                                        AP-Subchannel

AvailableMinimumSF-ListVCAM ::=     SEQUENCE (SIZE (1..maxPCPCH-SF)) OF
                                        AvailableMinimumSF-VCAM

AvailableMinimumSF-VCAM ::=         SEQUENCE {
    minimumSpreadingFactor              MinimumSpreadingFactor,
    nf-Max                              NF-Max,
    maxAvailablePCPCH-Number            MaxAvailablePCPCH-Number,
    availableAP-Signature-VCAMList      AvailableAP-Signature-VCAMList
}

AvailableSignatures ::=         BIT STRING {
                                    signature15(0),
                                    signature14(1),
                                    signature13(2),
                                    signature12(3),
                                    signature11(4),
                                    signature10(5),
                                    signature9(6),
                                    signature8(7),
                                    signature7(8),
                                    signature6(9),
                                    signature5(10),
                                    signature4(11),
                                    signature3(12),
                                    signature2(13),
                                    signature1(14),
                                    signature0(15)
                                    }    (SIZE(16))

AvailableSubChannelNumbers ::=      BIT STRING {
```

```
                                    subCh11(0),
                                    subCh10(1),
                                    subCh9(2),
                                    subCh8(3),
                                    subCh7(4),
                                    subCh6(5),
                                    subCh5(6),
                                    subCh4(7),
                                    subCh3(8),
                                    subCh2(9),
                                    subCh1(10),
                                    subCh0(11)
                                    }    (SIZE(12))

BurstType ::=                       ENUMERATED {
                                    short1, long2 }

CCTrCH-PowerControlInfo ::=         SEQUENCE {
    tfcs-Identity                       TFCS-Identity                       OPTIONAL,
    ul-DPCH-PowerControlInfo            UL-DPCH-PowerControlInfo
}

CCTrCH-PowerControlInfo-r4 ::=      SEQUENCE {
    tfcs-Identity                       TFCS-Identity                       OPTIONAL,
    ul-DPCH-PowerControlInfo            UL-DPCH-PowerControlInfo-r4
}

CD-AccessSlotSubchannel ::=         INTEGER (0..11)

CD-AccessSlotSubchannelList ::=     SEQUENCE (SIZE (1..maxPCPCH-CDsubCh)) OF
                                    CD-AccessSlotSubchannel

CD-CA-ICH-ChannelisationCode ::=    INTEGER (0..255)

CD-PreambleScramblingCode ::=       INTEGER (0..79)

CD-SignatureCode ::=                INTEGER (0..15)

CD-SignatureCodeList ::=            SEQUENCE (SIZE (1..maxPCPCH-CDsig)) OF
                                    CD-SignatureCode

CellAndChannelIdentity ::=          SEQUENCE {
    burstType                           BurstType,
    midambleShift                       MidambleShiftLong,
    timeslot                            TimeslotNumber,
    cellParametersID                    CellParametersID
    }

CellParametersID ::=                INTEGER (0..127)

Cfntargetsfnframeoffset ::=             INTEGER(0..255)

ChannelAssignmentActive ::=         CHOICE {
    notActive                           NULL,
    isActive                            AvailableMinimumSF-ListVCAM
}

ChannelisationCode256 ::=           INTEGER (0..255)

ChannelReqParamsForUCSM ::=         SEQUENCE {
    availableAP-SignatureList           AvailableAP-SignatureList,
    availableAP-SubchannelList          AvailableAP-SubchannelList          OPTIONAL
}

ClosedLoopTimingAdjMode ::=         ENUMERATED {
                                    slot1, slot2 }

CodeNumberDSCH ::=                  INTEGER (0..255)

CodeRange ::=                       SEQUENCE {
    pdsch-CodeMapList                   PDSCH-CodeMapList
}

CodeWordSet ::=                     ENUMERATED {
                                    longCWS,
                                    mediumCWS,
                                    shortCWS,
                                    ssdtOff }
```

```
CommonTimeslotInfo ::=              SEQUENCE {
    -- TABULAR: The IE below is MD, but since it can be encoded in a single
    -- bit it is not defined as OPTIONAL.
    secondInterleavingMode              SecondInterleavingMode,
    tfci-Coding                         TFCI-Coding                         OPTIONAL,
    puncturingLimit                     PuncturingLimit,
    repetitionPeriodAndLength           RepetitionPeriodAndLength           OPTIONAL
}

CommonTimeslotInfoSCCPCH ::=        SEQUENCE {
    -- TABULAR: The IE below is MD, but since it can be encoded in a single
    -- bit it is not defined as OPTIONAL.
    secondInterleavingMode              SecondInterleavingMode,
    tfci-Coding                         TFCI-Coding                         OPTIONAL,
    puncturingLimit                     PuncturingLimit,
    repetitionPeriodLengthAndOffset     RepetitionPeriodLengthAndOffset     OPTIONAL
}

ConstantValue ::=                   INTEGER (-35..-10)

CPCH-PersistenceLevels ::=          SEQUENCE {
    cpch-SetID                          CPCH-SetID,
    dynamicPersistenceLevelTF-List      DynamicPersistenceLevelTF-List
}

CPCH-PersistenceLevelsList ::=      SEQUENCE (SIZE (1..maxCPCHsets)) OF
                                        CPCH-PersistenceLevels

CPCH-SetInfo ::=                    SEQUENCE {
    cpch-SetID                          CPCH-SetID,
    transportFormatSet                  TransportFormatSet,
    tfcs                                TFCS,
    ap-PreambleScramblingCode           AP-PreambleScramblingCode,
    ap-AICH-ChannelisationCode          AP-AICH-ChannelisationCode,
    cd-PreambleScramblingCode           CD-PreambleScramblingCode,
    cd-CA-ICH-ChannelisationCode        CD-CA-ICH-ChannelisationCode,
    cd-AccessSlotSubchannelList         CD-AccessSlotSubchannelList         OPTIONAL,
    cd-SignatureCodeList                CD-SignatureCodeList                OPTIONAL,
    deltaPp-m                           DeltaPp-m,
    ul-DPCCH-SlotFormat                 UL-DPCCH-SlotFormat,
    n-StartMessage                      N-StartMessage,
    n-EOT                               N-EOT,
    channelAssignmentActive             ChannelAssignmentActive,
    -- TABULAR: VCAM info has been nested inside ChannelAssignmentActive,
    -- which in turn is mandatory since it's only a binary choice.
    cpch-StatusIndicationMode           CPCH-StatusIndicationMode,
    pcpch-ChannelInfoList               PCPCH-ChannelInfoList
}

CPCH-SetInfoList ::=                SEQUENCE (SIZE (1..maxCPCHsets)) OF
                                        CPCH-SetInfo

CPCH-StatusIndicationMode ::=       ENUMERATED {
                                        pa-mode,
                                        pamsf-mode }

CSICH-PowerOffset ::=               INTEGER (-10..5)

-- DefaultDPCH-OffsetValueFDD and DefaultDPCH-OffsetValueTDD corresponds to
-- IE "Default DPCH Offset Value" depending on the mode.
-- Actual value = IE value * 512
DefaultDPCH-OffsetValueFDD ::=      INTEGER (0..599)

DefaultDPCH-OffsetValueTDD ::=      INTEGER (0..7)

DeltaPp-m ::=                       INTEGER (-10..10)

-- Actual value = IE value * 0.1
DeltaSIR ::=                        INTEGER (0..30)

DL-CCTrCh ::=                       SEQUENCE {
    tfcs-ID                             TFCS-IdentityPlain                  DEFAULT 1,
    timeInfo                            TimeInfo,
    commonTimeslotInfo                  CommonTimeslotInfo                  OPTIONAL,
    dl-CCTrCH-TimeslotsCodes            DownlinkTimeslotsCodes              OPTIONAL,
    ul-CCTrChTPCList                    UL-CCTrChTPCList                    OPTIONAL
}
```

```
DL-CCTrCh-r4 ::=                    SEQUENCE {
    tfcs-ID                             TFCS-IdentityPlain                  DEFAULT 1,
    timeInfo                            TimeInfo,
    commonTimeslotInfo                  CommonTimeslotInfo                  OPTIONAL,
    tddOption                           CHOICE {
        tdd384                              SEQUENCE {
            dl-CCTrCH-TimeslotsCodes            DownlinkTimeslotsCodes  OPTIONAL
        },
        tdd128                              SEQUENCE {
            dl-CCTrCH-TimeslotsCodes            DownlinkTimeslotsCodes-LCR-r4   OPTIONAL
        }
    },
    ul-CCTrChTPCList                    UL-CCTrChTPCList        OPTIONAL
}

DL-CCTrChList ::=                   SEQUENCE (SIZE (1..maxCCTrCH)) OF
                                        DL-CCTrCh

DL-CCTrChList-r4 ::=                SEQUENCE (SIZE (1..maxCCTrCH)) OF
                                        DL-CCTrCh-r4

DL-CCTrChTPCList ::=                SEQUENCE (SIZE (0..maxCCTrCH)) OF
                                            TFCS-Identity

DL-ChannelisationCode ::=           SEQUENCE {
    secondaryScramblingCode             SecondaryScramblingCode             OPTIONAL,
    sf-AndCodeNumber                    SF512-AndCodeNumber,
    scramblingCodeChange                ScramblingCodeChange                OPTIONAL
}

DL-ChannelisationCodeList ::=       SEQUENCE (SIZE (1..maxDPCH-DLchan)) OF
                                        DL-ChannelisationCode

DL-CommonInformation ::=            SEQUENCE {
    dl-DPCH-InfoCommon                  DL-DPCH-InfoCommon          OPTIONAL,
    modeSpecificInfo                    CHOICE {
        fdd                                 SEQUENCE {
            defaultDPCH-OffsetValue             DefaultDPCH-OffsetValueFDD  OPTIONAL,
            dpch-CompressedModeInfo             DPCH-CompressedModeInfo     OPTIONAL,
            tx-DiversityMode                    TX-DiversityMode            OPTIONAL,
            ssdt-Information                    SSDT-Information            OPTIONAL
        },
        tdd                                 SEQUENCE {
            defaultDPCH-OffsetValue             DefaultDPCH-OffsetValueTDD  OPTIONAL
        }
    }
}

DL-CommonInformation-r4 ::=         SEQUENCE {
    dl-DPCH-InfoCommon                  DL-DPCH-InfoCommon          OPTIONAL,
    modeSpecificInfo                    CHOICE {
        fdd                                 SEQUENCE {
            defaultDPCH-OffsetValue             DefaultDPCH-OffsetValueFDD  OPTIONAL,
            dpch-CompressedModeInfo             DPCH-CompressedModeInfo     OPTIONAL,
            tx-DiversityMode                    TX-DiversityMode            OPTIONAL,
            ssdt-Information                    SSDT-Information-r4         OPTIONAL
        },
        tdd                                 SEQUENCE {
            tddOption                           CHOICE {
                tdd384                              NULL,
                tdd128                              SEQUENCE {
                    tstd-Indicator                      BOOLEAN
                }
            },
            defaultDPCH-OffsetValue             DefaultDPCH-OffsetValueTDD  OPTIONAL
        }
    }
}

DL-CommonInformationPost ::=        SEQUENCE {
    dl-DPCH-InfoCommon                  DL-DPCH-InfoCommonPost
}

DL-CommonInformationPredef ::=      SEQUENCE {
    dl-DPCH-InfoCommon                  DL-DPCH-InfoCommonPredef    OPTIONAL
}
```

```
DL-CompressedModeMethod ::=         ENUMERATED {
                                        puncturing, sf-2,
                                        higherLayerScheduling }

DL-DPCH-InfoCommon ::=              SEQUENCE {
    cfnHandling                         CHOICE {
        maintain                            NULL,
        initialise                          SEQUENCE {
            cfntargetsfnframeoffset             Cfntargetsfnframeoffset             OPTIONAL
        }
    },
    modeSpecificInfo                    CHOICE {
        fdd                                 SEQUENCE {
            dl-DPCH-PowerControlInfo            DL-DPCH-PowerControlInfo            OPTIONAL,
            powerOffsetPilot-pdpdch             PowerOffsetPilot-pdpdch,
            dl-rate-matching-restriction        Dl-rate-matching-restriction        OPTIONAL,
            spreadingFactorAndPilot             SF512-AndPilot,
    -- TABULAR: The number of pilot bits is nested inside the spreading factor.
            positionFixedOrFlexible             PositionFixedOrFlexible,
            tfci-Existence                      BOOLEAN
        },
        tdd                                 SEQUENCE {
            dl-DPCH-PowerControlInfo            DL-DPCH-PowerControlInfo            OPTIONAL
        }
    }
}

DL-DPCH-InfoCommonPost ::=          SEQUENCE {
    dl-DPCH-PowerControlInfo            DL-DPCH-PowerControlInfo                OPTIONAL
}

DL-DPCH-InfoCommonPredef ::=        SEQUENCE {
    modeSpecificInfo                    CHOICE {
        fdd                                 SEQUENCE {
            spreadingFactorAndPilot             SF512-AndPilot,
    -- TABULAR: The number of pilot bits is nested inside the spreading factor.
            positionFixedOrFlexible             PositionFixedOrFlexible,
            tfci-Existence                      BOOLEAN
        },
        tdd                                 SEQUENCE {
            commonTimeslotInfo                  CommonTimeslotInfo
        }
    }
}

DL-DPCH-InfoPerRL ::=               CHOICE {
    fdd                                 SEQUENCE {
        pCPICH-UsageForChannelEst           PCPICH-UsageForChannelEst,
        dpch-FrameOffset                    DPCH-FrameOffset,
        secondaryCPICH-Info                 SecondaryCPICH-Info             OPTIONAL,
        dl-ChannelisationCodeList           DL-ChannelisationCodeList,
        tpc-CombinationIndex                TPC-CombinationIndex,
        ssdt-CellIdentity                   SSDT-CellIdentity               OPTIONAL,
        closedLoopTimingAdjMode             ClosedLoopTimingAdjMode         OPTIONAL
    },
    tdd                                 DL-CCTrChList
}

DL-DPCH-InfoPerRL-r4 ::=            CHOICE {
    fdd                                 SEQUENCE {
        pCPICH-UsageForChannelEst           PCPICH-UsageForChannelEst,
        dpch-FrameOffset                    DPCH-FrameOffset,
        secondaryCPICH-Info                 SecondaryCPICH-Info             OPTIONAL,
        dl-ChannelisationCodeList           DL-ChannelisationCodeList,
        tpc-CombinationIndex                TPC-CombinationIndex,
        ssdt-CellIdentity                   SSDT-CellIdentity               OPTIONAL,
        closedLoopTimingAdjMode             ClosedLoopTimingAdjMode         OPTIONAL
    },
    tdd                                 DL-CCTrChList-r4
}

DL-DPCH-InfoPerRL-PostFDD ::=                           SEQUENCE {
        pCPICH-UsageForChannelEst               PCPICH-UsageForChannelEst,
        dl-ChannelisationCode                   DL-ChannelisationCode,
        tpc-CombinationIndex                    TPC-CombinationIndex
}

DL-DPCH-InfoPerRL-PostTDD ::=               SEQUENCE {
```

```
    dl-DPCH-TimeslotsCodes                      DownlinkTimeslotsCodes
}

DL-DPCH-InfoPerRL-PostTDD-LCR-r4 ::=        SEQUENCE {
    dl-CCTrCH-TimeslotsCodes                    DownlinkTimeslotsCodes-LCR-r4
}

DL-DPCH-PowerControlInfo ::=            SEQUENCE {
    modeSpecificInfo                        CHOICE {
        fdd                                     SEQUENCE {
            dpc-Mode                                DPC-Mode
        },
        tdd                                     SEQUENCE {
            tpc-StepSizeTDD                         TPC-StepSizeTDD         OPTIONAL
        }
    }
}

DL-FrameType ::=                    ENUMERATED {
                                        dl-FrameTypeA, dl-FrameTypeB }

DL-InformationPerRL ::=             SEQUENCE {
    modeSpecificInfo                    CHOICE {
        fdd                                 SEQUENCE {
            primaryCPICH-Info                   PrimaryCPICH-Info,
            pdsch-SHO-DCH-Info                  PDSCH-SHO-DCH-Info          OPTIONAL,
            pdsch-CodeMapping                   PDSCH-CodeMapping           OPTIONAL
        },
        tdd                                 PrimaryCCPCH-Info
    },
    dl-DPCH-InfoPerRL                   DL-DPCH-InfoPerRL                   OPTIONAL,
    sccpch-InfoforFACH                  SCCPCH-InfoForFACH                  OPTIONAL
}

DL-InformationPerRL-r4 ::=          SEQUENCE {
    modeSpecificInfo                    CHOICE {
        fdd                                 SEQUENCE {
            primaryCPICH-Info                   PrimaryCPICH-Info,
            pdsch-SHO-DCH-Info                  PDSCH-SHO-DCH-Info          OPTIONAL,
            pdsch-CodeMapping                   PDSCH-CodeMapping           OPTIONAL
        },
        tdd                                 PrimaryCCPCH-Info-r4
    },
    dl-DPCH-InfoPerRL                   DL-DPCH-InfoPerRL-r4                OPTIONAL,
    secondaryCCPCH-Info                 SecondaryCCPCH-Info-r4              OPTIONAL
}

DL-InformationPerRL-List ::=        SEQUENCE (SIZE (1..maxRL)) OF
                                        DL-InformationPerRL

DL-InformationPerRL-List-r4 ::=     SEQUENCE (SIZE (1..maxRL)) OF
                                        DL-InformationPerRL-r4

DL-InformationPerRL-ListPostFDD ::= SEQUENCE (SIZE (1..maxRL)) OF
                                        DL-InformationPerRL-PostFDD

DL-InformationPerRL-PostFDD ::=     SEQUENCE {
            primaryCPICH-Info               PrimaryCPICH-Info,
    dl-DPCH-InfoPerRL                   DL-DPCH-InfoPerRL-PostFDD
}

DL-InformationPerRL-PostTDD ::=     SEQUENCE {
    primaryCCPCH-Info                   PrimaryCCPCH-InfoPost,
    dl-DPCH-InfoPerRL                   DL-DPCH-InfoPerRL-PostTDD
}

DL-InformationPerRL-PostTDD-LCR-r4 ::=  SEQUENCE {
    primaryCCPCH-Info                   PrimaryCCPCH-InfoPostTDD-LCR-r4,
    dl-DPCH-InfoPerRL                   DL-DPCH-InfoPerRL-PostTDD-LCR-r4
}

DL-PDSCH-Information ::=            SEQUENCE {
    pdsch-SHO-DCH-Info                  PDSCH-SHO-DCH-Info                  OPTIONAL,
    pdsch-CodeMapping                   PDSCH-CodeMapping                   OPTIONAL
}

Dl-rate-matching-restriction ::=    SEQUENCE {
    restrictedTrCH-InfoList             RestrictedTrCH-InfoList             OPTIONAL
```

```
}

DL-TS-ChannelisationCode ::=        ENUMERATED {
                                        cc16-1, cc16-2, cc16-3, cc16-4,
                                        cc16-5, cc16-6, cc16-7, cc16-8,
                                        cc16-9, cc16-10, cc16-11, cc16-12,
                                        cc16-13, cc16-14, cc16-15, cc16-16 }

DL-TS-ChannelisationCodesShort ::=  SEQUENCE {
    codesRepresentation                 CHOICE {
        consecutive                         SEQUENCE {
            firstChannelisationCode             DL-TS-ChannelisationCode,
            lastChannelisationCode              DL-TS-ChannelisationCode
        },
        bitmap                              BIT STRING {
                                                chCode16-SF16(0),
                                                chCode15-SF16(1),
                                                chCode14-SF16(2),
                                                chCode13-SF16(3),
                                                chCode12-SF16(4),
                                                chCode11-SF16(5),
                                                chCode10-SF16(6),
                                                chCode9-SF16(7),
                                                chCode8-SF16(8),
                                                chCode7-SF16(9),
                                                chCode6-SF16(10),
                                                chCode5-SF16(11),
                                                chCode4-SF16(12),
                                                chCode3-SF16(13),
                                                chCode2-SF16(14),
                                                chCode1-SF16(15)
                                                }   (SIZE (16))
    }
}

DownlinkAdditionalTimeslots ::=     SEQUENCE {
    parameters                          CHOICE {
        sameAsLast                          SEQUENCE {
            timeslotNumber                      TimeslotNumber
        },
        newParameters                       SEQUENCE {
            individualTimeslotInfo              IndividualTimeslotInfo,
            dl-TS-ChannelisationCodesShort      DL-TS-ChannelisationCodesShort
        }
    }
}

DownlinkAdditionalTimeslots-LCR-r4 ::=  SEQUENCE {
    parameters                          CHOICE {
        sameAsLast                          SEQUENCE {
            timeslotNumber                      TimeslotNumber-LCR-r4
        },
        newParameters                       SEQUENCE {
            individualTimeslotInfo              IndividualTimeslotInfo-LCR-r4,
            dl-TS-ChannelisationCodesShort      DL-TS-ChannelisationCodesShort
        }
    }
}

DownlinkTimeslotsCodes ::=      SEQUENCE {
    firstIndividualTimeslotInfo         IndividualTimeslotInfo,
    dl-TS-ChannelisationCodesShort      DL-TS-ChannelisationCodesShort,
    moreTimeslots                       CHOICE {
        noMore                              NULL,
        additionalTimeslots                 CHOICE {
            consecutive                         INTEGER (1..maxTS-1),
            timeslotList                        SEQUENCE (SIZE (1..maxTS-1)) OF
                                                    DownlinkAdditionalTimeslots
        }
    }
}

DownlinkTimeslotsCodes-LCR-r4 ::=   SEQUENCE {
    firstIndividualTimeslotInfo         IndividualTimeslotInfo-LCR-r4,
    dl-TS-ChannelisationCodesShort      DL-TS-ChannelisationCodesShort,
    moreTimeslots                       CHOICE {
        noMore                              NULL,
        additionalTimeslots                 CHOICE {
```

```
            consecutive                         INTEGER (1..maxTS-LCR-1),
            timeslotList                        SEQUENCE (SIZE (1..maxTS-LCR-1)) OF
                                                    DownlinkAdditionalTimeslots-LCR-r4
        }
    }
}

DPC-Mode ::=                        ENUMERATED {
                                        singleTPC,
                                        tpcTripletInSoft }

-- The actual value of DPCCH power offset is the value of this IE * 2.
DPCCH-PowerOffset ::=               INTEGER (-82..-3)


-- The actual value of DPCCH power offset is the value of this (2 + IE * 4).
DPCCH-PowerOffset2 ::=              INTEGER (-28..-13)

DPCH-CompressedModeInfo ::=         SEQUENCE {
    tgp-SequenceList                    TGP-SequenceList
}

DPCH-CompressedModeStatusInfo ::=   SEQUENCE {
    tgps-Reconfiguration-CFN            TGPS-Reconfiguration-CFN,
    tgp-SequenceShortList                   SEQUENCE (SIZE (1..maxTGPS)) OF
                                            TGP-SequenceShort
}

-- TABULAR: Actual value = IE value * 256
DPCH-FrameOffset::=                 INTEGER (0..149)

DSCH-Mapping ::=                    SEQUENCE {
    maxTFCI-Field2Value                 MaxTFCI-Field2Value,
    spreadingFactor                     SF-PDSCH,
    codeNumber                          CodeNumberDSCH,
    multiCodeInfo                       MultiCodeInfo
}

DSCH-MappingList ::=                SEQUENCE (SIZE (1..maxPDSCH-TFCIgroups)) OF
                                        DSCH-Mapping

DSCH-RadioLinkIdentifier ::=        INTEGER (0..511)

DurationTimeInfo ::=                INTEGER (1..4096)

-- TABULAR : value [Duration = infinite] is the value by default,
-- and is encoded by absence of the full sequence. If the sequence is present,
-- thefield is absent, the default is respectivelyinfinite. Presence of the
-- field absent should not be used, but shall be understood as if the
-- sequence was absent.

DynamicPersistenceLevel ::=         INTEGER (1..8)

DynamicPersistenceLevelList ::=     SEQUENCE (SIZE (1..maxPRACH)) OF
                                        DynamicPersistenceLevel

DynamicPersistenceLevelTF-List ::= SEQUENCE (SIZE (1..maxTF-CPCH)) OF
                                        DynamicPersistenceLevel

FACH-PCH-Information ::=            SEQUENCE {
    transportFormatSet                  TransportFormatSet,
    transportChannelIdentity            TransportChannelIdentity,
    ctch-Indicator                      BOOLEAN
}

FACH-PCH-InformationList ::=        SEQUENCE (SIZE (1..maxFACHPCH)) OF
                                        FACH-PCH-Information

FPACH-Info-r4 ::=                       SEQUENCE {
    timeslot                            TimeslotNumber-PRACH-LCR-r4,
    channelisationCode                  TDD-FPACH-CCode16-r4,
    midambleShiftAndBurstType           MidambleShiftAndBurstType-LCR-r4,
    wi                                  Wi-LCR
}

FrequencyInfo ::=                   SEQUENCE {
    modeSpecificInfo                    CHOICE {
        fdd                                 FrequencyInfoFDD,
```

```
        tdd                                 FrequencyInfoTDD    }
}

FrequencyInfoFDD ::=                SEQUENCE {
    uarfcn-UL                           UARFCN              OPTIONAL,
    uarfcn-DL                           UARFCN
}

FrequencyInfoTDD ::=                SEQUENCE {
    uarfcn-Nt                           UARFCN
}

IndividualTimeslotInfo ::=          SEQUENCE {
    timeslotNumber                      TimeslotNumber,
    tfci-Existence                      BOOLEAN,
    midambleShiftAndBurstType           MidambleShiftAndBurstType
}

IndividualTimeslotInfo-LCR-r4 ::=   SEQUENCE {
    timeslotNumber                      TimeslotNumber-LCR-r4,
    tfci-Existence                      BOOLEAN,
    midambleShiftAndBurstType           MidambleShiftAndBurstType-LCR-r4,
    modulation                          ENUMERATED { mod-QPSK, mod-8PSK },
    ss-TPC-Symbols                      ENUMERATED { zero, one, sixteenOverSF }
}

IndividualTimeslotInfo-LCR-r4-ext ::=       SEQUENCE {
-- timeslotNumber and tfci-Existence is taken from IndividualTimeslotInfo.
-- midambleShiftAndBurstType in IndividualTimeslotInfo shall be ignored.
    midambleShiftAndBurstType           MidambleShiftAndBurstType-LCR-r4,
    modulation                          ENUMERATED { mod-QPSK, mod-8PSK },
    ss-TPC-Symbols                      ENUMERATED { zero, one, sixteenOverSF }
}

IndividualTS-Interference ::=       SEQUENCE {
    timeslot                            TimeslotNumber,
    ul-TimeslotInterference             UL-Interference
}

IndividualTS-Interference-LCR-r4 ::=        SEQUENCE {
    timeslot                            TimeslotNumber-LCR-r4,
    ul-TimeslotInterference             UL-Interference
}

IndividualTS-InterferenceList ::=   SEQUENCE (SIZE (1..maxTS)) OF
                                        IndividualTS-Interference

IndividualTS-InterferenceList-r4 ::=    CHOICE {
    tdd384                                  SEQUENCE (SIZE (1..maxTS)) OF
                                                IndividualTS-Interference,
    tdd128                                  SEQUENCE (SIZE (1..maxTS-LCR)) OF
                                                IndividualTS-Interference-LCR-r4
}

ITP ::=                             ENUMERATED {
                                        mode0, mode1 }

NidentifyAbort ::=  INTEGER (1..128)

MaxAllowedUL-TX-Power ::=           INTEGER (-50..33)

MaxAvailablePCPCH-Number ::=        INTEGER (1..64)

MaxPowerIncrease-r4 ::=             INTEGER (0..3)

MaxTFCI-Field2Value ::=             INTEGER (1..1023)

MidambleConfigurationBurstType1and3 ::= ENUMERATED {ms4, ms8, ms16}

MidambleConfigurationBurstType2 ::=     ENUMERATED {ms3, ms6}

MidambleShiftAndBurstType ::=       SEQUENCE {
    burstType                           CHOICE {
        type1                               SEQUENCE {
            midambleConfigurationBurstType1and3 MidambleConfigurationBurstType1and3,
            midambleAllocationMode              CHOICE {
                defaultMidamble                     NULL,
                commonMidamble                      NULL,
```

```
                ueSpecificMidamble                  SEQUENCE {
                    midambleShift                       MidambleShiftLong
                }
            }
        },
        type2                               SEQUENCE {
            midambleConfigurationBurstType2     MidambleConfigurationBurstType2,
            midambleAllocationMode              CHOICE {
                defaultMidamble                     NULL,
                commonMidamble                      NULL,
                ueSpecificMidamble                  SEQUENCE {
                    midambleShift                       MidambleShiftShort
                }
            }
        },
        type3                               SEQUENCE {
            midambleConfigurationBurstType1and3 MidambleConfigurationBurstType1and3,
            midambleAllocationMode              CHOICE {
                defaultMidamble                     NULL,
                ueSpecificMidamble                  SEQUENCE {
                    midambleShift                       MidambleShiftLong
                }
            }
        }
    }
}

MidambleShiftAndBurstType-LCR-r4 ::=    SEQUENCE {
    midambleAllocationMode              CHOICE {
        defaultMidamble                     NULL,
        commonMidamble                      NULL,
        ueSpecificMidamble                  SEQUENCE {
            midambleShift                       INTEGER (0..15)
        }
    },
    midambleConfiguration               INTEGER (1..8)   -- Actual value = IE value * 2
}

MidambleShiftLong ::=               INTEGER (0..15)


MidambleShiftShort ::=              INTEGER (0..5)

MinimumSpreadingFactor ::=          ENUMERATED {
                                        sf4, sf8, sf16, sf32,
                                        sf64, sf128, sf256 }

MultiCodeInfo ::=                   INTEGER (1..16)


N-EOT ::=                           INTEGER (0..7)

N-GAP ::=                           ENUMERATED {
                                        f2, f4, f8 }

N-PCH ::=                           INTEGER (1..8)

N-StartMessage ::=                  INTEGER (1..8)

NB01 ::=                            INTEGER (0..50)

NF-Max ::=                          INTEGER (1..64)

NumberOfDPDCH ::=                   INTEGER (1..maxDPDCH-UL)

NumberOfFBI-Bits ::=                INTEGER (1..2)

OpenLoopPowerControl-TDD ::=        SEQUENCE {
    primaryCCPCH-TX-Power               PrimaryCCPCH-TX-Power,
-- The following IEs shall be ignored in 1.28Mcps TDD mode.
    alpha                               Alpha                       OPTIONAL,
    prach-ConstantValue                 ConstantValue,
    dpch-ConstantValue                  ConstantValue,
    pusch-ConstantValue                 ConstantValue               OPTIONAL
}

OpenLoopPowerControl-IPDL-TDD-r4 ::=    SEQUENCE {
    ipdl-alpha                          Alpha,
```

```
    maxPowerIncrease                    MaxPowerIncrease-r4
}

PagingIndicatorLength ::=           ENUMERATED {
                                        pi4, pi8, pi16 }

PC-Preamble ::=                     INTEGER (0..7)

PCP-Length ::=                      ENUMERATED {
                                        as0, as8 }

PCPCH-ChannelInfo ::=               SEQUENCE {
    pcpch-UL-ScramblingCode             INTEGER (0..79),
    pcpch-DL-ChannelisationCode         INTEGER (0..511),
    pcpch-DL-ScramblingCode             SecondaryScramblingCode             OPTIONAL,
    pcp-Length                          PCP-Length,
    ucsm-Info                           UCSM-Info                           OPTIONAL
}

PCPCH-ChannelInfoList ::=           SEQUENCE (SIZE (1..maxPCPCHs)) OF
                                        PCPCH-ChannelInfo

PCPICH-UsageForChannelEst ::=       ENUMERATED {
                                        mayBeUsed,
                                        shallNotBeUsed }

PDSCH-CapacityAllocationInfo ::=    SEQUENCE {
    pdsch-PowerControlInfo              PDSCH-PowerControlInfo              OPTIONAL,
    -- pdsch-PowerControlInfo is conditional on new-configuration branch below, if this
    -- selected the IE is OPTIONAL otherwise it should not be sent
    pdsch-AllocationPeriodInfo          AllocationPeriodInfo,
    tfcs-ID                             TFCS-IdentityPlain                  DEFAULT 1,
    configuration                       CHOICE {
        old-Configuration                   SEQUENCE {
            pdsch-Identity                      PDSCH-Identity
        },
        new-Configuration                   SEQUENCE {
            pdsch-Info                          PDSCH-Info,
            pdsch-Identity                      PDSCH-Identity              OPTIONAL
        }
    }
}

PDSCH-CapacityAllocationInfo-r4 ::= SEQUENCE {
    pdsch-PowerControlInfo              PDSCH-PowerControlInfo              OPTIONAL,
    -- pdsch-PowerControlInfo is conditional on new-configuration branch below, if this
    -- selected the IE is OPTIONAL otherwise it should not be sent
    pdsch-AllocationPeriodInfo          AllocationPeriodInfo,
    tfcs-ID                             TFCS-IdentityPlain                  DEFAULT 1,
    configuration                       CHOICE {
        old-Configuration                   SEQUENCE {
            pdsch-Identity                      PDSCH-Identity
        },
        new-Configuration                   SEQUENCE {
            pdsch-Info                          PDSCH-Info-r4,
            pdsch-Identity                      PDSCH-Identity              OPTIONAL
        }
    }
}

PDSCH-CodeInfo ::=                  SEQUENCE {
    spreadingFactor                     SF-PDSCH,
    codeNumber                          CodeNumberDSCH,
    multiCodeInfo                       MultiCodeInfo
}

PDSCH-CodeInfoList ::=              SEQUENCE (SIZE (1..maxTFCI-2-Combs)) OF
                                        PDSCH-CodeInfo

PDSCH-CodeMap ::=                   SEQUENCE {
    spreadingFactor                     SF-PDSCH,
    multiCodeInfo                       MultiCodeInfo,
    codeNumberStart                     CodeNumberDSCH,
    codeNumberStop                      CodeNumberDSCH
}

PDSCH-CodeMapList ::=               SEQUENCE (SIZE (1..maxPDSCH-TFCIgroups)) OF
                                        PDSCH-CodeMap
```

```
PDSCH-CodeMapping ::=               SEQUENCE {
    dl-ScramblingCode                   SecondaryScramblingCode             OPTIONAL,
    signallingMethod                    CHOICE {
        codeRange                           CodeRange,
        tfci-Range                          DSCH-MappingList,
        explicit-config                     PDSCH-CodeInfoList,
        replace                             ReplacedPDSCH-CodeInfoList
    }
}

PDSCH-Identity ::=                  INTEGER (1..hiPDSCHidentities)


PDSCH-Info ::=                      SEQUENCE {
    tfcs-ID                             TFCS-IdentityPlain                  DEFAULT 1,
    commonTimeslotInfo                  CommonTimeslotInfo                  OPTIONAL,
    pdsch-TimeslotsCodes                DownlinkTimeslotsCodes              OPTIONAL
}

PDSCH-Info-r4 ::=                       SEQUENCE {
    tfcs-ID                             TFCS-IdentityPlain                  DEFAULT 1,
    commonTimeslotInfo                  CommonTimeslotInfo                  OPTIONAL,
    tddOption                           CHOICE {
        tdd384                              SEQUENCE {
            pdsch-TimeslotsCodes                DownlinkTimeslotsCodes      OPTIONAL
        },
        tdd128                              SEQUENCE {
            pdsch-TimeslotsCodes                DownlinkTimeslotsCodes-LCR-r4   OPTIONAL
        }
    }
}

PDSCH-Info-LCR-r4 ::=               SEQUENCE {
    tfcs-ID                             TFCS-IdentityPlain                  DEFAULT 1,
    commonTimeslotInfo                  CommonTimeslotInfo                  OPTIONAL,
    pdsch-TimeslotsCodes                DownlinkTimeslotsCodes-LCR-r4       OPTIONAL
}

PDSCH-PowerControlInfo ::=          SEQUENCE {
    tpc-StepSizeTDD                     TPC-StepSizeTDD                     OPTIONAL,
    ul-CCTrChTPCList                    UL-CCTrChTPCList                    OPTIONAL
}

PDSCH-SHO-DCH-Info ::=              SEQUENCE {
    dsch-RadioLinkIdentifier            DSCH-RadioLinkIdentifier,
    rl-IdentifierList                   RL-IdentifierList                   OPTIONAL
}

PDSCH-SysInfo ::=                   SEQUENCE {
    pdsch-Identity                      PDSCH-Identity,
    pdsch-Info                          PDSCH-Info,
    dsch-TFS                            TransportFormatSet                  OPTIONAL,
    dsch-TFCS                           TFCS                                OPTIONAL
}

PDSCH-SysInfo-LCR-r4 ::=            SEQUENCE {
    pdsch-Identity                      PDSCH-Identity,
    pdsch-Info                          PDSCH-Info-LCR-r4,
    dsch-TFS                            TransportFormatSet                  OPTIONAL,
    dsch-TFCS                           TFCS                                OPTIONAL
}

PDSCH-SysInfoList ::=               SEQUENCE (SIZE (1..maxPDSCH)) OF
                                        PDSCH-SysInfo

PDSCH-SysInfoList-LCR-r4 ::=        SEQUENCE (SIZE (1..maxPDSCH)) OF
                                        PDSCH-SysInfo-LCR-r4

PDSCH-SysInfoList-SFN ::=           SEQUENCE (SIZE (1..maxPDSCH)) OF
                                        SEQUENCE {
    pdsch-SysInfo                           PDSCH-SysInfo,
    sfn-TimeInfo                            SFN-TimeInfo                    OPTIONAL
}

PDSCH-SysInfoList-SFN-LCR-r4 ::=    SEQUENCE (SIZE (1..maxPDSCH)) OF
                                        SEQUENCE {
    pdsch-SysInfo                           PDSCH-SysInfo-LCR-r4,
```

```
    sfn-Timeinfo                        SFN-TimeInfo                    OPTIONAL
}

PersistenceScalingFactor ::=        ENUMERATED {
                                        psf0-9, psf0-8, psf0-7, psf0-6,
                                        psf0-5, psf0-4, psf0-3, psf0-2 }

PersistenceScalingFactorList ::=    SEQUENCE (SIZE (1..maxASCpersist)) OF
                                        PersistenceScalingFactor

PI-CountPerFrame ::=                ENUMERATED {
                                        e18, e36, e72, e144 }

PichChannelisationCodeList-LCR-r4   ::=          SEQUENCE (SIZE (1..2)) OF
                                                    DL-TS-ChannelisationCode

PICH-Info ::=                       CHOICE {
    fdd                                 SEQUENCE {
        channelisationCode256               ChannelisationCode256,
        pi-CountPerFrame                    PI-CountPerFrame,
        sttd-Indicator                      BOOLEAN
    },
    tdd                                 SEQUENCE {
        channelisationCode                  TDD-PICH-CCode                  OPTIONAL,
        timeslot                            TimeslotNumber                  OPTIONAL,
        midambleShiftAndBurstType           MidambleShiftAndBurstType,
        repetitionPeriodLengthOffset        RepPerLengthOffset-PICH         OPTIONAL,
        pagingIndicatorLength               PagingIndicatorLength           DEFAULT pi4,
        n-GAP                               N-GAP                           DEFAULT f4,
        n-PCH                               N-PCH                           DEFAULT 2
    }
}

PICH-Info-LCR-r4 ::=                SEQUENCE {
    timeslot                            TimeslotNumber-LCR-r4               OPTIONAL,
    pichChannelisationCodeList-LCR-r4   PichChannelisationCodeList-LCR-r4,
    midambleShiftAndBurstType           MidambleShiftAndBurstType-LCR-r4,
    repetitionPeriodLengthOffset        RepPerLengthOffset-PICH             OPTIONAL,
    pagingIndicatorLength               PagingIndicatorLength           DEFAULT pi4,
    n-GAP                               N-GAP                           DEFAULT f4,
    n-PCH                               N-PCH                           DEFAULT 2
}

PICH-PowerOffset ::=                INTEGER (-10..5)

PilotBits128 ::=                    ENUMERATED {
                                        pb4, pb8 }

PilotBits256 ::=                    ENUMERATED {
                                        pb2, pb4, pb8 }

PositionFixedOrFlexible ::=         ENUMERATED {
                                        fixed,
                                        flexible }

PowerControlAlgorithm ::=           CHOICE {
    algorithm1                          TPC-StepSizeFDD,
    algorithm2                          NULL
}

PowerOffsetPilot-pdpdch ::=         INTEGER (0..24)

PowerRampStep ::=                   INTEGER (1..8)

PRACH-ChanCodes-LCR-r4 ::=          SEQUENCE (SIZE (1..4)) OF
                                        TDD-PRACH-CCode-LCR-r4

PRACH-Definition-LCR-r4 ::=         SEQUENCE {
    timeslot                            TimeslotNumber-PRACH-LCR-r4,
    prach-ChanCodes-LCR                 PRACH-ChanCodes-LCR-r4,
    midambleShiftAndBurstType           MidambleShiftAndBurstType-LCR-r4,
    fpach-Info                          FPACH-Info-r4
}

PRACH-Midamble ::=                  ENUMERATED {
                                        direct,
                                        direct-Inverted }
```

```
PRACH-Partitioning ::=              CHOICE {
    fdd                                 SEQUENCE (SIZE (1..maxASC)) OF
                                            ASCSetting-FDD,
    tdd                                 SEQUENCE (SIZE (1..maxASC)) OF
                                            ASCSetting-TDD
}

PRACH-Partitioning-LCR-r4 ::=       SEQUENCE (SIZE (1..maxASC)) OF
                                            ASCSetting-TDD-LCR-r4

PRACH-PowerOffset ::=               SEQUENCE {
    powerRampStep                       PowerRampStep,
    preambleRetransMax                  PreambleRetransMax
}

PRACH-RACH-Info ::=                 SEQUENCE {
    modeSpecificInfo                    CHOICE {
        fdd                                 SEQUENCE {
            availableSignatures                 AvailableSignatures,
            availableSF                         SF-PRACH,
            preambleScramblingCodeWordNumber    PreambleScramblingCodeWordNumber,
            puncturingLimit                     PuncturingLimit,
            availableSubChannelNumbers          AvailableSubChannelNumbers
        },
        tdd                                 SEQUENCE {
            timeslot                            TimeslotNumber,
            channelisationCodeList              TDD-PRACH-CCodeList,
            prach-Midamble                      PRACH-Midamble
        }
    }
}

PRACH-RACH-Info-LCR-r4 ::=          SEQUENCE {
    sync-UL-Info                        SYNC-UL-Info-r4,
    prach-DefinitionList                SEQUENCE (SIZE (1..maxPRACH-FPACH)) OF
                                            PRACH-Definition-LCR-r4
}

PRACH-SystemInformation ::=         SEQUENCE {
    prach-RACH-Info                     PRACH-RACH-Info,
    transportChannelIdentity            TransportChannelIdentity,
    rach-TransportFormatSet             TransportFormatSet                  OPTIONAL,
    rach-TFCS                           TFCS                                OPTIONAL,
    prach-Partitioning                  PRACH-Partitioning                  OPTIONAL,
    persistenceScalingFactorList        PersistenceScalingFactorList        OPTIONAL,
    ac-To-ASC-MappingTable              AC-To-ASC-MappingTable              OPTIONAL,
    modeSpecificInfo                    CHOICE {
        fdd                                 SEQUENCE {
            primaryCPICH-TX-Power               PrimaryCPICH-TX-Power       OPTIONAL,
            constantValue                       ConstantValue               OPTIONAL,
            prach-PowerOffset                   PRACH-PowerOffset           OPTIONAL,
            rach-TransmissionParameters         RACH-TransmissionParameters OPTIONAL,
            aich-Info                           AICH-Info                   OPTIONAL
        },
        tdd                                 NULL
    }
}

PRACH-SystemInformationList ::=     SEQUENCE (SIZE (1..maxPRACH)) OF
                                        PRACH-SystemInformation

PreambleRetransMax ::=              INTEGER (1..64)

PreambleScramblingCodeWordNumber ::=    INTEGER (0..15)

PreDefPhyChConfiguration ::=        SEQUENCE {
    ul-DPCH-InfoPredef                  UL-DPCH-InfoPredef,
    dl-CommonInformationPredef          DL-CommonInformationPredef  OPTIONAL
}

PrimaryCCPCH-Info ::=               CHOICE {
    fdd                                 SEQUENCE {
        tx-DiversityIndicator               BOOLEAN
    },
    tdd                                 SEQUENCE {
        -- syncCase should be absent for 1.28Mcps TDD mode
        syncCase                            CHOICE {
            syncCase1                           SEQUENCE {
```

```
                timeslot                            TimeslotNumber
            },
            syncCase2                           SEQUENCE {
                timeslotSync2                       TimeslotSync2
            }
        }                                                                   OPTIONAL,
        cellParametersID                CellParametersID                    OPTIONAL,
        blockSTTD-Indicator             BOOLEAN
    }
}

PrimaryCCPCH-Info-r4 ::=            CHOICE {
    fdd                                 SEQUENCE {
        tx-DiversityIndicator               BOOLEAN
    },
    tdd                                 SEQUENCE {
        tddOption                           CHOICE {
            tdd384                              SEQUENCE {
                syncCase                            CHOICE {
                    syncCase1                           SEQUENCE {
                        timeslot                            TimeslotNumber
                    },
                    syncCase2                           SEQUENCE {
                        timeslotSync2                       TimeslotSync2
                    }
                }                                                           OPTIONAL
            },
            tdd128                              SEQUENCE {
                tstd-Indicator                      BOOLEAN
            }
        },
        cellParametersID                CellParametersID                    OPTIONAL,
        blockSTTD-Indicator             BOOLEAN
    }
}

PrimaryCCPCH-Info-LCR-r4 ::=        SEQUENCE {
    tstd-Indicator                      BOOLEAN,
    cellParametersID                    CellParametersID                OPTIONAL,
    blockSTTD-Indicator                 BOOLEAN
}

-- For 1.28Mcps TDD, the following IE includes elements for the PCCPCH Info additional to those
-- in PrimaryCCPCH-Info
PrimaryCCPCH-Info-LCR-r4-ext ::=    SEQUENCE {
    tstd-Indicator                      BOOLEAN
}

PrimaryCCPCH-InfoPost ::=           SEQUENCE {
    syncCase                            CHOICE {
        syncCase1                           SEQUENCE {
            timeslot                            TimeslotNumber
        },
        syncCase2                           SEQUENCE {
            timeslotSync2                       TimeslotSync2
        }
    },
    cellParametersID                    CellParametersID,
    blockSTTD-Indicator                 BOOLEAN
}

PrimaryCCPCH-InfoPostTDD-LCR-r4 ::= SEQUENCE {
    tstd-Indicator                      BOOLEAN,
    cellParametersID                    CellParametersID,
    blockSTTD-Indicator                 BOOLEAN
}

PrimaryCCPCH-TX-Power ::=           INTEGER (6..43)

PrimaryCPICH-Info ::=               SEQUENCE {
    primaryScramblingCode               PrimaryScramblingCode
}

PrimaryCPICH-TX-Power ::=           INTEGER (-10..50)

PrimaryScramblingCode ::=           INTEGER (0..511)

PuncturingLimit ::=                 ENUMERATED {
```

```
                                        pl0-40, pl0-44, pl0-48, pl0-52, pl0-56,
                                        pl0-60, pl0-64, pl0-68, pl0-72, pl0-76,
                                        pl0-80, pl0-84, pl0-88, pl0-92, pl0-96, pl1 }

PUSCH-CapacityAllocationInfo ::=    SEQUENCE {
    pusch-Allocation                    CHOICE {
        pusch-AllocationPending             NULL,
        pusch-AllocationAssignment          SEQUENCE {
            pusch-AllocationPeriodInfo          AllocationPeriodInfo,
            pusch-PowerControlInfo              UL-TargetSIR                OPTIONAL,
            tfcs-ID                             TFCS-IdentityPlain          DEFAULT 1,
            configuration                       CHOICE {
                old-Configuration                   SEQUENCE {
                    pusch-Identity                      PUSCH-Identity
                },
                new-Configuration                   SEQUENCE {
                    pusch-Info                          PUSCH-Info,
                    pusch-Identity                      PUSCH-Identity      OPTIONAL
                }
            }
        }
    }
}

PUSCH-CapacityAllocationInfo-r4 ::= SEQUENCE {
    pusch-Allocation                    CHOICE {
        pusch-AllocationPending             NULL,
        pusch-AllocationAssignment          SEQUENCE {
            pusch-AllocationPeriodInfo          AllocationPeriodInfo,
            pusch-PowerControlInfo              PUSCH-PowerControlInfo-r4   OPTIONAL,
            tfcs-Identity                       TFCS-IdentityPlain          OPTIONAL,
            configuration                       CHOICE {
                old-Configuration                   SEQUENCE {
                    pusch-Identity                      PUSCH-Identity
                },
                new-Configuration                   SEQUENCE {
                    pusch-Info                          PUSCH-Info-r4,
                    pusch-Identity                      PUSCH-Identity      OPTIONAL
                }
            }
        }
    }
}

PUSCH-Identity ::=                  INTEGER (1..hiPUSCHidentities)

PUSCH-Info ::=                      SEQUENCE {
    tfcs-ID                             TFCS-IdentityPlain                  DEFAULT 1,
    commonTimeslotInfo                  CommonTimeslotInfo                  OPTIONAL,
    pusch-TimeslotsCodes                UplinkTimeslotsCodes                OPTIONAL
}

PUSCH-Info-r4 ::=                   SEQUENCE {
    tfcs-ID                             TFCS-IdentityPlain                  DEFAULT 1,
    commonTimeslotInfo                  CommonTimeslotInfo                  OPTIONAL,
    tddOption                           CHOICE {
        tdd384                              SEQUENCE {
            pusch-TimeslotsCodes                UplinkTimeslotsCodes        OPTIONAL
        },
        tdd128                              SEQUENCE {
            pusch-TimeslotsCodes                UplinkTimeslotsCodes-LCR-r4 OPTIONAL
        }
    }
}

PUSCH-Info-LCR-r4 ::=               SEQUENCE {
    tfcs-ID                             TFCS-IdentityPlain                  DEFAULT 1,

    commonTimeslotInfo                  CommonTimeslotInfo                  OPTIONAL,
    pusch-TimeslotsCodes                UplinkTimeslotsCodes-LCR-r4         OPTIONAL
}

PUSCH-PowerControlInfo-r4 ::=       SEQUENCE {
    ul-TargetSIR                        UL-TargetSIR,
    tddOption                           CHOICE {
        tdd384                              NULL,
        tdd128                              SEQUENCE {
            tpc-StepSize                        TPC-StepSizeTDD             OPTIONAL,
```

```
            dl-CCTrChTPCList                    DL-CCTrChTPCList            OPTIONAL
        }
    }
}

PUSCH-SysInfo ::=                   SEQUENCE {
    pusch-Identity                      PUSCH-Identity,
    pusch-Info                          PUSCH-Info,
    usch-TFS                            TransportFormatSet                  OPTIONAL,
    usch-TFCS                           TFCS                                OPTIONAL
}

PUSCH-SysInfo-LCR-r4 ::=            SEQUENCE {
    pusch-Identity                      PUSCH-Identity,
    pusch-Info                          PUSCH-Info-LCR-r4,
    usch-TFS                            TransportFormatSet                  OPTIONAL,
    usch-TFCS                           TFCS                                OPTIONAL
}

PUSCH-SysInfoList ::=               SEQUENCE (SIZE (1..maxPUSCH)) OF
                                        PUSCH-SysInfo

PUSCH-SysInfoList-LCR-r4 ::=        SEQUENCE (SIZE (1..maxPUSCH)) OF
                                        PUSCH-SysInfo-LCR-r4

PUSCH-SysInfoList-SFN ::=           SEQUENCE (SIZE (1..maxPUSCH)) OF
                                        SEQUENCE {
    pusch-SysInfo                           PUSCH-SysInfo,
    sfn-TimeInfo                            SFN-TimeInfo                    OPTIONAL
}

PUSCH-SysInfoList-SFN-LCR-r4 ::=        SEQUENCE (SIZE (1..maxPDSCH)) OF
                                        SEQUENCE {
    pusch-SysInfo                           PUSCH-SysInfo-LCR-r4,
    sfn-TimeInfo                            SFN-TimeInfo                    OPTIONAL
}

RACH-TransmissionParameters ::=     SEQUENCE {
    mmax                                INTEGER (1..32),
    nb01Min                             NB01,
    nb01Max                             NB01
}

ReducedScramblingCodeNumber ::=     INTEGER (0..8191)

RepetitionPeriodAndLength ::=       CHOICE {
    repetitionPeriod1                   NULL,
    repetitionPeriod2                   INTEGER (1..1),
    -- repetitionPeriod2 could just as well be NULL also.
    repetitionPeriod4                   INTEGER (1..3),
    repetitionPeriod8                   INTEGER (1..7),
    repetitionPeriod16                  INTEGER (1..15),
    repetitionPeriod32                  INTEGER (1..31),
    repetitionPeriod64                  INTEGER (1..63)
}

RepetitionPeriodLengthAndOffset ::= CHOICE {
    repetitionPeriod1                   NULL,
    repetitionPeriod2                   SEQUENCE {
        length                              NULL,
        offset                              INTEGER (0..1)
    },
    repetitionPeriod4                   SEQUENCE {
        length                              INTEGER (1..3),
        offset                              INTEGER (0..3)
    },
    repetitionPeriod8                   SEQUENCE {
        length                              INTEGER (1..7),
        offset                              INTEGER (0..7)
    },
    repetitionPeriod16                  SEQUENCE {
        length                              INTEGER (1..15),
        offset                              INTEGER (0..15)
    },
    repetitionPeriod32                  SEQUENCE {
        length                              INTEGER (1..31),
        offset                              INTEGER (0..31)
    },
```

```
    repetitionPeriod64                  SEQUENCE {
        length                              INTEGER (1..63),
        offset                              INTEGER (0..63)
    }
}

ReplacedPDSCH-CodeInfo ::=          SEQUENCE {
    tfci-Field2                         MaxTFCI-Field2Value,
    spreadingFactor                     SF-PDSCH,
    codeNumber                          CodeNumberDSCH,
    multiCodeInfo                       MultiCodeInfo
}

ReplacedPDSCH-CodeInfoList ::=      SEQUENCE (SIZE (1..maxTFCI-2-Combs)) OF
                                        ReplacedPDSCH-CodeInfo

RepPerLengthOffset-PICH ::=         CHOICE {
    rpp4-2                              INTEGER (0..3),
    rpp8-2                              INTEGER (0..7),
    rpp8-4                              INTEGER (0..7),
    rpp16-2                             INTEGER (0..15),
    rpp16-4                             INTEGER (0..15),
    rpp32-2                             INTEGER (0..31),
    rpp32-4                             INTEGER (0..31),
    rpp64-2                             INTEGER (0..63),
    rpp64-4                             INTEGER (0..63)
}

RestrictedTrCH ::=                  SEQUENCE {
    dl-restrictedTrCh-Type              DL-TrCH-Type,
    restrictedDL-TrCH-Identity          TransportChannelIdentity,
    allowedTFIList                      AllowedTFI-List
}

RestrictedTrCH-InfoList ::=         SEQUENCE (SIZE(1..maxTrCH)) OF
                                        RestrictedTrCH

RL-AdditionInformation ::=          SEQUENCE {
    primaryCPICH-Info                   PrimaryCPICH-Info,
    dl-DPCH-InfoPerRL                   DL-DPCH-InfoPerRL,
    tfci-CombiningIndicator             BOOLEAN,
    sccpch-InfoforFACH                  SCCPCH-InfoForFACH                  OPTIONAL
}

RL-AdditionInformationList ::=      SEQUENCE (SIZE (1..maxRL-1)) OF
                                        RL-AdditionInformation

RL-IdentifierList ::=               SEQUENCE (SIZE (1..maxRL)) OF
                                        PrimaryCPICH-Info

RL-RemovalInformationList ::=       SEQUENCE (SIZE (1..maxRL)) OF
                                        PrimaryCPICH-Info

RPP ::=                             ENUMERATED {
                                        mode0, mode1 }

S-Field ::=                         ENUMERATED {
                                        e1bit, e2bits }

SCCPCH-ChannelisationCode ::=       ENUMERATED {
                                        cc16-1, cc16-2, cc16-3, cc16-4,
                                        cc16-5, cc16-6, cc16-7, cc16-8,
                                        cc16-9, cc16-10, cc16-11, cc16-12,
                                        cc16-13, cc16-14, cc16-15, cc16-16 }

SCCPCH-ChannelisationCodeList ::=   SEQUENCE (SIZE (1..16)) OF
                                        SCCPCH-ChannelisationCode

SCCPCH-InfoForFACH ::=              SEQUENCE {
    secondaryCCPCH-Info                 SecondaryCCPCH-Info,
    tfcs                                TFCS,
    modeSpecificInfo                CHOICE {
        fdd                             SEQUENCE {
            fach-PCH-InformationList            FACH-PCH-InformationList,
            sib-ReferenceListFACH               SIB-ReferenceListFACH
        },
        tdd                             SEQUENCE {
            fach-PCH-InformationList            FACH-PCH-InformationList
```

```
        }
    }
}

SCCPCH-SystemInformation ::=        SEQUENCE {
    secondaryCCPCH-Info                 SecondaryCCPCH-Info,
    tfcs                                TFCS                                OPTIONAL,
    fach-PCH-InformationList            FACH-PCH-InformationList            OPTIONAL,
    pich-Info                           PICH-Info                           OPTIONAL
}

SCCPCH-SystemInformation-LCR-r4-ext ::= SEQUENCE {
    secondaryCCPCH-LCR-Extensions   SecondaryCCPCH-Info-LCR-r4-ext,
-- pich-Info in the SCCPCH-SystemInformation IE shall be absent, and instead the following used.
    pich-Info                       PICH-Info-LCR-r4                    OPTIONAL
}

SCCPCH-SystemInformationList ::=    SEQUENCE (SIZE (1..maxSCCPCH)) OF
                                        SCCPCH-SystemInformation

-- The following list includes elements additional to those in
-- SCCPCH-SystemInformationList for the 1.28Mcps TDD. The order of the IEs
-- indicates which SCCPCH-SystemInformation-LCR-r4-ext IE extends which
-- SCCPCH-SystemInformation IE.
SCCPCH-SystemInformationList-LCR-r4-ext ::= SEQUENCE (SIZE (1..maxSCCPCH)) OF
                                            SCCPCH-SystemInformation-LCR-r4-ext

ScramblingCodeChange ::=            ENUMERATED {
                                        codeChange, noCodeChange }

ScramblingCodeType ::=              ENUMERATED {
                                        shortSC,
                                        longSC }

SecondaryCCPCH-Info ::=             SEQUENCE {
    modeSpecificInfo                CHOICE {
        fdd                             SEQUENCE {
            -- This IE is not used in this version of the specification and should be ignored.
            dummy1                          PCPICH-UsageForChannelEst,
            -- This IE is not used in this version of the specification.  It should not
            -- be sent and if received it should be ignored.
            dummy2                          SecondaryCPICH-Info             OPTIONAL,
            secondaryScramblingCode         SecondaryScramblingCode         OPTIONAL,
            sttd-Indicator                  BOOLEAN,
            sf-AndCodeNumber                SF256-AndCodeNumber,
            pilotSymbolExistence            BOOLEAN,
            tfci-Existence                  BOOLEAN,
            positionFixedOrFlexible         PositionFixedOrFlexible,
            timingOffset                    TimingOffset                    DEFAULT 0
        },
        tdd                             SEQUENCE {
            -- TABULAR: the offset is included in CommonTimeslotInfoSCCPCH
            commonTimeslotInfo              CommonTimeslotInfoSCCPCH,
            individualTimeslotInfo          IndividualTimeslotInfo,
            channelisationCode              SCCPCH-ChannelisationCodeList
        }
    }
}

SecondaryCCPCH-Info-r4 ::=      SEQUENCE {
    modeSpecificInfo                CHOICE {
        fdd                             SEQUENCE {
            pCPICH-UsageForChannelEst       PCPICH-UsageForChannelEst,
            secondaryCPICH-Info             SecondaryCPICH-Info             OPTIONAL,
            secondaryScramblingCode         SecondaryScramblingCode         OPTIONAL,
            sttd-Indicator                  BOOLEAN,
            sf-AndCodeNumber                SF256-AndCodeNumber,
            pilotSymbolExistence            BOOLEAN,
            tfci-Existence                  BOOLEAN,
            positionFixedOrFlexible         PositionFixedOrFlexible,
            timingOffset                    TimingOffset                    DEFAULT 0
        },
        tdd                             SEQUENCE {
            -- TABULAR: the offset is included in CommonTimeslotInfoSCCPCH
            commonTimeslotInfo              CommonTimeslotInfoSCCPCH,
            tddOption                       CHOICE {
                tdd384                          SEQUENCE {
                    individualTimeslotInfo          IndividualTimeslotInfo
```

```
                },
                tdd128                          SEQUENCE {
                    individualTimeslotInfo          IndividualTimeslotInfo-LCR-r4
                }
            },
            channelisationCode              SCCPCH-ChannelisationCodeList
        }
    }
}

SecondaryCCPCH-Info-LCR-r4-ext ::=  SEQUENCE {
    individualTimeslotLCR-Ext           IndividualTimeslotInfo-LCR-r4-ext
}

SecondaryCPICH-Info ::=             SEQUENCE {
    secondaryDL-ScramblingCode          SecondaryScramblingCode             OPTIONAL,
    channelisationCode                  ChannelisationCode256
}

SecondaryScramblingCode ::=         INTEGER (1..15)

SecondInterleavingMode ::=          ENUMERATED {
                                        frameRelated, timeslotRelated }

-- SF256-AndCodeNumber encodes both "Spreading factor" and "Code Number"
SF256-AndCodeNumber ::=             CHOICE {
    sf4                                 INTEGER (0..3),
    sf8                                 INTEGER (0..7),
    sf16                                INTEGER (0..15),
    sf32                                INTEGER (0..31),
    sf64                                INTEGER (0..63),
    sf128                               INTEGER (0..127),
    sf256                               INTEGER (0..255)
}

-- SF512-AndCodeNumber encodes both "Spreading factor" and "Code Number"
SF512-AndCodeNumber ::=             CHOICE {
    sf4                                 INTEGER (0..3),
    sf8                                 INTEGER (0..7),
    sf16                                INTEGER (0..15),
    sf32                                INTEGER (0..31),
    sf64                                INTEGER (0..63),
    sf128                               INTEGER (0..127),
    sf256                               INTEGER (0..255),
    sf512                               INTEGER (0..511)
}

-- SF512-AndPilot encodes both "Spreading factor" and "Number of bits for Pilot bits"
SF512-AndPilot ::=                  CHOICE {
    sfd4                                NULL,
    sfd8                                NULL,
    sfd16                               NULL,
    sfd32                               NULL,
    sfd64                               NULL,
    sfd128                              PilotBits128,
    sfd256                              PilotBits256,
    sfd512                              NULL
}
SF-PDSCH ::=                        ENUMERATED {
                                        sfp4, sfp8, sfp16, sfp32,
                                        sfp64, sfp128, sfp256 }

SF-PRACH ::=                        ENUMERATED {
                                        sfpr32, sfpr64, sfpr128, sfpr256 }

SFN-TimeInfo ::=                    SEQUENCE {
    activationTimeSFN                   INTEGER (0..4095),
    physChDuration                  DurationTimeInfo
}

SpecialBurstScheduling ::=              INTEGER (0..7)

SpreadingFactor::=                  ENUMERATED {
                                        sf4, sf8, sf16, sf32,
                                        sf64, sf128, sf256 }

SRB-delay ::=                       INTEGER (0..7)
```

```
SSDT-CellIdentity ::=               ENUMERATED {
                                        ssdt-id-a, ssdt-id-b, ssdt-id-c,
                                        ssdt-id-d, ssdt-id-e, ssdt-id-f,
                                        ssdt-id-g, ssdt-id-h }

SSDT-Information ::=                SEQUENCE {
    s-Field                             S-Field,
    codeWordSet                         CodeWordSet
}

SSDT-Information-r4 ::=             SEQUENCE {
    s-Field                             S-Field,
    codeWordSet                         CodeWordSet,
    ssdt-UL                             SSDT-UL-r4                          OPTIONAL
}

-- The following information element is used to extend the
-- SSDT-Information IE from Release 4 onwards.
SSDT-UL-r4 ::=                      ENUMERATED {
                                        ul, ul-AndDL }

SynchronisationParameters-r4 ::=    SEQUENCE {
    sync-UL-CodesBitmap                 BIT STRING {
                                            code7(0),
                                            code6(1),
                                            code5(2),
                                            code4(3),
                                            code3(4),
                                            code2(5),
                                            code1(6),
                                            code0(7)
                                            } (SIZE (8))                OPTIONAL,
    fpach-Info                          FPACH-Info-r4,
    sync-UL-Procedure                   SYNC-UL-Procedure-r4                OPTIONAL
}

SYNC-UL-Procedure-r4 ::=            SEQUENCE {
    max-SYNC-UL-Transmissions           ENUMERATED { tr1, tr2, tr4, tr8 },
    powerRampingStep                    INTEGER (0..3)
}

SYNC-UL-Info-r4 ::=                 SEQUENCE {
    sync-UL-Codes-Bitmap                BIT STRING {
                                            code7(0),
                                            code6(1),
                                            code5(2),
                                            code4(3),
                                            code3(4),
                                            code2(5),
                                            code1(6),
                                            code0(7)
                                            } ( SIZE (8)),
    prxUpPCHdes                         INTEGER (0..62),
-- Actual value = (IE value * 0.5) - 11
    powerRampingStep                    INTEGER (0..3),
    max-SYNC-UL-Transmissions           ENUMERATED { tr1, tr2, tr4, tr8 } ,
    mmax                                INTEGER(1..32)
}

TDD-FPACH-CCode16-r4 ::=            ENUMERATED {
                                        cc16-1, cc16-2, cc16-3, cc16-4,
                                        cc16-5, cc16-6, cc16-7, cc16-8,
                                        cc16-9, cc16-10, cc16-11, cc16-12,
                                        cc16-13, cc16-14, cc16-15, cc16-16 }

TDD-PICH-CCode ::=                  ENUMERATED {
                                        cc16-1, cc16-2, cc16-3, cc16-4,
                                        cc16-5, cc16-6, cc16-7, cc16-8,
                                        cc16-9, cc16-10, cc16-11, cc16-12,
                                        cc16-13, cc16-14, cc16-15, cc16-16 }

TDD-PRACH-CCode8 ::=                ENUMERATED {
                                        cc8-1, cc8-2, cc8-3, cc8-4,
                                        cc8-5, cc8-6, cc8-7, cc8-8 }

TDD-PRACH-CCode16 ::=               ENUMERATED {
                                        cc16-1, cc16-2, cc16-3, cc16-4,
                                        cc16-5, cc16-6, cc16-7, cc16-8,
```

```
                                        cc16-9, cc16-10, cc16-11, cc16-12,
                                        cc16-13, cc16-14, cc16-15, cc16-16 }

TDD-PRACH-CCode-LCR-r4 ::=          ENUMERATED {
                                        cc4-1, cc4-2, cc4-3, cc4-4,
                                        cc8-1, cc8-2, cc8-3, cc8-4,
                                        cc8-5, cc8-6, cc8-7, cc8-8,
                                        cc16-1, cc16-2, cc16-3, cc16-4,
                                        cc16-5, cc16-6, cc16-7, cc16-8,
                                        cc16-9, cc16-10, cc16-11, cc16-12,
                                        cc16-13, cc16-14, cc16-15, cc16-16 }

TDD-PRACH-CCodeList ::=             CHOICE {
    sf8                                 SEQUENCE (SIZE (1..8)) OF
                                            TDD-PRACH-CCode8,
    sf16                                SEQUENCE (SIZE (1..8)) OF
                                            TDD-PRACH-CCode16
}

TFC-ControlDuration ::=             ENUMERATED {
                                        tfc-cd1, tfc-cd2, tfc-cd4, tfc-cd8,
                                        tfc-cd16, tfc-cd24, tfc-cd32,
                                        tfc-cd48, tfc-cd64, tfc-cd128,
                                        tfc-cd192, tfc-cd256, tfc-cd512 }

TFCI-Coding ::=                     ENUMERATED {
                                        tfci-bits-4, tfci-bits-8,
                                        tfci-bits-16, tfci-bits-32 }

TGCFN ::=                           INTEGER (0..255)

-- The value 270 represents "undefined" in the tabular description.
TGD ::=                             INTEGER (15..270)

TGL ::=                             INTEGER (1..14)

TGMP ::=                            ENUMERATED {
                                        tdd-Measurement, fdd-Measurement,
                                        gsm-CarrierRSSIMeasurement,
                                        gsm-initialBSICIdentification, gsmBSICReconfirmation,
                                        multi-carrier }

TGP-Sequence ::=                    SEQUENCE {
    tgpsi                               TGPSI,
    tgps-Status                         CHOICE {
        activate                            SEQUENCE {
            tgcfn                               TGCFN
        },
        deactivate                          NULL
    },
    tgps-ConfigurationParams            TGPS-ConfigurationParams            OPTIONAL
}

TGPS-Reconfiguration-CFN ::=        INTEGER (0..255)

TGP-SequenceList ::=                SEQUENCE (SIZE (1..maxTGPS)) OF
                                        TGP-Sequence

TGP-SequenceShort ::=               SEQUENCE {
    tgpsi                               TGPSI,
    tgps-Status                         CHOICE {
        activate                            SEQUENCE {
            tgcfn                               TGCFN
        },
        deactivate                          NULL
    }
}

TGPL ::=                            INTEGER (1..144)

-- TABULAR: The value 0 represents "infinity" in the tabular description.
TGPRC ::=                           INTEGER (0..511)

TGPS-ConfigurationParams ::=        SEQUENCE {
    tgmp                                TGMP,
    tgprc                               TGPRC,
    tgsn                                TGSN,
    tgl1                                TGL,
```

```
    tgl2                            TGL                             OPTIONAL,
    tgd                             TGD,
    tgpl1                           TGPL,
    tgpl2                           TGPL                            OPTIONAL,
    rpp                             RPP,
    itp                             ITP,
    ul-DL-Mode                      UL-DL-Mode,
    -- TABULAR: Compressed mode method is nested inside UL-DL-Mode
    dl-FrameType                    DL-FrameType,
    deltaSIR1                       DeltaSIR,
    deltaSIRAfter1                  DeltaSIR,
    deltaSIR2                       DeltaSIR                        OPTIONAL,
    deltaSIRAfter2                  DeltaSIR                        OPTIONAL,
    nidentifyAbort                  NidentifyAbort                  OPTIONAL,
    treconfirmAbort                 TreconfirmAbort                 OPTIONAL
}

TGPSI ::=                           INTEGER (1..maxTGPS)

TGSN ::=                            INTEGER (0..14)

TimeInfo ::=                        SEQUENCE {
    activationTime                      ActivationTime              OPTIONAL,
    durationTimeInfo                    DurationTimeInfo            OPTIONAL
}

TimeslotList ::=                    SEQUENCE (SIZE (1..maxTS)) OF
                                        TimeslotNumber

TimeslotList-r4 ::=                 CHOICE {
    tdd384                              SEQUENCE (SIZE (1..maxTS)) OF
                                            TimeslotNumber,
    tdd128                              SEQUENCE (SIZE (1..maxTS-LCR)) OF
                                            TimeslotNumber-LCR-r4
}

-- If TimeslotNumber is included for a 1.28Mcps TDD description, it shall take values from 0..6
TimeslotNumber ::=                  INTEGER (0..14)

TimeslotNumber-LCR-r4 ::=               INTEGER (0..6)

TimeslotNumber-PRACH-LCR-r4 ::=     INTEGER (1..6)

TimeslotSync2 ::=               INTEGER (0..6)

-- Actual value = IE value * 256
TimingOffset ::=                    INTEGER (0..149)

TPC-CombinationIndex ::=            INTEGER (0..5)

TPC-StepSizeFDD ::=                 INTEGER (0..1)

-- Actual value = IE value + 1

TPC-StepSizeTDD ::=                 INTEGER (1..3)

-- Actual value = IE value * 0.5 seconds
TreconfirmAbort ::= INTEGER (1..20)

TX-DiversityMode ::=                ENUMERATED {
                                        noDiversity,
                                        sttd,
                                        closedLoopMode1,
                                        closedLoopMode2 }

UARFCN ::=                      INTEGER (0..16383)

UCSM-Info ::=                       SEQUENCE {
    minimumSpreadingFactor              MinimumSpreadingFactor,
    nf-Max                              NF-Max,
    channelReqParamsForUCSM             ChannelReqParamsForUCSM
}

UL-CCTrCH ::=                       SEQUENCE {
    tfcs-ID                             TFCS-IdentityPlain          DEFAULT 1,
    ul-TargetSIR                        UL-TargetSIR,
    timeInfo                            TimeInfo,
    commonTimeslotInfo                  CommonTimeslotInfo          OPTIONAL,
```

```
    ul-CCTrCH-TimeslotsCodes            UplinkTimeslotsCodes                OPTIONAL
}

UL-CCTrCH-r4 ::=                    SEQUENCE {
    tfcs-ID                             TFCS-IdentityPlain                  DEFAULT 1,
    ul-TargetSIR                        UL-TargetSIR,
    timeInfo                            TimeInfo,
    commonTimeslotInfo                  CommonTimeslotInfo                  OPTIONAL,
    tddOption                           CHOICE {
        tdd384                              SEQUENCE {
            ul-CCTrCH-TimeslotsCodes            UplinkTimeslotsCodes        OPTIONAL
        },
        tdd128                              SEQUENCE {
            ul-CCTrCH-TimeslotsCodes            UplinkTimeslotsCodes-LCR-r4 OPTIONAL
        }
    }
}

UL-CCTrCHList ::=                   SEQUENCE (SIZE (1..maxCCTrCH)) OF
                                        UL-CCTrCH

UL-CCTrCHList-r4 ::=                SEQUENCE (SIZE (1..maxCCTrCH)) OF
                                        UL-CCTrCH-r4

UL-CCTrChTPCList ::=                SEQUENCE (SIZE (0..maxCCTrCH)) OF
                                            TFCS-Identity

UL-ChannelRequirement ::=           CHOICE {
    ul-DPCH-Info                        UL-DPCH-Info,
    cpch-SetInfo                        CPCH-SetInfo
}

UL-ChannelRequirement-r4 ::=        CHOICE {
    ul-DPCH-Info                        UL-DPCH-Info-r4,
    cpch-SetInfo                        CPCH-SetInfo
}

UL-ChannelRequirementWithCPCH-SetID ::= CHOICE {
    ul-DPCH-Info                        UL-DPCH-Info,
    cpch-SetInfo                        CPCH-SetInfo,
    cpch-SetID                          CPCH-SetID
}

UL-ChannelRequirementWithCPCH-SetID-r4 ::=  CHOICE {
    ul-DPCH-Info                        UL-DPCH-Info-r4,
    cpch-SetInfo                        CPCH-SetInfo,
    cpch-SetID                          CPCH-SetID
}

UL-CompressedModeMethod ::=         ENUMERATED {
                                        sf-2,
                                        higherLayerScheduling }

UL-DL-Mode ::=                      CHOICE {
    ul                                  UL-CompressedModeMethod,
    dl                                  DL-CompressedModeMethod,
    ul-and-dl                           SEQUENCE {
        ul                                  UL-CompressedModeMethod,
        dl                                  DL-CompressedModeMethod
    }}

UL-DPCCH-SlotFormat ::=             ENUMERATED {
                                        slf0, slf1, slf2 }

UL-DPCH-Info ::=                    SEQUENCE {
    ul-DPCH-PowerControlInfo            UL-DPCH-PowerControlInfo            OPTIONAL,
    modeSpecificInfo                    CHOICE {
        fdd                                 SEQUENCE {
            scramblingCodeType                  ScramblingCodeType,
            scramblingCode                      UL-ScramblingCode,
            numberOfDPDCH                       NumberOfDPDCH               DEFAULT 1,
            spreadingFactor                     SpreadingFactor,
            tfci-Existence                      BOOLEAN,
            numberOfFBI-Bits                    NumberOfFBI-Bits            OPTIONAL,
            -- The IE above is conditional based on history
            puncturingLimit                     PuncturingLimit
        },
        tdd                                 SEQUENCE {
```

```
            ul-TimingAdvance                    UL-TimingAdvanceControl     OPTIONAL,
            ul-CCTrCHList                       UL-CCTrCHList
        }
    }
}

UL-DPCH-Info-r4 ::=                 SEQUENCE {
    ul-DPCH-PowerControlInfo            UL-DPCH-PowerControlInfo-r4         OPTIONAL,
    modeSpecificInfo                    CHOICE {
        fdd                                 SEQUENCE {
            scramblingCodeType                  ScramblingCodeType,
            scramblingCode                      UL-ScramblingCode,
            numberOfDPDCH                       NumberOfDPDCH               DEFAULT 1,
            spreadingFactor                     SpreadingFactor,
            tfci-Existence                      BOOLEAN,
            numberOfFBI-Bits                    NumberOfFBI-Bits            OPTIONAL,
            -- The IE above is conditional based on history
            puncturingLimit                     PuncturingLimit
        },
        tdd                                 SEQUENCE {
            ul-TimingAdvance                    UL-TimingAdvanceControl-r4  OPTIONAL,
            ul-CCTrCHList                       UL-CCTrCHList-r4
        }
    }
}

UL-DPCH-InfoPostFDD ::=             SEQUENCE {
    ul-DPCH-PowerControlInfo            UL-DPCH-PowerControlInfoPostFDD,
            scramblingCodeType                  ScramblingCodeType,
            reducedScramblingCodeNumber         ReducedScramblingCodeNumber,
            spreadingFactor                     SpreadingFactor

}

UL-DPCH-InfoPostTDD ::=             SEQUENCE {
    ul-DPCH-PowerControlInfo            UL-DPCH-PowerControlInfoPostTDD,
    ul-TimingAdvance                    UL-TimingAdvanceControl                 OPTIONAL,
    ul-CCTrCH-TimeslotsCodes            UplinkTimeslotsCodes
}

UL-DPCH-InfoPostTDD-LCR-r4 ::=      SEQUENCE {
    ul-DPCH-PowerControlInfo            UL-DPCH-PowerControlInfoPostTDD-LCR-r4,
    ul-TimingAdvance                    UL-TimingAdvanceControl-LCR-r4              OPTIONAL,
    ul-CCTrCH-TimeslotsCodes            UplinkTimeslotsCodes-LCR-r4
}

UL-DPCH-InfoPredef ::=              SEQUENCE {
    ul-DPCH-PowerControlInfo            UL-DPCH-PowerControlInfoPredef,
    modeSpecificInfo                    CHOICE {
        fdd                                 SEQUENCE {
            tfci-Existence                      BOOLEAN,
            puncturingLimit                     PuncturingLimit
        },
        tdd                                 SEQUENCE {
            commonTimeslotInfo                  CommonTimeslotInfo
        }
    }
}

UL-DPCH-PowerControlInfo ::=        CHOICE {
    fdd                                 SEQUENCE {
        dpcch-PowerOffset                   DPCCH-PowerOffset,
        pc-Preamble                         PC-Preamble,
        sRB-delay                           SRB-delay,
        powerControlAlgorithm               PowerControlAlgorithm
        -- TABULAR: TPC step size nested inside PowerControlAlgorithm
    },
    tdd                                 SEQUENCE {
        ul-TargetSIR                        UL-TargetSIR                OPTIONAL,
        ul-OL-PC-Signalling                 CHOICE {
            broadcast-UL-OL-PC-info             NULL,
            handoverGroup                       SEQUENCE {
                individualTS-InterferenceList       IndividualTS-InterferenceList,
                dpch-ConstantValue                  ConstantValue,
                primaryCCPCH-TX-Power               PrimaryCCPCH-TX-Power
            }
        }                                                               OPTIONAL
    }
```

```
}

UL-DPCH-PowerControlInfo-r4 ::=     CHOICE {
    fdd                                 SEQUENCE {
        dpcch-PowerOffset                   DPCCH-PowerOffset,
        pc-Preamble                         PC-Preamble,
        powerControlAlgorithm               PowerControlAlgorithm
        -- TABULAR: TPC step size nested inside PowerControlAlgorithm
    },
    tdd                                 SEQUENCE {
        ul-TargetSIR                        UL-TargetSIR                OPTIONAL,
        ul-OL-PC-Signalling                 CHOICE {
            broadcast-UL-OL-PC-info             NULL,
            handoverGroup                       SEQUENCE {
                tddOption                           CHOICE {
                    tdd384                              SEQUENCE {
                        individualTS-InterferenceList       IndividualTS-InterferenceList,
                        dpch-ConstantValue                  ConstantValue
                    },
                    tdd128                              SEQUENCE {
                        tpc-StepSize                        TPC-StepSizeTDD
                    }
                },
                primaryCCPCH-TX-Power               PrimaryCCPCH-TX-Power
            }
        }
    }
}

UL-DPCH-PowerControlInfoPostFDD ::= SEQUENCE {
            dpcch-PowerOffset                   DPCCH-PowerOffset2, -- smaller range to save bits
            pc-Preamble                         PC-Preamble,
            sRB-delay                           SRB-delay
}

UL-DPCH-PowerControlInfoPostTDD ::= SEQUENCE {
    ul-TargetSIR                        UL-TargetSIR,
    ul-TimeslotInterference             UL-Interference
}

UL-DPCH-PowerControlInfoPostTDD-LCR-r4 ::=  SEQUENCE {
    ul-TargetSIR                        UL-TargetSIR
}

UL-DPCH-PowerControlInfoPredef ::=      CHOICE {
    fdd                                 SEQUENCE {
        powerControlAlgorithm               PowerControlAlgorithm
        -- TABULAR: TPC step size nested inside PowerControlAlgorithm
    },
    tdd                                 SEQUENCE {
-- The following IE shall be ignored if in 1.28Mcps TDD mode.
        dpch-ConstantValue                  ConstantValue
    }
}

UL-Interference ::=                 INTEGER (-110..-70)

UL-ScramblingCode ::=               INTEGER (0..16777215)


UL-SynchronisationParameters-r4 ::= SEQUENCE {
    stepSize                            INTEGER (1..8),
    frequency                           INTEGER (1..8)
}

-- Actual value = (IE value * 0.5) - 11
UL-TargetSIR ::=                    INTEGER (0..62)

UL-TimingAdvance ::=                INTEGER (0..63)

UL-TimingAdvanceControl ::=         CHOICE {
    disabled                            NULL,
    enabled                             SEQUENCE {
        ul-TimingAdvance                    UL-TimingAdvance                OPTIONAL,
        activationTime                      ActivationTime                  OPTIONAL
    }
}
```

```
UL-TimingAdvanceControl-r4 ::=      CHOICE {
    disabled                            NULL,
    enabled                             SEQUENCE {
        tddOption                           CHOICE {
            tdd384                              SEQUENCE {
                ul-TimingAdvance                    UL-TimingAdvance                OPTIONAL,
                activationTime                      ActivationTime                  OPTIONAL
            },
            tdd128                              SEQUENCE {
                ul-SynchronisationParameters        UL-SynchronisationParameters-r4 OPTIONAL,
                synchronisationParameters           SynchronisationParameters-r4    OPTIONAL
            }
        }
    }
}

UL-TimingAdvanceControl-LCR-r4 ::=  CHOICE {
    disabled                            NULL,
    enabled                             SEQUENCE {
        ul-SynchronisationParameters        UL-SynchronisationParameters-r4 OPTIONAL,
        synchronisationParameters           SynchronisationParameters-r4    OPTIONAL
    }
}

UL-TS-ChannelisationCode ::=        ENUMERATED {
                                        cc1-1, cc2-1, cc2-2,
                                        cc4-1, cc4-2, cc4-3, cc4-4,
                                        cc8-1, cc8-2, cc8-3, cc8-4,
                                        cc8-5, cc8-6, cc8-7, cc8-8,
                                        cc16-1, cc16-2, cc16-3, cc16-4,
                                        cc16-5, cc16-6, cc16-7, cc16-8,
                                        cc16-9, cc16-10, cc16-11, cc16-12,
                                        cc16-13, cc16-14, cc16-15, cc16-16 }

UL-TS-ChannelisationCodeList ::=    SEQUENCE (SIZE (1..2)) OF
                                        UL-TS-ChannelisationCode

UplinkAdditionalTimeslots ::=       SEQUENCE {
    parameters                          CHOICE {
        sameAsLast                          SEQUENCE {
            timeslotNumber                      TimeslotNumber
        },
        newParameters                       SEQUENCE {
            individualTimeslotInfo                  IndividualTimeslotInfo,
            ul-TS-ChannelisationCodeList            UL-TS-ChannelisationCodeList
        }
    }
}

UplinkAdditionalTimeslots-LCR-r4 ::=    SEQUENCE {
    parameters                          CHOICE {
        sameAsLast                          SEQUENCE {
            timeslotNumber                      TimeslotNumber
        },
        newParameters                       SEQUENCE {
            individualTimeslotInfo                  IndividualTimeslotInfo-LCR-r4,
            ul-TS-ChannelisationCodeList            UL-TS-ChannelisationCodeList
        }
    }
}

UplinkTimeslotsCodes ::=            SEQUENCE {
    dynamicSFusage                      BOOLEAN,
    firstIndividualTimeslotInfo         IndividualTimeslotInfo,
    ul-TS-ChannelisationCodeList        UL-TS-ChannelisationCodeList,
    moreTimeslots                       CHOICE {
        noMore                              NULL,
        additionalTimeslots                 CHOICE {
            consecutive                         SEQUENCE {
                numAdditionalTimeslots              INTEGER (1..maxTS-1)
            },
            timeslotList                        SEQUENCE (SIZE (1..maxTS-1)) OF
                                                    UplinkAdditionalTimeslots
        }
    }
}

UplinkTimeslotsCodes-LCR-r4 ::=     SEQUENCE {
```

```
    dynamicSFusage                      BOOLEAN,
    firstIndividualTimeslotInfo         IndividualTimeslotInfo-LCR-r4,
    ul-TS-ChannelisationCodeList        UL-TS-ChannelisationCodeList,
    moreTimeslots                       CHOICE {
        noMore                              NULL,
        additionalTimeslots                 CHOICE {
            consecutive                         SEQUENCE {
                numAdditionalTimeslots              INTEGER (1..maxTS-LCR-1)
            },
            timeslotList                        SEQUENCE (SIZE (1..maxTS-LCR-1)) OF
                                                    UplinkAdditionalTimeslots-LCR-r4
        }
    }
}

Wi-LCR ::=                                                          INTEGER(1..4)
```

3GPP TSG-RAN WG2 Meeting#25
Makuhari, Japan, 26-30 November 2001

R2-012569

CR-Form-v3

CHANGE REQUEST

⌘ **25.331** **CR** **1200** ⌘ rev **-** ⌘ Current version: **4.2.1** ⌘

*For **HELP** on using this form, see bottom of this page or look at the pop-up text over the ⌘ symbols.*

Proposed change affects: ⌘ (U)SIM ☐ ME/UE **X** Radio Access Network **X** Core Network ☐

Title:	⌘	Correction of 1.28Mcps TDD for TS25.331
Source:	⌘	TSG-RAN WG2
Work item code:	⌘	LCRTDD-L23
Date:	⌘	31-10-01
Category:	⌘	**F**
Release:	⌘	REL-4

Use one of the following categories:
F *(correction)*
A *(corresponds to a correction in an earlier release)*
B *(Addition of feature),*
C *(Functional modification of feature)*
D *(Editorial modification)*
Detailed explanations of the above categories can be found in 3GPP TR 21.900.

Use one of the following releases:
2 (GSM Phase 2)
R96 (Release 1996)
R97 (Release 1997)
R98 (Release 1998)
R99 (Release 1999)
REL-4 (Release 4)
REL-5 (Release 5)

Reason for change: ⌘ Corrections to 1.28Mcps TDD aspect.

Summary of change: ⌘ Corrections of 1.28Mcps TDD in TS25.331:

1. 10.3.6.11 Constant value for open loop power control of PRACH, USCH and UL DPCH is not used in 1.28Mcps TDD.
2. Change the parameter “Power Ramping Step” to “Power Ramp Step” in order to keep consistent with WG1.

Isolated impact:
The changes have isolated impact and were corrections to a function where the specification was ambiguous or not sufficiently explicit. They would not affect implementations behaving like indicated in the CR, would affect implementations supporting the corrected functionality otherwise.

Consequences if not approved: ⌘

Clauses affected: ⌘ 8.5.7, 10.3.6.11, 10.3.6.49, 10.3.6.78a, 10.3.6.96, 11.3

Other specs affected: ⌘
☐ Other core specifications ⌘
☐ Test specifications
☐ O&M Specifications

Other comments: ⌘

8.5.7 Open loop power control

For FDD and prior to PRACH or PCPCH transmission the UE shall:

- read the IEs "Primary CPICH DL TX power", "UL interference" and "Constant value" in System Information Block type 6 (or System Information Block type 5, if system information block type 6 is not being broadcast) and System Information Block type 7;
- measure the value for the CPICH_RSCP;
- calculate the power for the first preamble as:

  Preamble_Initial_Power = Primary CPICH DL TX power – CPICH_RSCP + UL interference + Constant Value

  Where,

  Primary CPICH DL TX power shall have the value of IE "Primary CPICH DL TX power",

  UL interference shall have the value of IE "UL interference"; and

  Constant Value shall have the value of IE "Constant Value".

- as long as the physical layer is configured for PRACH or PCPCH transmission:
  - continuously recalculate the Preamble_Initial_Power when any of the broadcast parameters used in the above formula changes; and
  - resubmit to the physical layer the new calculated Preamble_Initial_Power.

For 3.84 Mcps TDD the UE shall:

- if in the IE "Uplink DPCH Power Control info" the "CHOICE UL OL PC info" has the value "Broadcast UL OL PC info":
  - acquire Reference Power, Constant Values from System Information Block type 6 (or System Information Block type 5, according to subclause 8.1.1.6.5), and I_{BTS} for all active UL timeslots from System Information Block type 14 on the BCH;
- otherwise:
  - acquire Reference Power, Constant Values and I_{BTS} for all active UL timeslots from the IE "Uplink DPCH Power Control info".
- for PUSCH and PRACH power control:
  - acquire Reference Power, Constant Values and I_{BTS} for all active UL timeslots from System Information Block type 6 (or System Information Block type 5, according to subclause 8.1.1.6.5) and System Information Block type 14 on the BCH,

  calculate the UL transmit power according to the following formula for the PRACH continuously while the physical channel is active:

  $$P_{PRACH} = L_{PCCPCH} + I_{BTS} + \text{RACH Constant value},$$

  - 3dB shall be added to RACH Constant Value in the above equation for the case where RACH Spreading Factor = 8
- calculate the UL transmit power according to the following formula for the DPCH continuously while the physical channel is active:

  $$P_{DPCH} = \alpha L_{PCCPCH} + (1-\alpha)L_0 + I_{BTS} + SIR_{TARGET} + \text{DPCH Constant value}$$

- calculate the UL transmit power according to the following formula for the PUSCH continuously while the physical channel is active:

$$P_{USCH} = \alpha L_{PCCPCH} + (1-\alpha)L_0 + I_{BTS} + SIR_{TARGET} + \text{USCH Constant value}$$

Where, for all the above equations for TDD the following apply:

- $P_{PRACH,}$ P_{DPCH}, & P_{USCH}: Transmitter power level in dBm;
- Pathloss values:
  - L_{PCCPCH}: Measurement representing path loss in dB based on beacon channels (the reference transmit power is signalled as the value of the IE "Primary CCPCH Tx Power" on BCH in System Information Block type 6 (or System Information Block type 5, according to subclause 8.1.1.6.5), or individually signalled in the IE" Uplink DPCH Power Control info").
  - L_0: Long term average of path loss in dB;
  - If the midamble is used in the evaluation of L_{PCCPCH} and L_0, and the Tx diversity scheme used for the P-CCPCH involves the transmission of different midambles from the diversity antennas, the received power of the different midambles from the different antennas shall be combined prior to evaluation of the variables.
- I_{BTS}: Interference signal power level at cell's receiver in dBm. I_{BTS} shall have the value of the IE "UL Timeslot Interference" (IE "UL Timeslot Interference" is broadcast on BCH in System Information Block type 14 or individually signalled to each UE in the IE "Uplink DPCH Power Control info" for each active uplink timeslot).
- α: α is a weighting parameter, which represents the quality of path loss measurements. α may be a function of the time delay between the uplink time slot and the most recent down link PCCPCH time slot. α is calculated at the UE. α shall be smaller or equal to the value of the IE "Alpha". If the IE "Alpha" is not explicitly signalled to the UE α shall be set to 1. If UE is capable of estimating its position by using the OTDOA IPDL method, the UE shall use the IPDL-α parameter.
- SIR_{TARGET}: Target SNR in dB. This value is individually signalled to UEs in IE "UL target SIR" in IE "UL DPCH Power Control Info" or in IE "PUSCH Power Control Info" respectively.
- RACH Constant value: RACH Constant value shall have the value of the IE "RACH Constant value".
- DPCH Constant value: DPCH Constant value shall have the value of the IE "DPCH Constant value".
- USCH Constant Value: USCH Constant value shall have the value of the IE "USCH Constant value".
- Values received by dedicated signalling shall take precedence over broadcast values.
- If IPDLs are applied, the UE may increase UL Tx power by the value given in the IE "Max power increase". This power increase is only allowed in the slots between an idle slot and the next beacon slot.

For 1.28 Mcps TDD the UE shall:

- calculate the UL transmit power according to the following formula for each UpPCH code transmission:

$$P_{UpPCH} = L_{PCCPCH} + PRX_{UpPCHdes} + i * Pwr_{ramp}$$

- calculate the UL transmit power according to the following formula for each PRACH transmission:

$$P_{PRACH} = L_{PCCPCH} + PRX_{PRACHdes} + i * Pwr_{ramp}$$

- calculate the initial UL transmit power according to the following formula for the PUSCH. Once the UE receives TPC bits relating to the PUSCH then it transitions to closed loop power control. If successive PUSCH resource allocations are contiguous then no return is made to open loop power control at the beginning of the succeeding resource allocation.

$$P_{USCH} = SIR_{TARGET} + L_{PCCPCH}$$

- calculate the initial UL transmit power according to the following formula for the DPCH. Once the UE receives TPC bits relating to the uplink DPCH then it transitions to closed loop power control.

$$P_{DPCH} = SIR_{TARGET} + L_{PCCPCH}$$

Where:

- P_{UpPCH}, $P_{PRACH,}$ P_{DPCH}, & P_{USCH}: Transmitter power level in dBm,
- L_{PCCPCH}: Measurement representing path loss in dB (reference transmit power "Primary CCPCH Tx Power" is broadcast on BCH in System Information Block type 5 and System Information Block type 6, or individually signalled to each UE in the IE" Uplink DPCH Power Control info").
- SIR_{TARGET}: Target SIR in dB. This value is individually signalled to UEs in IEs "UL DPCH Power Control Info" and "PUSCH Power Control Info".
- i is the number of transmission attempts on UpPCH
- $PRX_{PRACHdes}$: Desired PRACH RX power at the cell's receiver in dBm signalled to the UE by the network in the FPACH response to the UE's successful SYNC_UL transmission.
- $PRX_{UpPCHdes}$: Desired UpPCH RX power at the cell's receiver in dBm. The value is broadcast in "$PRX_{UpPCHdes}$" in IE "SYNC_UL info" on BCH and shall be read on System Information Block type 5 and System Information Block type 6. It can also be signalled directly to the UE in a protocol message triggering a hard handover.
- Pwr_{ramp}: The UE shall increase its transmission power by the value of the IE "Power Ramp~~ing~~ step" by every UpPCH transmission.

10.3.6.11 Constant value

This constant value is used by the UE to calculate the initial output power on PRACH according to the Open loop power control procedure. In 3.84Mcps TDD constant values are used for open loop power control of PRACH, USCH and UL DPCH as defined in subclause 8.5.7.

Information Element/Group name	Need	Multi	Type and reference	Semantics description
Constant value	MP		Integer (-35..-10)	

10.3.6.49 PICH Info

Information Element/Group name	Need	Multi	Type and reference	Semantics description	Version
CHOICE *mode*	MP				
>FDD					
>>Channelisation code	MP		Integer(0..255)	SF is fixed and equal to 256	
>>Number of PI per frame	MP		Integer (18, 36, 72, 144)		
>>STTD indicator	MP		STTD Indicator 10.3.6.78		
>TDD					
>>Timeslot number	MD		Timeslot number 10.3.6.84	Default value is the timeslot used by the SCCPCH carrying the associated PCH.	
>>Midamble shift and burst type	MP		Midamble shift and burst type 10.3.6.41		
>>>>CHOICE *TDD option*	MP				REL-4
>>>>3.84 Mcps TDD					REL-4
>>>>>Channelisation code	MD		Enumerated ((16/1)...(16/16))	Default value is the channelisation code used by the SCCPCH carrying the associated PCH.	
>>>1.28 Mcps TDD					REL-4
>>>>Codes list	MP	1..2			REL-4
>>>>>Channelisation code	MP		Enumerated ((16/1)...(16/16))		REL-4
>>Repetition period/length	MD		Enumerated((4/2),(8/2), (8/4),(16/2), (16/4), (32/2),(32/4), (64/2),(64/4))	Default value is "(64/2)".	
>>Offset	MP		Integer (0...Repetition period -1)	SFN mod Repetitionperiod = Offset.	
>>Paging indicator length	MD		Integer (4, 8, 16)	Indicates the length of one paging indicator in Bits. Default value is 4.	
>>N_{GAP}	MD		Integer(2, 4, 8)	Number of frames between the last frame carrying PICH for this Paging Occasion and the first frame carrying paging messages for this Paging Occasion. Default value is 4.	
>>N_{PCH}	MD		Integer(1 .. 8)	Number of paging groups. Default value is 2.	

10.3.6.78a SYNC_UL info

NOTE: Only for 1.28 Mcps TDD.

Information Element/ Group name	Need	Multi	Type and reference	Semantics description	Version
SYNC_UL codes bitmap	MP		Bitstring(8)	Each bit indicates availability of a SYNC_UL code, where the SYNC_UL codes are numbered "code 0" to "code 7". The value 1 of a bit indicates that the corresponding SYNC_UL code can be used. The value 0 of a bit indicates that the corresponding SYNC_UL code can not be used.	REL-4
$PRX_{UpPCHdes}$	MP		Real(-11 .. 20 by step of 0.5)	In dB	REL-4
Power Ramp~~ing~~ Step	MP		Integer(0,1,2,3)	In dB	REL-4
Max SYNC_UL Transmissions	MP		Integer(1,2,4,8)	Maximum numbers of SYNC_UL transmissions in a power ramping sequence.	REL-4
Mmax	MP		Integer(1..32)	Maximum number of synchronisation attempts.	REL-4

10.3.6.96 Uplink Timing Advance Control

NOTE: Only for TDD

Information Element/Group name	Need	Multi	Type and reference	Semantics description	Version
CHOICE *Timing Advance*	MP				
>Disabled			Null	Indicates that no timing advance is applied	
>Enabled					
>>CHOICE *TDD option*	MP				REL-4
>>>3.84 Mcps TDD					REL-4
>>>>UL Timing Advance	MD		Uplink Timing Advance 10.3.6.95	Absolute timing advance value to be used to avoid large delay spread at the NodeB. Default value is the existing value for uplink timing advance.	
>>>>Activation Time	OP		Activation Time 10.3.3.1	Frame number timing advance is to be applied. This IE is required when a new UL Timing Advance adjustment is specified and Activation Time is not otherwise specified in the RRC message.	
>>1.28 Mcps TDD				(no data)	REL-4
>>>Uplink synchronisation parameters	MD			Default: Uplink synchronisation step size is 1. Uplink synchronisation frequency is 1.	REL-4
>>>>Uplink synchronisation step size	MP		Integer(1..8)	This parameter specifies the step size to be used for the adjustment of the uplink transmission timing	REL-4
>>>>Uplink synchronisation frequency	MP		Integer(1..8)	This parameter specifies the frequency of the adjustment of the uplink transmission timing	REL-4
>>>Synchronization parameters	OP				
>>>>SYNC_UL codes bitmap	MD		Bitstring(8)	Each bit indicates availability of a SYNC_UL code, where the SYNC_UL codes are numbered "code 0" to "code 7". The value 1 of a bit indicates that the corresponding SYNC_UL code can be used. The value 0 of a	REL-4

				bit indicates that the corresponding SYNC_UL code can not be used.	
>>>>FPACH info	MP		FPACH info 10.3.6.35a		REL-4
>>>>SYNC_UL procedure	MD			Default is: Max SYNC_UL Transmission is 2. Power Ramp~~ing~~ Step is 2.	REL-4
>>>>>Max SYNC_UL Transmissions	MP		Integer(1,2,4,8)	Maximum numbers of SYNC_UL transmissions in a power ramping sequence.	REL-4
>>>>>Power Ramp~~ing~~ Step	MP		Integer(0,1,2,3)	In dB	REL-4

11.3 Information element definitions

```
< *** Uneccessary Definitions Removed *** >
-- **************************************************
--
--     PHYSICAL CHANNEL INFORMATION ELEMENTS (10.3.6)
--
-- **************************************************
…….
SYNC-UL-Procedure-r4 ::=            SEQUENCE {
    max-SYNC-UL-Transmissions           ENUMERATED { tr1, tr2, tr4, tr8 },
    powerRamp~~ing~~Step                INTEGER (0..3)
}

SYNC-UL-Info-r4 ::=                 SEQUENCE {
    sync-UL-Codes-Bitmap                BIT STRING {
                                            code7(0),
                                            code6(1),
                                            code5(2),
                                            code4(3),
                                            code3(4),
                                            code2(5),
                                            code1(6),
                                            code0(7)
                                            } ( SIZE (8)),
    prxUpPCHdes                         INTEGER (0..62),
-- Actual value = (IE value * 0.5) - 11
    powerRamp~~ing~~Step                INTEGER (0..3),
    max-SYNC-UL-Transmissions           ENUMERATED { tr1, tr2, tr4, tr8 } ,
    mmax                                INTEGER(1..32)
}
```

3GPP TSG-RAN WG2 Meeting #25 **R2-012572**
Makuhari, Japan, 26 - 30 November 2001

CR-Form-v4

CHANGE REQUEST

⌘ **25.331 CR 1201** ⌘ ev **-** ⌘ Current version: **4.2.1** ⌘

*For **HELP** on using this form, see bottom of this page or look at the pop-up text over the ⌘ symbols.*

Proposed change affects: ⌘ (U)SIM ☐ ME/UE **X** Radio Access Network **X** Core Network ☐

Title:	⌘	Correction and Clarification to Open Loop Power Control in 1.28 Mcps TDD
Source:	⌘	TSG-RAN WG2
Work item code:	⌘	LCRTDD-L23
Date:	⌘	14.11.2001
Category:	⌘	**F**
Release:	⌘	REL-4

Use one of the following categories:
F *(correction)*
A *(corresponds to a correction in an earlier release)*
B *(addition of feature),*
C *(functional modification of feature)*
D *(editorial modification)*
Detailed explanations of the above categories can be found in 3GPP TR 21.900.

Use one of the following releases:
2 *(GSM Phase 2)*
R96 *(Release 1996)*
R97 *(Release 1997)*
R98 *(Release 1998)*
R99 *(Release 1999)*
REL-4 *(Release 4)*
REL-5 *(Release 5)*

Reason for change: ⌘

- It is not sufficiently explicit described that in the formulas for calculation of the UL transmit power for each UpPCH code transmission:

  $P_{UpPCH} = L_{PCCPCH} + PRX_{UpPCHdes} + i* Pwr_{ramp}$

  and for calculation of the UL transmit power for each PRACH transmission:

  $P_{PRACH} = L_{PCCPCH} + PRX_{PRACHdes} + i * Pwr_{ramp}$

  the i have different meaning.

  Whilst in the first formula i is increased for power ramping, in the second formula i is the final value for i after power ramping procedure is used.

  For clarification different variables should be used.

  Because the value range of i is defined from 1 to Mmax, so i needs to be replaced by i-1.

- For open loop power control for 1.28 Mcps TDD the initial UL transmit power for PUSCH and the DPCH are calculated using SIR_{TARGET} and L_{PCCPH} as follows:

  $P_{USCH} = SIR_{TARGET} + L_{PCCPCH}$

  $P_{DPCH} = SIR_{TARGET} + L_{PCCPCH}$

  On the left hand of the equations P_{USCH} and P_{DPCH} are power values and therefore measured in dBm.

  On the right hand of the equation SIR_{TARGET} and L_{PCCPCH} are relations measured in dB.

  Therefore the formula needs to be corrected.

	• Clarification of term Initial_Signature_power that is used in 25.224
Summary of change: ⌘	• In the formula for P_{UpPCH} i is replaced by i-1 $P_{UpPCH} = L_{PCCPCH} + PRX_{UpPCHdes} + (i-1)* Pwrramp$ • In the formula for P_{PRACH} i is replaced by i_{UpPCH}-1: $P_{PRACH} = L_{PCCPCH} + PRX_{PRACHdes} + (i_{UpPCH}-1) * Pwr_{ramp}$ • SIR_{TARGET} is replaced by $PRX_{PUSCHdes}$ and $PRX_{PDPCHdes}$: $P_{USCH} = PRX_{PUSCHdes} + L_{PCCPCH}$ $P_{DPCH} = PRX_{PDPCHdes} + L_{PCCPCH}$ • Clarification of term Initial_Signature_power that is used in 25.224 • Correction the value of $PRX_{UpPCHdes}$ • Appropriate corrections in ASN.1 **Isolated Impact Analysis:** • Correction to a function where the specification was: o ambiguous or not sufficiently explicit. o containing some contradictions Would not affect implementations behaving like indicated in the CR, would affect implementations supporting the corrected functionality otherwise.
Consequences if not approved: ⌘	The formulas for calculation of P_{UpPCH} and P_{PRACH} is not sufficiently explicit and might be misinterpreted. The formulas for calculation of P_{USCH} and P_{DPCH} are wrong, so it is impossible to calculate the power values.

Clauses affected:	⌘	8.5.7, 10.3.6.65, 10.3.6.78a, 10.3.6.91, 11.3	
Other specs affected:	⌘	☐ Other core specifications ☐ Test specifications ☐ O&M Specifications	⌘
Other comments:	⌘		

How to create CRs using this form:
Comprehensive information and tips about how to create CRs can be found at: http://www.3gpp.org/3G_Specs/CRs.htm. Below is a brief summary:

1) Fill out the above form. The symbols above marked ⌘ contain pop-up help information about the field that they are closest to.
2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under ftp://ftp.3gpp.org/specs/ For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.
3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

8.5.7 Open loop power control

For FDD and prior to PRACH or PCPCH transmission the UE shall:

- read the IEs "Primary CPICH DL TX power", "UL interference" and "Constant value" in System Information Block type 6 (or System Information Block type 5, if system information block type 6 is not being broadcast) and System Information Block type 7;
- measure the value for the CPICH_RSCP;
- calculate the power for the first preamble as:

  Preamble_Initial_Power = Primary CPICH DL TX power – CPICH_RSCP + UL interference + Constant Value

  Where,

  Primary CPICH DL TX power shall have the value of IE "Primary CPICH DL TX power",

  UL interference shall have the value of IE "UL interference"; and

  Constant Value shall have the value of IE "Constant Value".

- as long as the physical layer is configured for PRACH or PCPCH transmission:
  - continuously recalculate the Preamble_Initial_Power when any of the broadcast parameters used in the above formula changes; and
  - resubmit to the physical layer the new calculated Preamble_Initial_Power.

For 3.84 Mcps TDD the UE shall:

- if in the IE "Uplink DPCH Power Control info" the "CHOICE UL OL PC info" has the value "Broadcast UL OL PC info":
  - acquire Reference Power, Constant Values from System Information Block type 6 (or System Information Block type 5, according to subclause 8.1.1.6.5), and I_{BTS} for all active UL timeslots from System Information Block type 14 on the BCH;
- otherwise:
  - acquire Reference Power, Constant Values and I_{BTS} for all active UL timeslots from the IE "Uplink DPCH Power Control info".
- for PUSCH and PRACH power control:
  - acquire Reference Power, Constant Values and I_{BTS} for all active UL timeslots from System Information Block type 6 (or System Information Block type 5, according to subclause 8.1.1.6.5) and System Information Block type 14 on the BCH,

  calculate the UL transmit power according to the following formula for the PRACH continuously while the physical channel is active:

  $$P_{PRACH} = L_{PCCPCH} + I_{BTS} + \text{RACH Constant value},$$

  - 3dB shall be added to RACH Constant Value in the above equation for the case where RACH Spreading Factor = 8
- calculate the UL transmit power according to the following formula for the DPCH continuously while the physical channel is active:

  $$P_{DPCH} = \alpha L_{PCCPCH} + (1-\alpha)L_0 + I_{BTS} + SIR_{TARGET} + \text{DPCH Constant value}$$

- calculate the UL transmit power according to the following formula for the PUSCH continuously while the physical channel is active:

  $$P_{USCH} = \alpha L_{PCCPCH} + (1-\alpha)L_0 + I_{BTS} + SIR_{TARGET} + \text{USCH Constant value}$$

Where, for all the above equations for TDD the following apply:

- $P_{PRACH,}$ P_{DPCH}, & P_{USCH}: Transmitter power level in dBm;
- Pathloss values:
  - L_{PCCPCH}: Measurement representing path loss in dB based on beacon channels (the reference transmit power is signalled as the value of the IE "Primary CCPCH Tx Power" on BCH in System Information Block type 6 (or System Information Block type 5, according to subclause 8.1.1.6.5), or individually signalled in the IE" Uplink DPCH Power Control info").
  - L_0: Long term average of path loss in dB;
  - If the midamble is used in the evaluation of L_{PCCPCH} and L_0, and the Tx diversity scheme used for the P-CCPCH involves the transmission of different midambles from the diversity antennas, the received power of the different midambles from the different antennas shall be combined prior to evaluation of the variables.
- I_{BTS}: Interference signal power level at cell's receiver in dBm. I_{BTS} shall have the value of the IE "UL Timeslot Interference" (IE "UL Timeslot Interference" is broadcast on BCH in System Information Block type 14 or individually signalled to each UE in the IE "Uplink DPCH Power Control info" for each active uplink timeslot).
- α: α is a weighting parameter, which represents the quality of path loss measurements. α may be a function of the time delay between the uplink time slot and the most recent down link PCCPCH time slot. α is calculated at the UE. α shall be smaller or equal to the value of the IE "Alpha". If the IE "Alpha" is not explicitly signalled to the UE α shall be set to 1. If UE is capable of estimating its position by using the OTDOA IPDL method, the UE shall use the IPDL-α parameter.
- SIR_{TARGET}: Target SNR in dB. This value is individually signalled to UEs in IE "UL target SIR" in IE "UL DPCH Power Control Info" or in IE "PUSCH Power Control Info" respectively.
- RACH Constant value: RACH Constant value shall have the value of the IE "RACH Constant value".
- DPCH Constant value: DPCH Constant value shall have the value of the IE "DPCH Constant value".
- USCH Constant Value: USCH Constant value shall have the value of the IE "USCH Constant value".
- Values received by dedicated signalling shall take precedence over broadcast values.
- If IPDLs are applied, the UE may increase UL Tx power by the value given in the IE "Max power increase". This power increase is only allowed in the slots between an idle slot and the next beacon slot.

For 1.28 Mcps TDD the UE shall:

- calculate the UL transmit power according to the following formula for each UpPCH code transmission:

$$P_{UpPCH} = L_{PCCPCH} + PRX_{UpPCHdes} + \underline{(i-1)} * Pwr_{ramp}$$

NOTE:

When i equals 1, the initial signature power “Signature_Initial_Power” defined in [33] corresponds to P_{UpPCH} with i set to 1 .

- calculate the UL transmit power according to the following formula for each PRACH transmission:

$$P_{PRACH} = L_{PCCPCH} + PRX_{PRACHdes} + \underline{(i_{UpPCH}-1)} * Pwr_{ramp}$$

- calculate the initial UL transmit power according to the following formula for the PUSCH. Once the UE receives TPC bits relating to the PUSCH then it transitions to closed loop power control. If successive PUSCH resource allocations are contiguous then no return is made to open loop power control at the beginning of the succeeding resource allocation.

$$P_{USCH} = \underline{PRX_{PUSCHdes}} + L_{PCCPCH}$$

- calculate the initial UL transmit power according to the following formula for the DPCH. Once the UE receives TPC bits relating to the uplink DPCH then it transitions to closed loop power control.

$$P_{DPCH} = \text{~~SIR}_{\text{~~TARGET~~}} \underline{\text{PRX}_{PDPCHdes}} + L_{PCCPCH}$$

Where:

- P_{UpPCH}, P_{PRACH}, P_{DPCH}, & P_{USCH}: Transmitter power level in dBm,
- L_{PCCPCH}: Measurement representing path loss in dB (reference transmit power "Primary CCPCH Tx Power" is broadcast on BCH in System Information Block type 5 and System Information Block type 6, or individually signalled to each UE in the IE" Uplink DPCH Power Control info").
- ~~SIR_{TARGET}: Target SIR in dB. This value is individually signalled to UEs in IEs "UL DPCH Power Control Info" and "PUSCH Power Control Info".~~
- ~~i~~ i is the number of transmission attempts on UpPCH, i=1…Mmax
- i_{UpPCH} is the final value of i
- $PRX_{PRACHdes}$: Desired PRACH RX power at the cell's receiver in dBm signalled to the UE by the network in the FPACH response to the UE's successful SYNC_UL transmission.
- ~~PRX~~ $PRX_{UpPCHdes}$: Desired UpPCH RX power at the cell's receiver in dBm. The value is broadcast in "$PRX_{UpPCHdes}$" in IE "SYNC_UL info" on BCH and shall be read on System Information Block type 5 and System Information Block type 6. It can also be signalled directly to the UE in a protocol message triggering a hard handover.
- $PRX_{PUSCHdes}$: Desired PUSCH RX power at the cell's receiver in dBm signalled to the UE in IE “PUSCH Power Control Info”.
- $PRX_{PDPCHdes}$: Desired PDPCH RX power at the cell's receiver in dBm signalled to the UE in IE “Uplink DPCH Power Control Info”.
- Pwr_{ramp}: The UE shall increase its transmission power by the value of the IE "Power Ramp~~ing~~ step" by every UpPCH transmission.

10.3.6.65 PUSCH power control info

NOTE: Only for TDD.

Interference level measured for a frequency at the UTRAN access point used by UE to set PUSCH output power.

Information Element/Group name	Need	Multi	Type and reference	Semantics description	Version
~~UL target SIR~~	~~MP~~		~~Real (-11 .. 20 by step of 0.5)~~	~~in dB~~	
CHOICE *TDD option*	MP				REL-4
>3.84 Mcps TDD				~~(no data)~~	REL-4
>>UL target SIR	MP		Real (-11 .. 20 by step of 0.5)	in dB	
>1.28 Mcps TDD					REL-4
>>$PRX_{PUSCHdes}$	MP		Integer(-120…-58 by step of 1)	in dBm	REL-4
>>TPC Step Size	OP		Integer (1, 2, 3)	In dB	REL-4
>>DL CCTrCH TPC List	OP	0..<maxCCTrCH>		DL CCTrCH identities for TPC commands associated with this UL CCTrCH	REL-4
>>>DL TPC TFCS Identity	MP		Transport Format Combination Set Identity 10.3.5.21		REL-4

10.3.6.78a SYNC_UL info

NOTE: Only for 1.28 Mcps TDD.

Information Element/ Group name	Need	Multi	Type and reference	Semantics description	Version
SYNC_UL codes bitmap	MP		Bitstring(8)	Each bit indicates availability of a SYNC_UL code, where the SYNC_UL codes are numbered "code 0" to "code 7". The value 1 of a bit indicates that the corresponding SYNC_UL code can be used. The value 0 of a bit indicates that the corresponding SYNC_UL code can not be used.	REL-4
$PRX_{UpPCHdes}$	MP		~~Real(-11 .. 20 by step of 0.5)~~ Integer(-120…-58 by step of 1)	In dBm	REL-4
Power Ramping Step	MP		Integer(0,1,2,3)	In dB	REL-4
Max SYNC_UL Transmissions	MP		Integer(1,2,4,8)	Maximum numbers of SYNC_UL transmissions in a power ramping sequence.	REL-4
Mmax	MP		Integer(1..32)	Maximum number of synchronisation attempts.	REL-4

10.3.6.91 Uplink DPCH power control info

Parameters used by UE to set DPCH initial output power and to use for closed-loop power control in FDD and 1.28 Mcps TDD and parameters for uplink open loop power control in 3.84 Mcps TDD.

Information Element/Group name	Need	Multi	Type and reference	Semantics description	Version
CHOICE *mode*	MP				
>FDD					
>>DPCCH Power offset	MP		Integer(-164,..-6 by step of 2)	In dB	
>>PC Preamble	MP		Integer (0..7)	In number of frames	
>>SRB delay	MP		Integer(0..7)	In number of frames	
>>Power Control Algorithm	MP		Enumerated (algorithm 1, algorithm 2)	Specifies algorithm to be used by UE to interpret TPC commands	
>>TPC step size	CV-*algo*		Integer (1, 2)	In dB	
>TDD					
~~>>UL target SIR~~	~~OP~~		~~Real (-11 .. 20 by step of 0.5dB)~~	~~In dB~~	
>>CHOICE *TDD option*					REL-4
>>>3.84 Mcps TDD					REL-4
>>>>UL target SIR	OP		Real (-11 .. 20 by step of 0.5dB)	In dB	
>>>1.28 Mcps TDD					REL-4
>>>> $PRX_{PDPCHdes}$	OP		Integer(-120…-58 by step of 1)	in dBm	REL-4
>>CHOICE *UL OL PC info*	MP				
>>>Broadcast UL OL PC info			Null	No data	
>>>Individually Signalled	OP				
>>>>CHOICE *TDD option*	MP				REL-4
>>>>>3.84 Mcps TDD					REL-4
>>>>>>Individual timeslot interference info	MP	1 to <maxTS>			
>>>>>>>Individual timeslot interference	MP		Individual timeslot interference 10.3.6.38		
>>>>>>DPCH Constant Value	OP		Constant Value 10.3.6.11	Quality Margin	
>>>>>1.28 Mcps TDD					REL-4
>>>>>>TPC step size	MP		Integer(1,2,3)		REL-4
>>>>Primary CCPCH Tx Power	OP		Primary CCPCH Tx Power 10.3.6.59	For Pathloss Calculation	

Condition	Explanation
algo	The IE is mandatory present if the IE "Power Control Algorithm" is set to "algorithm 1", otherwise the IE is not needed

11.3 Information element definitions

```
PUSCH-PowerControlInfo-r4 ::=       SEQUENCE {
    -- The IE ul-TargetSIR corresponds to PRX_PUSCHdes for 1.28Mcps TDD
    -- Actual value PRX_PUSCHdes = (value of IE "ul-TargetSIR" – 120)
    ul-TargetSIR                        UL-TargetSIR,
    tddOption                           CHOICE {
        tdd384                              NULL,
        tdd128                              SEQUENCE {
            tpc-StepSize                        TPC-StepSizeTDD             OPTIONAL,
            dl-CCTrChTPCList                    DL-CCTrChTPCList            OPTIONAL
        }
    }
}



UL-DPCH-PowerControlInfo-r4 ::=     CHOICE {
    fdd                                 SEQUENCE {
        dpcch-PowerOffset                   DPCCH-PowerOffset,
        pc-Preamble                         PC-Preamble,
        powerControlAlgorithm               PowerControlAlgorithm
        -- TABULAR: TPC step size nested inside PowerControlAlgorithm
    },
    tdd                                 SEQUENCE {
        -- The IE ul-TargetSIR corresponds to PRX_PDPCHdes for 1.28Mcps TDD
        -- Actual value PRX_PDPCHdes = (value of IE "ul-TargetSIR" – 120)
        ul-TargetSIR                        UL-TargetSIR                OPTIONAL,
        ul-OL-PC-Signalling                 CHOICE {
            broadcast-UL-OL-PC-info             NULL,
            handoverGroup                       SEQUENCE {
                tddOption                           CHOICE {
                    tdd384                              SEQUENCE {
                        individualTS-InterferenceList       IndividualTS-InterferenceList,
                        dpch-ConstantValue                  ConstantValue
                    },
                    tdd128                              SEQUENCE {
                        tpc-StepSize                        TPC-StepSizeTDD
                    }
                },
                primaryCCPCH-TX-Power               PrimaryCCPCH-TX-Power
            }
        }
    }
}


SYNC-UL-Info-r4 ::=                 SEQUENCE {
    sync-UL-Codes-Bitmap                BIT STRING {
                                            code7(0),
                                            code6(1),
                                            code5(2),
                                            code4(3),
                                            code3(4),
                                            code2(5),
                                            code1(6),
                                            code0(7)
                                            } ( SIZE (8)),
    prxUpPCHdes                         INTEGER (0..62),
-- Actual value = (IE value * 0.5) - 11-- Actual value = IE value – 120
    powerRampingStep                    INTEGER (0..3),
    max-SYNC-UL-Transmissions           ENUMERATED { tr1, tr2, tr4, tr8 } ,
    mmax                                INTEGER(1..32)
}
```

3GPP TSG-RAN WG2 meeting #25 ***Tdoc R2-012630***
Makuhari, Japan, November 26th – 30th, 2001

CR-Form-v4

CHANGE REQUEST

⌘ **25.331 CR 1206** ⌘ ev **-** ⌘ Current version: **4.2.1** ⌘

*For **HELP** on using this form, see bottom of this page or look at the pop-up text over the ⌘ symbols.*

Proposed change affects: ⌘ (U)SIM ☐ ME/UE **X** Radio Access Network **X** Core Network ☐

Title:	⌘	Extensions of IE value ranges in tabular
Source:	⌘	TSG-RAN WG2
Work item code:	⌘	TEI4
Date:	⌘	2001-11-27
Category:	⌘	**F**
Release:	⌘	REL-4

Use one of the following categories:
F *(correction)*
A *(corresponds to a correction in an earlier release)*
B *(addition of feature),*
C *(functional modification of feature)*
D *(editorial modification)*
Detailed explanations of the above categories can be found in 3GPP TR 21.900.

Use one of the following releases:
2 (GSM Phase 2)
R96 (Release 1996)
R97 (Release 1997)
R98 (Release 1998)
R99 (Release 1999)
REL-4 (Release 4)
REL-5 (Release 5)

Reason for change: ⌘ When IE value ranges, multiplicity values and type constant values have been added in the tabular format in REL-4 compared to R99, there is a need to clearly indicate which of them that have been added in REL-4.

The current practice is to include text in the version column on which values that are added in which release. However, this would cause a lack of overview as the number of releases and added values will increase. Therefore an improvement would be beneficial, which principles is outlined in a separate CR to TR 25.921.

Summary of change: ⌘ An extra subrow is added for the REL-4 specific values with a REL-4 indication in the version column.

In the "multiplicity values and type constants" table a version column has been added and filled in for those constants that have been added in release 4.

Isolated impact
This CR concerns the application of a specification guidelines that deals with how the information is represented in the tabular format. The CR does not involve technical changes and hence will not affect implementations.

Consequences if not approved: ⌘ The indications about which release a cerian value or constant is added may become hard to follow in the future.

Clauses affected: ⌘ 10.3.3.42, 10.3.5.11, 10.3.7.79, 10.3.10

Other specs affected: ⌘
☐ Other core specifications ⌘
☐ Test specifications
☐ O&M Specifications

Other comments: ⌘

How to create CRs using this form:

Comprehensive information and tips about how to create CRs can be found at: http://www.3gpp.org/3G_Specs/CRs.htm. Below is a brief summary:

1) Fill out the above form. The symbols above marked ⌘ contain pop-up help information about the field that they are closest to.
2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under ftp://ftp.3gpp.org/specs/ For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.
3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

10.3.3.42 UE radio access capability

Information Element/Group name	Need	Multi	Type and reference	Semantics description	Version
ICS version	MP		Enumerated(R99, ~~REL-4)~~	Indicates the release version of [42]-2 (Implementation Conformance Statement (ICS) proforma specification) that is applicable for the UE.	~~Value REL-4 added in REL-4~~
			REL-4)		REL-4
PDCP capability	MP		PDCP capability 10.3.3.24		
RLC capability	MP		RLC capability 10.3.3.34		
Transport channel capability	MP		Transport channel capability 10.3.3.40		
RF capability FDD	OP		RF capability FDD 10.3.3.33		
RF capability TDD	OP	~~1 to 2~~	RF capability TDD 10.3.3.33b	One "TDD RF capability" entity shall be included for every Chip rate capability supported.	~~Multi=2 is included in REL-4~~
		1 to 2			REL-4
Physical channel capability	MP		Physical channel capability 10.3.3.25		
UE multi-mode/multi-RAT capability	MP		UE multi-mode/multi-RAT capability 10.3.3.41		
Security capability	MP		Security capability 10.3.3.37		
UE positioning capability	MP		UE positioning capability 10.3.3.45		
Measurement capability	CH-*fdd_req_sup*		Measurement capability 10.3.3.21		

Condition	Explanation
fdd_req_sup	The IE is mandatory present if the IE "Multi-mode capability" has the value "FDD" or "FDD/TDD" and a FDD capability update has been requested in a previous message. Otherwise this field is not needed in the message.

10.3.5.11 Semi-static Transport Format Information

Information Element/Group name	Need	Multi	Type and reference	Semantics description	Version
Transmission time interval	MP		Integer(~~5,~~ 10, 20, 40, 80, dynamic~~,~~) ~~In ms. The value dynamic is only used in TDD mode.~~	In ms. The value dynamic is only used in TDD mode.. ~~5 is only applicable for the RACH in 1.28 Mcps TDD~~	~~REL-4~~
			5)	5 is only applicable for the RACH in 1.28 Mcps TDD	REL-4
Type of channel coding	MP		Enumerated(No coding, Convolutional, Turbo)		
Coding Rate	CV-*Coding*		Enumerated(1/2, 1/3)		
Rate matching attribute	MP		Integer(1..hiRM)		
CRC size	MP		Integer(0, 8, 12, 16, 24)	in bits	

Condition	Explanation
Coding	This IE is mandatory present if IE "Type of channel coding" is "Convolutional" and not needed otherwise.

10.3.7.79 UE internal measurement quantity

The quantity the UE shall measure in case of UE internal measurement.

Information Element/Group name	Need	Multi	Type and reference	Semantics description	Version
CHOICE *mode*	MP				
>FDD					
>>Measurement quantity	MP		Enumerated(UE Transmitted Power, UTRA Carrier RSSI, UE Rx-Tx time difference)		
>TDD					
>>Measurement quantity	MP		Enumerated(UE Transmitted Power, UTRA Carrier RSSI, ~~T~~$_{ADV}$~~)~~	~~Measurement on Timing Advance is for 1.28 Mcps TDD~~	~~REL-4~~
			T_{ADV})	Measurement on Timing Advance is for 1.28 Mcps TDD	REL-4
Filter coefficient	MP		Filter coefficient 10.3.7.9		

10.3.10 Multiplicity values and type constraint values

The following table includes constants that are either used as multi bounds (name starting with "max") or as high or low value in a type specification (name starting with "lo" or "hi"). Constants are specified only for values appearing more than once in the RRC specification. In case a constant is related to one or more other constants, an expression is included in the "value" column instead of the actual value.

Constant	Explanation	Value	Version
CN information			
maxCNdomains	Maximum number of CN domains	4	
UTRAN mobility information			
maxRAT	Maximum number or Radio Access Technologies	maxOtherRAT + 1	
maxOtherRAT	Maximum number or other Radio Access Technologies	15	
maxURA	Maximum number of URAs in a cell	8	
maxInterSysMessages	Maximum number of Inter System Messages	4	
maxRABsetup	Maximum number of RABs to be established	16	
UE information			
maxtransactions	Maximum number of parallel RRC transactions in downlink	25	
maxPDCPalgoType	Maximum number of PDCP algorithm types	8	
maxDRACclasses	Maximum number of UE classes which would require different DRAC parameters	8	
maxFreqBandsFDD	Maximum number of frequency bands supported by the UE as defined in [21]	8	
maxFreqBandsTDD	Maximum number of frequency bands supported by the UE as defined in [22]	4	
maxFreqBandsGSM	Maximum number of frequency bands supported by the UE as defined in [45]	16	
maxPage1	Number of UEs paged in the Paging Type 1 message	8	
maxSystemCapability	Maximum number of system specific capabilities that can be requested in one message.	16	
RB information			
maxPredefConfig	Maximum number of predefined configurations	16	
maxRB	Maximum number of RBs	32	
maxSRBsetup	Maximum number of signalling RBs to be established	8	
maxRBperRAB	Maximum number of RBs per RAB	8	
maxRBallRABs	Maximum number of non signalling RBs	27	
maxRBMuxOptions	Maximum number of RB multiplexing options	8	
maxLoCHperRLC	Maximum number of logical channels per RLC entity	2	
MaxROHC-PacketSizes	Maximum number of packet sizes that are allowed to be produced by ROHC.	16	
MaxROHC-Profiles	Maximum number of profiles supported by ROHC on a given RB.	8	
TrCH information			
maxTrCH	Maximum number of transport channels used in one direction (UL or DL)	32	
maxTrCHpreconf	Maximum number of preconfigured Transport channels, per direction	16	
maxCCTrCH	Maximum number of CCTrCHs	8	
maxTF	Maximum number of different transport formats that can be included in the Transport format set for one transport channel	32	
maxTF-CPCH	Maximum number of TFs in a CPCH set	16	
maxTFC	Maximum number of Transport Format Combinations	1024	
maxTFCI-1-Combs	Maximum number of TFCI (field 1) combinations	512	
maxTFCI-2-Combs	Maximum number of TFCI (field 2) combinations	512	
maxCPCHsets	Maximum number of CPCH sets per cell	16	
maxSIBperMsg	Maximum number of complete system information blocks per SYSTEM INFORMATION message	16	
maxSIB	Maximum number of references to other system information blocks.	32	
maxSIB-FACH	Maximum number of references to system information blocks on the FACH	8	
PhyCH information			
maxPCPCH-APsubCH	Maximum number of available sub-channels for AP signature on PCPCH	12	
maxPCPCH-CDsubCH	Maximum number of available sub-channels for CD signature on PCPCH	12	
maxPCPCH-APsig	Maximum number of available signatures for AP on	16	

	PCPCH		
maxPCPCH-CDsig	Maximum number of available signatures for CD on PCPCH	16	
maxAC	Maximum number of access classes	16	
maxASC	Maximum number of access service classes	8	
maxASCmap	Maximum number of access class to access service classes mappings	7	
maxASCpersist	Maximum number of access service classes for which persistence scaling factors are specified	6	
maxPRACH	Maximum number of PRACHs in a cell	16	
MaxPRACH_FPACH	Maximum number of PRACH / FPACH pairs in a cell (1.28 Mcps TDD)	8	REL-4
maxFACHPCH	Maximum number of FACHs and PCHs mapped onto one secondary CCPCHs	8	
maxRL	Maximum number of radio links	8	
maxSCCPCH	Maximum number of secondary CCPCHs per cell	16	
maxDPDCH-UL	Maximum number of DPDCHs per cell	6	
maxDPCH-DLchan	Maximum number of channelisation codes used for DL DPCH	8	
maxPUSCH	Maximum number of PUSCHs	(8)	
maxPDSCH	Maximum number of PDSCHs	8	
maxPDSCHcodes	Maximum number of codes for PDSCH	16	
maxPDSCH-TFCIgroups	Maximum number of TFCI groups for PDSCH	256	
maxPDSCHcodeGroups	Maximum number of code groups for PDSCH	256	
maxPCPCHs	Maximum number of PCPCH channels in a CPCH Set	64	
maxPCPCH-SF	Maximum number of available SFs on PCPCH	7	
maxTS	Maximum number of timeslots used in one direction (UL or DL)	14 (3.84 Mcps TDD) ~~6 (1.28 Mcps TDD)~~	
		6 (1.28 Mcps TDD)	REL-4
hiPUSCHidentities	Maximum number of PUSCH Identities	64	
hiPDSCHidentities	Maximum number of PDSCH Identities	64	
Measurement information			
maxTGPS	Maximum number of transmission gap pattern sequences	6	
maxAdditionalMeas	Maximum number of additional measurements for a given measurement identity	4	
maxMeasEvent	Maximum number of events that can be listed in measurement reporting criteria	8	
maxMeasParEvent	Maximum number of measurement parameters (e.g. thresholds) per event	2	
maxMeasIntervals	Maximum number of intervals that define the mapping function between the measurements for the cell quality Q of a cell and the representing quality value	1	
maxCellMeas	Maximum number of cells to measure	32	
maxReportedGSMCells	Maximum number of GSM cells to be reported	6	
maxFreq	Maximum number of frequencies to measure	8	
maxSat	Maximum number of satellites to measure	16	
HiRM	Maximum number that could be set as rate matching attribute for a transport channel	256	
Frequency information			
maxFDDFreqList	Maximum number of FDD carrier frequencies to be stored in USIM	4	
maxTDDFreqList	Maximum number of TDD carrier frequencies to be stored in USIM	4	
maxFDDFreqCellList	Maximum number of neighbouring FDD cells to be stored in USIM	32	
maxTDDFreqCellList	Maximum number of neighbouring TDD cells to be stored in USIM	32	
maxGSMCellList	Maximum number of GSM cells to be stored in USIM	32	
Other information			
maxNumGSMFreqRanges	Maximum number of GSM Frequency Ranges to store	32	

maxNumFDDFreqs	Maximum number of FDD centre frequencies to store	8	
maxNumTDDFreqs	Maximum number of TDD centre frequencies to store	8	
maxNumCDMA200Freqs	Maximum number of CDMA2000 centre frequencies to store	8	