TSG-RAN Meeting #10
Bangkok, Thailand, 6 - 8 December 2000

RP-000546

Title: **Agreed CRs to TR 25.944**

Source: **TSG-RAN WG1**

Agenda item: **5.1.3**

No.	R1 T-doc	TR	CR	Rev	Subject	Cat	V_old	V_new
1	R1-001471	25.944	003	2	Corrections for FDD part of TR 25.944	F	3.2.0	3.3.0
2	R1-000997	25.944	004	-	TDD related changes for TR25.944, update	F	3.2.0	3.3.0

3GPP TSG RAN Meeting #10
Bangkok, Thailand, 6-8, December 2000

Document R1-00-1471
e.g. for 3GPP use the format TP-99xxx
or for SMG, use the format P-99-xxx

CHANGE REQUEST

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TR 25.944	CR	003r2	Current Version:	3.2.0

GSM (AA.BB) or 3G (AA.BBB) specification number ↑

↑ CR number as allocated by MCC support team

For submission to: RAN#10 *list expected approval meeting # here ↑*	for approval	X	strategic		*(for SMG use only)*
	for information		non-strategic		

Form: CR cover sheet, version 2 for 3GPP and SMG

The latest version of this form is available from: ftp://ftp.3gpp.org/Information/CR-Form-v2.doc

Proposed change affects: (U)SIM [ ] ME [ ] UTRAN / Radio [X] Core Network [ ]
(at least one should be marked with an X)

Source: TSG RAN WG1 **Date:** 21st Nov. 2000

Subject: Corrections for FDD part of TR 25.944

Work item: TR 25.944

Category: *(only one category shall be marked with an X)*

F	Correction	X	**Release:**	Phase 2	
A	Corresponds to a correction in an earlier release			Release 96	
B	Addition of feature			Release 97	
C	Functional modification of feature			Release 98	
D	Editorial modification			Release 99	X
				Release 00	

Reason for change: This CR includes corrections for FDD part of TR 25.944, which is based on technical reasons. These corrections are aligned with latest version of the Typical Radio Parameter Sets from GSMA ISG.

Clauses affected: Section 4

Other specs affected:

Other 3G core specifications	[ ]	→ List of CRs:
Other GSM core specifications	[ ]	→ List of CRs:
MS test specifications	[ ]	→ List of CRs:
BSS test specifications	[ ]	→ List of CRs:
O&M specifications	[ ]	→ List of CRs:

Other comments:

4.1.1.2 Example for PCH and FACH

Table 2: Parameter examples for PCH and FACH

Transport block size	PCH	N_{PCH}=8064 or 240 bits
	FACH1	360 bits
	FACH2	168 bits
Transport block set size	PCH	8064*B_{PCH} or 240* B_{PCH} bits (B_{PCH}=0, 1)
	FACH1	360*B_{FACH1} bits (B_{FACH1}=0, 1)
	FACH2	168*B_{FACH2} bits (B_{FACH2}=0, 1, 2, 3)
Coding	PCH, FACH2	CC, coding rate = 1/2
	FACH1	TC
TTI		10 ms
The numbers of codes		1
SF		64

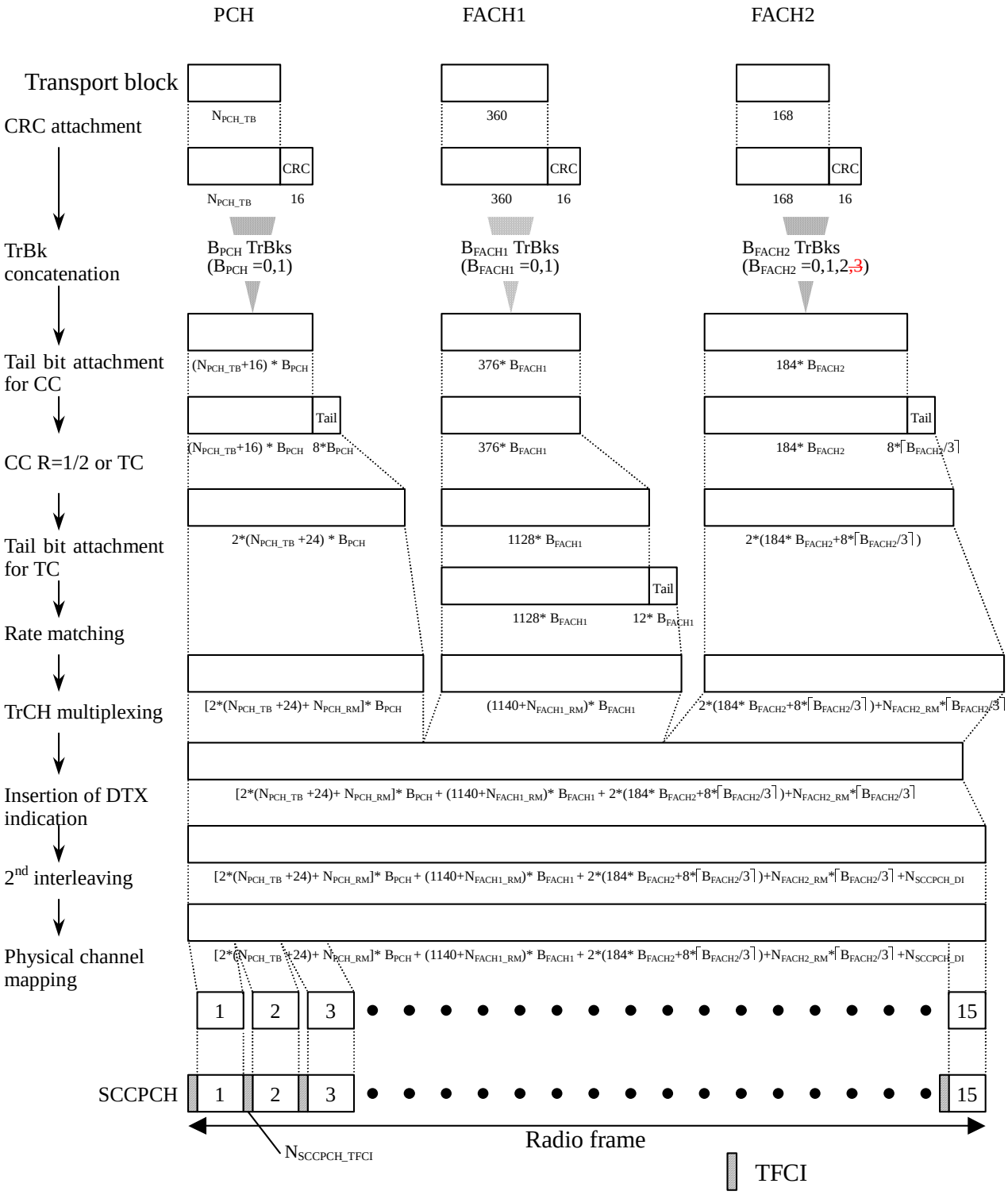


Figure 2: Channel coding and multiplexing example for PCH and FACH

4.1.1.3 Example for DCH

4.1.1.3.1 DCH-> Radio frame segmentation

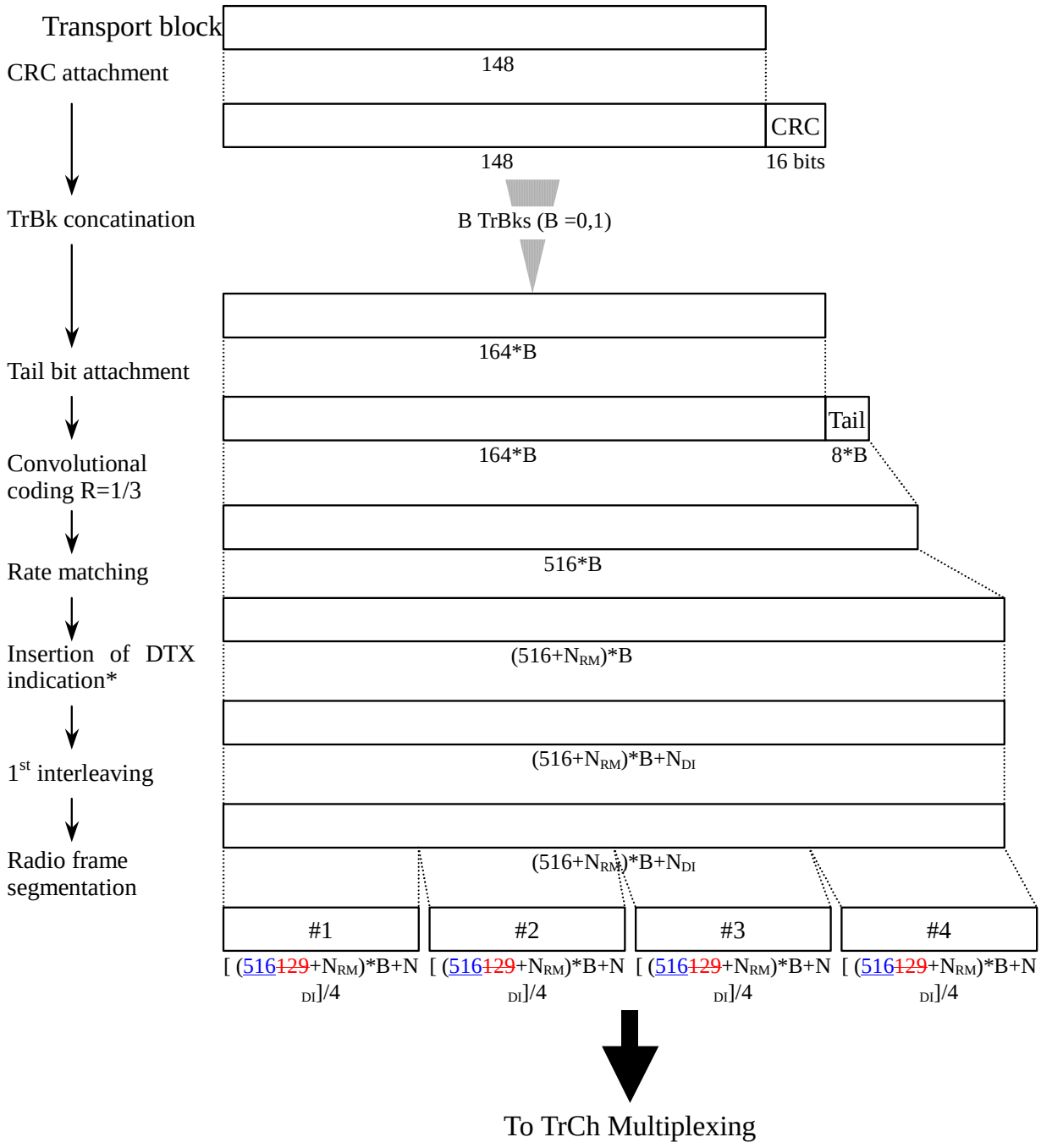
4.1.1.3.1.1 Example for 3.4 kbps data

NOTE: This example can be applied to DCCH.

NOTE: In this example, it is assumed that maximum data rate of RLC payload is 3.4 kbps, and that MAC and RLC overhead in a transport block is 12 bits.

Table 3: Parameter examples for 3.4 kbps data

Transport block size	148 bits
Transport block set size	148*B bits (B=0, 1)
CRC	16 bits
Coding	CC, coding rate = 1/3
TTI	40 ms



* Insertion of DTX indication is used only if the position of the TrCHs in the radio frame is fixed.

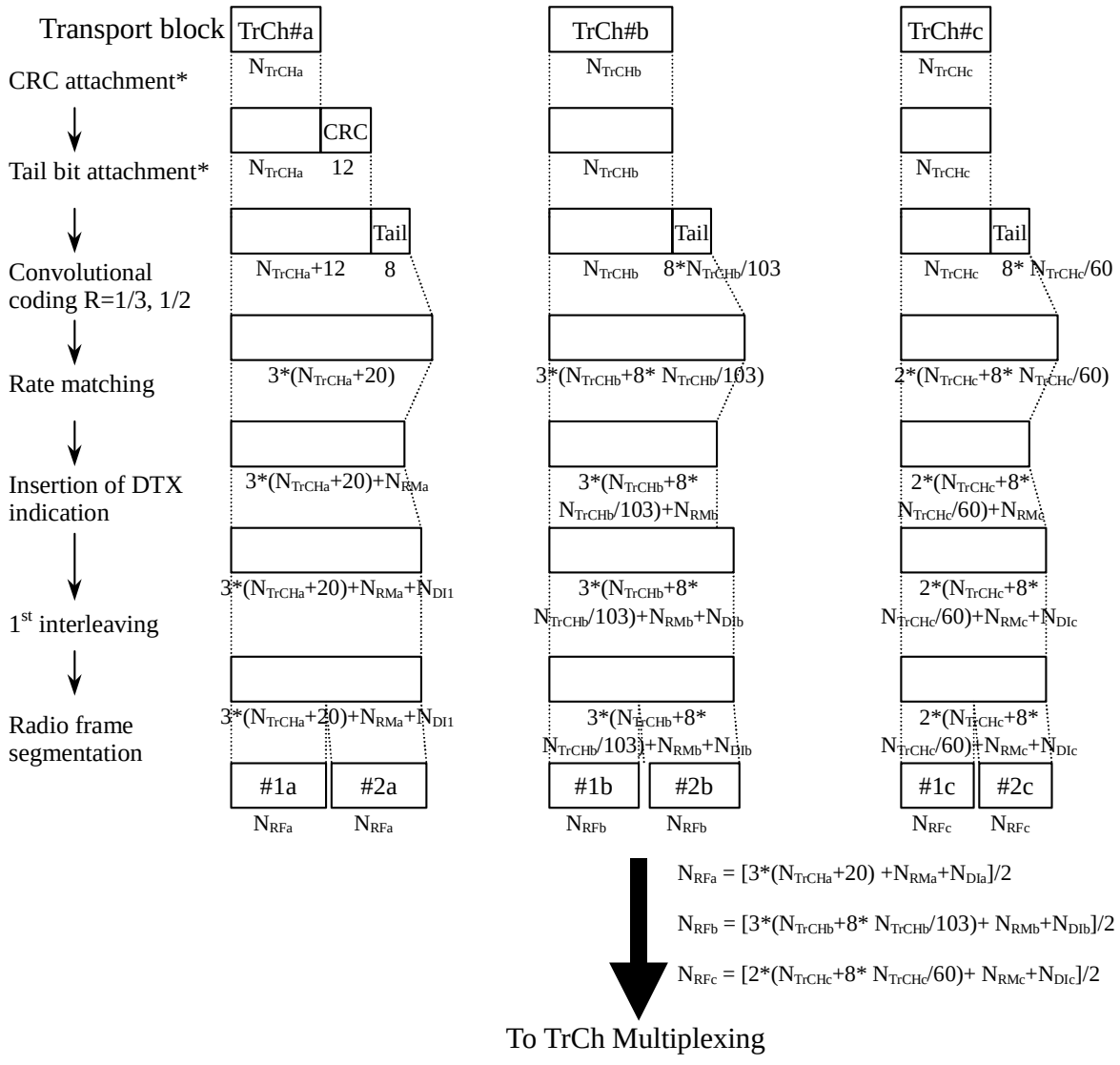
Figure 3: Channel coding and multiplexing example for 3.4 kbps data

4.1.1.3.1.2 Example for 12.2 kbps data

NOTE: This example can be applied to AMR speech.

Table 4: Parameter examples for 12.2 kbps data

The number of TrChs		3
Transport block size	TrCH#a	~~N_{TrCHa}=0~~, 39 or 81bits
	TrCH#b	~~N_{TrCHb}=0 or~~ 103 bits
	TrCH#c	~~N_{TrCHc}=0 or~~ 60 bits
TFCS	#1	N_{TrCHa}=1*81, N_{TrCHb}=1*103, N_{TrCHc}=1*60 bits
	#2	N_{TrCHa}=1*39, N_{TrCHb}=0*103, N_{TrCHc}=0*60 bits
	#3	N_{TrCHa}=1*0, N_{TrCHb}=0*103, N_{TrCHc}=0*60 bits
CRC		12 bits (attached only to TrCh#a)
CRC parity bit attachment for 0 bit transport block		Applied only to TrCH#a
Coding		CC, coding rate = 1/3 for TrCh#a, b coding rate = 1/2 for TrCh#c
TTI		20 ms



* CRC and tail bits for TrCH#a is attached even if N_{TrCha}=0 bits since CRC parity bit attachment for 0 bit transport block is applied.

Figure 4: Channel coding and multiplexing example for 12.2 kbps data

4.1.1.3.1.3 Example for 28.8/57.6 kbps data

NOTE: This example can be applied to Modem or FAX.

Table 5: Parameters for 28.8/57.6 kbps data

The number of TrChs		1
Transport block size		576 bits
Transport block Set size	28.8 kbps	576*B bits (B = 0, 1, 2)
	57.6 kbps	576*B bits (B = 0, 1, 2, 3, 4)
CRC		16 bits
Coding		Turbo coding, coding rate = 1/3
TTI		40 ms

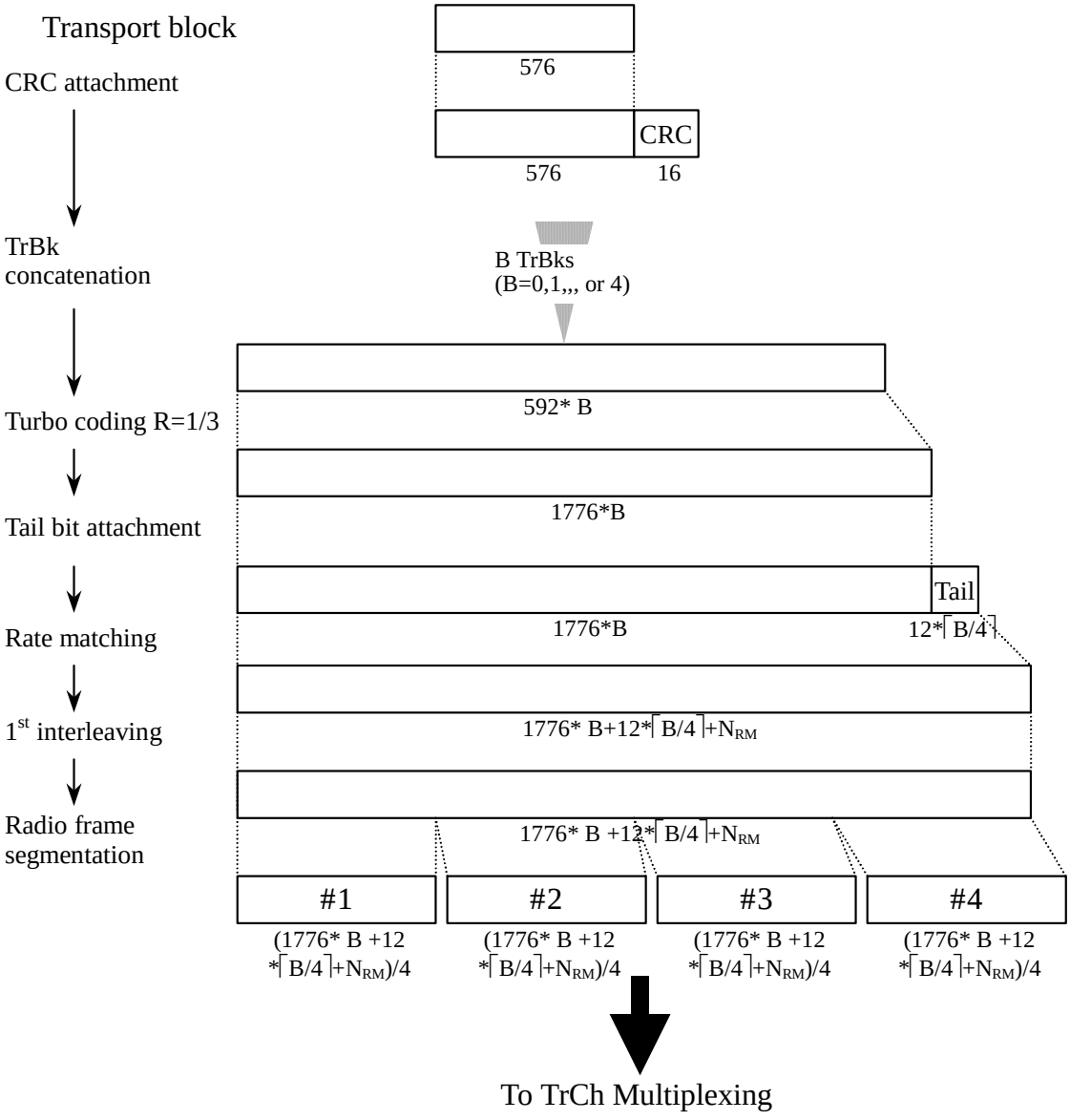


Figure 5: Channel coding and multiplexing example for 28.8/57.6 kbps data

4.1.1.3.1.4 Example for 64/128/144 kbps packet data

NOTE: In this example, it is assumed that maximum data rate of RLC payload is 64/128/144 kbps, and MAC and RLC overhead in a transport block is 16 bits.

Table 6: Parameters for 64/128/144 kbps packet data

The number of TrChs		1
Transport block size		336 bits
Transport block Set size	64 kbps	336*B bits (B = 0, 1, 2, 3, 4)
	128 kbps	336*B bits (B = 0, 1, 2, 4, 8)
	144 kbps	336*B bits (B = 0, 1, 2, 4, 8, 9)
CRC		16 bits
Coding		Turbo coding, coding rate = 1/3
TTI		20 ms

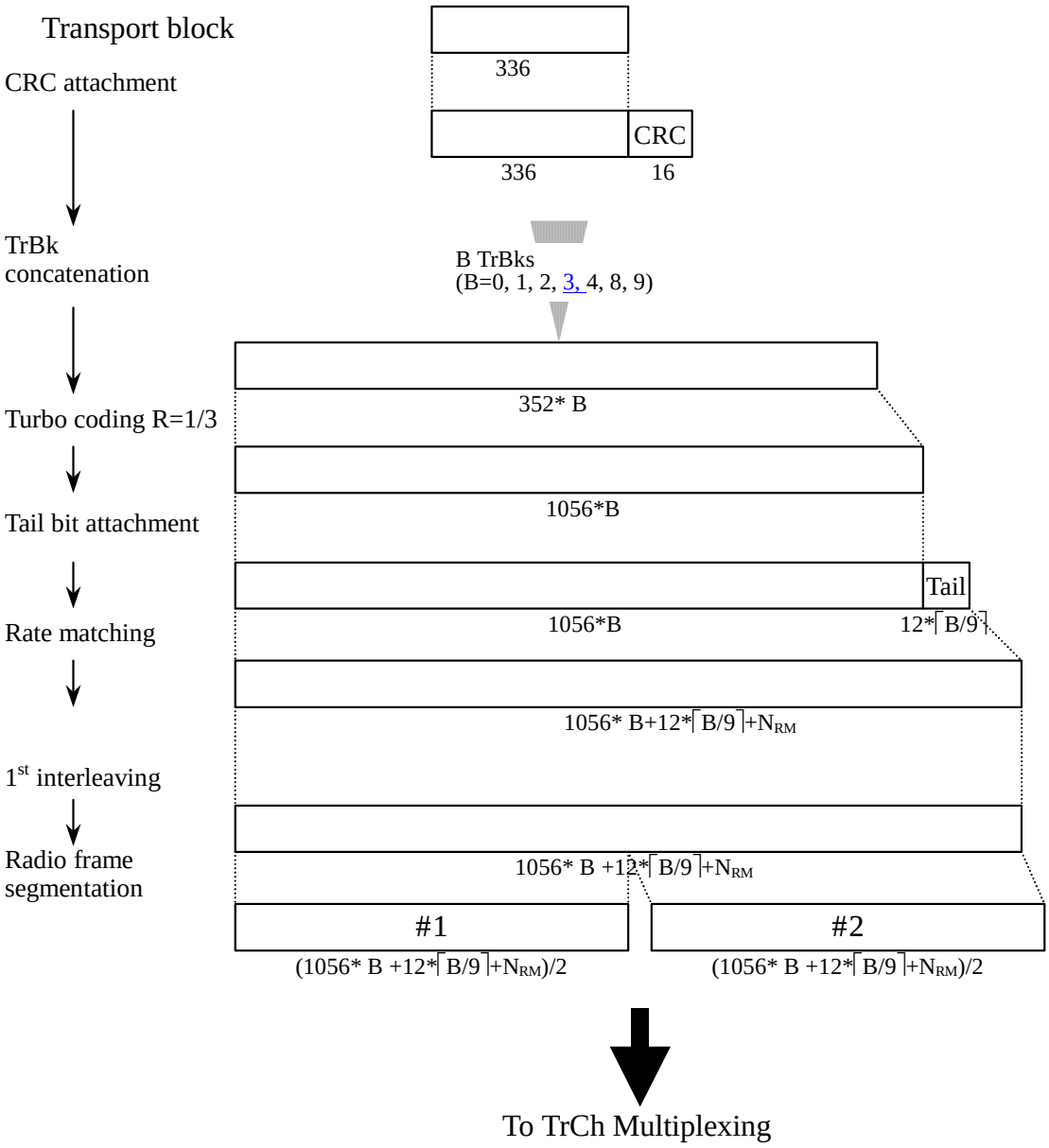


Figure 6: Channel coding and multiplexing example for 64/128/144 kbps packet data

4.1.1.3.1.5 Example for 384 kbps packet data

NOTE: In this example, it is assumed that maximum data rate of RLC payload is 384kbps, and MAC and RLC overhead in a transport block is 16 bits.

Table 7: Parameters for 384 kbps packet data

The number of TrChs	1
Transport block size	336 bits
Transport block Set size	336*B bits (B = 0, 1, 2, 4, 8, 12 for TTI=10 ms, B = 0, 1, 2, 4, 8, 12, 16, 20, 24 for TTI=20 ms)
CRC	16 bits
Coding	Turbo coding, coding rate = 1/3
TTI	10 or 20 ms

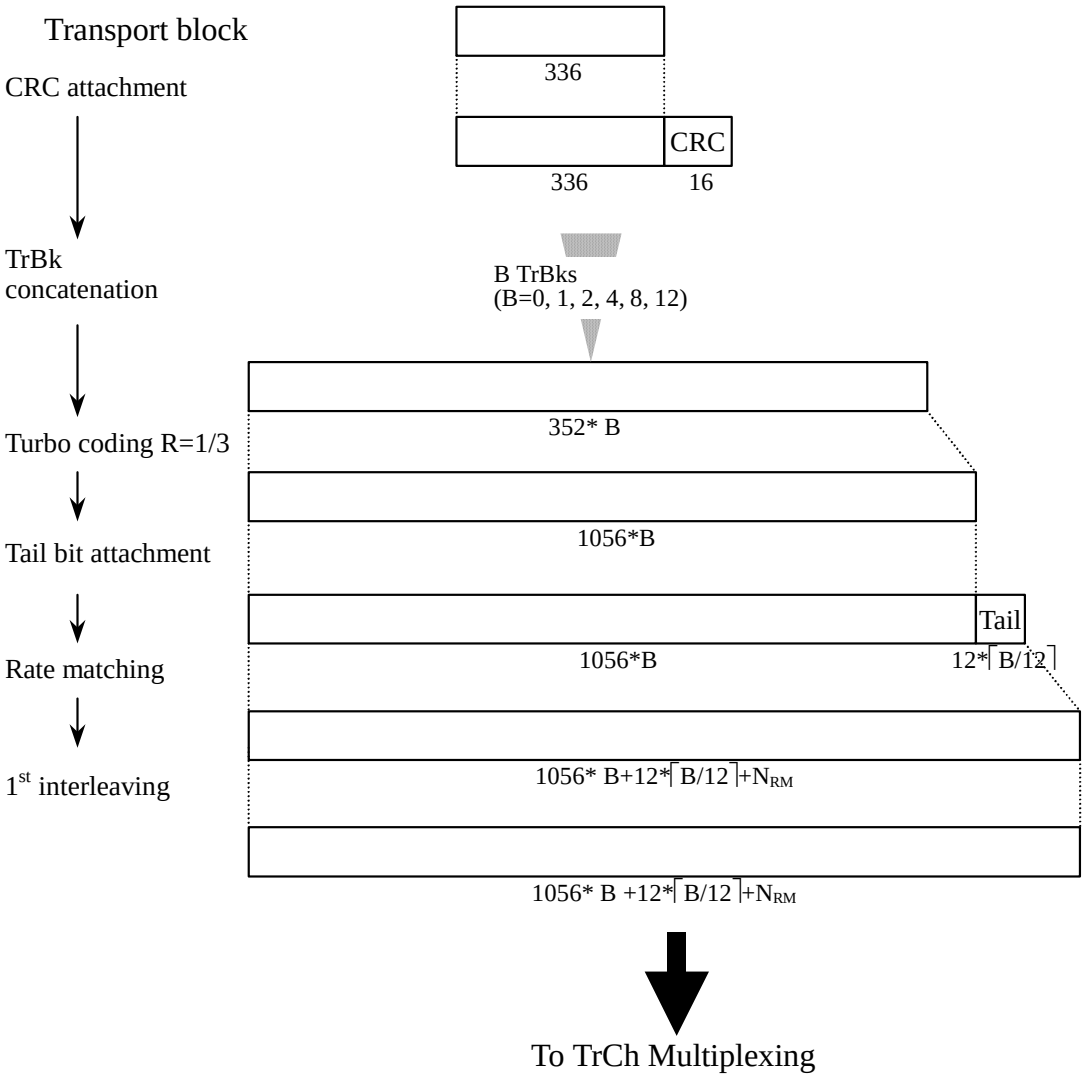


Figure 7: Channel coding and multiplexing example for 384 kbps packet data in case of TTI=10 ms

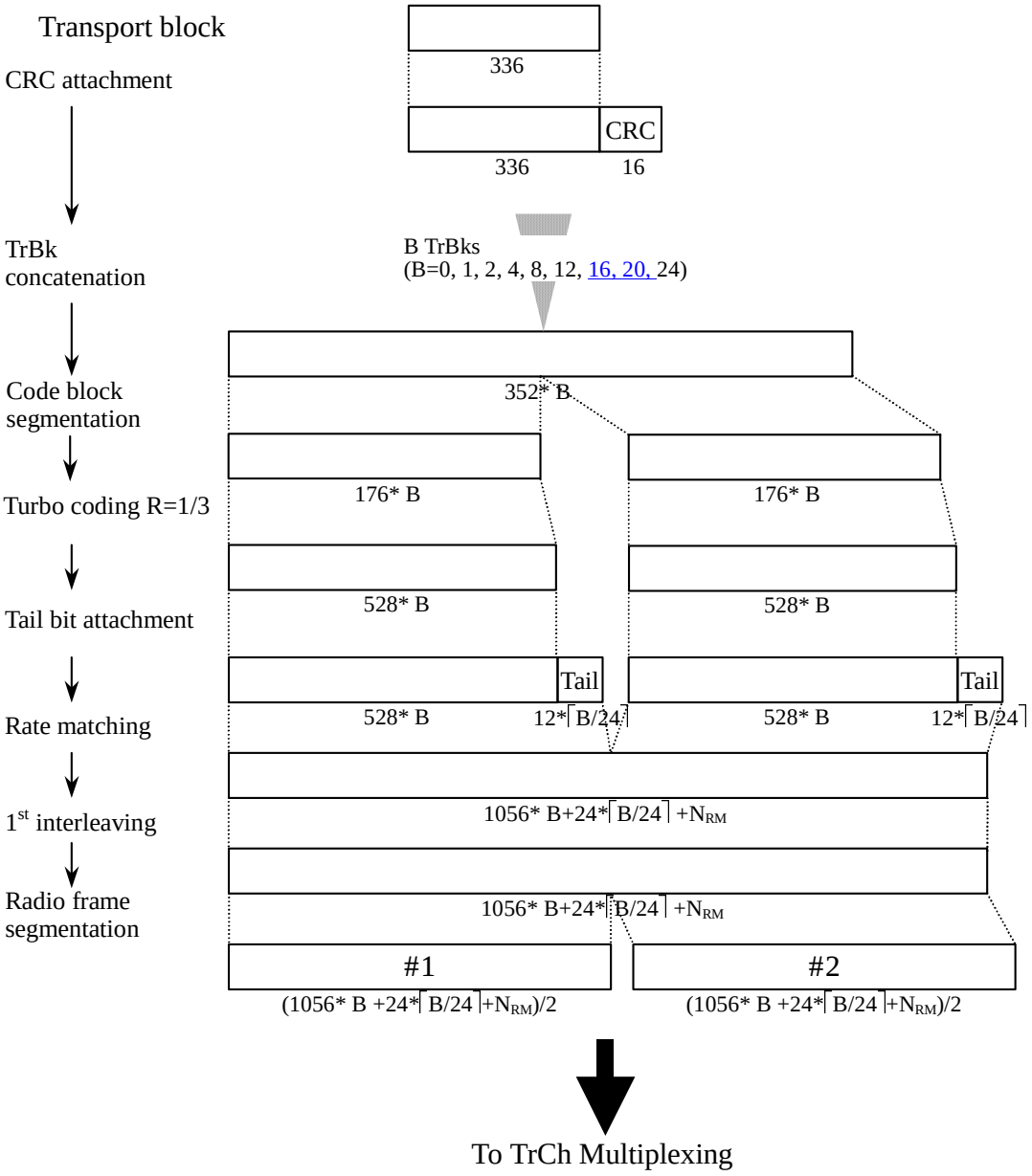


Figure 8: Channel coding and multiplexing example for 384 kbps packet data in case of TTI=20 ms

4.1.1.3.1.6 Example for 64 kbps data

NOTE: This example can be applied to ISDN service.

Table 8: Parameters for 64 kbps data

The number of TrChs	1
Transport block size	640 bits
Transport block set size	4*640 bits
CRC	16 bits
Coding	Turbo coding, coding rate = 1/3
TTI	40 ms

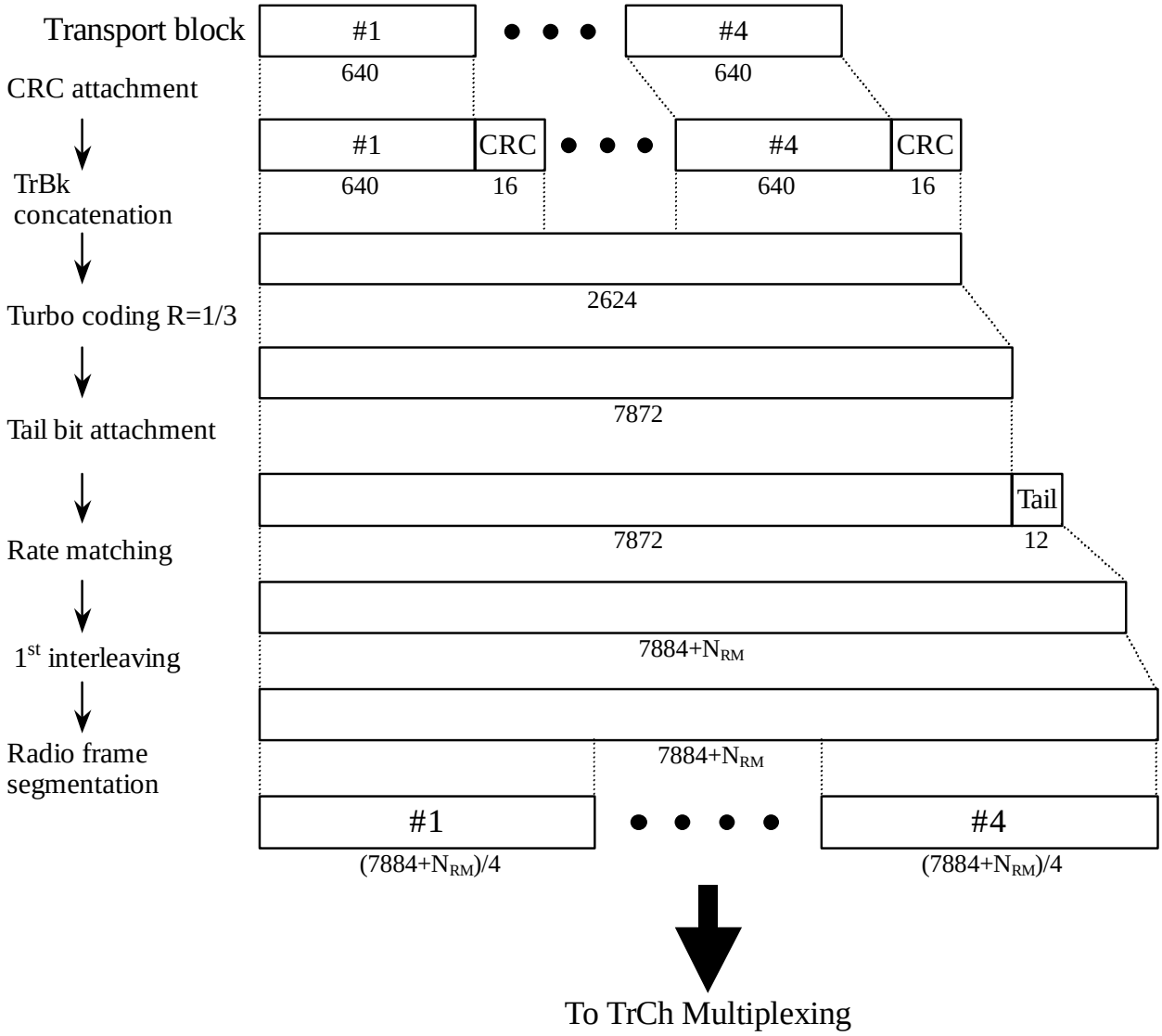


Figure 9: Channel coding and multiplexing example for 64 kbps data

4.1.1.3.2 TrCh multiplexing -> Physical channel mapping

4.1.1.3.2.1 Example for Stand-alone mapping of 3.4 kbps data

NOTE: This example can be applied to Stand-alone mapping of DCCH.

Table 9 shows example of physical channel parameters for stand-alone mapping of 3.4 kbps data.

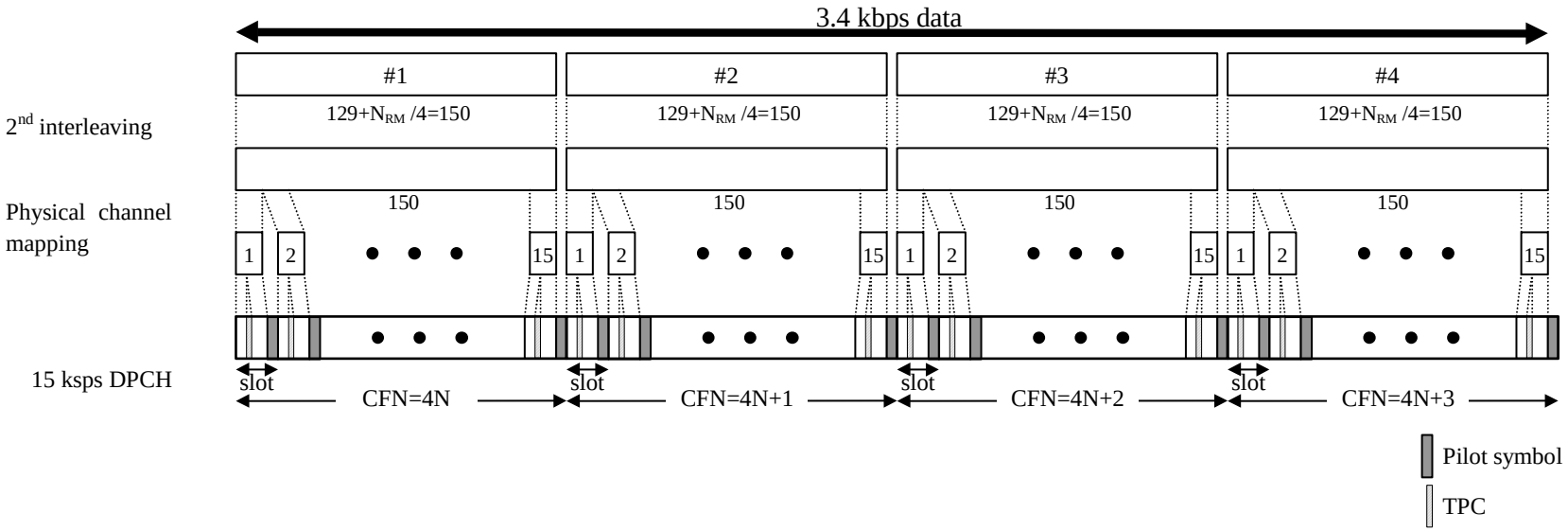


Figure 10: Channel coding and multiplexing example for stand-alone mapping of 3.4 kbps data

Table 9: Physical channel parameters for stand-alone mapping of 3.4 kbps data

Symbol rate (ksps)	N_{pilot} **(bits)**	N_{TFCI} (bits)	N_{TPC} (bits)	N_{data1} (bits)	N_{data2} (bits)
15	4~~8~~	0	2	2	12~~8~~

4.1.2 Uplink

4.1.2.1 Example for RACH

Table 15: Parameter examples for RACH

Transport block size	N_{RACH}=168 or 360 bits
CRC	16 bits
Coding	CC, coding rate = 1/2
TTI	2010 ms
Minimum spreading factor	32

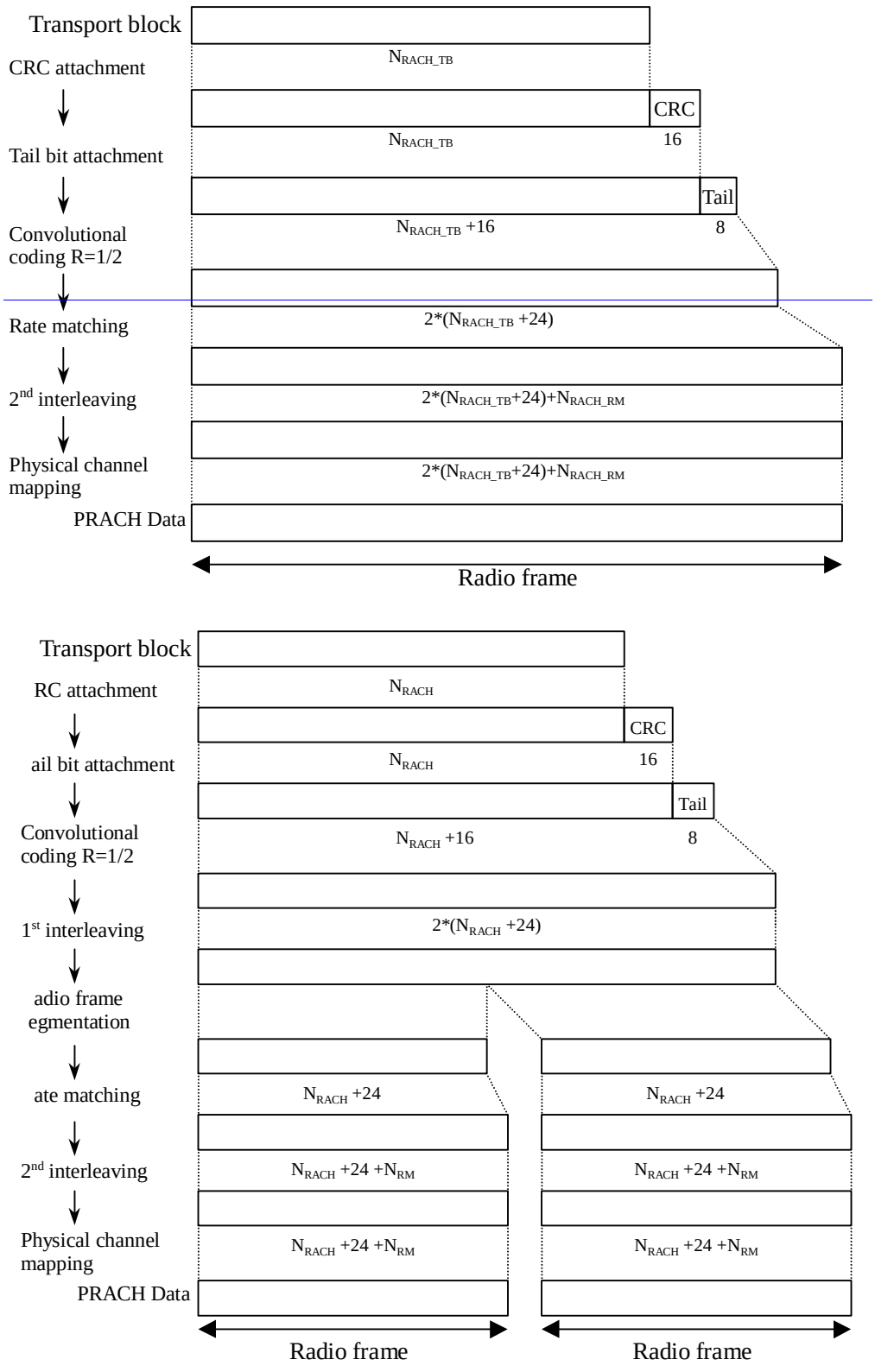


Figure 16: Channel coding and multiplexing example for PRACH

4.1.2.2 Example for DCH

4.1.2.2.1 DCH -> Radio frame segmentation

4.1.2.2.1.1 Example for 3.4 kbps data

NOTE: This example can be applied to DCCH.

NOTE: In this example, it is assumed that maximum data rate of RLC payload is 3.4 kbps, and that MAC and RLC overhead in a transport block is 12 bits.

Table 16: Parameter examples for 3.4 kbps data

Transport block size	148 bits
Transport block set size	0, 148 bits
CRC	16 bits
Coding	CC, coding rate = 1/3
TTI	40 ms

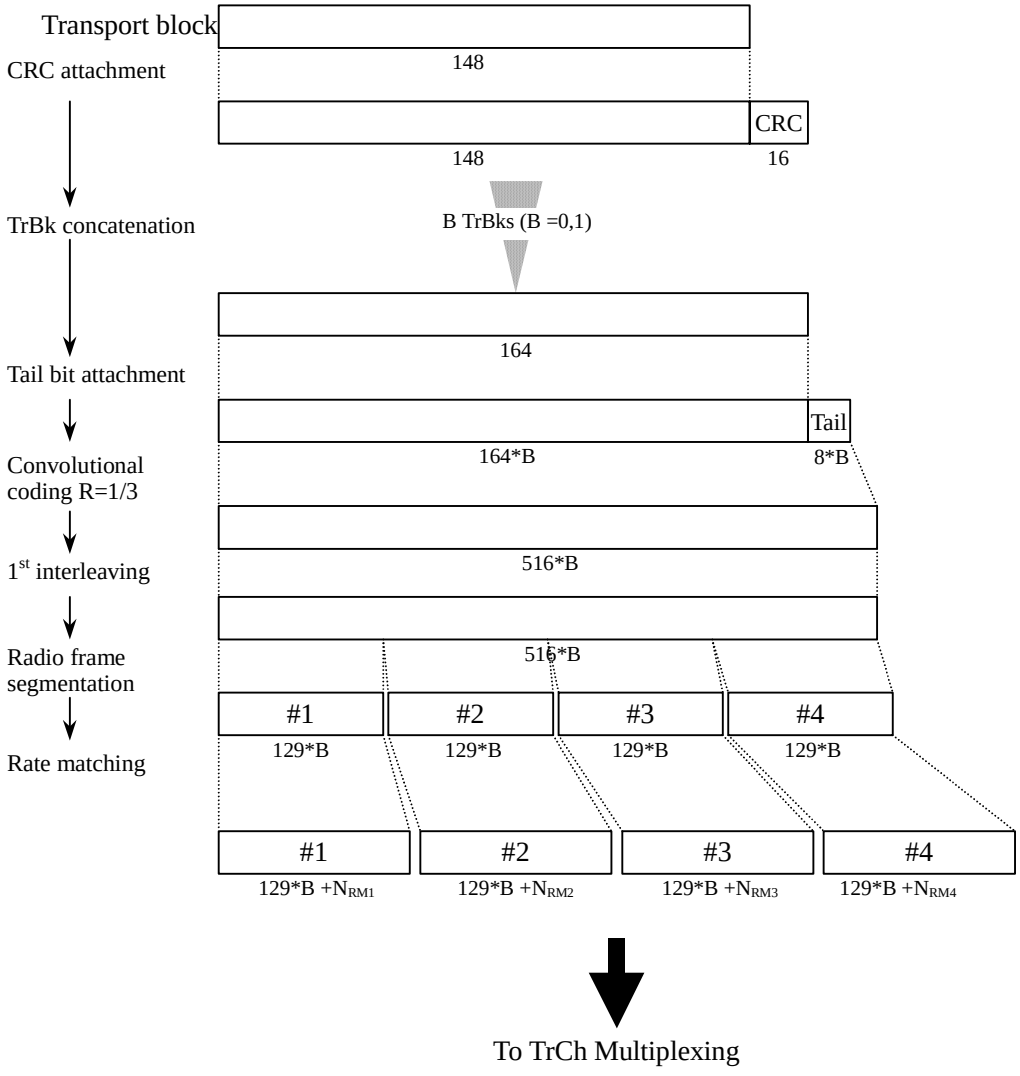


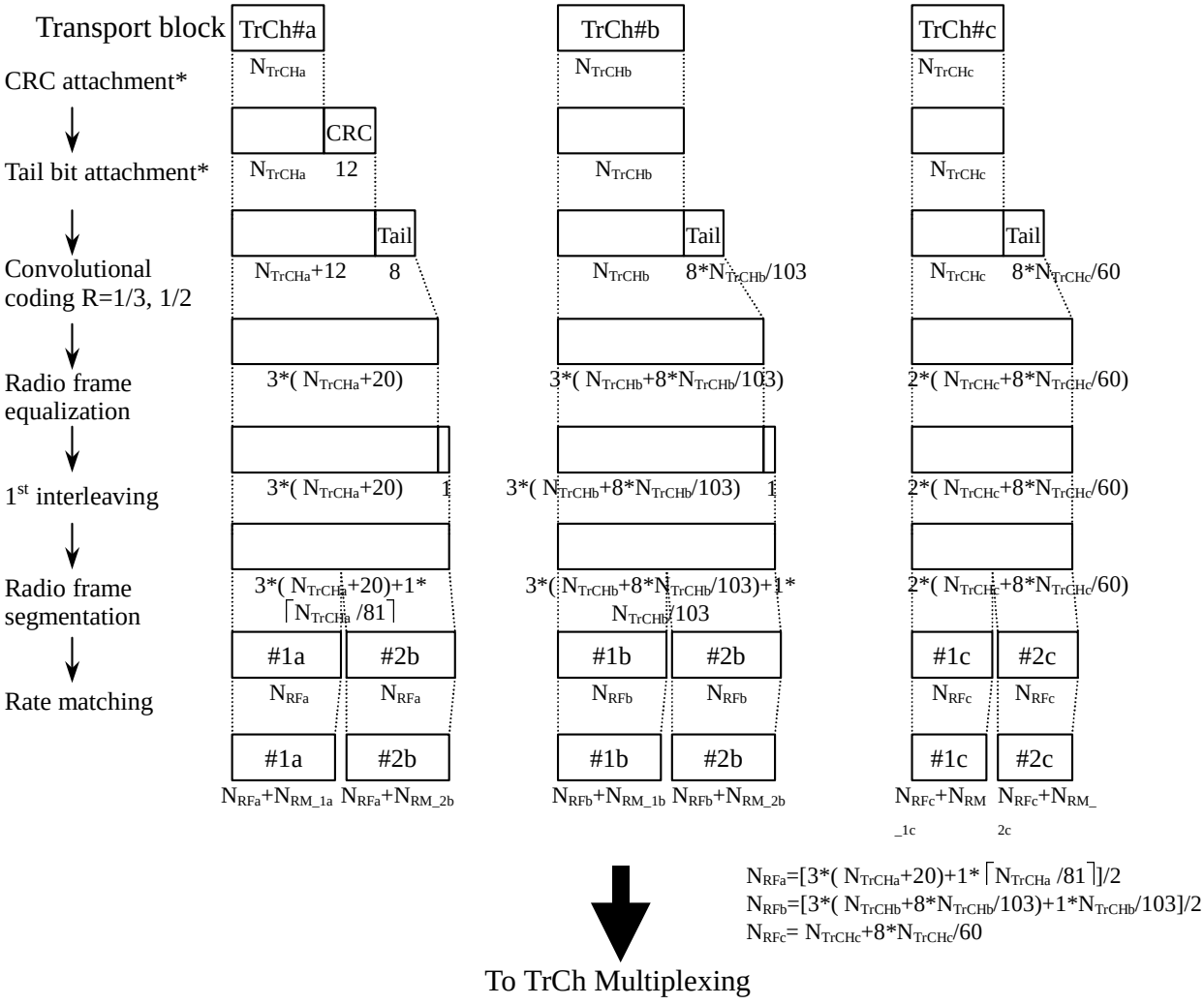
Figure 17: Channel coding and multiplexing example for 3.4 kbps data

4.1.2.2.1.2 Example for 12.2 kbps data

NOTE: This example can be applied to AMR speech.

Table 17: Parameter examples for 12.2 kbps data

The number of TrChs		3
Transport block size	TrCH#a	~~N_{TrCHa}=0,~~ 39 or 81bits
	TrCH#b	~~N_{TrCHb}=0 or~~ 103 bits
	TrCH#c	~~N_{TrCHc}=0 or~~ 60 bits
TFCS	#1	N_{TrCHa}=1*81, N_{TrCHb}=1*103, N_{TrCHc}=1*60 bits
	#2	N_{TrCHa}=1*39, N_{TrCHb}=0*103, N_{TrCHc}=0*103 bits
	#3	N_{TrCHa}=0*810, N_{TrCHb}=0*103, N_{TrCHc}=0*60 bits
CRC		12 bits (attached only to TrCh#a)
~~CRC parity bit attachment for 0 bit transport block~~		~~Applied only to TrCH#a~~
Coding		CC, coding rate = 1/3 for TrCh#a, b coding rate = 1/2 for TrCh#c
TTI		20 ms



~~* CRC and tail bits for TrCH#a is attached even if N_{TrCHa}=0 bits since CRC parity bit attachment for 0 bit transport block is applied.~~

Figure 18: Channel coding and multiplexing example for 12.2 kbps data

4.1.2.2.1.3 Example for 28.8/57.6 kbps data

NOTE: This example can be applied to Modem or FAX.

Table 18: Parameters for 28.8/57.6 kbps packet data

The number of TrChs		1
Transport block size		576 bits
Transport block Set size	28.8 kbps	576*B bits (B = 0, 1, 2)
	57.6 kbps	576*B bits (B = 0, 1, 2, 3, 4)
CRC		16 bits
Coding		Turbo coding, coding rate = 1/3
TTI		40 ms

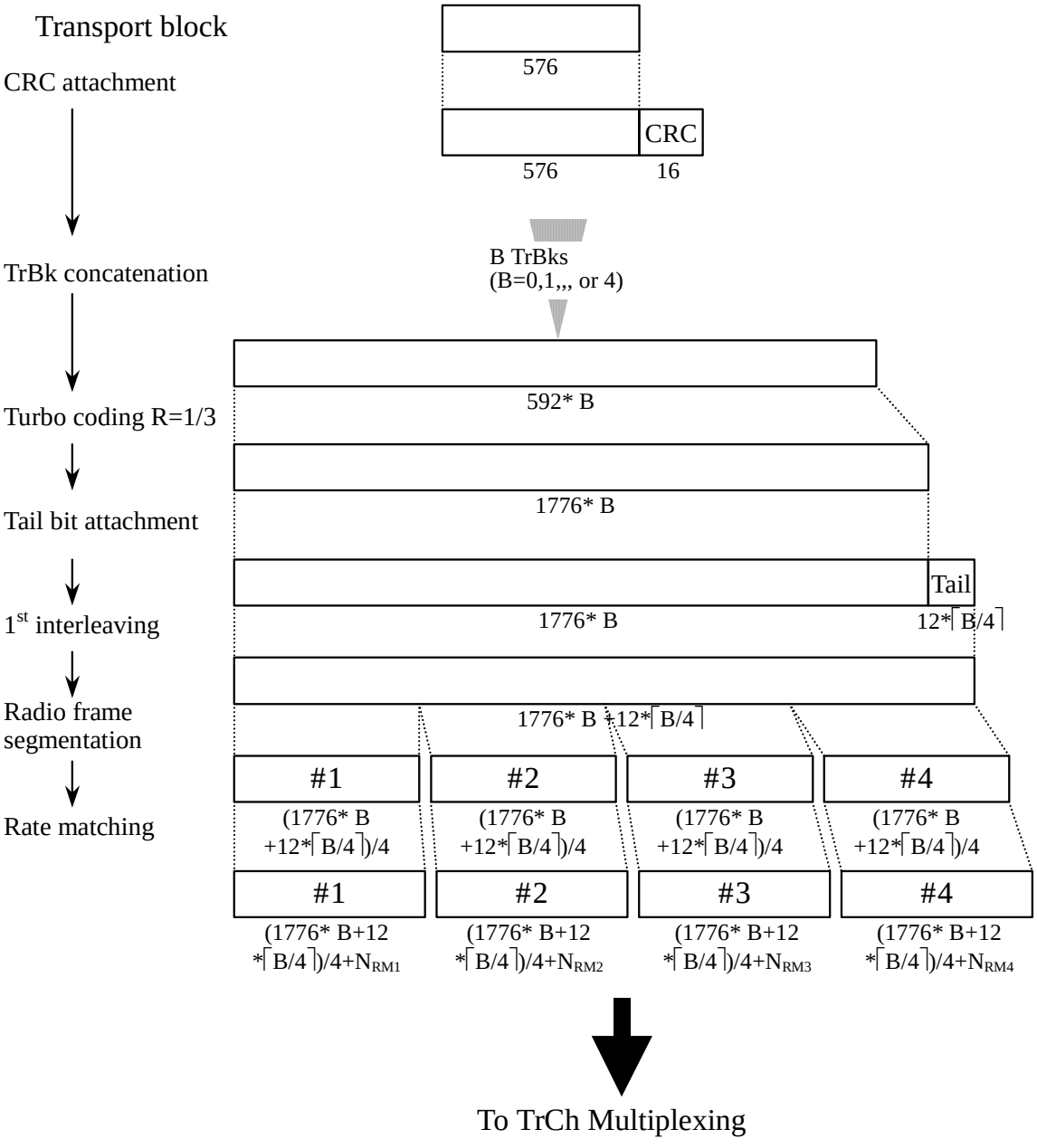


Figure 19: Channel coding and multiplexing example for ~~64/128/144~~28.8/57.6 kbps ~~packet~~ data

4.1.2.2.1.4 Example for 64/128/144 kbps packet data

NOTE: In this example, it is assumed that maximum data rate of RLC payload is 64/128/144 kbps, and MAC and RLC overhead in a transport block is 16 bits.

Table 19: Parameters for 64/128/144 kbps packet data

The number of TrChs		1
Transport block size		336 bits
Transport block Set size	64 kbps	336*B bits (B = 0, 1, 2, 3, 4)
	128 kbps	336*B bits (B = 0, 1, 2, 4, 8)
	144 kbps	336*B bits (B = 0, 1, 2, 4, 8, 9)
CRC		16 bits
Coding		Turbo coding, coding rate = 1/3
TTI		20 ms

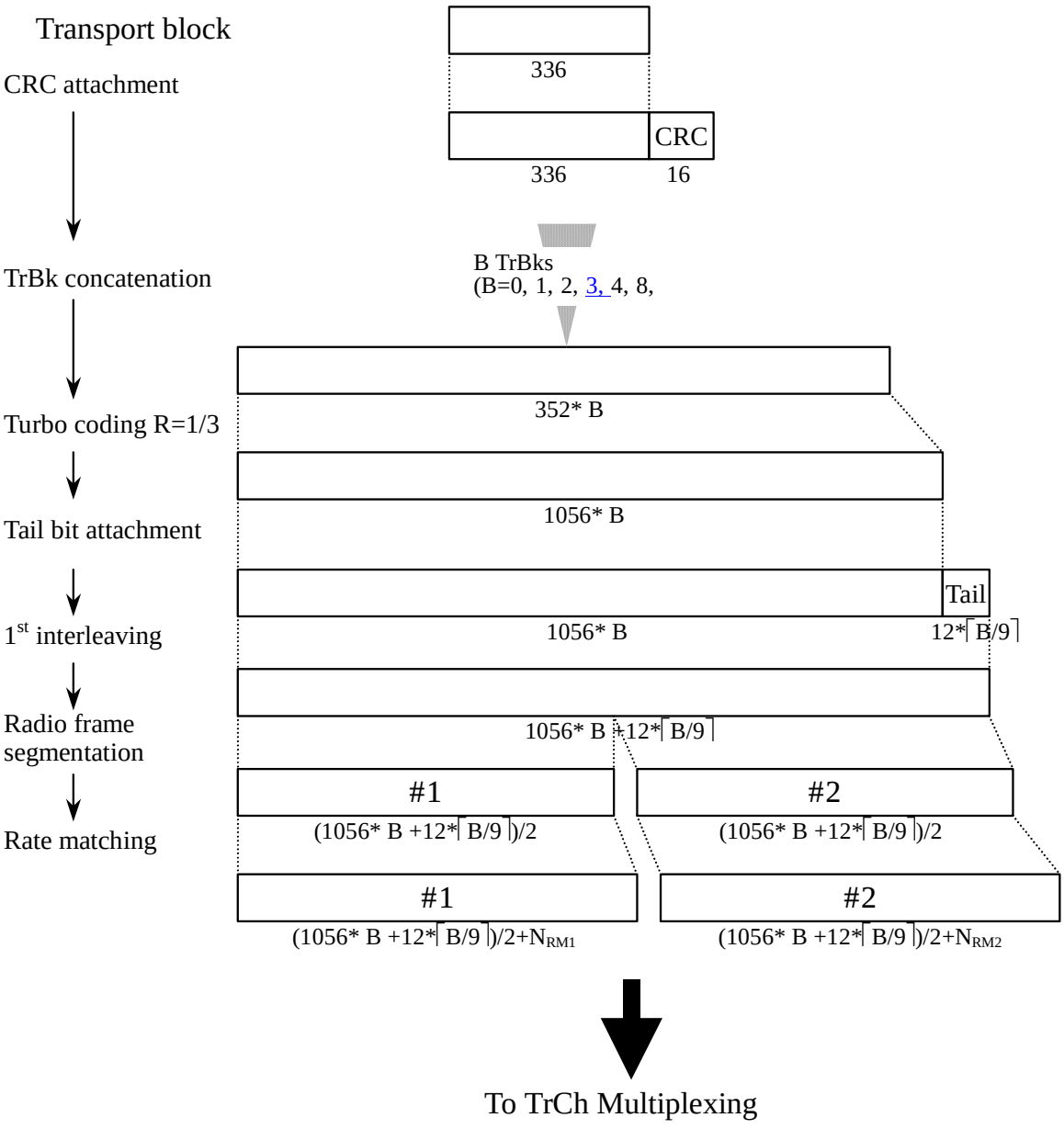


Figure 20: Channel coding and multiplexing example for 64/128/144 kbps packet data

4.1.2.2.1.5 Example for 384 kbps packet data

NOTE: In this example, it is assumed that maximum data rate of RLC payload is 384kbps, and MAC and RLC overhead in a transport block is 16 bits.

Table 20: Parameters for 384 kbps packet data

The number of TrChs		1
Transport block size		336 bits
Transport block Set size	384 kbps	336*B bits (B = 0, 1, 2, 4, 8, 12, 16, 20, 24)
CRC		16 bits
Coding		Turbo coding, coding rate = 1/3
TTI		20 ms

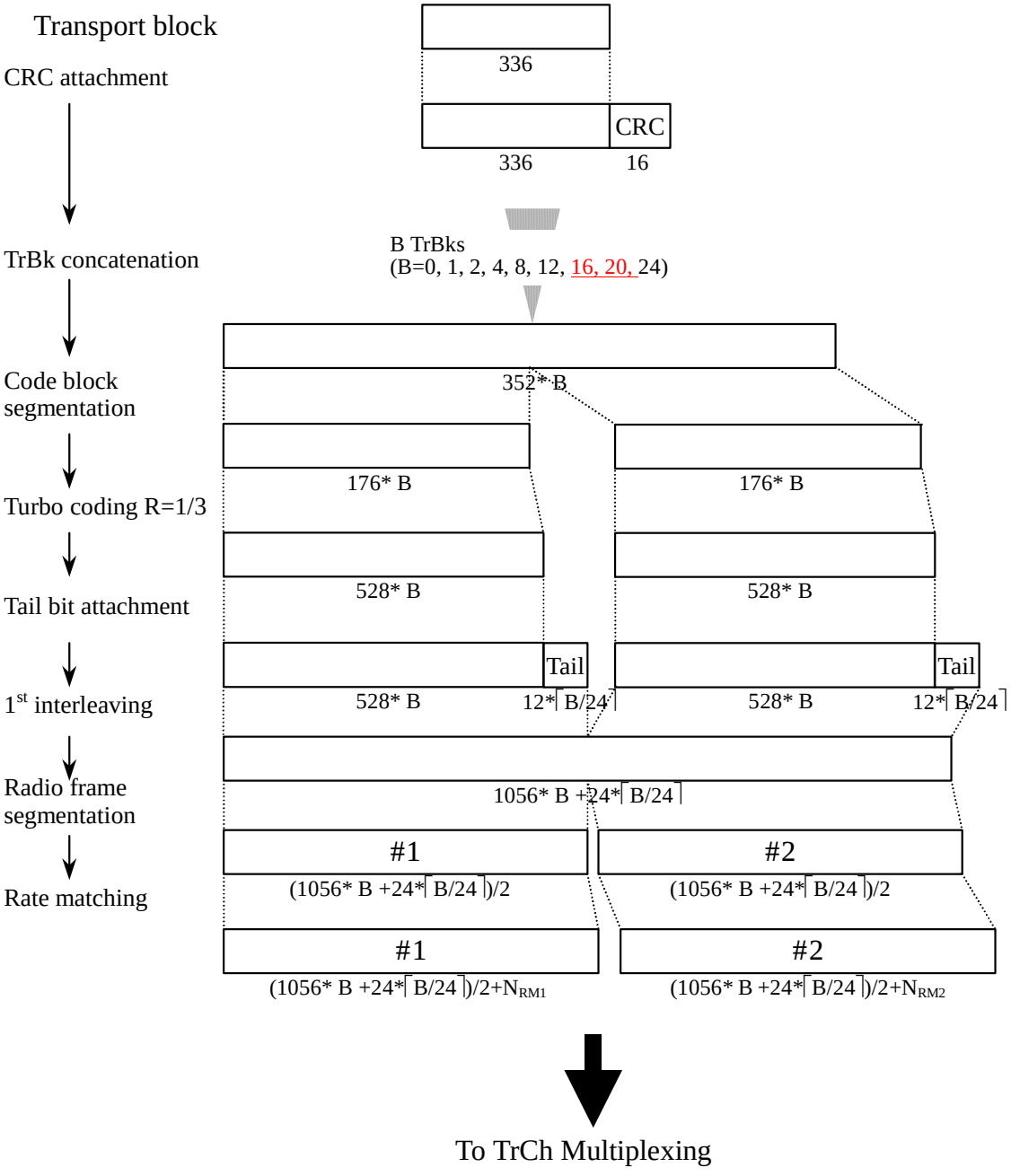


Figure 21: Channel coding and multiplexing example for 384 kbps packet data

3GPP TSG RAN Meeting #10
Bangkok, Thailand, 6-8, December 2000

Document R1-00-0997
e.g. for 3GPP use the format TP-99xxx
or for SMG, use the format P-99-xxx

CHANGE REQUEST

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TR 25.944	CR	004	Current Version:	3.2.0

GSM (AA.BB) or 3G (AA.BBB) specification number ↑ *↑ CR number as allocated by MCC support team*

For submission to: RAN #10	for approval	X	strategic		(for SMG use only)
list expected approval meeting # here ↑	for information		non-strategic		

Form: CR cover sheet, version 2 for 3GPP and SMG *The latest version of this form is available from: ftp://ftp.3gpp.org/Information/CR-Form-v2.doc*

Proposed change affects: *(at least one should be marked with an X)* (U)SIM [ ] ME [ ] UTRAN / Radio [X] Core Network [ ]

Source: TSG RAN WG1 **Date:** 22.11.2000

Subject: TDD related changes for TR25.944, update

Work item: TR 25.944

Category: *(only one category shall be marked with an X)*

F	Correction	X	**Release:**	Phase 2	
A	Corresponds to a correction in an earlier release			Release 96	
B	Addition of feature			Release 97	
C	Functional modification of feature			Release 98	
D	Editorial modification			Release 99	X
				Release 00	

Reason for change: This CR includes corrections for TDD part of TR 25.944, which is based on technical reasons. These corrections are aligned with latest version of the Typical Radio Parameter Sets from GSMA ISG version 1.3.

Clauses affected: 4.2.1.2, 4.2.1.3.1.2, 4.2.1.3.1.4, 4.2.1.3.1.4

Other specs affected:

Other 3G core specifications	[ ]	→ List of CRs:
Other GSM core specifications	[ ]	→ List of CRs:
MS test specifications	[ ]	→ List of CRs:
BSS test specifications	[ ]	→ List of CRs:
O&M specifications	[ ]	→ List of CRs:

Other comments:


help.doc

<--------- double-click here for help and instructions on how to create a CR.

4.2.1.2 Example for PCH and FACH

Table 26: Parameters for PCH and FACH

Transport block size	PCH	N_{PCH}=~~64~~ 80 or 240 bits
	FACH1	360 bits
	FACH2	168 bits
Transport block set size	PCH	~~64~~80*B_{PCH} or 240*B_{PCH} bits (B_{PCH}=0,1)
	FACH1	360*B_{FACH1} bits (B_{FACH1}=0,1)
	FACH2	168*B_{FACH2} bits (B_{FACH2}=0,1,2~~,3~~)
Coding	PCH, FACH2	CC, coding rate = 1/2
	FACH1	TC
TTI		10 ms
Midamble		512 chips
Codes and time slots		SF = 16 x 2 x 1 time slot
TFCI		16 bit
TPC		0 bit

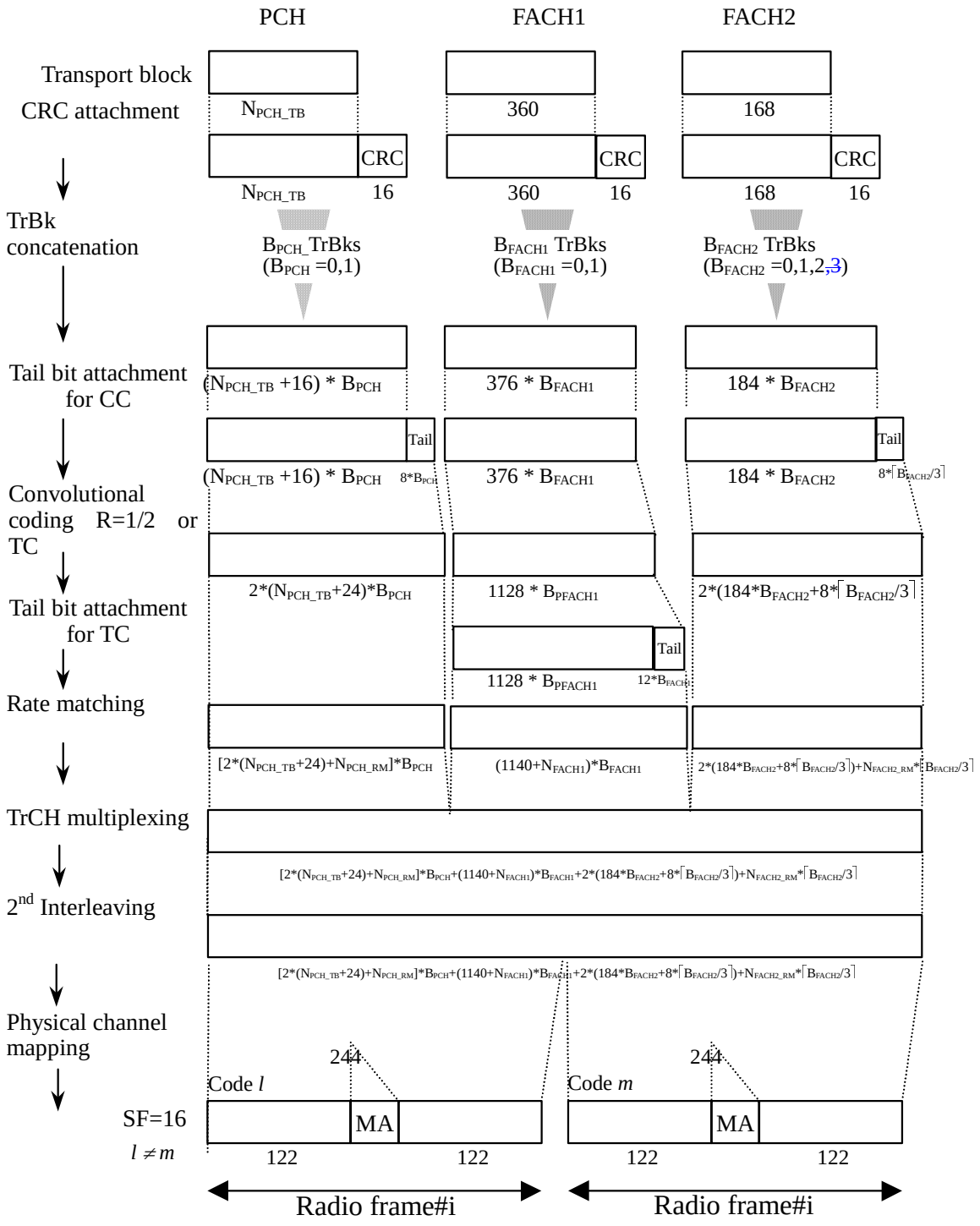


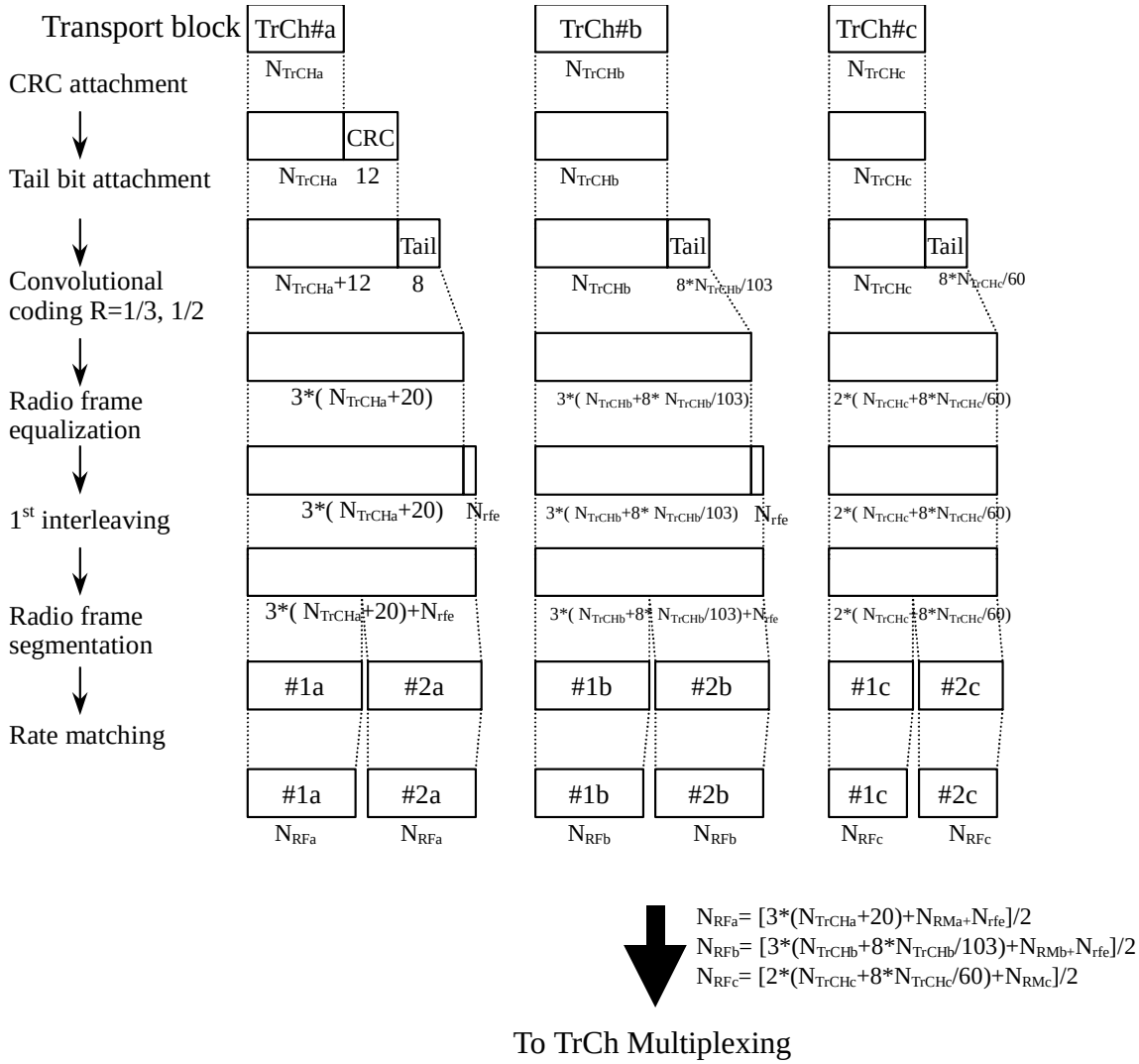
Figure 30: Channel coding and multiplexing example for PCH and FACH

4.2.1.3.1.2 Example for 12.2 kbps data

NOTE: This example can be applied to AMR speech.

Table 28: Parameter examples for 12.2 kbps data

The number of TrChs		3
Transport block size	TrCH#a	~~N_{TrCHa}= 0,~~ 39 or 81 bits
	TrCH#b	~~N_{TrCHb}= 0 or~~ 103 bits
	TrCH#c	~~N_{TrCHc}= 0 or~~ 60 bits
Transport block set size	#1	N_{TrCHa}=1*81, N_{TrCHb}=1*103, N_{TrCHc}=1*60 bits
	#2	N_{TrCHa}=1*39, N_{TrCHb}=0*103, N_{TrCHc}=0*60 bits
	#3	N_{TrCHa}=0*81, N_{TrCHb}=0*103, N_{TrCHc}=0*60 bits
CRC		12 bits (attached only to TrCh#a)
~~CRC parity bit attachment for 0 bit transport block~~		~~Applied only to TrCH#a~~
Coding TTI		CC, coding rate = 1/3 for TrCh#a, b coding rate = 1/2 for TrCh#c
		20 ms



N_{RFa}= [3*(N_{TrCHa}+20)+$N_{RMa+}N_{rfe}$]/2
N_{RFb}= [3*(N_{TrCHb}+8*N_{TrCHb}/103)+$N_{RMb+}N_{rfe}$]/2
N_{RFc}= [2*(N_{TrCHc}+8*N_{TrCHc}/60)+N_{RMc}]/2

~~*** CRC and tail bits for TrCH#a is attached even if N_{TrCHa}=0 bits since CRC parity bit attachment for 0 bit transport block is applied.**~~

Figure 32: Channel coding and multiplexing example for 12.2 kbps data

4.2.1.3.1.4 Example of 64/128/144 kbps packet data

NOTE: In this example it is assumed, that maximum data rate of RLC payload is 64/128/144 kbps, and MAC and RLC overhead in a transport block is 16 bits.

Table 30: Parameters for 64/128/144 kbps packet data

The number of TrChs		1
Transport block size		336 bits
Transport block set size	64 kbps	336*B bits (B = 0, 1, 2, 3, 4)
	128 kbps	336*B bits (B = 0, 1, 2, 4, 8)
	144 kbps	336*B bits (B = 0, 1, 2, 4, 8, 9)
CRC		16 bits
Coding		Turbo coding, coding rate = 1/3
TTI		20 ms

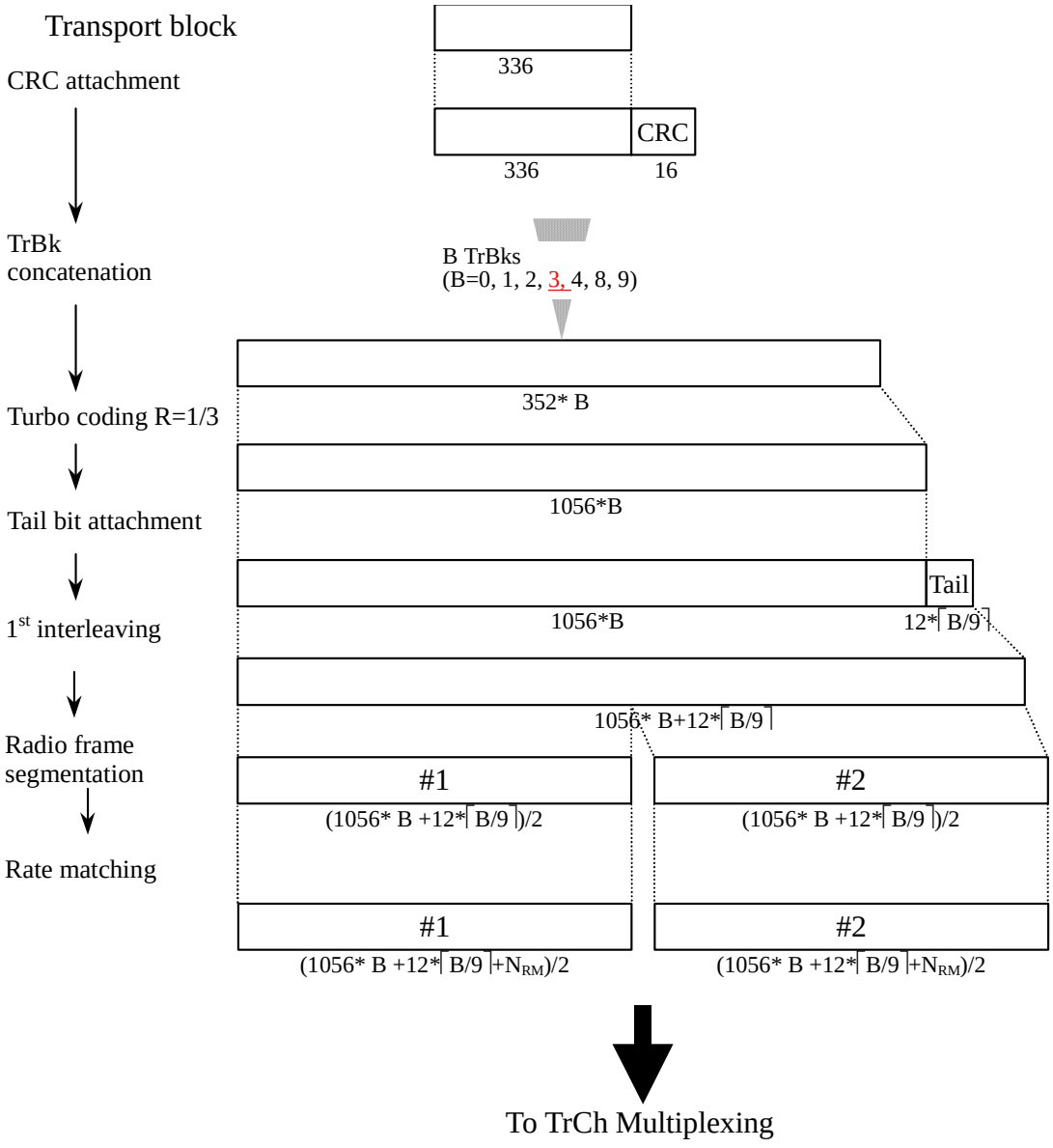


Figure 34: Channel coding and multiplexing example for 64/128/144 kbps packet data

4.2.1.3.1.5 Example of 384 kbps packet data

NOTE: In this example it is assumed, that the maximum data rate of RLC payload is 384 kbps, and MAC and RLC overhead in a transport block is 16 bits.

Table 31: Parameters for 384 kbps packet data

The number of TrChs	1
Transport block size	336 bits
Transport block set size	336*B bits (B = 0, 1, 2, 4, 8, 12 for TTI=10ms, B=0,1,2,4,8,12, 16, 20, 24 for TTI=20ms)
CRC	16 bits
Coding	Turbo coding, coding rate = 1/3
TTI	10 or 20 ms

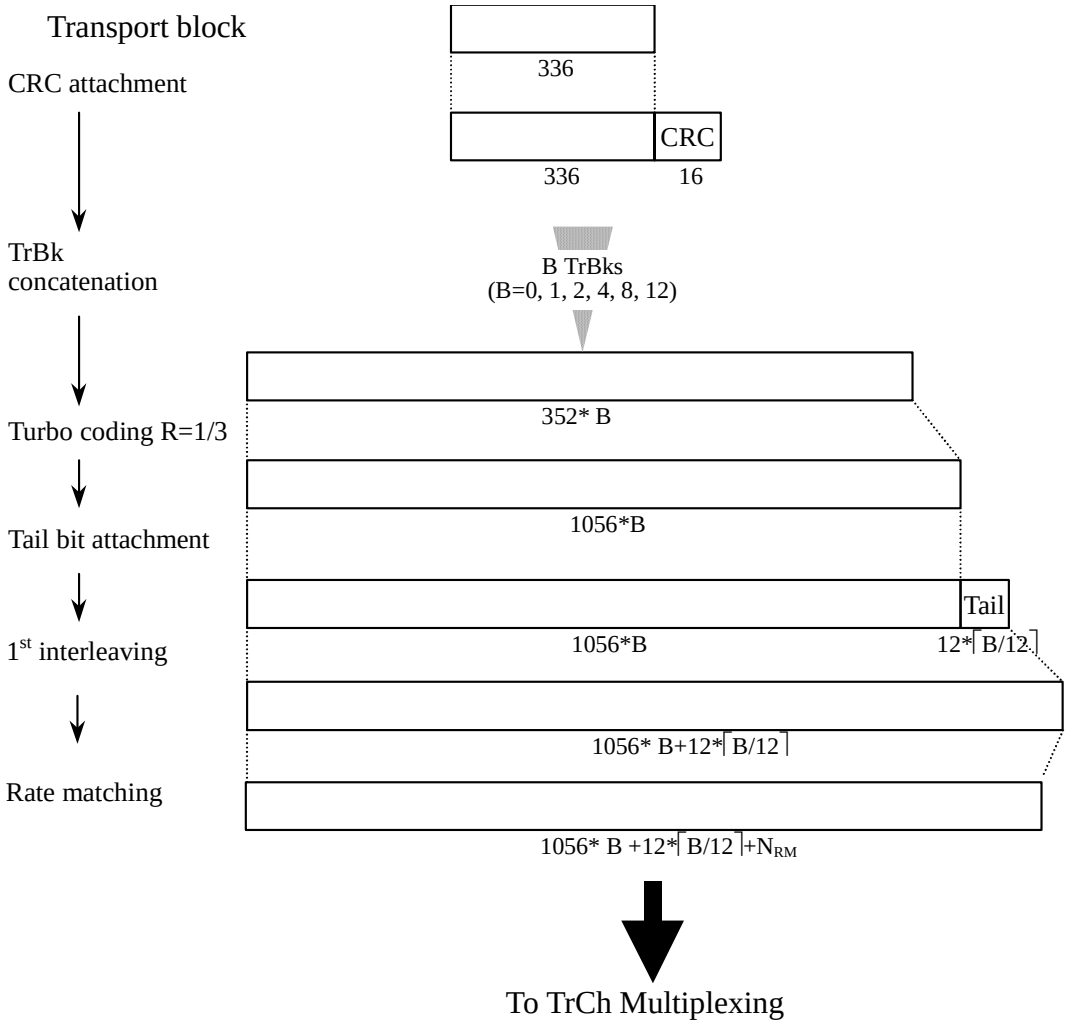


Figure 35: Channel coding and multiplexing example for 384 kbps packet data in case of TTI=10ms

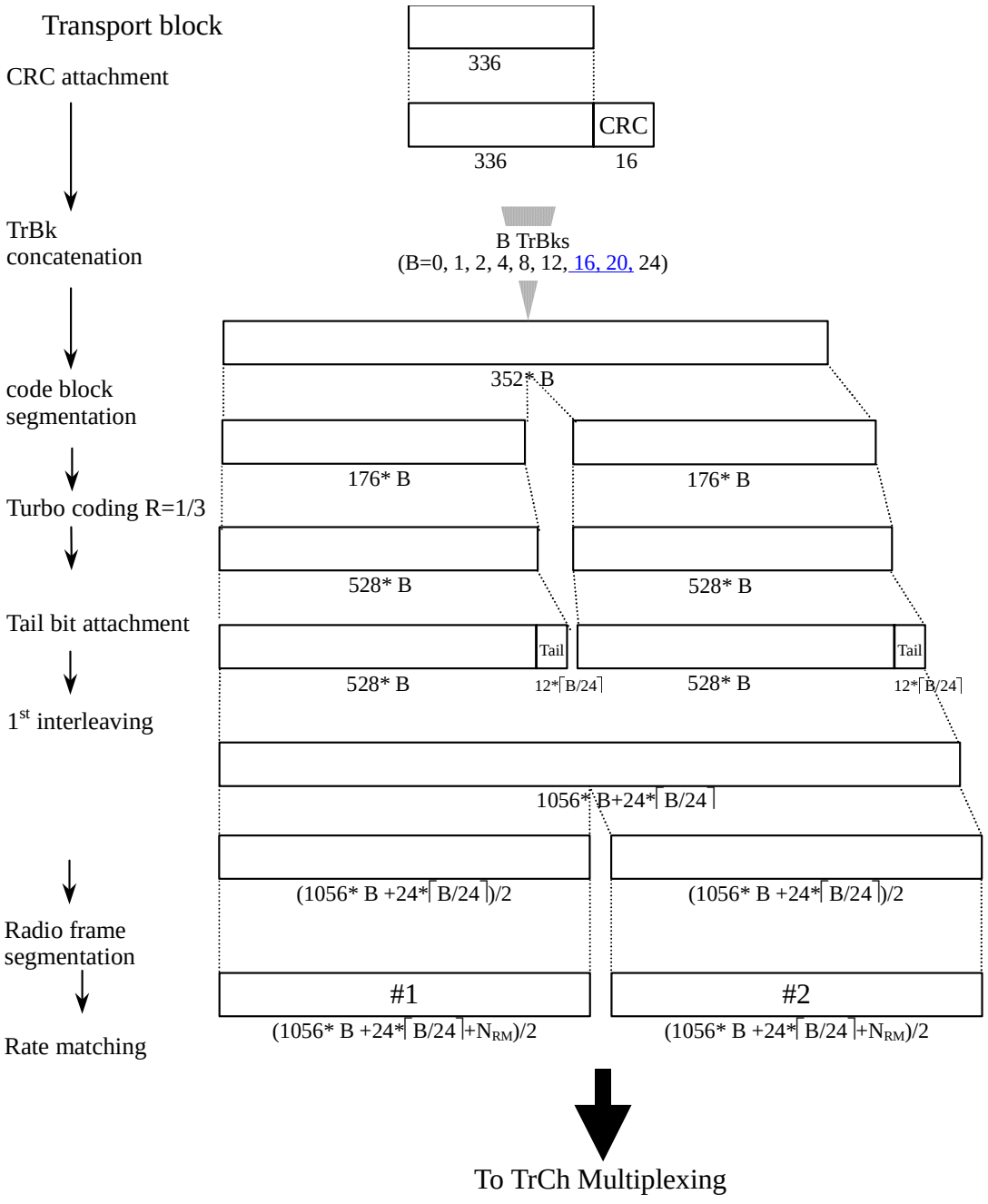


Figure 36: Channel coding and multiplexing example for 384 kbps packet data in case of TTI=20ms