Source :   RAN WG1 Chairman

Title: Revised TSG RAN WG1 time plan for Year 2000

The following time-plan is submitted for approval and discussion in RAN plenary.
(Changes against Tdoc RP-000089 are shown in bold)

The work in year 2000 will be prioritised according to the following order:
- 1st priority: Corrections and clarifications of release 99 specifications
- 2nd Specification/feasibility study on release 00 work items

RELEASE 1999 ISSUES

For the work in year 2000, but still part of release-99, only very minor items have been identified at this point, such as out-of-synch default parameter reconfiguration by higher layers and downlink power control alignment with RAN WG3 (from the latter the issue is on the level of normative downlink inner loop power control specification in WG1/WG3)

The identified minor Release -99 issues are proposed to be solved with following schedule.
RAN WG1#12 (4/2000) is proposed to have joint Ad Hoc with WG3 on 11th of April 2000 on downlink power control and to clarify the necessary changes.

Also minor issue on out of synch parameterisation is to be solved at RAN WG1#12

Related CRs to be presented to TSG RAN#8.

RELEASE 2000 ISSUES

The following schedule is proposed for the Release 2000 work items with respect to RAN WG1.
(NOTE: THIS TO BE REVISED SUBJECT TO RAN APPROVAL OF WORK ITEMS)
Note also that the names do not exactly map to the WI names and plan will be then updated accordingly)

- TDD 1.28 Mchips functionality.
  RAN#8 Decisions whether to reflect the issue in the existing TDD (Rel.00) specifications or whether to create 1.28 Mchips/s specific specifications.
  RAN#9 & RAN 10 CRs for approval with main part of the CRs for RAN#9. New specifications (if any) under CR procedure from RAN#9 onwards (i.e. version 3.0.0 after RAN#9)

- Terminal power saving features (Techniques like DPCCH **gating** & Paging channel configuration change indicator)
  Principles agreed and presented to RAN#8 for co-ordination with other RAN WGs
  CRs presented for approval for RAN#9

- Compressed mode improvements
  - Summary of the methods for RAN#8 for co-ordination with other RAN WGs.
  - CRs for RAN#9 for approval.

- Downlink packet access high bit rate feasibility study
  - Results of the feasibility study from WG1 presented for RAN#10 approval for Rel.01 work.
  - Progress will be reported earlier (RAN#9 and RAN#8 if progress is made)

- Packet data Hybrid ARQ feasibility study
  - Results for RAN#8 for co-ordination with other RAN WGs
  - CRs RAN#9

- Radio link performance enhancements (including TX diversity enhancements or power control improvements with DCH or DSCH)
  - CRs for approval in RAN#9 (as WG4 needs to be given time to derive possible performance requirements)

- Uplink packet data optimisation (Proposed technologies such as FAUSCH)
  - Status update and co-ordination wit other RAN WGs **RAN#9**
  - CRs of the proposal for **RAN#10** approval.

- **"DL CPCH" (<u>Name to be changed</u>!, e.g. Improvements for downlink packet data on FACH/CPCH state, etc….)**
  - **Results for TSN RAN#10**

- **Location Service Support in the physical layer (TDD)**
  - **Results for TSG RAN#9**

- **Node B synchronisation (TDD)**
  - **Results for TSG RAN#9**

  REFERENCES (WI descriptions)

  TDoc 0032 Downlink packet data
  TDoc 0053, Support of Location Services in UTRA TDD
  TDoc 0054 Hybrid ARQ II/III
  TDoc 0055 NodeB Synchronisation for UTRA TDD
  TDoc 0056 DL CPCH
  TDoc 0057 Low Chip Rate TDD Option
  TDoc 0084 FAUSCH
  TDoc 0121 Gated DPCCH transmission
  TDoc 0158 Compressed mode improvements
  TDoc 0161 Radio link performance enhancements